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| 0xVM |
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Glossary

Registers and Operands

**PC** – Program counter

Stores the next instruction to execute.

**ACC** – Accumulator

Stores the result of the last arithmetic operation.

**SP** – Stack pointer

**FP** – Frame pointer

**Rd** – Destination (and source) register

**Rr** – Source register

**K** – Constant data

**k** – Constant address

**mX** – Maximum memory address

**rX** – Highest register

**s** – Bit in status register

**sX** – Highest bit in status register

Status Register

**SR** – Status register

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |

**Z –** Zero flag

1 if last operation resulted in zero.

**O** – Overflow flag

1 if last operation resulted in an overflow.

Instruction set

HALT – Halt

**Description:**

Halts the program.

**Operation:** None

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| HALT | None | None |

**Opcode:** 1111 1111

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | **-** | **-** |

NOP – No Operation

**Description:**

Does nothing.

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| NOP | None | PC 🡨 PC + 1 |

**Opcode:** 0000 0000

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | **-** | **-** |

MOVR – Move to register

**Description:**

Moves a value into a register.

**Operation:** Rd 🡨 K

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| MOVR K, Rd |  | PC 🡨 PC + 1 |

**Opcode:** 0001 0000

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

MOVM – Move to memory

**Description:**

Moves a value into memory.

**Operation:** k 🡨 K

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| MOVM K, k |  | PC 🡨 PC + 1 |

**Opcode:** 0001 0001

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

MOVRR – Move register to register

**Description:**

Moves a register into another register.

**Operation:** Rd 🡨 Rr

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| MOVM Rr, Rd |  | PC 🡨 PC + 1 |

**Opcode:** 0001 0010

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

MOVRM – Move register to memory

**Description:**

Moves a register into memory.

**Operation:** k 🡨 Rr

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| MOVM Rr, k |  | PC 🡨 PC + 1 |

**Opcode:** 0001 0011

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

MOVMR – Move memory to register

**Description:**

Moves a value from memory into a register.

**Operation:** Rd 🡨 k

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| MOVM k, Rd |  | PC 🡨 PC + 1 |

**Opcode:** 0001 0100

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

POP – Pop

**Description:**

Pops a value from the stack into a register and increments SP.

**Operation:** Rd 🡨 k, SP 🡨 SP + 4

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| POP Rd |  | PC 🡨 PC + 1 |

**Opcode:** 0000 0101

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

PUSH – Push

**Description:**

Pushes a value onto the stack and decrements SP.

**Operation:** k 🡨 K, SP 🡨 SP - 4

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| PUSH K |  | PC 🡨 PC + 1 |

**Opcode:** 0001 0101

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

PUSHR – Push register

**Description:**

Pushes a register onto the stack and decrements SP.

**Operation:** k 🡨 Rr, SP 🡨 SP - 4

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| POP Rr |  | PC 🡨 PC + 1 |

**Opcode:** 0001 0110

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

ADD – Add

**Description:**

Adds a value and a register and stores it in ACC.

**Operation:** ACC 🡨 Rr + K

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| ADD K, Rr |  | PC 🡨 PC + 1 |

**Opcode:** 0010 0000

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | x | x |

**Z** – 1 if the operation results in 0

**O** – 1 if the operation overflows

ADDR – Add register

**Description:**

Adds two registers together and stores the result in ACC.

**Operation:** ACC 🡨 Rr1 + Rr2

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| ADD Rr1, Rr2 |  | PC 🡨 PC + 1 |

**Opcode:** 0010 0001

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | x | x |

**Z** – 1 if the operation results in 0

**O** – 1 if the operation overflows

BRBS – Branch if bit is set

**Description:**

Conditional branch. Checks if a bit in the SR is set and branches to the absolute address if true.

**Operation:** If SR(s) = 1 then PC 🡨 k + 1 else PC 🡨 PC + 1

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| BRBS s, k |  | PC 🡨 k + 1  PC 🡨 PC + 1 |

**Opcode:** 0011 0000

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |

BRBS – Branch if bit is set

**Description:**

Conditional branch. Checks if a bit in the SR is set and branches to the absolute address if true.

**Operation:** If SR(s) = 1 then PC 🡨 k + 1 else PC 🡨 PC + 1

|  |  |  |
| --- | --- | --- |
| **Syntax** | **Operands** | **Program counter** |
| BRBS s, k |  | PC 🡨 k + 1  PC 🡨 PC + 1 |

**Opcode:** 0011 0000

**Status Register:**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  |  |  | **O** | **Z** |
|  |  |  |  |  |  | - | - |