



# RISC-V Documentation Template

Version 0.01, 03/2021: Pre-release version

# Table of Contents

Preamble . . . . .	1
Preface . . . . .	2
1. A few basics . . . . .	3
1.1. Headers . . . . .	3
1.2. Code Blocks . . . . .	4
1.3. Hyperlinks and cross references . . . . .	4
1.4. Admonitions and sidebars . . . . .	4
1.5. Stem content . . . . .	5
1.5.1. AsciiDoctor Mathematical . . . . .	5
2. Tables and graphics . . . . .	7
2.1. Some table examples . . . . .	7
2.2. Handling images . . . . .	8
2.3. Unicode symbols . . . . .	9
Appendix A: Terminology . . . . .	11
Index . . . . .	14

# Preamble

This is the preamble, which can contain copyright information.

# Preface

This document demonstrates the use of asciidoc for RISC-V specifications, with the goal of capturing information that will result in effective and efficient collaboration throughout the community.

Asciidoc is the most feature-rich of the popular lightweight markup languages based on markdown. Most of the markup that you will need is simple, and much is similar to what you use for git-flavored Markdown.

It's helpful to think of asciidoc as [Markdown grown up](#). People in tech often have impulses to re-invent it with a brand new lightweight markup language of their own. As appealing as that idea can be, it is inherently flawed. Publishing, like music, can have simple forms, but when fully featured is quite complex. Everyone who has attempted to build upon Markdown to create a simple and feature-rich publishing solution faces the same reality.

RISC-V specifications require the use of asciidoc/asciidoc advanced publishing features. These templates are here to allow you to jump in with a hands-on approach and build the example pdf.

Because asciidoc is gaining in popularity, there are opportunities contributors to their specification while it is still being developed. You might want to view what Dan Allen, who supports asciidoc/asciidoc, is currently doing.

Asciidoc is gaining wider adoption. Feel free to find out about the working group, the specification under development, the various plugins, and ways in which asciidoc is in use.

## *Copyright and licensure*

Copyright and license information can go here.

# Chapter 1. A few basics

Asciidoc is fully documented, and its documentation is actively maintained. This document contains some information on asciidoc markup to get you started.

For details and additional options:

- Asciidoc/asciidoc writers' guide: [asciidoc.org/docs/asciidoc-writers-guide/](https://asciidoc.org/docs/asciidoc-writers-guide/)
- Asciidoc quick reference: [asciidoc.org/docs/asciidoc-syntax-quick-reference/](https://asciidoc.org/docs/asciidoc-syntax-quick-reference/)
- Asciidoc user manual: [asciidoc.org/docs/user-manual/](https://asciidoc.org/docs/user-manual/)

In addition, you have the option of asking questions in the asciidoc discussion list:

As is true of any complex process, asciidoc/asciidoc has some quirks. Please be certain to make use of these templates because they provide you with files that will result in fully featured pdf output.

Best practice is to test the pdf build frequently to ensure that you have not accidentally introduced something that breaks the build.

## WARNING

PDFs require the use of Ruby 2.7.2.

## 1.1. Headers

In asciidoc you cannot jump directly from a Head 1 to a Head 3 or 4. Your headers must appear in numerical sequence from Head 1 to Head 2, and onward. If you skip over a header in the sequence, asciidoc throws an error.

```
= Title head (book or report title)

[colophon]
= Colophon head (in frontmatter, used for preface)

[[chapter_title]]
== Head 1 (chapter)

=== Head 2 (section)

==== Head 3 (subsection)

===== Head 4 (sub-subsection)

[appendix]
== Appendix title

[index]
Index
```

**NOTE**

Settings in the header file trigger auto-generation of Appendix prefixes and of the Index (among other things).

## 1.2. Code Blocks

```
[source,python]
\----
mono-spaced code block
add a1,a2,a3; # do an ADD
\----
```

alternate:

```
\```python
\----
mono-spaced code block
add a1,a2,a3; # do an ADD
\----
\```
```

## 1.3. Hyperlinks and cross references

blah blah

## 1.4. Admonitions and sidebars

Block admonition:

```
[IMPORTANT]
.Feeding the Werewolves
====
While werewolves are hardy community members, keep in mind the following dietary
concerns:

. They are allergic to cinnamon.
. More than two glasses of orange juice in 24 hours makes them howl in harmony
with alarms and sirens.
. Celery makes them sad.
====
```

Sidebar:

```
.AsciiDoc history
****
AsciiDoc was first released in Nov 2002 by Stuart Rackham.
It was designed from the start to be a shorthand syntax
for producing professional documents like DocBook and LaTeX.
****
```

[github.com/asciidoctor/asciidoctor-pdf/blob/master/docs/theming-guide.adoc#key-prefix-admonition-icon](https://github.com/asciidoctor/asciidoctor-pdf/blob/master/docs/theming-guide.adoc#key-prefix-admonition-icon)

Alternate admonitions are available: [github.com/jessedoyle/prawn-icon/tree/master/data/fonts](https://github.com/jessedoyle/prawn-icon/tree/master/data/fonts)

## 1.5. Stem content

The `:stem: latexmath` setting works for HTML output from asciidoc, however, it doesn't yet work for asciidoctor-pdf output.

As a workaround, you will need to create image files rendered equations and import the images for pdfs.

```
[stem]
++++
sqrt(4) = 2
++++
```

```
sqrt(4) = 2
```

It looks like there is not much need for actual equations in the specifications. Rather, there is a set of specific unicode characters that are used:

Asciidoctor PDF currently only supports decimal character references. See [github.com/asciidoctor/asciidoctor-pdf/issues/486](https://github.com/asciidoctor/asciidoctor-pdf/issues/486)

Hexadecimal unicode looks like it has problems in the pdf. This is gnarley.

Updates to asciidoctor-pdf: [github.com/asciidoctor/asciidoctor-pdf](https://github.com/asciidoctor/asciidoctor-pdf)

### 1.5.1. Asciidoctor Mathematical

Asciidoctor Mathematical is an extension for Asciidoctor that provides alternate processing of STEM blocks and inline macros. After the document has been parsed, the extension locates each `asciimath`, `latexmath`, and `stem` block and inline macro, converts the expression to an image, and replaces the expression with an image. It uses Mathematical to render the LaTeX notation as an image. If the expression is AsciiMath, it first uses AsciiMath gem to convert to LaTeX. Conversion then proceeds as normal.

Asciidoctor Mathematical is a Ruby gem that uses native extensions. It has a few system prerequisites which limit installation to Linux and macOS. Please refer to the installation section in the Asciidoctor Mathematical README to learn how to install it.

Once Asciidoctor Mathematical is installed, you can enable it when invoking Asciidoctor PDF using the `-r` flag:

```
$ asciidoctor-pdf -r asciidoctor-mathematical sample.adoc
```

To get started, first create a Gemfile in the root of your project. In that file, declare the gem source, the asciidoctor-pdf gem, and the prawn-table gem (plus any other development gems you want to use).

Gemfile

```
source 'https://rubygems.org'
```

```
gem 'asciidoctor-pdf'  
gem 'prawn-table', github: 'prawnpdf/prawn-table'
```

You can then install the gems into your project using the bundle command:

```
$ bundle config set --local path .bundle/gems && bundle
```

Since you're using Bundler to manage the gems, you'll need to prefix all commands with bundle exec. For example:

```
$ bundle exec asciidoctor-pdf -v
```

Thus, to any command present in the following sections, be sure to include this prefix.



## Chapter 2. Tables and graphics

AsciiDoc makes standard tables easy and also supports the creation of complex tables.

### 2.1. Some table examples

AsciiDoc tables can also be created directly from CSV data. Just set the format block attribute to csv and insert CSV data inside the block delimiters directly:

```
[%header,format=csv]
|===
Artist,Track,Genre
Baauer,Harlem Shake,Hip Hop
The Lumineers,Ho Hey,Folk Rock
|===
```

The above renders as follows:

Artist	Track	Genre
Baauer	Harlem Shake	Hip Hop
The Lumineers	Ho Hey	Folk Rock

Here is an additional example of what can be done with tables:

```
[cols="e,m,^,>s",width="25%"]
|=====
|1 >s|2 |3 |4
^|5 2.2+^.^|6 .3+<.>m|7
^|8
|9 2+>|10
|=====
```

Which renders as follows:

1	2	3	4
5	6		
8			
9	10		7

Code for a numbered encoding table with link target.

#### NOTE

Annotations have been added to the code to illustrate their use.

```
[#proposed-16bit-encodings-1] (1)
.proposed 16-bit encodings-1 (2)
[width="100%",options=header]
|===
|15 |14 |13 |12 |11 |10 |9 |8 |7 |6 |5 |4 |3 |2 |1 |0 |instruction
3+|100|1|0|0|0 2+|field|0 |0 2+|00 | field 2+|00|mnemonic1
3+|100|1|0|0 3+|field|bit|1 3+|field 2+|00|mnemonic2
3+|110|1|0|0 3+|field|1 |0 3+|field 2+|00|mnemonic3
17+|This row spans the whole table
3+|100|1|1|1 8+| field 2+| 00 | mnemonic4
|===
```

- 1. Link target.
- 2. Numbered table title.

Table 1. proposed 16-bit encodings-1

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	instr uction
100			1	0	0	0	field		0	0	00		field	00		mne moni c1
100			1	0	0	field			bit	1	field			00		mne moni c2
110			1	0	0	field			1	0	field			00		mne moni c3
This row spans the whole table																
100			1	1	1	field								00		mne moni c4

## 2.2. Handling images

The graphical elements—those for the various diagrams—add the most complexity. Even as we are using wavedrom to simplify the creation of accurate svgs for register diagrams, it’s impossible to eliminate some inherent complexity.

While asciidoc can render graphics in all popular formats, by far the highest quality graphics rendering is from .svg format.

For some graphical elements, RISC-V specifcations can make use of [Wavedrom diagrams](#).

A build that incorprates building of Wavedrom diagrams is under development. The build will support making use of the git repo as the single source of truth for a specification.

Until the automation has been developed and tested, the code for the wavedrom diagrams should be maintained in the repository and the filename for the svg output should have the same name (with the svg extension).

## 2.3. Unicode symbols

For pdf, some unicode symbols are buggy. There are some workarounds. I noticed the need for a mathematical  $w$  and because its encoding uses an integer that is in the 5+ digit category it doesn't work.

Here are a few unicode examples from [en.wikipedia.org/wiki/List\\_of\\_XML\\_and\\_HTML\\_character\\_entity\\_references](https://en.wikipedia.org/wiki/List_of_XML_and_HTML_character_entity_references):

As an example,  $\blacklozenge$  is encoded as follows:

```
&#9830;
```

Table 2. Useful unicode for specifications

sym	num	name
$\blacklozenge$	9830	name
"	0034	name
w	0077	w
$\therefore$	8756	therefore
#	9839	sharp
$\mathfrak{w}$	1096	shcy
$\varpi$	982	piv varpi
$\omega$	969	omega
$\wp$	8472	weierp wp
$\sum$	8721	sum
$\infty$	8734	infin
$\int$	8747	integral
$\neq$	8800	not equal to
$\leq$	8804	le
$\geq$	8805	ge
$\approx$	8776	numerical approximation
D	68	mathematical D?
$\Rightarrow$	8658	rightwards double arrow

The fact that some unicode doesn't work is connected to prawn, which is used by asciidoctor-pdf, and not to asciidoctor-pdf itself. The workaround makes use of the fact that it's possible to map fonts to numbers for which you don't need to make use of the existing unicode mapping.

If a mathematical  $w$  is important, we can implement the workaround until fixes are implemented.

Table 3. Unicode identified as not working

sym	num	name
☒	9084	angzarr not working
☒	8921	ggg not working
☒	8617	hookleftarrow not working

# Appendix A: Terminology

## AIA

xxx

## ALU

Arithmetic Logical Unit

## ASIC

Application-Specific Integrated Circuit

## AT

xxx

## Atomic Layer Deposition

A layer-by-layer process that results in the deposition of thin films one atomic layer at a time in a highly controlled manner.

## BF

## CPU Cache

Many CPUs three kinds of caches to speed up data retrieval: an instruction cache for executable instruction fetch, a data cache for data store and fetch, and a translation lookaside buffer (TLB) for virtual-to-physical address translation for executable instructions and data.

## CMOS

Complementary Metal Oxide Semiconductor

## Chemical Vapor Deposition

A chemical deposition process in which the wafer is exposed to one or more volatile precursors, which react and/or decompose on the substrate surface to produce the desired film.

## DRAM

Dynamic Random Access Memory

## eDRAM

Embedded DRAM

## ELEN

Element length

## GE

Gate Equivalent

## IC

Integrated Circuit

## IRC

The IRC [Internet Relay Chat](#) protocol is for use with text based conferencing; the simplest client being any socket program capable of connecting to the server.

**IIRC**

The International Integrated Reporting Council (IIRC) (previously the International Integrated Reporting Committee). was formed in August 2010 and aims to create a globally accepted framework for a process that results in communications by an organisation about value creation over time.

**IMSIC**

(Instruction Memory Set ? ?)

**ISA**

Instruction Set Architecture

**MCM**

Multi-chip Module

**MMU**

Memory Management Unit

**NAND**

Not-and

**NOR**

Logical NOR, known as Pierce's Equivalent, Quine's Dagger, the ampcheck (from the Greek for "cutting both ways"), joint denial, or neither-nor, operates on two logical values, typically from two propositions, that produces a value of true if and only if both operands are false. In other words, it produces a value of false if and only if at least one operand is true.

**Photolithography**

In microprocessor manufacturing, a process of using light to transfer a geometric pattern from a photomask (also called an optical mask) pattern parts to a photosensitive substrate on a thin film (substrate or wafer). The process can also make use of chemical photoresist on the substrate.

**PLIC**

Progressive Lossless Image Coding

**PTE**

Page Table Entry

**PTEP**

xxx

**PUD**

xxx

**QEMU**

QEMU (Quick EMUlator) is a free and open-source emulator and virtualizer that can perform hardware virtualization.

**RV**

Reliability verification is a category of physical verification that helps ensure the robustness of a design by considering the context of schematic and layout information to perform user-definable checks against various electrical and physical design rules that reduce susceptibility to premature or catastrophic electrical failures, usually over time.

---

**Rocket**

Parameterized SoC generator written in Chisel, designed to help tune the design under different performance, power, area constraints, and diverse technology nodes.

**SFENCE**

Orders processor execution relative to all memory stores prior to the SFENCE instruction. The processor ensures that every store prior to SFENCE is globally visible before any store after SFENCE becomes globally visible. The SFENCE instruction is ordered with respect to memory stores, other SFENCE instructions, MFENCE instructions, and any serializing instructions (such as the CPUID instruction). It is not ordered with respect to memory loads or the LFENCE instruction.

**SFENCE.VMA**

(instruction wrapper?)

**SoC**

(System on Chip?)

**SRAM**

Static Random Access Memory

**TLB**

Translation Lookaside Buffer; enhances speed in retrieving a value by storing a memory address.

**VM**

Virtual Machine

**VMA**

(..Virtual Memory Allocation ??)

**ZBT**

Zero Bus Turnaround

**ZFew**

# Index