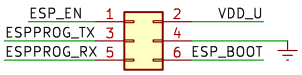
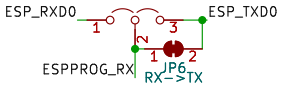
ESP-PROG UART

J2
CONN ESP-PROG

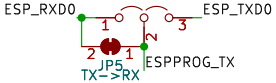


UART Configuration Jumpers

JP3
ESPPROG_RX_JUMP

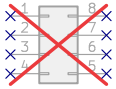


JP4
ESPPROG_TX_JUMP

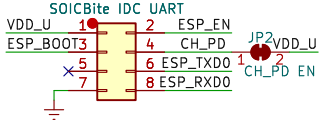


In case you mix up the UART directions on your board, these allow you to fix it. Just cut the associated solderjumper, attach pin headers, and use jumpers to configure as desired.

J8
SOICBite connector numbering order



J7
SOICBite IDC UART



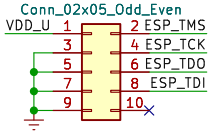
The numbering order of the SOICBite connector is counterclockwise (above), but the IDC connector uses Odd/Even.

The red circle on the above shows the position of the red wire on the clip relative to the pinout.

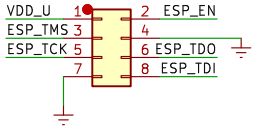
J5
SOICBite connector UART Loopback

ESP-PROG JTAG

J1
Conn_02x05_Odd_Even

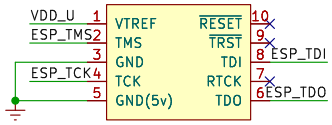


J9
SOICBite IDC JTAG



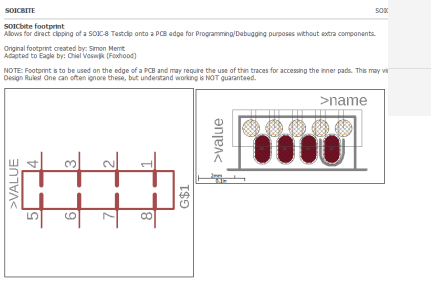
There is no nRST on the ESP-PROG's JTAG header – but instead of just pulling it high, it's connected to the EN pin on the ESP prog, which is the ESP's RST pin.

J3
DebugHeader_Cortex-M_JTAG_10p_TagConnect
TagConnect 2050 Cortex-M JTAG

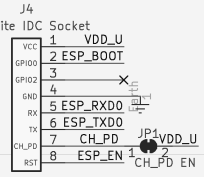


Links & Info

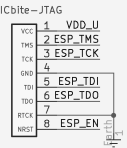
ESP-PROG HW-Reference from Espressif (Link)
SOICbite connector Github (Link)
ESP-PROG-Adapter Github (Link)



J4
SOICBite IDC Socket



J6
SOICbite-JTAG



Project Github:
https://github.com/0xjmux/ESP-PROG-Adapter

jacobbokor.com

Sheet: /
File: ESPPROG-Adapter.kicad_sch

Title: ESP-PROG Universal Adapter

Size: A4	Date: 2024-01-30	**Rev: v1.3**
KiCad E.D.A. kicad 7.0.10-7.0.10~ubuntu22.04.1		Id: 1/1