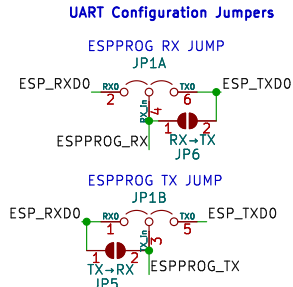
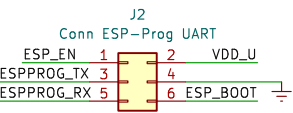
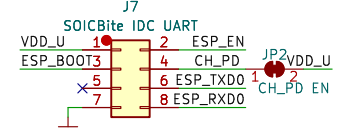
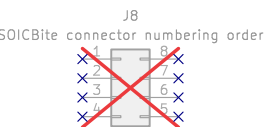


ESP-PROG UART



In case you mix up the UART directions on your board, these allow you to fix it. Use the jumpers to configure as desired; the bolded connection is the default.

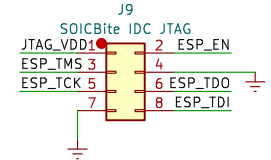
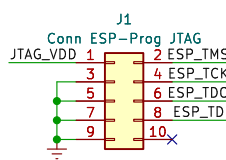


The numbering order of the SOICBite connector is counterclockwise (above), but the IDC connector uses Odd/Even. The red circle on the symbol shows the position of the red wire (pin 1) on the clip relative to the pinout.

J5
SOICBite connector UART Loopback

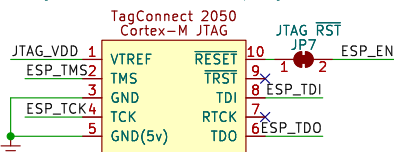


ESP-PROG JTAG



There is no nRST on the ESP-PROG's JTAG header – but instead of just pulling it high, it's connected to the EN pin on the ESP prog, which is the ESP's RST pin.

J3
DebugHeader_Cortex-M_JTAG_10p_TagConnect

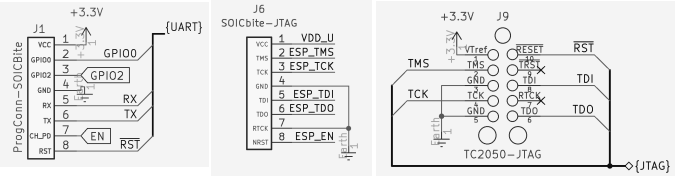


ESP_EN is connected to the RESET pin here just like in J9. However, if you want the UART and JTAG interfaces to be completely separate this can be disabled by cutting the jumper.

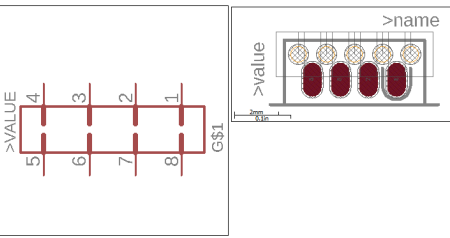
Links & Info

- ESP-PROG HW-Reference from Espressif (Link)
- SOICbite connector Github (Link)
- ESP-PROG-Adapter Github (Link)

Below symbols for integrating SOICBite and TC2050 connector footprints into your ESP-based designs (jmux-kicad-things Link)



SOICBITE
SOICBite_footprint
Allows for direct clipping of a SOIC-8 Testclip onto a PCB edge for Programming/Debugging purposes without extra components.
Original footprint created by: Simon Merrit
Adapted to Eagle by: Chiel Voswijk (Foohood)
NOTE: Footprint is to be used on the edge of a PCB and may require the use of thin traces for accessing the inner pads. This may violate some Design Rules! One can often ignore these, but understand working is NOT guaranteed.



Project Github:
<https://github.com/0xjmux/ESP-PROG-Adapter>

jacobbokor.com

Sheet: /
File: ESPPROG-Adapter.kicad_sch

Title: ESP-PROG Universal Adapter

Size: A4 Date: 2024-02-19

KiCad E.D.A. kicad 7.0.10-7.0.10-ubuntu22.04.1

Rev: v1.4
Id: 1/1