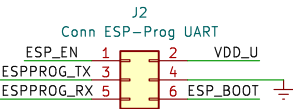
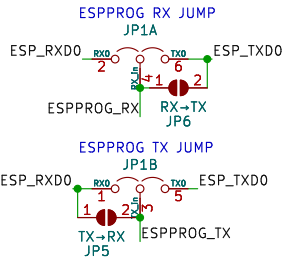


ESP-PROG UART

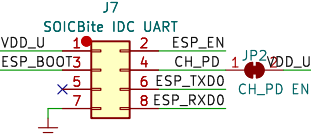
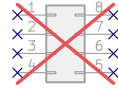


UART Configuration Jumpers



In case you mix up the UART directions on your board, these allow you to fix it. Use the jumpers to configure as desired; the bolded connection is the default.

J8
SOICBite connector numbering order



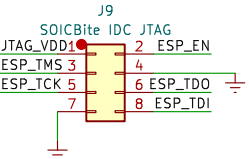
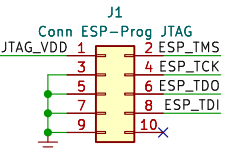
The numbering order of the SOICBite connector is counterclockwise (above), but the IDC connector uses Odd/Even.

The red circle on the symbol shows the position of the red wire (pin 1) on the clip relative to the pinout.

J5
SOICBite connector UART Loopback

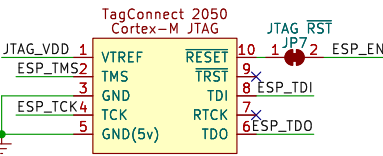


ESP-PROG JTAG



There is no nRST on the ESP-PROG's JTAG header – but instead of just pulling it high, it's connected to the EN pin on the ESP prog, which is the ESP's RST pin.

J3
DebugHeader_Cortex-M_JTAG_10p_TagConnect



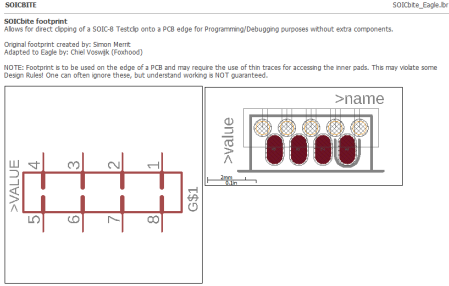
ESP_EN is connected to the RESET pin here just like in J9.

However, if you want the UART and JTAG interfaces to be completely separate this can be disabled by cutting the jumper.

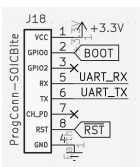
Connection Examples

Links & Info

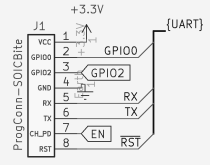
Check out the ESP-EZ-Debug-KicadLib/ directory in the repo for symbols and footprints! (Link)
ESP-PROG HW-Reference from Espressif (Link)
SOICbite connector Github (Link)
Original symbols for integrating SOICBite and TC2050 connector footprints into your ESP-based designs (jmux-kicad-things Link)



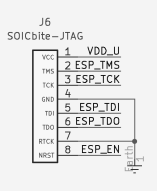
ESP32 UART



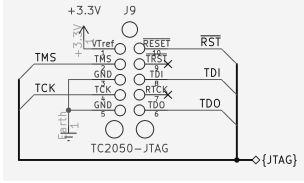
ESP8266 UART



SOICBite JTAG



Tag-Connect ESP32 JTAG



Project Github:
<https://github.com/0xjmux/ESP-PROG-Adapter>
ESP-PROG-Adapter Github (Link)
[jacobbokor.com](https://github.com/0xjmux/ESP-PROG-Adapter)

Sheet: /
File: ESPPROG-Adapter.kicad_sch

Title: ESP-PROG Universal Adapter

Size: A4 Date: 2024-03-30

KiCad E.D.A. 8.0.1

Rev: v1.5

Id: 1/1