

Question 1: data_ready must remain asserted for two clock cycles. The Data must remain valid three cycles data_ready is asserted

Question 2: The image files come out distorted. This makes sense given that the circuit is acting as a filter on the image.

Question 3: The general syntax is to use fopen to generate the file, then scanf to read in data. Very similar to C.

Question 4: This testbench uses %c but there are other specifiers for other formats such as %d for decimal