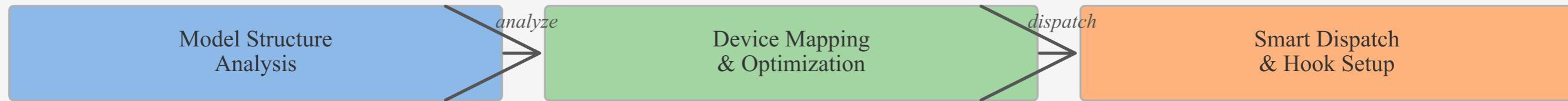


## CPU Lane Scheduling & Management

## PCIe/Bus Lane Data Transfer Streams

## GPU Lane Parallel Computing

### Phase 1: Smart Initialization



### Phase 2: Training Loop



■ CPU

■ PCIe

■ GPU

— Sync

- - - □ Async

□ Hook