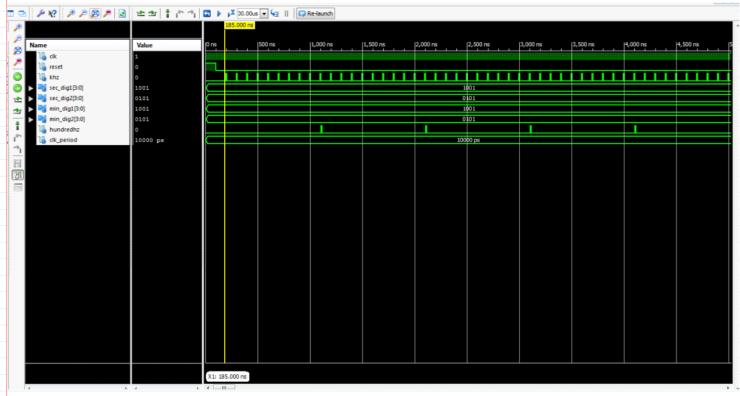
```
Lab 3 prelab
February 23, 2015 9:31 PM
```

```
Clh - M6
                                        Switch
                                               1 - PII
                                        ļŕ
                                                      L3
                                                2
                                               3 -
 Push button 1
                   G12
                                                      K3
                                                  - 134
                   CII
                                               4
           2
                                               5 - G3
       11
           3
                   M4
           Ч
                                                6 - F3
                   A7
                                               7 - E2
8 - N3
HSYNC - J14
                   Red 0 - C14
                                 GRN 0 - FILL
                                               BLUE 0 - HI3
                   " " 1 - 013
                                 " " 1 - G13
VSYNC - KB
                                               1 - J13
                   " " 2 - F13
                                 " " 2 - G14
```



```
G4 scanlineX <= (currentHPos - HSyncNidth - HBackPorch) when i_Blank = '0' else (others => '0');

65 -- my code:
67 verSync <= '0' when currentVPos < VsyncNidth + USackPorch) and (currentVPos < VsyncNidth + VBackPorch + VDisplayArea) else '1';
68 VerBlank <= '0' when (currentVPos - VSyncNidth - VBackPorch) when i_Blank = '0' else (others => '0');
70 -- :end my code
71 |
72 i_Blank <= '1' when HorBlank = '1' or VerBlank = '1' else '0';
```

