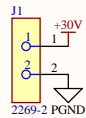


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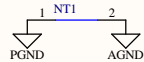
VOEDING

3.3V stepdown switching voltage regulator. Niet dicht bij DAC chip plaatsen!!

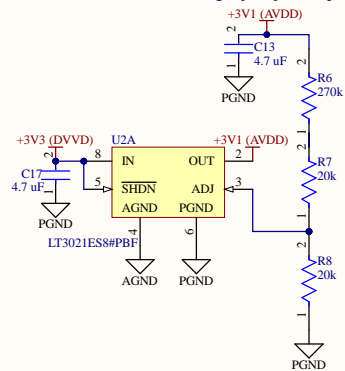
Vin min. 4V max 36V!!



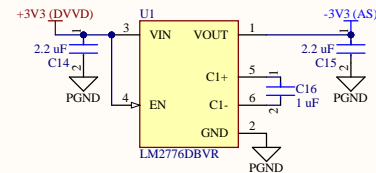
DGND en AGND pas bij voedingschip in PCB layout samen koppelen!!



LDO DAC +3.3V zo dicht mogelijk bij DAC plaatsen

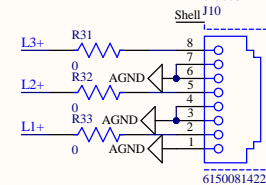
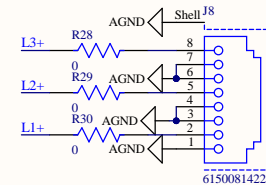
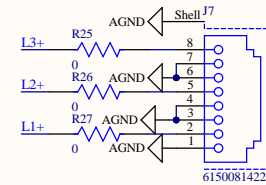
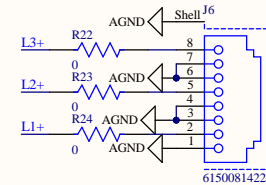
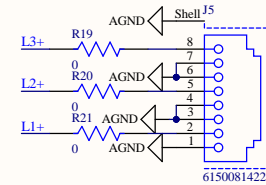
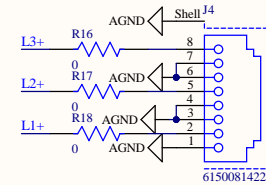
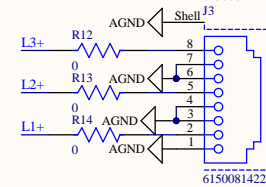
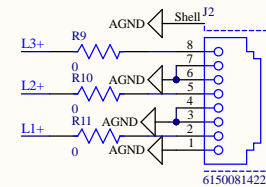
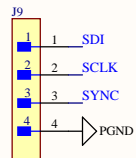
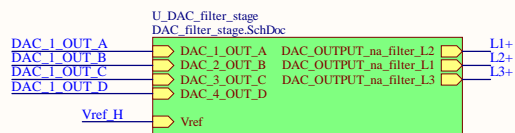
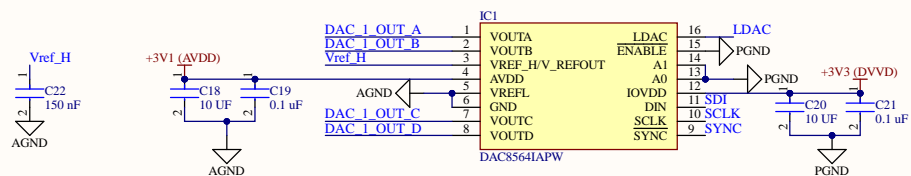
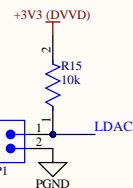
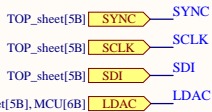


-3.3V inverting switching regulator. Niet dicht bij DAC chip plaatsen!!



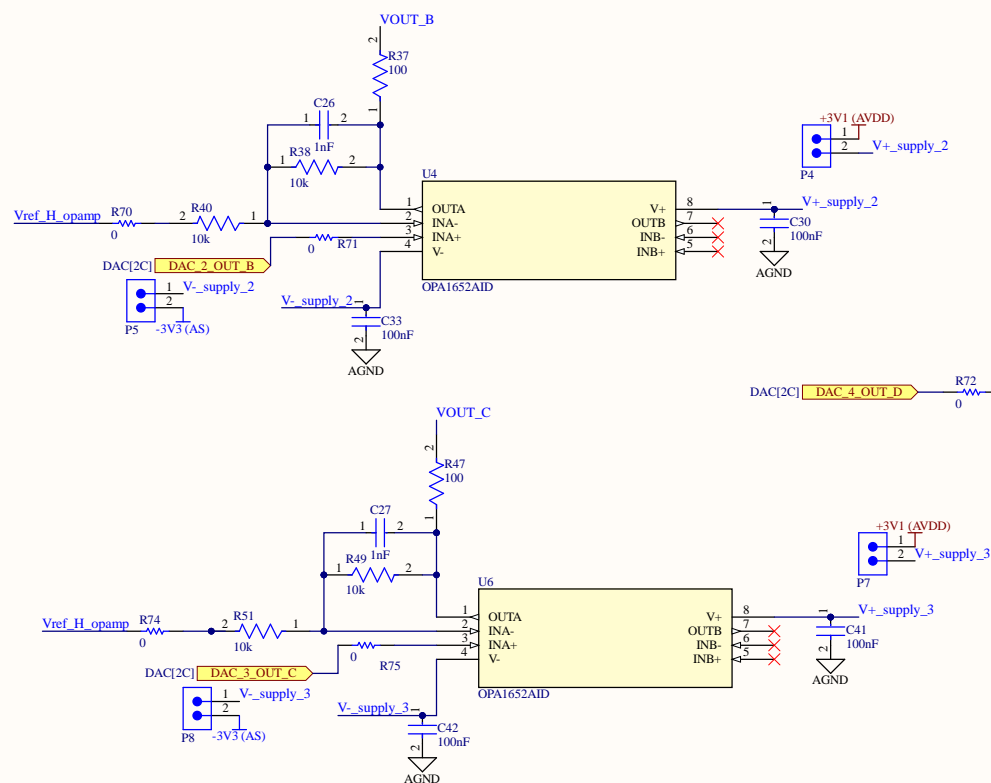
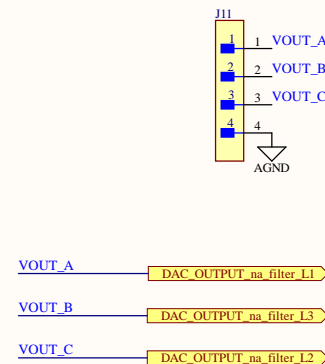
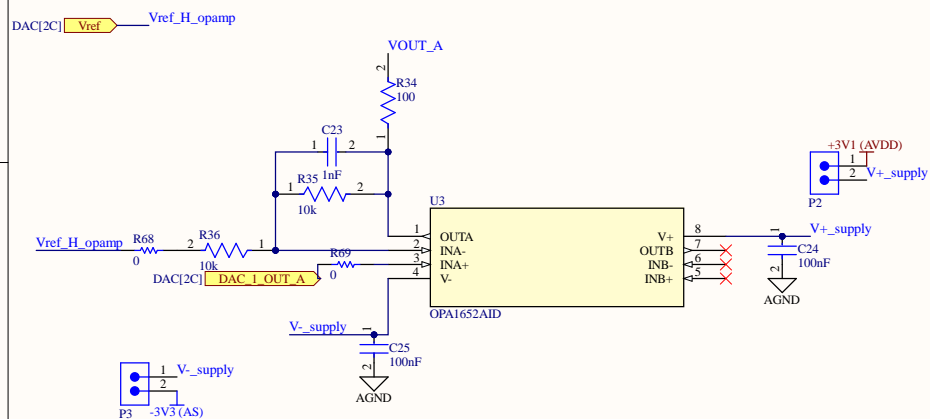
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DAC

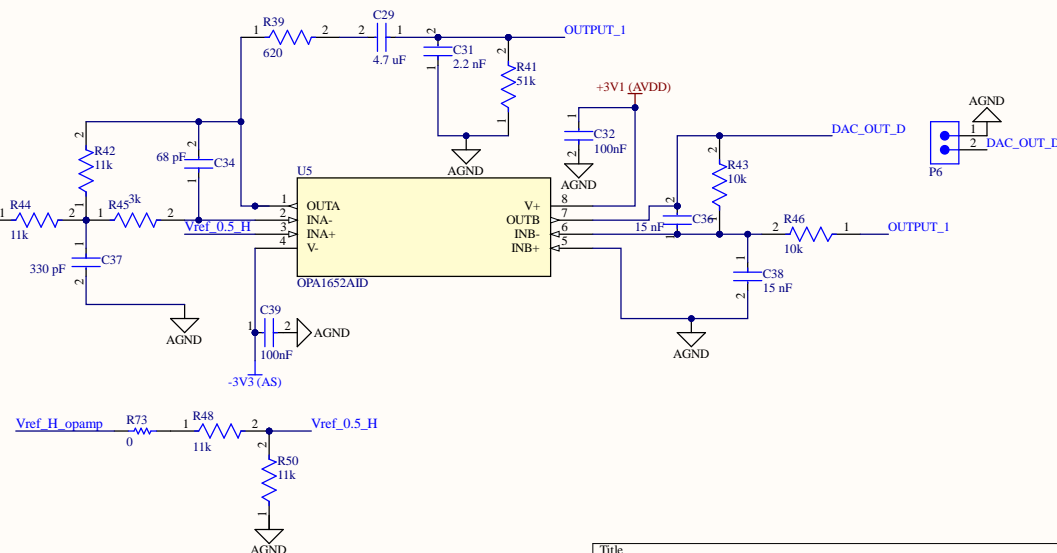


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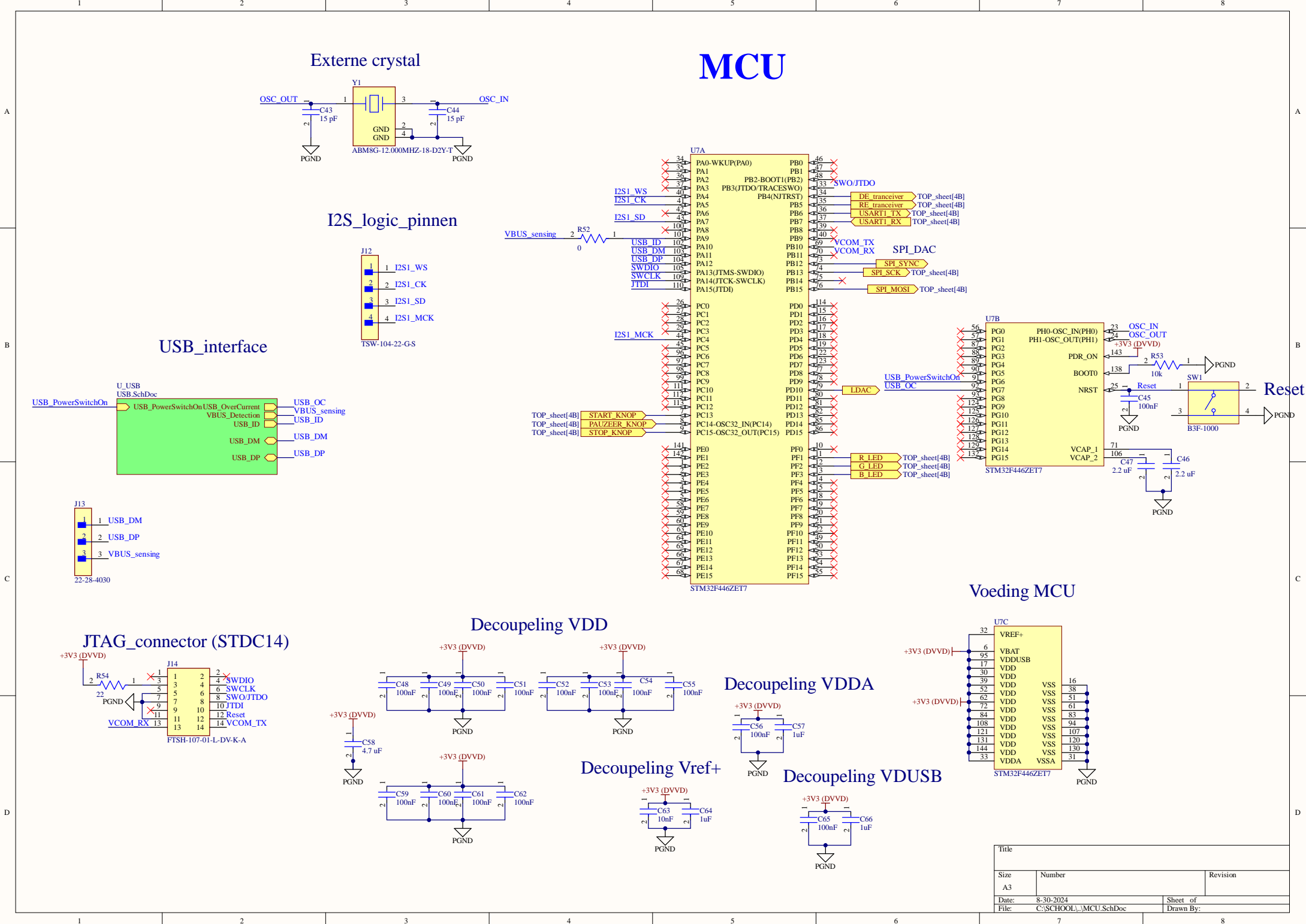
DAC output schematic (typical application, datasheet)



DAC 3e orde filter (typical application)



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Externe crystal

MCU

I2S_logic_pinnen

USB_interface

Decoupling VDD

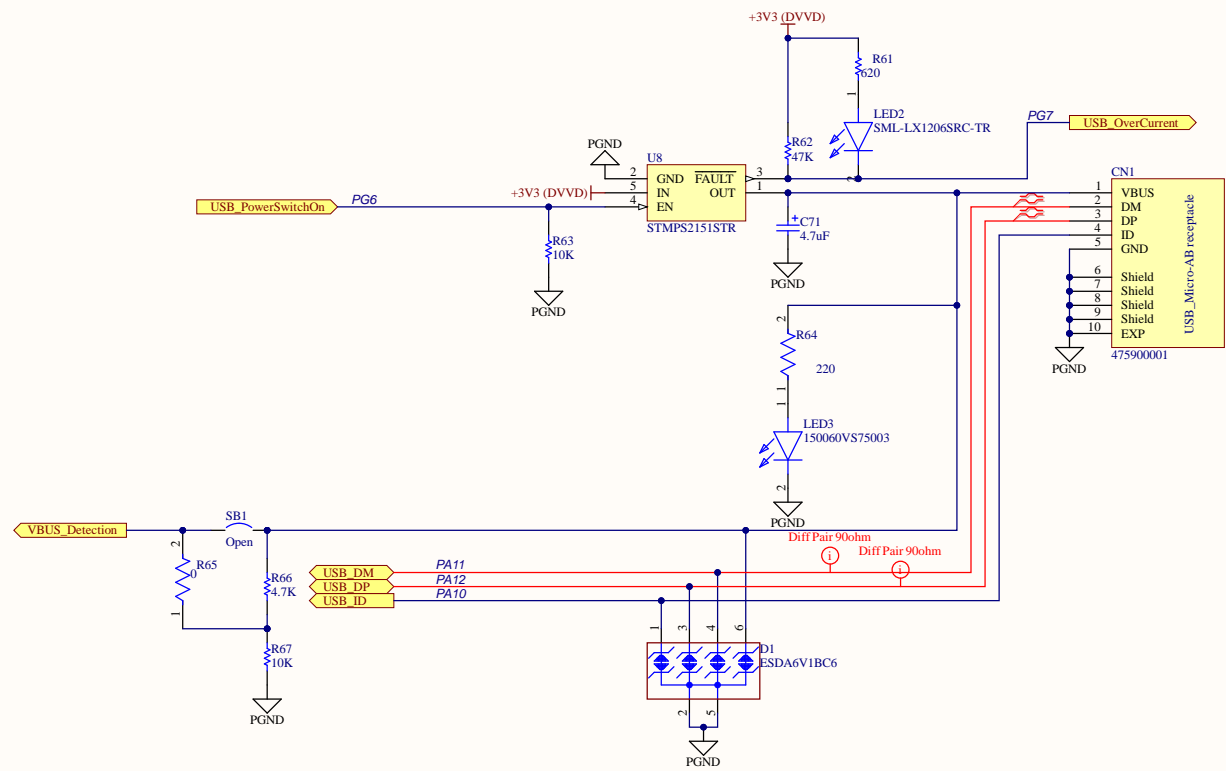
Decoupling VDDA

Decoupling Vref+

Decoupling VDUSB

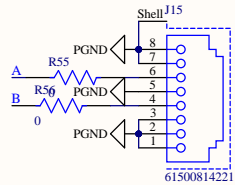
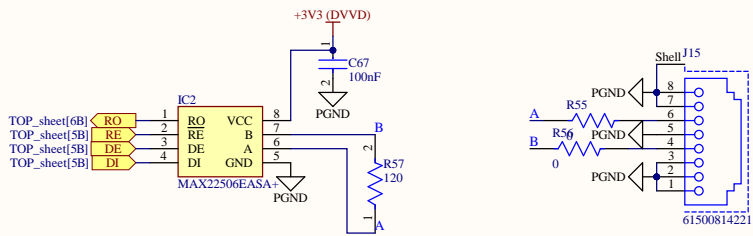
Voeding MCU

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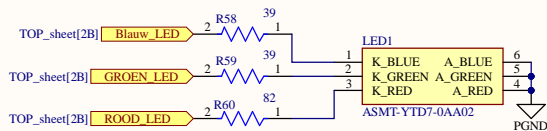
RS485 TRANCEIVER CHIP



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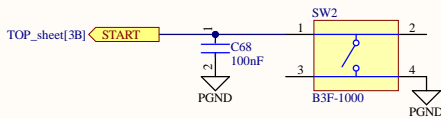
User_interface

RGB LED

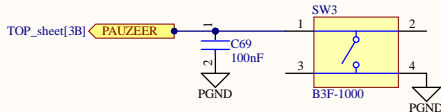


Bedieningspaneel

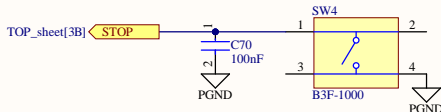
START knop



Pauzeer knop



STOP knop



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Size A3	Number	Revision
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File:	C:\SCHOOL\...\User_interface.SchDoc	Drawn By:

