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| 數位積體電路  Lab3  智能系統所 312581006 張宸瑋 |
| 3-1 Implement the kernels **without** pipeline techniques. |
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| 驗證指令 |
|  |
| 確認功能正確 |
|  |
| 進行合成 |
|  |
| 確認合成結果是否正確 |
|  |
| Gate level驗證 |
|  |
| 確認Gate level功能正確 |
|  |
| Measure Operation time |
|  |
| Area |
| Throughput(OPS) = Operations / operating time =  = 3.972457627 (GOPS) |
| 3-2 Implement the kernels **with** pipeline techniques. |
|  |
| 驗證指令 |
|  |
| 確認功能是否正確 |
|  |
| 進行合成 |
|  |
| 確認合成結果是否正確 |
|  |
| Gate level驗證 |
|  |
| 確認Gate level功能正確 |
|  |
| Measure Operation time |
|  |
| AREA |
| Throughput(OPS) = Operations / operating time =  = 7.30519481 (GOPS) |