# MULTI-GPU TRAINING WITH NCCL

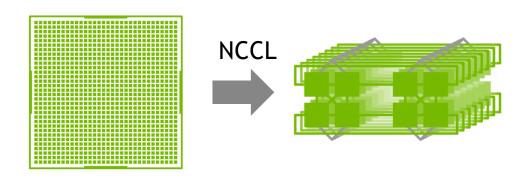
Sylvain Jeaugey





### MULTI-GPU COMPUTING

Harvesting the power of multiple GPUs



1 GPU

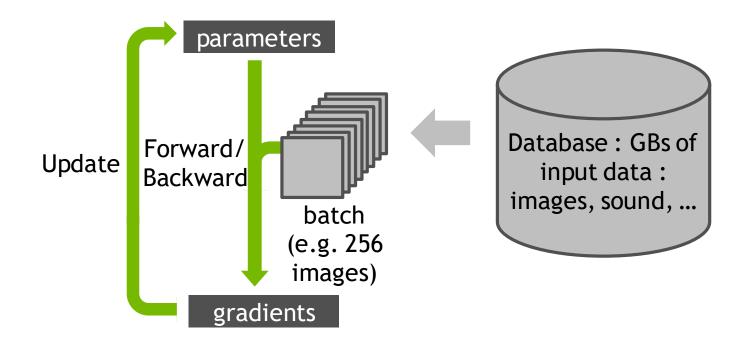
Multiple GPUs per system Multiple systems connected

NCCL: NVIDIA Collective Communication Library



### **MULTI-GPU DL TRAINING**

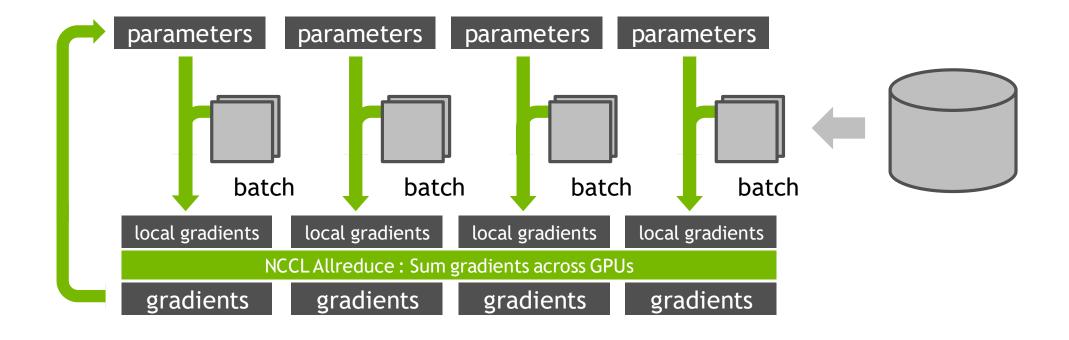
Single-GPU





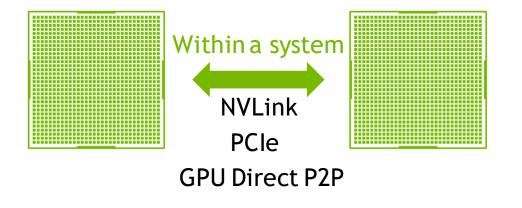
### **MULTI-GPU DL TRAINING**

#### Data parallel



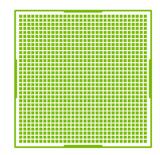
### **NCCL**

#### A multi-GPU communication library

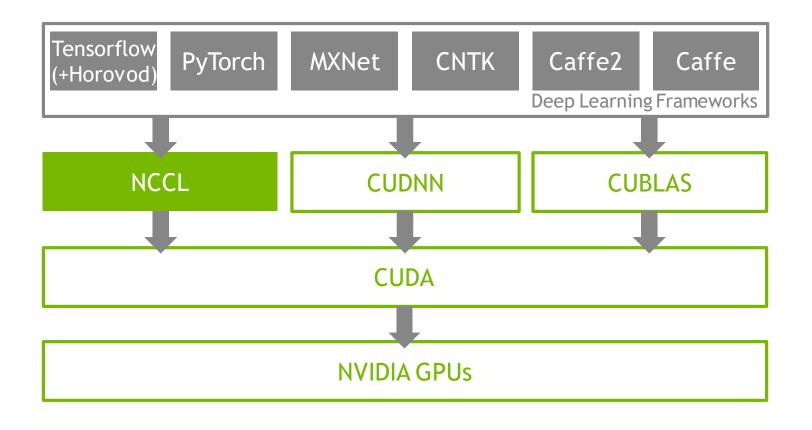


Between systems

Sockets (Ethernet)
Infiniband,
with GPU Direct RDMA

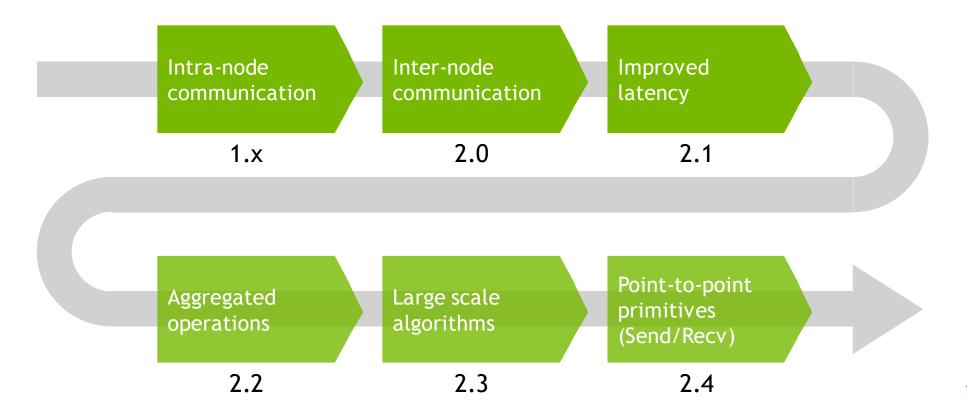


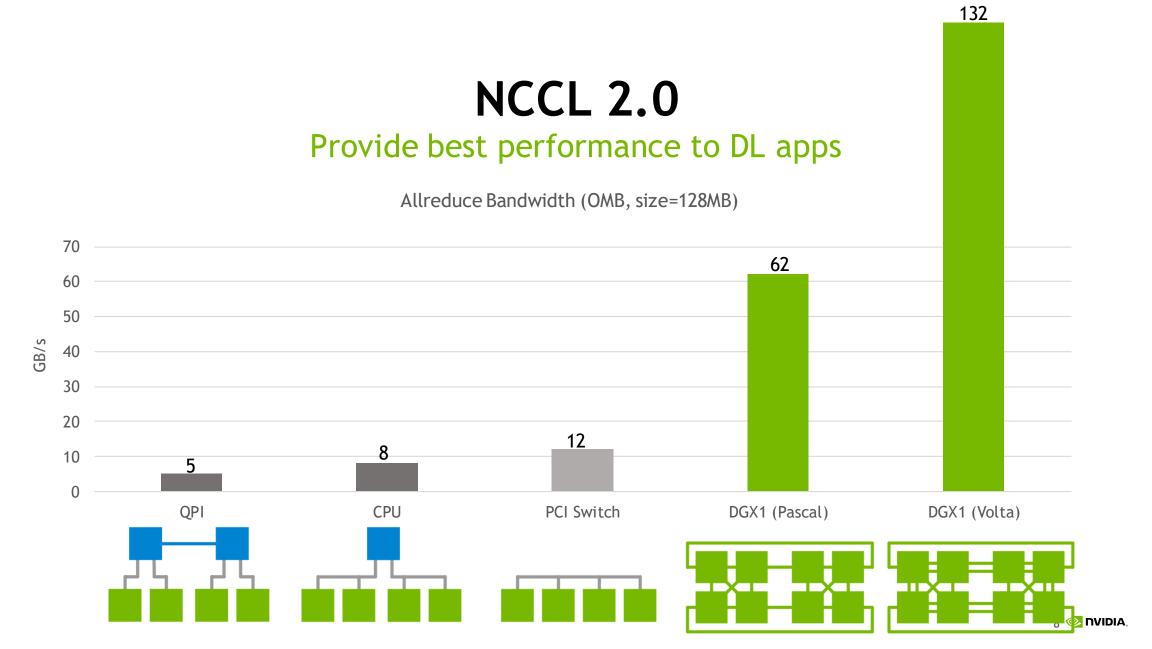
### **NCCL** Architecture



### TIMELINE

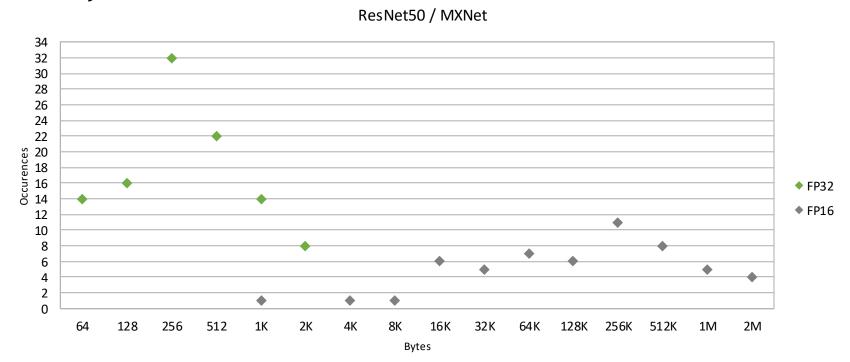
#### NCCL history & roadmap



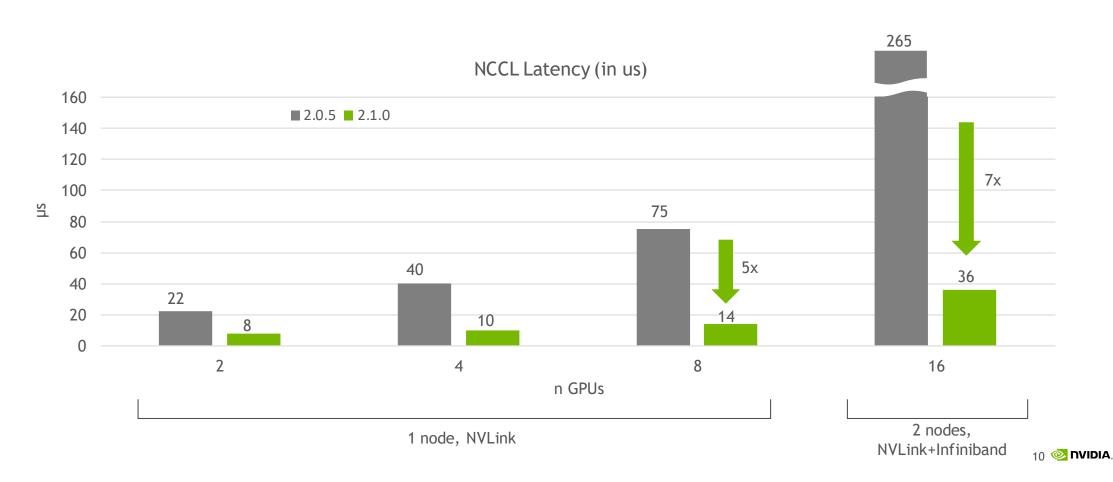


#### ResNet50 buffer size

Latency is important in some workloads, e.g. ResNet 50, in particular when reductions are done for each layer.



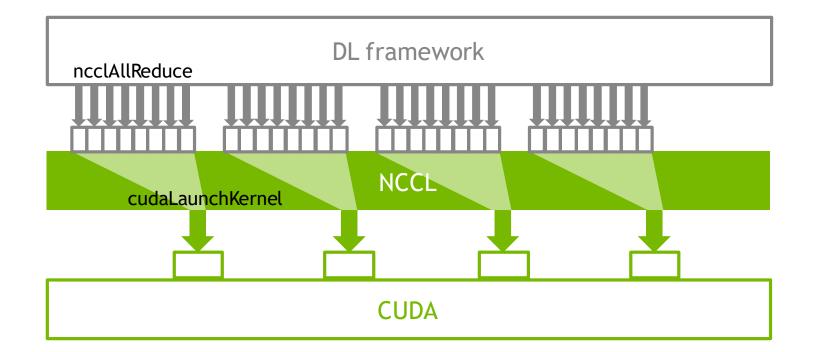
### Latency improvement



#### Aggregated operations: principle

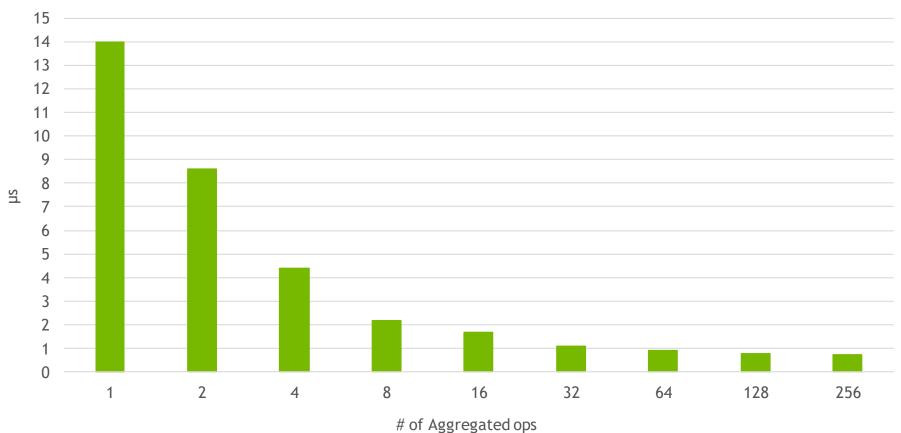
Principle: Merge multiple operations on the same CUDA device

Pay the launch overhead only once (more operations per second) Use multiple NVLinks simultaneously (more bandwidth)



### Aggregated operations: overhead

Per-operation time, 8 GPUs, 8 Bytes reduction



#### Aggregated operations: usage

Use ncclGroupStart() / ncclGroupEnd() around the NCCL operations we want to aggregate:

```
ncclGroupStart();
for (int op=0; op<nops; op++) {
   ncclAllReduce(
     layers[op].localGradients,
     layers[op].globalGradients,
     layers[op].gradientSize,
     ncclFloat, ncclSum, ncclComm, ncclStream);
}
ncclGroupEnd();
// All operations are only guaranteed to be posted on the stream after ncclGroupEnd cudaStreamSynchronize(ncclStream);</pre>
```



#### Aggregated operations: usage

Can be combined/nested with multi-GPU grouping:

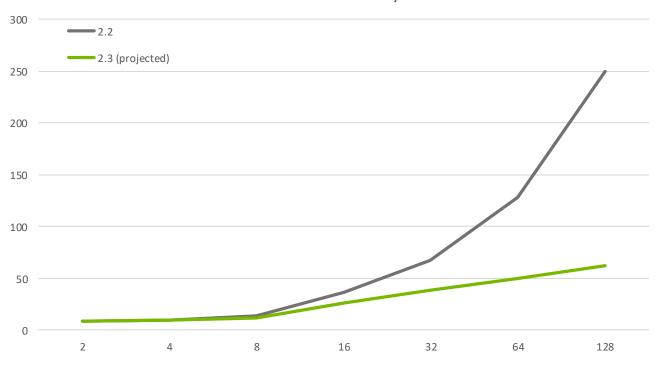
```
ncclGroupStart();
for (int op=0; op<nops; op++) {</pre>
  for (int qpu=0; qpu<nqpus; qpu++) {</pre>
    ncclGroupStart();
    ncclAllReduce(
      layers[op].localGradients[gpu],
      layers[op].globalGradients[qpu],
      layers[op].gradientSize,
      ncclFloat, ncclSum, ncclComms[gpu], ncclStreams[gpu]);
    ncclGroupEnd();
ncclGroupEnd();
// All operations are only guaranteed to be posted on the stream after the last ncclGroupEnd
for (int qpu=0; qpu<nqpus; qpu++)</pre>
  cudaStreamSynchronize(ncclStreams[gpu]);
```

#### Aggregated operations: other uses

```
ReduceScatterV = Aggregation of multiple reductions operations
  ncclGroupStart();
  for (int rank=0; rank<nranks; rank++) {</pre>
    ncclReduce(sendbuff+offsets[rank], recvbuff+offsets[rank],
        recvcounts[rank], datatype, redOp, rank, comm, stream);
  ncclGroupEnd();
AllGatherV = Aggregation of multiple broadcasts operations
  ncclGroupStart();
  for (int rank=0; rank<nranks; rank++) {</pre>
    ncclBroadcast(sendbuff+offsets[rank], recvbuff+offsets[rank],
       recvcounts[rank], datatype, rank, comm, stream);
  ncclGroupEnd();
```

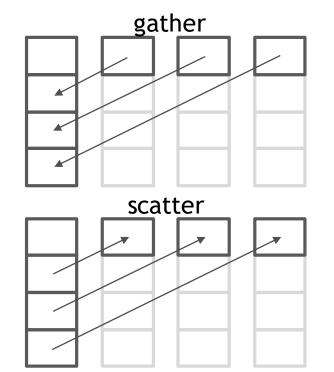
### Large scale algorithms

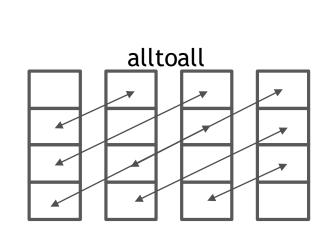
#### Allreduce Latency

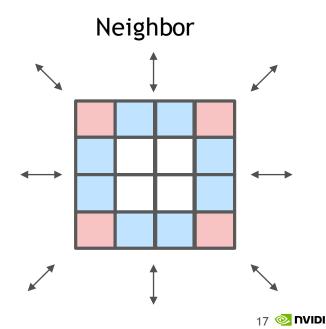


#### Point-to-point primitives

Send / Receive, Scatter[v], Gather[v], Alltoall[v,w], neighbor collectives, ...









Optimized inter-GPU communication for DL and HPC Optimized for all NVIDIA platforms, most OEMs and Cloud Scales to 100s of GPUs, targeting 10,000s in the near future.

Aims at covering all communication needs for multi-GPU computing.

Only relies on CUDA. No dependency on MPI or any parallel environment.

More questions? Connect with the Experts: NCCL Wed 28, 3pm

