PAR-Final Exam Theory/Problems-Course 2023/24-Q2 $_{\mathbf{June} \ 11^{th}, \ \mathbf{2024}}$

Problem 1 (2.5 points) Given the following C program instrumented with Tareador:

```
#define BS  4
#define N    16
int ii, jj, i, j;
char stringMessage[128];
int M[N][N];

for (ii=0; ii<N; ii+=BS)
    for (jj=0; jj<N; jj+=BS) {
        sprintf(stringMessage, "CoP(%d, %d)", ii, jj);
        tareador_start_task(stringMessage);

    for (i=ii; i<min(ii+BS,N); i++)
        for (j=max(1,jj); j<min(jj+BS,N-1); j++)
            M[i][j]= M[i][j] + M[i][j-1] + M[i][j+1] + color_pixel(i,j); // 10 t.u.
    tareador_end_task(stringMessage);
}</pre>
```

Assume each iteration of the innermost loop body takes 10 time units, BS and N are 4 and 16, respectively, only matrix M is stored in memory (rest of variables are in registers), and function color_pixel only computes a value function of the induction variables i and j. We ask you to:

1. Draw the TDG of the parallel strategy above. Indicate which is the cost of each task.

- 2. Compute T_1 , T_{∞} and P_{min} .
- 3. Write the expression that determines the execution time T_4 , clearly indicating the contribution of the computation time T_4^{comp} and data sharing overhead T_4^{mov} , for the two following assignments of tasks to processors:

$\#\mathrm{proc}$	Assignment 1 (task id)	Assignment 2 (task id)
0	CoP(0,0), CoP(0,4), CoP(0,8), CoP(0,12)	CoP(0,0), CoP(4,0), CoP(8,0), CoP(12,0)
1	CoP(4,0), CoP(4,4), CoP(4,8), CoP(4,12)	CoP(0,4), CoP(4,4), CoP(8,4), CoP(12,4)
2	CoP(8,0), CoP(8,4), CoP(8,8), CoP(8,12)	CoP(0,8), CoP(4,8), CoP(8,8), CoP(12,8)
3	CoP(12,0), CoP(12,4), CoP(12,8), CoP(12,12)	CoP(0,12), CoP(4,12), CoP(8,12), CoP(12,12)

You can assume: 1) a distributed-memory architecture with 4 processors; 2) matrix M, is initially distributed by rows in Assignment 1 (N/BS consecutive rows per processor) and initially distributed by columns in Assignment 2 (N/BS consecutive columns per processor); 3) once the loop is finished, you don't need the return matrix to their original distribution; 4) data sharing model with communication time per message $t_{comm} = t_s + m \times t_w$, being t_s , m, t_w the start-up time, the number of elements, and transfer time of one element, respectively; 5) BS perfectly divides N; and 6) the execution time for a single iteration of the innermost loop body takes 10 t.u..

Problem 2 (1.5 points) Consider the following sequential program that performs a matrix multiplication $(C = A \times B)$:

```
int A[L][M], B[M][N], C[L][N];
int main() {
    int l, n, m;
    ...
    // Matrix Mutiplication
    for (l=0; l<L; l++)
        for (n=0; n<N; n++)
            for (m=0; m<M; m++)
            C[l][n] += A[l][m]*B[m][n];
    ...
    // Output results
    for (l=0; l<L; l++)
        for (n=0; n<N; n++)
            printf("C[%d][%d]=%d\n",l,n,C[l][n]);
}</pre>
```

We ask you to:

1. Write an OpenMP version to parallelize Matrix multiplication computation code using an iterative task decomposition strategy where: 1) the granularity of the tasks should be 2 iterations of the middle loop (loop n); and 2) the synchronization overheads are minimized.

2. Do you think parallelizing the Output results code can improve the performance of the program while obtaining the expected output? Justify briefly your answer.

Problem 3 (1.5 points) Consider the following recursive program that copies an array into another one:

```
double X[N],Y[N];
int copy(double * __restrict__ input, double * __restrict__ output, int n) {
   if (n<=32)
      for (int i=0; i<n; i++) output[i] = input[i];
   else {
      copy(input, output, n/2);
      copy(input+n/2, output+n/2, n-n/2);
} }
int main() {
   ...
   copy(X,Y,N);
   ...
}</pre>
```

We ask you to: write a parallel OpenMP program that performs a parallel and efficient recursive task decomposition, reducing the task creation overheads and minimizing data sharing synchronizations. Note that <u>__restrict__</u> indicates that input and output are not overlapped in memory.

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Problem 4 (4.5 points) Given the following C code excerpt:

```
#define MAXHISTO P // P is the number of cores
typedef struct {
    double total;
    unsigned long long num;
} telem;
telem histo[MAXHISTO];
double v[MAXELEM]; // MAXELEM is a very large value
int i, pos;
// histo initialization phase
for (i=0; i<MAXHISTO; i++) {</pre>
  histo[i].total = 0;
  histo[i].num = 0;
// histo computation phase
for (i=0; i<MAXELEM; i++) {</pre>
  pos = getpos (v[i], MAXHISTO); // returns a value between 0 and MAXHISTO-1
   // complex update of field "total" from vector "histo" at position "pos"
   complex_update (&histo[pos].total, v[i]);
   histo[pos].num ++;
}
```

We ask you to:

1. (1.5 points) Add the necessary OpenMP pragmas and clauses to parallelize the code in histo computation phase, on P cores, making use of implicit tasks and applying an INPUT geometric block data decomposition. The value MAXELEM is not necessarily a multiple of P. Your solution should maximize parallelism among implicit tasks.

2.	(1.5 points) Let's consider a shared-memory multiprocessor system composed by two NUMA nodes,
	each with two processors (cores) and 16 GBytes of shared main memory (MM). Each core has a private
	cache of 8MBytes. Cache and memory lines are 32 bytes wide. Cores 0 and 1 are allocated in NUMA
	node 0, and cores 2 and 3 are allocated in NUMA node 1. Data coherence is maintained using Write-
	Invalidate MSI protocols, with a Snoopy attached to each cache memory to provide coherency within
	each NUMA node and Write-Invalidate MSU directory-based coherence among the two NUMA nodes.

(a) Consider P=4 and draw a picture to show how many memory lines are necessary to allocate vector histo in the MM of the previously described system. You need to know that sizeof(double) = 8 Bytes and sizeof(unsigned long long) = 8 Bytes, the allocation of vectors v and histo are aligned to the start of memory line.

(b) Compute the amount of bits taken by each snoopy to maintain the coherence between caches inside a NUMA node and, compute the amount of bits in each node directory to maintain the coherence among NUMA nodes ONLY for vector histo.

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(c) Assuming that vector histo is entirely allocated in the MM of Numa Node 0, and all cache memories of the multiprocessor system are empty, fill in the attached table with the sequence of processor commands (Core), bus transactions within NUMA nodes (Snoopy), transactions yes/no between NUMA nodes (Directory), the presence bits, state for each cache and memory line, to keep cache coherence, AFTER the execution of each of the following sequence of commands:

- i. $core_2$ reads the contents of histo[1].total
- ii. $core_2$ writes the contents of histo[2].num
- iii. $core_1$ reads the contents of histo[0].total

3.	(1.5 points) Let's assume that we want to execute the program on the shared-m system described previously. Decide the most appropriate geometric data decomp parallel code in order to minimize synchronizations and reduce coherence traffic. the data structures.	position and write the

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Answers to Question 4, Section 2.c

	Coherence actions					State in cache			
Command	Core	Snoopy	Directory (yes/no)	Presence bits	State in MM	cache0	cache1	cache2	cache3
$core_2$ reads histo[1].total									
$core_2$ writes histo[2].num									
$core_1$ reads histo[0].total									