Dan Otieno

EE 316 – LAB 4.

Pre-Lab Assignment.

This assignment required completing the theoretical column of the table below:

Input State					Theoretical	Simulation	Experimental
	VA	VB	Vc	V_D	Vo (V)	Vo (V)	Vo (V)
0	0	0	0	0	0	0.002033	
1	0	0	0	1	-0.313	-0.31	
2	0	0	1	0	-0.625	-0.623	
3	0	0	1	1	-0.938	-0.935	
4	0	1	0	0	-1.250	-1.248	
5	0	1	0	1	-1.563	-1.56	
6	0	1	1	0	-1.875	-1.873	
7	0	1	1	1	-2.188	-2.185	
8	1	0	0	0	-2.5	-2.498	
9	1	0	0	1	-2.813	-2.809	
10	1	0	1	0	-3.125	-3.12	
11	1	0	1	1	-3.438	-3.429	
12	1	1	0	0	-3.750	-3.733	
13	1	1	0	1	-4.063	-4.027	
14	1	1	1	0	-4.375	-4.301	
15	1	1	1	1	-4.688	-4.531	