Semester- III Professional Core Theory Courses

		Wol	chand Callaga	of Engineering, Sa	ngli					
		wan		ed Autonomous Institute)	ıngn					
	AY 2024-25									
			Course	Information						
Progra	amme		B. Tech. Electric							
	Semester		Second Year B.	Γech., Sem. III						
	e Code		7EL201							
	e Name		DC Machines and							
Desire	d Requisi	tes:	Fundamentals of	Electrical Engineering						
	Teaching	Schama		Examination Schen	na (Marks)					
Lectur		3 Hrs/week	MSE	ISE	ESE	Total				
Tutori		-	30	20	50	100				
				Credits: 3						
				e Objectives						
1				ot of DC machines and tra						
2		•	ls to evaluate rating	gs of DC machines and to	ansformers for	various				
3	application de		me on DC machine	as and transformers						
4				es and transformers. cations of special purpose	motors					
7	I III WIII			with Bloom's Taxonom						
At the	end of the		lents will be able to		20101					
со	Bloom's									
CO1	Explain and trans		n and working pri	nciples of DC machines	II	Understanding				
CO2	Describe	the operation of	f special purpose m	nachines	II	Understanding				
CO3	Solve the transform		oblems on DC made	chines and single phase	III	Applying				
CO4	Analyse	the performance	of three phase train	nsformers	IV	Analysing				
Modu	ıle		Module (Contents		Hours				
I	DC Machines Constructional Details: Construction of D.C. machines, EMF equation, power flow diagram of D.C. machines. Armsture Winding: Simple lan winding and wave winding winding diagram.									
II	Swinburn's test, Hopkinson's test, Retardation test, Field test on D.C. series motor.									
III	Single Phase Transformer Construction and type, EMF equation phasor diagram, equivalent circuit, efficiency, losses, regulation, Experimental determination of equivalent circuit parameters(O.C./S.C. test) Voltage regulation, parallel operation									
IV	Transformer Testing: Testing of transformer as per IS, polarity test, heat run test, Sumpner's test and equivalent delta test. Calculation of efficiency. Autotransformer: Construction, Operation, Applications, Rectifier transformer, Difference between rectifier transformer and power transformer									

V	Three phase transformer Construction, single phase bank, polarity test, transformer winding, V-V connection and Scott connection, Vector Grouping YD1, YD11, DY11, DZ0, DZ 6, YZ1, YZ11. Parallel operation of three phase transformer, Three winding transformer.	6						
VI	Special purpose motors Universal motor, DC Servomotors, Permanent magnet DC motors, Stepper motors, Applications.	4						
	Textbooks							
1	Ashfaq Husain, Haroon Ashfaq "Electric Machines", Dhanpat Rai and Co, 3rd Edition, 2018.							
2	J. B. Gupta, "Theory and Performance of Electrical Machines", S. K. Kataria and Sons, 1st Edition, 2013.							
3	Kothari and Nagrath, "Electric Machines", McGraw Hill, 5th Edition, 2018							
	References							
1	Purkait and Bandyopadhyay " <i>Electrical Machines</i> ", Oxford University Press, 1 st Edition, 2017.							
2	M. G. Say. "The Performance and Design of Alternating Current Machines", CBS Publishers, 3rd Edition, 2004							
	Useful Links							
1	https://nptel.ac.in/courses/108/105/108105017/							

	CO-PO Mapping													
		Programme Outcomes (PO)											PSO	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1	3												2	
CO2		3												2
CO3		3												2
CO4		2												2

Assessment

The assessment is based on MSE, ISE and ESE.

MSE shall be typically on modules 1 to 3.

ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.

ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.

Syllabus Prepared By	Mr. M.S. Mahagaonkar/ Mr. S. S. Medhekar
Syllabus Checked By	Mrs. S. L. Shaikh

		Wald	chand College	of Engineering, Sa	ngli					
			(Government Aide	d Autonomous Institute)						
				2024-25						
Drogr	Course InformationProgrammeB. Tech. Electrical Engineering									
	Semeste	P	Second Year B. 7							
	e Code	<u>.</u>	7EL202	ecii., Sciii. III						
	e Name		Electrical Circuit	Analysis						
Desire	d Requis	ites:	Fundamentals of	Electrical Engineering						
	Teaching			Examination Schen						
Lectur		3 Hrs/week	MSE	ISE	ESE	Total 100				
Tutor	ial	1 Hrs/week	30	20	50	100				
				Credits:	•					
			Course	e Objectives						
	This cou	ırse intends to de		ding of the fundamental	laws and elem	ents of electric				
1	circuits.		T	6 - 12 - 13 - 14 - 14 - 14 - 14 - 14 - 14 - 14						
				owerful engineering circ						
2			lysis, theorems, sou	arce transformation and	several method	ls of simplifying				
	network		1	1	1					
3		It will make students to analyze the first and second order transient and steady state response.								
4	The course intends to introduce open circuit, short circuit, transmission, hybrid parameters and their interrelationship.									
	Interret		Outcomes (CO) v	vith Bloom's Taxonom	Level					
At the	end of the		ents will be able to							
со	O Course Outcome Statement/s Bloom's Taxonomy									
CO1	1	L, KVL and Ohn ent circuit for a el	n's law to obtain vo	oltage, current and	III	Description Applying				
CO2				, current, power,circui						
002	1		parameters for a el	· • • • • • • • • • • • • • • • • • • •	III	Applying				
CO3				econd order circuits.	IV	Analyzing				
CO4	Constru	ct the parameters	of two port electric	cal circuits and	IV	Analyzing				
	network	s.			1 V	Anaryzing				
Madu	.la		Madula (Nomtonto		Hanna				
Modu		Circuite	Module (ontents		Hours				
I	DC Circuits Ohm's law, Kirchhoff's law, Dependent and independent sources, Nodes, Branches, Loops, Voltage and current division, Wye Delta transformations, Nodal analysis, Mesh analysis, linearity property, Superposition theorem, Source transformation, Thevenin's and Norton's theorem, Maximum power transfer, Reciprocity theorem, Tellegen's theorem, Millman's theorem.									
II	First Order Circuits Capacitors, Series and Parallel Capacitors, Inductors, Series and Parallel Inductors, Source free RC, RL circuits, Step response of RC, RL, circuits.									
III	Find		al values, Source fr	ee series and parallel RL uits, General second ord		6				
IV	Sinu Nod	response of series and parallel RLC circuits, General second order circuits. AC Circuits Sinusoids, Phasors, Impedance and Admittance, Sinusoidal steady state analysis, Nodal and Mesh analysis, Superposition theorem, Source transformation, Thevenin's and Norton's theorem.								

V	Power in AC Circuits Instantaneous and Average Power, Maximum Average Power, RMS Value, Apparent Power and Power factor, Complex Power, Mutual inductance, Dot convention, Energy in coupled circuits.	6						
VI	Two Port Network Impedance parameters, Admittance parameters, Hybrid parameters, Transmission parameters, Series connection of two two-port network, Parallel connection of two two-port network, Cascade connection of two two-port network.	6						
	Textbooks							
1	C. K. Alexandar and M.O. Sadiku, "Fundamentals of Flectric Circuits" McGraw Hill							
2	Hayt, Kemmerly, Durbin, "Engineering Circuit Analysis", TMH, 8th Edition, 2012.							
3	A Sudhakar Shyammahan S. "Cirquits and Natworks: Analysis and Synthesis" McGray Hill							
	References							
1	James W. Nilsson and Susan A. Riedel "Electric Circuits" Pearson, 11th Edition,	2018.						
2	2 L.P. Huelsman, "Basic Circuit Theory", Pearson, 3 rd Edition, 2015.							
	Useful Links							
1	https://nptel.ac.in/courses/108/106/108106172/							
2	https://nptel.ac.in/courses/108/105/108105159/							
3	https://nptel.ac.in/courses/108/104/108104139/							

	CO-PO Mapping													
		Programme Outcomes (PO) PSO												
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1	3													
CO2	3													
CO3		3												
CO4		2												

Assessment

The assessment is based on MSE, ISE and ESE.

MSE shall be typically on modules 1 to 3.

ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.

ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.

Syllabus Prepared By	Mr. N.V. Patel
Syllabus Checked By	Mr. A.B. Patil

		Wald	chand College	of Engineering, S	angli					
		vv arv		d Autonomous Institute)	ungn					
	AY 2024-25									
				Information						
Progra			B. Tech. Electrica							
	Semester	•	Second Year B. T	Tech., Sem. III						
	e Code		7EL203							
	e Name		Analog and Digit							
Desire	d Requis	ites:	Basic Electronics	Engineering						
	- I	~ ·	I							
	Teaching		N. F.C.F.	Examination Scher						
Lectur		3 Hrs/week	MSE	ISE	ESE	Total 100				
Tutor	ıaı	-	30	20	50	100				
				Credits:	3					
			Correc	Objectives						
1	This cor	rea gime to inter		e Objectives easic features of operation	nal amplifier					
				ence for implementing si		c circuits to meet				
2	I .	d design specific		nce for implementing st	mpie electroffi	circuits to illect				
3				ing combinational logic	circuits for va	rious applications				
4	It is aimed to enable students for implementing combinational logic circuits for various applications. It intends to provide knowledge for implementation of sequential circuits using flip-flops.									
	Tt IIItelia		<u> </u>	vith Bloom's Taxonom		у порз.				
At the	end of the		lents will be able to		y Ecvel					
7 It tile		course, the stad	ients will be uble to	·,	Bloom's	Bloom's				
CO		Cours	e Outcome Staten	nent/s	Taxonomy	Taxonomy				
		00415			Level	Description				
CO1	Summar circuits.	rize the fundamen	ntal principles unde	rlying analog and digital	II	Understanding				
CO2				et stated applications	III	Applying				
CO3	Constru	et basic analog fi	lters, combinationa	al and sequential circuits	III	Applying				
CO4		the performance	of analog and digi	tal electronic circuits for	IV	Analyzing				
Modu	ıle		Module C	Contents		Hours				
I	Char op-a ampl	mp powering,	eal and practical Opfeedback in op-ar	perational Amplifiers, B mp circuits, inverting, mparator, difference amp	non-inverting	6				
II	Applications of Opamps Instrumentation amplifier Integrator Differentiator Schmitt trigger Active									
III	Review of Transistor Configurations, Voltage Regulators and Multivibrators Introduction, Types of Configuration: common base, common emitter and common collector configurations, Voltage regulators, fixed voltage regulators (± 5 V, ± 12 V), Adjustable voltage regulators, Multivibrators: IC 555 Astable, Monostable and Bistable									
IV	Mult R la	iplexer, de-multi	p-flops- D F/F, J-	coder, half & full adders -K F/F,T F/F, master s						

Applications of Sequential circuits Counters: Modulus of Counter, Synchronous and Asynchronous counters, Ripple counters, drawbacks of ripple counters, Ring counters, Twisted Ring Counters, Shift registers, types of shift registers, design of shift registers using D, J-K FFs Digital to Analog and Analog to Digital Converters Need of Digital to Analog and Analog to Digital Converters, Binary weighted DAC, R-2R ladder DAC, Ramp ADC, dual slope ADC, successive approximation technique, flash ADC, voltage and current measurement(block level treatment only). Textbooks Sergio Franco, "Design with Op-Amps and analog Integrated Circuits", Tata McGraw-Hill Publication, Third Edition, 2001 Allen Mottershead, "Electronic Devices &Circuits: An Introduction", Prentice Hall India, 2010 A. Anand Kumar, "Fundamentals of Digital Circuits", Prentice Hall India, Fourth Edition, 2014 References R.A. Gayakwad, "Op-Amps & Linear Integrated Circuits", Prentice Hall India, Fourth Edition, 2012. R. L. Boylestad and Louis Nashelsky, "Electronic Devices & Circuit Theory", Pearson Publications, Tenth Edition, 2009. M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fifth Edition, 2013 Useful Links NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/ NPTEL Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/10810295/ NOC:Digital Electronic Circuits, IIT Kharagpur https://nptel.ac.in/courses/108/105/108102132/										
Need of Digital to Analog and Analog to Digital Converters, Binary weighted DAC, R-2R ladder DAC, Ramp ADC, dual slope ADC, successive approximation technique, flash ADC, voltage and current measurement(block level treatment only). Textbooks Sergio Franco, "Design with Op-Amps and analog Integrated Circuits", Tata McGraw-Hill Publication, Third Edition, 2001 Allen Mottershead, "Electronic Devices & Circuits: An Introduction", Prentice Hall India, 2010 A. Anand Kumar, "Fundamentals of Digital Circuits", Prentice Hall India, Fourth Edition, 2014 References R.A. Gayakwad, "Op-Amps & Linear Integrated Circuits", Prentice Hall India, Fourth Edition, 2012. R. L. Boylestad and Louis Nashelsky, "Electronic Devices & Circuit Theory", Pearson Publications, Tenth Edition, 2009. M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fifth Edition, 2013 Useful Links NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/ NPTEL Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102095/	V	Counters: Modulus of Counter, Synchronous and Asynchronous counters, Ripple counters, drawbacks of ripple counters, Ring counters, Twisted Ring Counters, Shift registers, types of shift registers, design of shift registers using								
Sergio Franco, "Design with Op-Amps and analog Integrated Circuits", Tata McGraw-Hill Publication, Third Edition, 2001 Allen Mottershead, "Electronic Devices & Circuits: An Introduction", Prentice Hall India, 2010 A. Anand Kumar, "Fundamentals of Digital Circuits", Prentice Hall India, Fourth Edition, 2014 References R.A. Gayakwad, "Op-Amps & Linear Integrated Circuits", Prentice Hall India, Fourth Edition, 2012. R. L. Boylestad and Louis Nashelsky, "Electronic Devices & Circuit Theory", Pearson Publications, Tenth Edition, 2009. M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fifth Edition, 2013 Useful Links NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102095/	VI	Digital to Analog and Analog to Digital Converters Need of Digital to Analog and Analog to Digital Converters, Binary weighted VI DAC, R-2R ladder DAC, Ramp ADC, dual slope ADC, successive approximation technique, flash ADC, voltage and current measurement(block								
Sergio Franco, "Design with Op-Amps and analog Integrated Circuits", Tata McGraw-Hill Publication, Third Edition, 2001 Allen Mottershead, "Electronic Devices & Circuits: An Introduction", Prentice Hall India, 2010 A. Anand Kumar, "Fundamentals of Digital Circuits", Prentice Hall India, Fourth Edition, 2014 References R.A. Gayakwad, "Op-Amps & Linear Integrated Circuits", Prentice Hall India, Fourth Edition, 2012. R. L. Boylestad and Louis Nashelsky, "Electronic Devices & Circuit Theory", Pearson Publications, Tenth Edition, 2009. M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fifth Edition, 2013 Useful Links NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102095/		Textbooks								
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References R.A. Gayakwad, "Op-Amps & Linear Integrated Circuits", Prentice Hall India, Fourth Edition, 2012. R. L. Boylestad and Louis Nashelsky, "Electronic Devices & Circuit Theory", Pearson Publications, Tenth Edition, 2009. M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fifth Edition, 2013 Useful Links NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/ NPTEL Analog Electronic Circuits , IIT Delhi https://nptel.ac.in/courses/108/102/108102095/	2									
R.A. Gayakwad, "Op-Amps & Linear Integrated Circuits", Prentice Hall India, Fourth Edition, 2012. R. L. Boylestad and Louis Nashelsky, "Electronic Devices & Circuit Theory", Pearson Publications, Tenth Edition, 2009. M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fifth Edition, 2013 Useful Links NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/ NPTEL Analog Electronic Circuits , IIT Delhi https://nptel.ac.in/courses/108/102/108102095/	3									
2 R. L. Boylestad and Louis Nashelsky, "Electronic Devices & Circuit Theory", Pearson Publications, Tenth Edition, 2009. 3 M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fifth Edition, 2013 Useful Links 1 NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/ 2 NPTEL Analog Electronic Circuits , IIT Delhi https://nptel.ac.in/courses/108/102/108102095/		References								
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Useful Links 1 NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/ 2 NPTEL Analog Electronic Circuits , IIT Delhi https://nptel.ac.in/courses/108/102/108102095/	2									
 NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/ NPTEL Analog Electronic Circuits , IIT Delhi https://nptel.ac.in/courses/108/102/108102095/ 	3	M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fit	fth Edition, 2013							
 NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/ NPTEL Analog Electronic Circuits , IIT Delhi https://nptel.ac.in/courses/108/102/108102095/ 										
NPTEL Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102095/										
		* * *								
3 NOC:Digital Electronic Circuits, IIT Kharagpur https://nptel.ac.in/courses/108/105/108105132/										
	3	NOC:Digital Electronic Circuits, IIT Kharagpur https://nptel.ac.in/courses/108/1	105/108105132/							

	CO-PO Mapping													
		Programme Outcomes (PO) PSO												
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1	3													
CO2			3											
CO3			3											
CO4		3												

Assessment

The assessment is based on MSE, ISE and ESE.

MSE shall be typically on modules 1 to 3.

ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.

ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.

Syllabus Prepared By	Dr. S. S. Karvekar
Syllabus Checked By	Mrs. A. A. Dhamangaonkar

Professional Core Laboratory Courses

Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

AY 2024-25

~	T 0	4.
Course	Informa	ation

Programme	B. Tech. Electrical Engineering			
Class, Semester	Second Year B. Tech., Sem. III			
Course Code	7EL251			

Course Name DC Machines and Transformer Lab

Desired Requisites: Fundamentals of Electrical Engineering

Teaching	g Scheme	Examination Scheme (Marks)							
Practical	2 Hrs/ Week	LA1	LA1 LA2 Lab ESE Total						
Interaction	-	30	30	40	100				
		Credits: 1							

Course Objectives

- 1 To develop skills to demonstrate performance operation of DC motors.
- 2 To develop skills to analyze operation and performance of DC machines using different tests.
- 3 To analyse the performance of single phase transformer by conducting different tests.
- 4 To perform different winding connections of three phase transformers.

Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

СО	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	Experiment for verification of different characteristics and performance of DC Machines.	III	Applying
CO2	Experiment to calculate efficiency and losses of DC motor by conducting different tests.	IV	Analysing
CO3	Determine circuit parameters and voltage regulation of single phase transformer.	IV	Analysing
CO4	Test the performance of three phase transformer	IV	Analysing

List of Experiments / Lab Activities/Topics

List of Lab Activities:

- 1. Speed control of dc shunt motor (i) Armature control method (ii) Field control method.
- 2. Determination of efficiency of DC motor by Swinburne's test.
- 3. Determination of efficiency of DC motor by Hopkinson's test.
- 4. Brake test on shunt motor to determine its performance and efficiency.
- 5. Load test on compound motor i) cumulative ii) differential.
- 6. To perform open circuit and short circuit test for determining equivalent circuit parameters of a single-phase transformer.
- 7. Parallel operation of single-phase transformer to demonstrate load sharing.
- 8. Scott connections for converting 3 phase to 2 phase supply.
- 9. Equivalent Delta test or Heat run Test for determination of temperature rise and efficiency of 3 phase transformer.
- 10. Parallel connection of 3 phase DY1 and DY11 transformers to demonstrate load sharing.
- 11. Load test on transformer (single and three phase) to determine losses and efficiency using Sumpner's test.

Textbooks					
1	M. G. Say. "The Performance and Design of Alternating Current Machines", CBS Publishers, 3rd Edition, 2004.				
2	O. E. Taylor, "Performance Design of AC Commutator motors", Wheeler Publisher, 15th Reprint.				

References

1	Purkaitand Bandyopadhyay "Electrical Machines", Oxford University Press, 1st Edition, 2017.			
2	J. B. Gupta, "Theory and Performance of Electrical Machines", S. K. Kataria and Sons, 1st Edition,			
2	2013.			
3	Fitzerald and Kingsley, "Electric Machines", Tata McGraw Hill, 7th Edition, 2007.			
4	Kothari and Nagrath, "Electric Machines", McGraw Hill, 5th Edition, 2018.			
	Useful Links			
1				

CO-PO Mapping														
	Programme Outcomes (PO) PSO										SO			
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1			3											
CO2			3											
CO3					3									
CO4					3									

The strength of mapping is to be written as 1,2,3; where, 1: Low, 2: Medium, 3: High Each CO of the course must map to at least one PO, and preferably to only one PO.

Assessment

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

	-			
Assessment	Based on	Conducted by	Typical Schedule	Marks
	Lab activities,		During Week 1 to Week 8	
LA1	attendance,	Lab Course Faculty	Marks Submission at the end of	30
	journal		Week 8	
	Lab activities,		During Week 9 to Week 16	
LA2	attendance,	Lab Course Faculty	Marks Submission at the end of	30
	journal		Week 16	
	Lab activities,	Lab Course Faculty and	During Week 18 to Week 19	
Lab ESE	journal/	External Examiner as	Marks Submission at the end of	40
	performance	applicable	Week 19	

Syllabus Prepared By	Mr. M. S. Mahagaonkar
Syllabus Checked By	Mrs. S. L. Shaikh

Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

AY 2024-25

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('niirse	Information	

ProgrammeB. Tech. Electrical EngineeringClass, SemesterSecond Year B. Tech., Sem. IIICourse Code7EL252

Course Name Electrical Circuit Analysis Lab

Desired Requisites: Fundamentals of Electrical Engineering Lab

Teaching	Scheme	Examination Scheme (Marks)							
Practical	2 Hrs/ Week	LA1	LA1 LA2 Lab ESE Total						
Interaction	-	30	30	40	100				
		Credits: 1							

Course Objectives

- 1 This course intends to provide basic practical knowledge of electrical circuit analysis.
- 2 It intends to develop skills to demonstrate transient and steady state response of first and second order electrical circuit.
- 3 It aims to develop an ability to simulate and implement various basic electrical circuits.
- 4 It will develop skills in students to build simple hardware circuits and analyze it.

Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

СО	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	Implement electrical circuits using simulations and hardware in order to obtain current, voltage and equivalent resistance.	III	Applying
CO2	Examine KCL, KVL and circuit theorems by building hardware circuit and simulations.	IV	Analyzing
CO3	Measure response of first order circuit with simulation and hardware.	V	Evaluating
CO4	Measure response of second order circuit with simulation and hardware.	V	Evaluating

List of Experiments / Lab Activities/Topics

List of Lab Activities:

- 1. Implementation of electrical circuits in software tool PSpice to measure current and voltage in D.C.
- 2. Verification of voltage and current division rule using hardware circuit.
- 3. Verification of Superposition Theorem to measure current and voltage in electrical circuit using hardware and validate the result using software tool PSpice.
- 4. Verification of Thevenin's Theorem to obtain equivalent circuit using hardware and validate the result using software tool PSpice.
- 5. Verification of Norton's Theorem to obtain equivalent circuit using hardware and validate the result using software tool PSpice.
- 6. Determine transient and steady state behaviour of a first order circuit (R-C circuit) on hardware and validate the results using software tool PSpice.
- 7. Determine transient and steady state behaviour of a second order circuit (R-L-C circuit) using software tool PSpice.
- 8. Demonstration of transient and steady state(underdamped and overdamped) behaviour of a second order circuit(R-L-C circuit) on hardware.
- 9. Implementation of electrical circuits in software tool PSpice to measure current and voltage in A.C. circuit.
- 10. Determine the active power of a A.C. circuit using software tool PSpice.

Textbooks

1	C. K. Alexandar and M.O. Sadiku, "Fundamentals of Electric Circuits", McGraw Hill Education, 7 th Edition,2022				
2	2 Hayt, Kemmerly, Durbin, "Engineering Circuit Analysis", TMH, 8th Edition, 2012.				
	References				
1	James W. Nilsson and Susan A. Riedel "Electric Circuits" Pearson, 11th Edition, 2018.				
	Useful Links				
1	https://nptel.ac.in/courses/108/105/108105153/				
2	https://nptel.ac.in/courses/108/105/108105064/				

	CO-PO Mapping													
	Programme Outcomes (PO)						PS	SO						
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1				2										
CO2				3										
CO3					2									
CO4					2									

The strength of mapping is to be written as 1,2,3; where, 1: Low, 2: Medium, 3: High Each CO of the course must map to at least one PO, and preferably to only one PO.

Assessment

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

Assessment	Based on	Conducted by	Typical Schedule	Marks
	Lab activities,		During Week 1 to Week 8	
LA1	attendance,	Lab Course Faculty	Marks Submission at the end of	30
	journal		Week 8	
	Lab activities,		During Week 9 to Week 16	
LA2	attendance,	Lab Course Faculty	Marks Submission at the end of	30
	journal	-	Week 16	
	Lab activities,	Lab Course Faculty and	During Week 18 to Week 19	
Lab ESE	journal/	External Examiner as	Marks Submission at the end of	40
	performance	applicable	Week 19	

Syllabus Prepared By	Mr. N. V. Patel
Syllabus Checked By	Mr. A. B. Patil

Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

AY 2024-25

Course Information	n
n. Electrical Engineering	g

Programme B. Tech Class, Semester Second Year B. Tech., Sem. III **Course Code** 7EL253

Course Name Analog and Digital Circuits Lab

Basic Electronics Engineering Desired Requisites:

Teaching	Scheme		Examination	Scheme (Marks)	
Practical	2 Hrs/ Week	LA1	LA2	Lab ESE	Total
Interaction	-	30	30	40	100
			Cr	edits: 1	

Course Objectives

- This lab course intends to provide basic practical knowledge of various ICs for developing linear 1 integrated circuits.
- 2 It intends to impart skills to implement different electronic circuits using operational amplifier.
- It aims to develop an ability to simulate and implement combinational and sequential circuits. 3
- This course will enable students to analyze the characteristics and behavior of analog and digital 4 circuits.

Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

СО	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	Implement circuits to gain practical experience in assembling and wiring both analog and digital circuits	III	Applying
CO2	Illustrate linear integrated circuits using operational amplifier	III	Applying
CO3	Implement applications of various analog and digital circuits.	III	Applying
CO4	Develop the ability to perform experiments and accurately measure circuit parameters to verify it with empirical data	III	Applying

List of Experiments / Lab Activities/Topics

List of Lab Activities:

- 1. Demonstration of the performance of opamp in inverting configuration using opamp
- 2. Demonstration of the performance of opamp in non-inverting configuration and buffer using opamp
- 3. Implementation of a difference amplifier using operational amplifier
- 4. Implementation of Instrumentation Amplifier using opamp
- 5. Construction of Schmitt Trigger using opamp
- 6. Design of Summing, Averaging and Scaling Amplifier using opamp
- 7. Design of a first order Active Low Pass filter using opamp
- 8. Design of a first order Active High Pass filter using opamp
- 9. Illustration of op-amp as zero crossing detector & peak detector.
- 10. Construct half-adder and full adder using logic gates
- 11. Design of the astable and mono stable multi vibrators using IC 555
- 12. To verify the truth table of D and JK flip flop
- 13. Demonstrate the operation of decoder using IC74138.
- 14. Demonstrate the operation of multiplexor using IC74151
- 15. Construct ring and twisted ring counter using D flip-flops.

	Textbooks						
1	Sergio Franco, "Design with Op-Amps and analog Integrated Circuits", Tata McGraw-Hill Publication, Third Edition, 2001						
2	Allen Mottershead, "Electronic Devices & Circuits: An Introduction", Prentice Hall India, 2010						

3	A. Anand Kumar, "Fundamentals of Digital Circuits", Prentice Hall India, Fourth Edition, 2014						
	References						
1	R.A. Gayakwad, "Op-Amps & Linear Integrated Circuits", Prentice Hall India, Fourth Edition, 2012.						
2	R. L. Boylestad and Louis Nashelsky, "Electronic Devices & Circuit Theory", Pearson Publications, Tenth Edition, 2009.						
3	M. Moris Mano and Michael Ciletti, "Digital Design", Pearson Publications, Fifth Edition, 2013						
	Useful Links						
1	NOC:Analog Electronic Circuits, IIT Delhi https://nptel.ac.in/courses/108/102/108102112/						
2	NPTEL Analog Electronic Circuits , IIT Delhi https://nptel.ac.in/courses/108/102/108102095/						
3	NOC:Digital Electronic Circuits, IIT Kharagpur https://nptel.ac.in/courses/108/105/108105132/						

	CO-PO Mapping													
	Programme Outcomes (PO)						PS	SO						
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1		3							3					
CO2			3						3					
CO3			3						3					
CO4				3					3					

The strength of mapping is to be written as 1,2,3; where, 1: Low, 2: Medium, 3: High Each CO of the course must map to at least one PO, and preferably to only one PO.

Assessment

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

Assessment	Based on	Conducted by	Typical Schedule	Marks
	Lab activities,		During Week 1 to Week 8	
LA1	attendance,	Lab Course Faculty	Marks Submission at the end of	30
	journal		Week 8	
	Lab activities,		During Week 9 to Week 16	
LA2	attendance,	Lab Course Faculty	Marks Submission at the end of	30
	journal		Week 16	
	Lab activities,	Lab Course Faculty and	During Week 18 to Week 19	
Lab ESE	journal/	External Examiner as	Marks Submission at the end of	40
	performance	applicable	Week 19	

Syllabus Prepared By	Dr. S. S. Karvekar
Syllabus Checked By	Mrs. A. A. Dhamangaonkar

Mandatory Courses

		Walc	hand College (Government Aided	of Engineering Autonomous Institu					
				2024 -25					
				nformation					
Program	me		B. Tech. Electrica						
Class, Se			Second Year B. T						
Course (7MA203	cen., beni. m					
Course N				Electrical Engineer	inα				
Desired 1		06.			ineering Mathematics	II			
Desireu i	Kequisii	ies.	Engineering Man	iematics I and Eng	meering Mamemanes	11			
Те	aching	Scheme		Evamination S	cheme (Marks)				
Lecture	acining	3 Hrs/week	MSE	ISE	ESE	Total			
Tutorial		J III S/ WEEK	30	20	50	100			
1 utoriai			30		lits: 3	100			
				Creu	1118. 5				
			Course	Objectives					
1 T	o dovole	n Mathamatica	skills and enhance		f etudonte				
				<u> </u>	pplications in enginee	ring fields			
4 1	o muou		Outcomes (CO) w		· · · · · · · · · · · · · · · · · · ·	illig fielus			
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			h constant coefficie		tue problems for fine	u Applying			
			for any periodic fur		ormulae	Applying			
			of Nonlinear Partial	•		Understa			
	nuersta	na me sommon (nommear Fartia	i differential equati	IOII	ding			
CO4 U	Indoneto	nd the Econies to	onafama and ita nu	omantias		Understa			
CO4 C	muersta	na me rouner u	ansform and its pro	opernes					
COF	Ica of bo	aia Irmanyladaa	of 7 tuon aforms to a	alva tha muahlam i	n Cional avatam	ding			
			of Z- transform to s		<u> </u>	Applying			
CO6 A	appiy va	irious probabilii	y distribution to fin	id the probabilities	•	Applying			
Module			Module	Contents		Hours			
Module	Lanla	ce Transform	and Its Applicatio			Hours			
_					Γransform of derivati	/e _			
I					neorem, Applications				
	1	linear differenti	•	ii, convolution in	reorem, rippireations				
		er Series	ar equation						
	Periodic functions, Dirichlet's conditions, Definition, Determination of Fourier								
II				formulae), Expansion of functions, Even and odd functions,					
			nd functions having						
		osine series.		s anomary portion,	11411 141180 1 041101 01				
	_		quations and its A	pplication					
III					tion to one dimension	al 6			
		equation.	, 1 0 61 5 611 1	ouro rorms, approv					
		er Transform							
IV			ine and Cosine Int	egral. Fourier sine	and Cosine transform	n, 6			
			nd Cosine transfor			,			
		ansform			J				
V	1		rm of standard fund	ctions, Properties of	f Z-transform, inverse	Z = 6			
			n to difference equa		, 111. 0100				
		ability Distribu							
				variable, continu	ous random variabl	e, _			
VI					, Poisson distribution				
			Exponential Distrib		,				
			T			l			
			<u>T</u> ex	tbooks					
1 2			g Mathematics, Erv	win Kreyszig, John	Wiley& Sons,Inc,10 ^t and J.N. Wartikar, V				

	Prakashan, Pune, 2010.							
3	Higher Engineering Maths, B.S.Grewal, Khanna Publication, 44 th Edition, 2017.							
4	Fundamental of Mathematical Statistics ,Gupta and Kapoor							
	References							
1	Higher Engineering Mathematics, B.V.Ramanna., Tata McGraw Hill Education Pvt. Ltd, 1st							
1	Edition 2007.							
2	Advanced Engineering Mathematics, H.K. Dass, S. Chand and company Ltd., 1st Edition 1988.							
3	An Introduction to probability and Statistics, V.K Rohatgi, Wiley Publication, 2 nd Edition 2008							
4								
	·							
	Useful Links							
1	https://www.youtube.com/watch?v=lkAvgVUvYvY							
2	https://www.youtube.com/watch?v=c9NibpoQjDk							

	CO-PO Mapping													
		Programme Outcomes (PO)									PS	SO		
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1	2													
CO2	2													
CO3	2													
CO4	2													
CO5	2													
CO6	2													

Assessment

The assessment is based on MSE, ISE and ESE.

MSE shall be typically on modules 1 to 3.

ISE shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be field visit, assignments etc. and is expected to map at least one higher order PO.

ESE shall be on all modules with around 40% weightage on modules 1 to 3 and 60% weightage on modules 4 to 6.

	W		ge of Engineer		angli		
		1	Y 2024-25	sillule)			
			rse Information				
Programme		All WCE Program					
Class, Seme		Second Year B.					
Course Cod		7EE201	200111, 20111. 111				
Course Nan			cubation and Entre	preneurs	ship		
Desired Rec			e double and Emile	pronoun	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
		I					
Teachi	ing Scheme		Examination	Schem	e (Marks)		
Lecture	03Hrs/week	MSE	ISE	ES		Total	
Tutorial	-	30	20	5	0	100	
	<u> </u>		Credits: 3		'		
		Cou	rse Objectives				
1	To familiarize		framework and the	e start-ur	projects which	ch help students to	
	navigate throu	gh their own entrep	oreneurial journey.				
2		entrepreneurial mi or a solution into	nd-set thereby enco a business	ouraging	the journey of	transformation to	
3							
	Cou	rse Outcomes (CC)) with Bloom's Ta	axonomy	y Level		
At the end of	f the course, the st	udents will be able	to,				
CO		Course Outcome S	Statement/s		Bloom's Taxonomy Level	Bloom's Taxonomy Descriptor	
CO1	Translate cre opportunity	ative ideas into	a sustainable b	usiness	II	Understand	
CO2		Apply principles and practice of new entrepreneurial enture planning to assess a business idea					
CO3		Differentiate among types of Business Models IV					
CO4		Evaluate decision making towards establishing enterprises					
CO4	in real life situ	ations		_	V	Evaluate	
Module		Modu	le Contents			Hours	
I	Hand holding	l Mind-Set, Corpo	nip eurship GDC star orate Entrepreneurs			7	
II	Innovation and Entrepreneurship Types					6	
III	The Innovation Process Innovation and Entrepreneurship, Solar Oven case-study Paradigm shift						
IV		el Canvas, Technol	logy led Entreprene Canvas SINE, Start-			7	
V		_	urship et Ideas, From Idea	to Proof	of Concept,	7	

VI	Cas	e Stud	v											
, -			•	amples	Start-u	ıp PITC	CHES -	Using	Lean C	anvas I	Model		6	
						Text	books							
1	Disc	cipline	d Entre	preneu	rship: 2	24 Steps	s to a S	uccessf	ful Star	tup by I	Bill Au	let		
2	The	Essen	ce of M	edical	Device	Innova	ation by	B Rav	vi					
3	THI	E FOR	TUNE	AT B	OTTO	M OF	PYRA	MID:	Eradica	ating P	overty	Throug	gh Prof	its by
3	C.K	.Praha	lad Stay	y Hung	ry									
						Refer	rences							
1			sh by R											
2												y Gurm		
3	Innovation By Design: Lessons from Post Box Design & Development by B. K. Chakravan						varthy							
	, Jai	, Janaki Krishnamoorthi												
4														
5														
Useful Links														
1														
CO-PO Mapping														
						mme (-				PS	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1		3												
CO2			3											
CO3			3											
CO4								3	3	3	3			

Assessment

There are three components of lab assessment, LA1, LA2 and Lab ESE.

IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%

Assessment	Based on	Conducted by	Typical Schedule	Marks
LA1	Lab activities, attendance, journal	Lab Course Faculty	During Week 1 to Week 8 Marks Submission at the end of Week 8	30
LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
Lab ESE	Lab activities, journal/ performance	Lab Course Faculty and External Examiner as applicable	During Week 18 to Week 19 Marks Submission at the end of Week 19	40

			llege of Engineer Aided Autonomou			
		(Government 1	AY 2024-25	3 msiiime	<i>)</i>	
		Co	urse Information			
Programme		All WCE Progr	amme			
Class, Semest	er	Second Year B.				
Course Code		7VE201				
Course Name	}	Value Educatio	n			
Desired Requ	isites:	Open mind and	a willingness to lea	arn		
Teaching	Scheme		Examination	on Scheme	(Marks)	
Lecture	01Hrs/week	LA1	LA2	ES	Е	Total
Tutorial	02 Hrs/week	30	30	40)	100
	THS/ WCCK		C	redits: -2		
		C	ourse Objectives			
1	Develop holic		professional skills b	av enhanci	ng communic	eation emotional
1	intelligence, a	and resilience to f	oster positive relati	onships an	d sustainable	living practices.
2		mindset to naviga	e leadership throug ate success and failu			
3	criticalthinkii		l contribution by reng to continuous selobal challenges.			
			CO) with Bloom's	Taxonomy	Level	
At the end of t	he course, the s	tudents will be ab	ole to,			
CO		Course Outcom	e Statement/s		Bloom's Taxonomy Level	Bloom's Taxonomy Descriptor
CO1	relationship-l	e communication, empathy, and uilding skills to foster positive personal andprofessional settings.				Remembering
CO2	Incorporate si resiliencethro	te sustainable habits into daily life and build through mindfulness and stress management to allenges and support environmental				Understanding
CO3	Develop goal manage succe	l-setting and achievement strategies, ess and failure, and deliver impactful a foroverall personal and professional				Applying
CO4	solving techn		d creative problem- formed decisions and as contexts.		IV	Analyzing
Module		Mo	odule Contents			Hours
I		Relationships, C	ommunication Skil			5
П		_	nvironmental Impa	ct, Sustain	able Practices	5, 5

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CO1 -	-	-	-	-	-	-	2	2	3	-	2		
CO2 -	-		-	-	2	3	2	2	-	-	2		
CO3 -			T .	1					2	2	2		
CO4 -		-	1	-	1	_	2	3	2		2		

Assessment

The assessment is based on LA1, LA2 and ESE.

LA1 shall be typically on modules 1 to 3.

LA2 shall be taken throughout the semester in the form of teacher's assessment. Mode of assessment can be Tests, assignments, oral, seminar etc. and is expected to map at least one higher order PO.

ESE shall be on all modules with around 30 - 40% weightage on modules 1 to 3 and 60 - 70% weightage on modules 4 to 6.

Walchand College of Engineering, Sangli

(Government Aided Autonomous Institute)

AY 2024-25

Course Information

Course Information							
Programme	Second Year B. Tech. Electrical						
Class, Semester	Second Year B. Tech., Sem III						
Course Code	7CEEL251						
Course Name	Community Engagement Project / Field Project						
Desired Requisites:	NII.						

Teaching	Scheme		Examination	n Scheme (Marks))
Practical	2 Hrs/ Week	LA1	LA2	Lab ESE	Total
Interaction	-	30	30	40	100
			C	redits: 1	

Course Objectives

- To conduct thorough community needs assessments and analyze data to identify specific challenges 1 and opportunities for engineering interventions.
- To apply engineering principles, techniques, and methodologies effectively to develop innovative 2 solutions that address identified community needs.

Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

СО	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	Apply technical knowledge and skills to develop and implement community service projects.	III	Applying
CO2	Identify and analyze community needs to design appropriate engineering solutions.	IV	Analyzing
CO3	Collaborate effectively with team members and community stakeholders to achieve project goals.	V	Evaluating
CO4	Reflect on the ethical, social, and professional implications of engineering projects within the community.	V	Evaluating

List of Experiments / Lab Activities/Topics

The Community Engagement Project/Field Project integrates academic learning with community service, allowing Electrical Engineering (EE) students to apply their technical skills in real-world settings. This course aims to provide social responsibility, enhance problem-solving skills, and provide practical experience through direct involvement in community projects.

List of Community Engagement Project / Field Project Activities:

- 1. Solar Power Installation for Community Centres: Design and install solar panels for local schools, libraries, or community centres to provide them with sustainable energy solutions.
- 2. Energy Audits for Local Homes and Businesses: Conduct energy audits to help residents and businesses identify ways to reduce energy consumption and costs.
- 3. Smart Lighting Systems: Develop and install smart lighting solutions for public parks or community areas to enhance energy efficiency and safety.
- 4. Water Purification Systems: Create and implement water purification systems in areas with limited access to clean drinking water.
- 5. Public Wi-Fi Networks: Set up free Wi-Fi hotspots in underserved areas to help bridge the digital
- 6. Electric Vehicle Charging Stations: Design and install EV charging stations in public spaces to encourage the use of electric vehicles.

- 7. Assistive Technology for Disabled Individuals: Create custom electronic devices or systems to aid individuals with disabilities in the community.
- 8. Renewable Energy Workshops: Conduct workshops on building small-scale renewable energy projects, like wind turbines or solar chargers, to educate and empower the community.
- 9. Smart Irrigation Systems: Design and implement smart irrigation systems for community gardens or local farms to optimize water usage and improve crop yields.
- 10. E-Waste Recycling Program: Set up a program to collect and properly recycle electronic waste, educating the community on the importance of e-waste management.
- 11. Home Automation for Elderly: Install simple home automation systems for elderly residents to enhance their safety and convenience.

*Note- Students must deliver a final presentation and submit a comprehensive report as the end of their project. The final presentation should be a concise, visually engaging slide deck that includes an introduction, methodology, results with data visualizations, discussion, and conclusion, followed by a Q&A session to address audience queries. Concurrently, students must submit a detailed report that documents every aspect of their project from start to finish. This report should adhere to the specified guidelines and include sections such as the title page, abstract, introduction, methodology, results, discussion, and conclusion, providing in-depth information and supporting evidence for the project's findings.

	Textbooks							
1								
2	Teamwork and Project Management" by Karl A. Smith							
3	3 "Engineering Your Community: The Professional Practice of Engineering in Public Service" based T. Wells							
4	Engineering Ethics: Concepts and Cases" by Charles E. Harris, Jr., Michael S. Pritchard, Michael J. Rabins, Ray W. James, and Elaine E. Englehardt							
	References							
1	Community-Based Participatory Research for Health: From Process to Outcomes" by Meredith Minkler and Nina Wallerstein							
2	Social Entrepreneurship: What Everyone Needs to Know" by David Bornstein and Susan Davis							
	Useful Links							
1	https://www.globalgiving.org/							
2	https://www.volunteermatch.org/							
3	https://www.councilofnonprofits.org/							

CO-PO Mapping														
	Programme Outcomes (PO)										F	PSO		
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1	3		3				3							
CO2		3		3					3					
CO3					3					3				
CO4						3		3			3	3		

The strength of mapping is to be written as 1,2,3; where, 1: Low, 2: Medium, 3: High Each CO of the course must map to at least one PO, and preferably to only one PO.

Assessment										
There are three components of lab assessment, LA1, LA2 and Lab ESE. IMP: Lab ESE is a separate head of passing.(min 40 %), LA1+LA2 should be min 40%										
Assessment Based on Conducted by Typical Schedule Mark										
	Lab activities,		During Week 1 to Week 8							
LA1	attendance,	Lab Course Faculty	Marks Submission at the end of	30						
	journal		Week 8							

LA2	Lab activities, attendance, journal	Lab Course Faculty	During Week 9 to Week 16 Marks Submission at the end of Week 16	30
	Lab activities,	Lab Course Faculty and	During Week 18 to Week 19	
Lab ESE	journal/	External Examiner as	Marks Submission at the end of	40
	performance	applicable	Week 19	

Syllabus Prepared By	Dr. V. P. Mohale
Syllabus Checked By	Mr. A. N. Inamdar

Walchand College of Engineering, Sangli (Government Aided Autonomous Institute) AY 2024-25 Course Information Programme B. Tech. Electrical Engineering Class, Semester Second Year B. Tech., Sem. III Course Code 7VSEL251 Course Name Simulation Lab Desired Requisites: NIL

Teaching	Scheme	Examination Scheme (Marks)						
Practical	2 Hrs/ Week	LA1	LA2 Lab ESE		Total			
Lecture	1	30	30	40	100			
		Credits: 2						

	Course Objectives						
1	This course intends to provide basic knowledge of MATLAB software for developing, modelling and						
1	programming techniques.						
2	It intends to impart skills to implement different tool boxes of MATLAB Simulink for electrical						
_ 4	engineering application						
3	To solve electrical engineering problems with different tool boxes of MATLAB Simulink for						
3	electrical engineering application.						
4	To design electrical systems with MATLAB Simulink software.						

Course Outcomes (CO) with Bloom's Taxonomy Level

At the end of the course, the students will be able to,

СО	Course Outcome Statement/s	Bloom's Taxonomy Level	Bloom's Taxonomy Description
CO1	Grasp the basic aspects of MATLAB programming.	II	Understanding
CO2	Solve simple mathematical equations using MATLAB.	III	Applying
CO3	Construct MATLAB software-based projects.	IV	Analyzing
CO4	Formulate electrical systems using MATLAB.	VI	Creating

List of Experiments / Lab Activities/Topics

List of Lab Activities:

- 1. Outline of MATLAB Programming and Computation of arithmetic, exponential, trigonometric and complex form operation using MATLB programming.
- 2. Demonstrate simple matrix and array manipulation using MATLAB.
- 3. Basic MATLAB Programming using control structures.
- 4. Develop a program for plotting various graphs (2D and 3D).
- 5. Outline to MATLAB Simulink.
- 6. To study different tool boxes of electrical engineering.
- 7. Solve electrical circuits using MATLAB Simulink.
- 8. To study Powergui block in MATLAB Simulink.
- 9. To build simple MATLAB simulations using power systems tool box.
- 10. To create simple MATLAB based projects.

	Textbooks							
"Modelling and simulation using MATLAB Simulink", Wiley Publication, Dr. Shailendra J								
1	,Reprint :2013							
	References							
1	"Matlab programming for Engineers", Stephen Chapman, Thomson Learning publication, 3rd							
1	Edition.							
2	"Contemporary linear systems using MATLAB", Robert Strum and Donald Kirk, Thomson							
2	Learning publication.							

3 "Power System Transient Analysis", Theory and Practice using simulation programs, Power System, Eiichi Haginomori Junichi Arai, WILEY Publication.						
Useful Links						
1	MATLAB Programming for Numerical Computation: https://nptel.ac.in/courses/103106118					

CO-PO Mapping														
		Programme Outcomes (PO)									PSO			
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
CO1	2				1									
CO2	1	2												
CO3			3		2									2
CO4	3	3		2										

The strength of mapping is to be written as 1,2,3; where, 1: Low, 2: Medium, 3: High Each CO of the course must map to at least one PO, and preferably to only one PO.

Assessment

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IMP: Lab ESE is a separate head of passing (min 40 %), LA1+LA2 should be min 40%

	•	1 0 1		
Assessment	Based on	Conducted by	Typical Schedule	Marks
	Lab activities,		During Week 1 to Week 8	
LA1	attendance,	Lab Course Faculty	Marks Submission at the end of	30
	journal		Week 8	
	Lab activities,		During Week 9 to Week 16	
LA2	attendance,	Lab Course Faculty	Marks Submission at the end of	30
	journal		Week 16	
	Lab activities,	Lab Course Faculty and	During Week 18 to Week 19	
Lab ESE	journal/	External Examiner as	Marks Submission at the end of	40
	performance	applicable	Week 19	

Syllabus Prepared By	Mr. S. S. Medhekar
Syllabus Checked By	Dr. R. P. Hasabe