

VIP REGISTER MAP

Master Base Address: {A006_0000, A007_0000}, Master High Address: {A006_FFFF, A007_FFFF}

VIP REGISTER MAP

Master Base Address: {A006_0000, A007_0000}, Master High Address: {A006_FFFF, A007_FFFF}

Module Name	Address Register (16-bits)				Absolute Register Index		Data Register (32-bits) 1 section = 4 Bytes 2 sections = 2 Bytes each 4 sections = 1 Byte each	Absolute Register Address		Register Count
	Module ID (0-511)		Register ID (0-31)							
CONFIG	0		0		0		RESET	0000		1
	0		1		1		WIDTH	0004		1
	0		2		2		HEIGHT	0008		1
	0		3		3		BITS	000C		1
	0		4	15	4	15	Reserved	0010	003C	12
CONFIG	0		16		16		TOP_EN*	0040		1
	0		17	31	17	31	Reserved	0044	007C	15
RGBC	1		0		32		in_conv_standard	0080		1
	1		1	31	33	63	Reserved	0084	00FC	31
IRC	2		0		64		CROP_X	0100		1
	2		1		65		CROP_Y	0104		1
	2		2		66		IRC_OUTPUT	0108		1
	2		3	31	67	95	Reserved	010C	017C	29
SCALE	3		0		96		s_in_crop_w	0180		1
	3		1		97		s_in_crop_h	0184		1
	3		2		98		s_out_crop_w	0188		1
	3		3		99		s_out_crop_h	018C		1
	3		4		100		dscale_w	0190		1
	3		5		101		dscale_h	0194		1
	3		6	31	102	127	Reserved	0198	01FC	26
OSD	4		0		128		OSD_X	0200		1
	4		1		129		OSD_Y	0204		1
	4		2		130		OSD_W	0208		1
	4		3		131		OSD_H	020C		1
	4		4		132		OSD_COLOR_FG	0210		1
	4		5		133		OSD_COLOR_BG	0214		1
	4		6		134		ALPHA	0218		1
	4		7	31	135	159	Reserved	021C	027C	25
YUV444TO422	5		0		160		YUV444TO422	0280		1
	5		1	31	161	191	Reserved	0284	02FC	31
Reserved	6	127	0	31	192	4095	Reserved	0300	3FFC	3904
OSD_RAM	128	255	0	31	4096	8191	OSD_RAM	4000	7FFC	4096
Reserved	256	511	0	31	8192	16383	Reserved	8000	FFFF	8192

TOP_EN*					
31-5	4	3	2	1	0
Reserved	yuv444TO422_en	osd_en	dscale_en	crop_en	yuv2rgb_en