Update on Ara

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Summary

- **WIP: T-Head board**
  - T-Head 906: a new hope

- **WIP: benchmarks**
  - T-Head NN library
  - FFT

- **WIP: new instructions**
  - Segment-memory operations

T-Head C906

- **Low-effort approach**
    - Ask in the communities

- Two answers pointed to the same direction
    - **Run** the **board** in **bare-metal** (no OS)
    - Enable the V accelerator
    - Source: https://mp.weixin.qq.com/s/TCG8-zrxdMaWVurAzN7GhA

- Reference to the following repository:

    - https://github.com/bigmagic123/d1-nezha-baremeta

T-Head C906

Need for some patchwork, the tutorial is not straightforward

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    - Avoid scatter/gather?
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- **Segmented memory operations**