TWG Meeting Agenda April 26 2021

- TG Reports
- TWG Chair Elections
- Project Launch for CORE-V MCU
- OpenHW Lightweight Specification Process
- CV-X-IF Specification Introduction
- PlatformIO Concept
- OpenHW Job Board

1. Voting Eligibility for April 2021 meeting

• A member needs to attend 3 of 4 previous meetings to be eligible to vote. Note that the current meeting forms part of the "4 previous meetings" as stated in the bylaws.

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Members Voting-Eligible for April Meeting		April 2021	March 2021	Feb 2021	Jan 2021
Υ	CMC	Υ	Υ	Υ	
Υ	Codeplay	Υ	Υ	Υ	Υ
Υ	Embecosm	Υ	Υ	Υ	Υ
Υ	EM Micro		Υ	Υ	Υ
	ETH Zurich			Υ	
Υ	Futurewei	Υ	Υ	Υ	Υ
N	Imagination	Υ	Υ		
Υ	Imperas	Υ	Υ	Υ	Υ
Υ	NXP	Υ	Υ	Υ	Υ
Υ	Quicklogic	Υ	Υ	Υ	Υ
Y	SiLabs	Υ	Υ	Υ	
Υ	THALES	Υ	Υ	Υ	Υ

2. TG reports highlighting changes to plan of record, critical issues on projects

Group	Reporting	Link to report	Key Issues & Actions Noted	
SW	Jeremy Bennet		 Projects progressing, but need to chart path from PC 	
HW	Hugh Pollitt Smith		 Verilator project - delayed due to key resource unavailable Embecosm and Florian can still progress this 	
Cores	Arjan Binks		 X is moving along Discussion with Imperas about modelled features in ISS RVFI is progressing CVA6 - specification phase Agreement on verification structure is in place Will hit verification resource problem. Early discussions (Jerome) is underway with potential participant 	
Verif.	Steve Richmond		 Prototype is being put in place for demonstration of approach agreed with CVA6 CVA6 verification task will be much longer than 40P 	

3. TWG Chair Elections

THANK YOU TO JERRY ZHENG and SEBASTIAN AHMED!!!

TWG Chair(s) Election

TWG Chair Structure

- Chair
- Vice-Chair

Nominations

- Self-Nominations open to Platinum/Gold Members
- Self-Nominations due by Thursday May 6 midnight EST
- Send email to duncan@openhwgroup.org

Voting

- OpenHW Staff will announce nominations and voting instructions Monday May 10
- Voting among all OpenHW Members
- Voting instructions via email will be announced (if required)
- Voting will finish Monday May 17 midnight EST

Staff will handle nominations as follows:

- If we have one nomination
 - Acclaimed as Chair
- If we have two nominations
 - Nominees may agree on roles, or:
 - o One vote per member to determine Chair
- If we have multiple nominations
 - Two votes per members to choose Chair and Vice-Chair
 - Chair is top recipient
 - Vice-Chair Next recipient

First meeting with new chairs

May 24 TWG meeting

TWG Goals

GOAL OF TWG

- Act as plenary technical body in OpenHW which provides direction to TG and reviews results from TG
- The "central" in the "central + distributed" technical organization of OpenHW
- Develop the open source hardware value proposition

TWG CHAIR ROLES

- Facilitate creation of overall OpenHW roadmap and release plan, working with OpenHW staff and member community
- Maintain focus on industrial quality IP creation
- Manage project/gate reviews, working with OpenHW Staff
- Encourage ideation and member participation in TWG discussion
- Facilitate TRL mapping on projects, working with OpenHW Staff
- Oversee application of specification and development process

CURRENTLY

The TWG is strong in:

- Proposing/investigating new projects across the ecosystem
- Assembling and reviewing project launch information

The TWG could do these better:

- Tracking projects, beyond PC
 - inter-project dependencies
 - Change control on feature specs
- Open discussion on priorities
- TWG mail list

Opportunities for TWG

- Technology roadmap for Core-V ecosystem
- Develop Release Plan across all projects
- Specification projects
- Drive OpenHW CI methodology
- Additional OpenHW scope
- Over the horizon view

4. Project Launch Gate Approval for CORE-V MCU

Tim Saxe presented the MCU Project Launch proposal.

MCU PL document

Project Plan at PL

Although a lot of planning work has already been done, additional integration planning for the software integration needs to happen prior to PA (Plan Approved).

Member Vote on Approval of PL Gate

Member	Eligible to Vote for April 2021 meeting	Votes to Approve PL Gate for CORE-V MCU
CMC	Υ	Υ
Codeplay	Υ	Υ
Embecosm	Υ	Υ
EM Micro	Υ	
Futurewei	Υ	Υ
Imagination	N	
Imperas	Υ	Υ
NXP	Υ	Υ
Quicklogic	Υ	Υ
SiLabs		
THALES	Υ	Υ

8 votes to approve PL Gate for CORE-V MCU

0 abstain

0 against

Project Launch Approved

5. OPENHW "LIGHTWEIGHT" SPECIFICATION PROCESS

This was not discussed due to lack of time

https://docs.google.com/document/d/1PuhmuG_ypy9hazqy7dJJVfhDge2dC3tVtN_-PvEcs1g/edit

Discussion Item:

- How to handle Test Compliance Kit in OpenHW Specification
- What are the types of compliance approaches that OpenHW may need for future specifications?
 - Feature checklist
 - Reference test code for APIs or s/w implementations
 - Verification test suite for RTL
 - other

6. CV-X-IF SPECIFICATION Introduction: Davide Schiavone

Davide Shiavone updated the TWG about the Project Concept in development for this specification proposal.

See the slide deck

CV-X-IF slidedeck

We made a call for interest in driving this project from a Project Manager perspective with the following responsibilities:

- pull it together via regular conf call which you will run
- assess where the specification is now, and the scope of any updates needed to get to release.
- strawman a table of contents and ask for contributions against it
- track progress and update any issues list (probably a Github board would help)
- speak to the progress in the TWG or on OpenHW TV
- help sort out any future compliance testing approach we want to develop

After the TWG meeting, Trefor Southwell volunteered to PM this. The action is to complete a Project Concept prior to the next TWG meeting.

7. PlatformIO Project Update (Ivan Kravetz)

Deferred for next TWG meeting

8. OPEN-HW Job Board Proposal (Jeremy Bennett)

Decided not to proceed with this proposal - OpenHW will instead be happy to spread the word on relevant open positions by member companies.