



Mike Thompson &lt;mike@openhwgroup.org&gt;

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**Re: UltraSoC-OpenHW technical sync-up**

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**Richard Oxland** <richard.oxland@ultrasoc.com>

Tue, Apr 21, 2020 at 3:15 PM

To: Davide Schiavone <davide@openhwgroup.org>, Mike Thompson <mike@openhwgroup.org>, Antonio Martinez Zambrana <antonio.martinez-zambrana@ultrasoc.com>, Iain Robertson <iain.robertson@ultrasoc.com>  
Cc: Rick O'Connor <rickoco@openhwgroup.org>, Andy Gothard <andy.gothard@ultrasoc.com>, Gajinder Panesar <gajinder.panesar@ultrasoc.com>, Rupert Baines <rupert.baines@ultrasoc.com>

Attending

Iain Robertson, VP HW Eng, UltraSoC

Antonio Martinez, HW Eng, UltraSoC

Richard Oxland, Head of Product, UltraSoC

Davide Schiavone, Technical Director, OHG

Mike Thompson, Technical Director, OHG

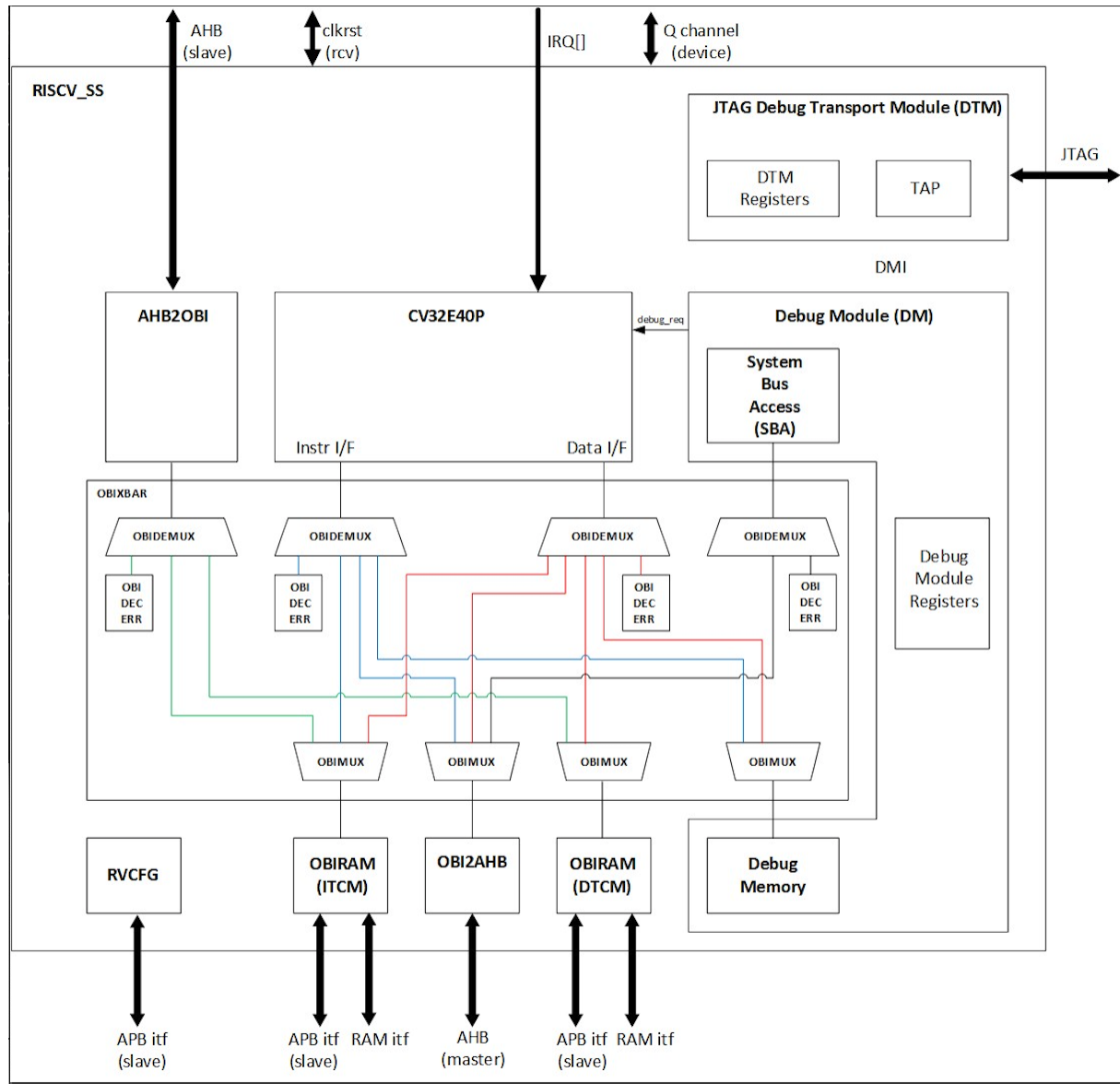
Discussion centered around the two main agenda items below.

Summary

- **Current status of CORE-V debug and trace**
  - “Core : Tiles” channel joined on the OHG Mattermost space
  - Current debug option is open source from PULP
  - Proposal to use UltraSoC JPAM instead
  - No trace interface as yet, no resource assigned for that
  - Not a major job, but decision must be taken at TWG level and resources identified
- **Eval platform design**
  - Two projects (for MCU and APU) under the TWG
  - Decisions will be at TWG level
- **Open-source Trace Encoder onto the OpenHW repo**
  - Discussed documentation, testbench, etc
  - UltraSoC will work on getting all that in place

Action items

- [Richard] When chairs are elected UltraSoC will follow up on eval platform design
- [Mike] Point us to the document that contains the CPU subsystem schematic below
- [Richard] Come back with UltraSoC feedback on the debug options
- [Mike] Follow up with Iain on the Github instructions



**From:** richard.oxland@ultrasoc.com

**When:** 13:00 - 14:00 21 April 2020

**Subject:** UltraSoC-OpenHW technical sync-up

**Location:** <https://zoom.us/j/97920720027?pwd=SmRkRkJsb0g0WG42TzZYTmYvekFLZz09>

Topics to cover:

- OHG debug/trace strategy
- Trace Encoder IP status and release process/requirements
- Any other items

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Richard Oxland is inviting you to a scheduled Zoom meeting.

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