**SYSC2310 – LAB 2 REPORT**

1. EXERCISE 1

* 1. Description of the Circuit’s Operation

The circuit created in Exercise 1 is known as a half-adder and is shown in Figure 1. It consists of two inputs 1-bit inputs and two 1-bit outputs. The circuit performs the addition of bits A and B and returns the sum and the value of the carried-over bit, known as the carry. For example, if the inputs consisted of 1 and 0, the sum will be 1 and the carry will be 0. However, if A and B are both 1, then the sum will be 0 and the carry will be 1.

Graphical user interface, application

Description automatically generatedFigure 1. Screenshot of circuit from Exercise 1

* 1. Verifying the Circuit’s Operation

The circuit’s operation is verified using e

Graphical user interface, application

Description automatically generated

Figure 2. Screenshot of Exercise 1 circuit log file

(Describe how log file

1.3 Further Comments

2. EXERCISE 2

2.1 Description of the Circuit’s Operation

(insert screenshot of circuit)

2.2 Verifying the Circuit’s Operation

(insert screenshot of log file)

2.3 Further Comments

3. EXERCISE 3

3.1 Description of the Circuit’s Operation

(insert screenshot of circuit)

3.2 Verifying the Circuit’s Operation

(insert screenshot of log file)

3.3 Further Comments