**SYSC2320 – Lab 1 Prelab**

1. BACKGROUND

1.1 Read Only Memory (ROM)

Read Only Memory is a memory component with an array of n-bit words. Each word is identified by an address and the contents of a ROM are static and determined during manufacture. ROM supports a read operation, but not a write operation.

1.2 Finite State Machine (FSM)

A Finite State Machine is…