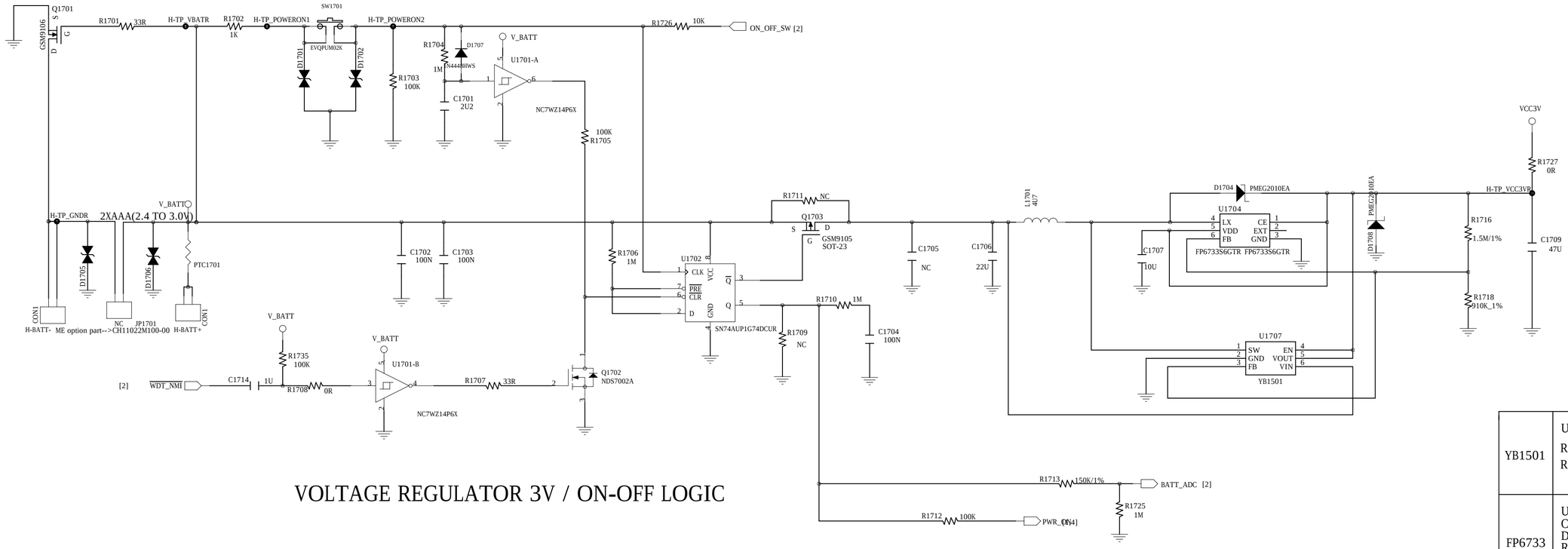
Power
Location : 17xx

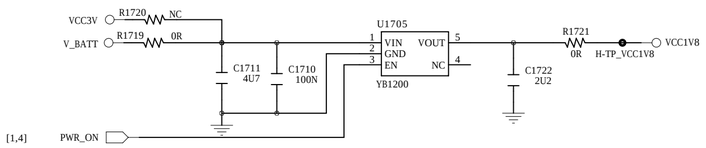


VOLTAGE REGULATOR 3V / ON-OFF LOGIC

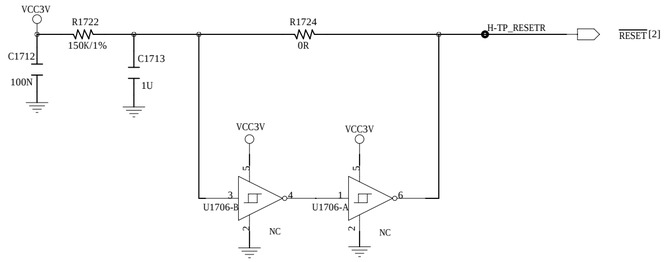
YB1501	U1707 R1716 R1718
FP6733	U1704 C1707 10U D1704 PMEG2010BEA R1716 768K 1% R1718 548K 1%

U1704/U1707_Boost IC component List

VOLTAGE REGULATOR 1.8V

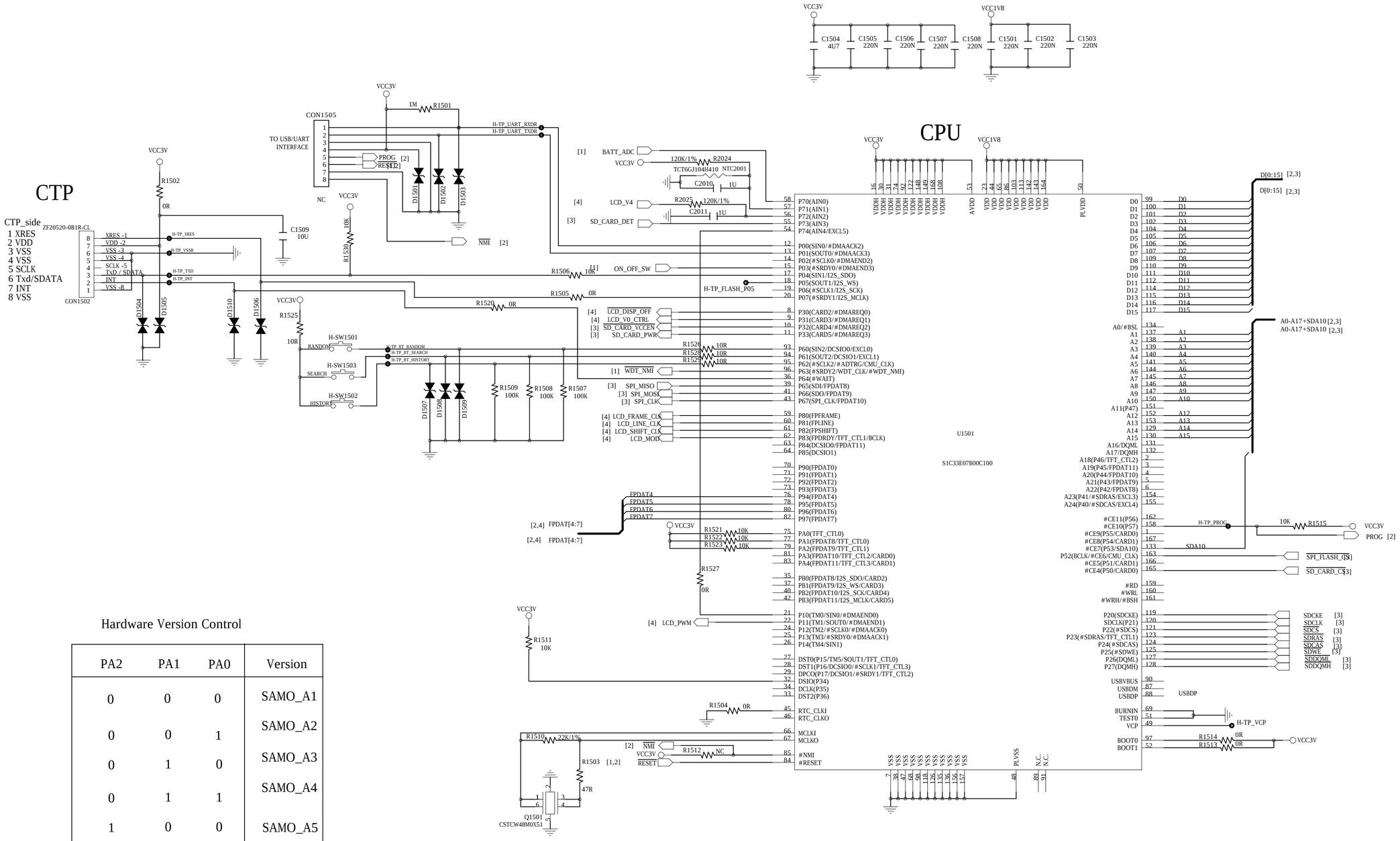


RESET



OPENMOKO	
LICENSE	CC-BY-SA
MODEL	SAMO
TITLE	Power
NUMBER	V3
DWG.	Tim Lee
SHEET	1
DATE	2010/05/06

CPU_Die form
Location : 15xx



Hardware Version Control

PA2	PA1	PA0	Version
0	0	0	SAMO_A1
0	0	1	SAMO_A2
0	1	0	SAMO_A3
0	1	1	SAMO_A4
1	0	0	SAMO_A5
1	0	1	SAMO_V1
1	1	0	SAMO_V2 COB Version
1	1	1	SAMO_V3 COB Version

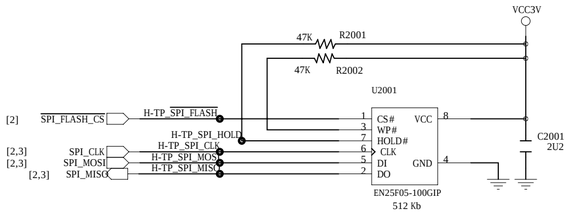
Package	BGA	Die
Pin Count	180pins	168pins
	1`VDD(N2`K7`H5) 2`VDDH(L9`K9) 3`VSS(E10`N9`N3`K11`K10) 4`NC(P14`P1)	
Part Type	S1C33E07B00C100_0	S1C33E07B00C100_168

Some defferences between BGA and Die form

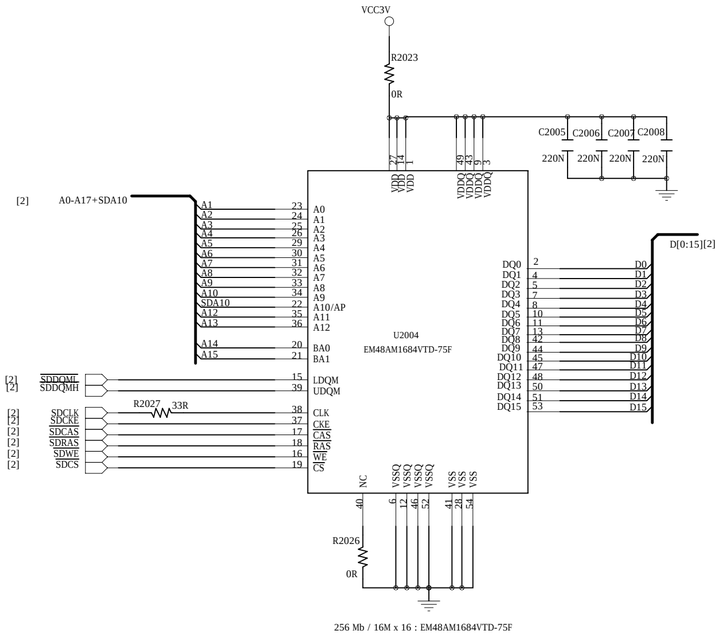
OPENMOKO			
LICENSE	CC-BY-SA		
MODEL	SAMO		
TITLE	MCU_COB Version		
NUMBER	V3		
DWG.	Tim Lee	SHEET	2
		DATE	2009/10/26

Memory
Location : 20xx

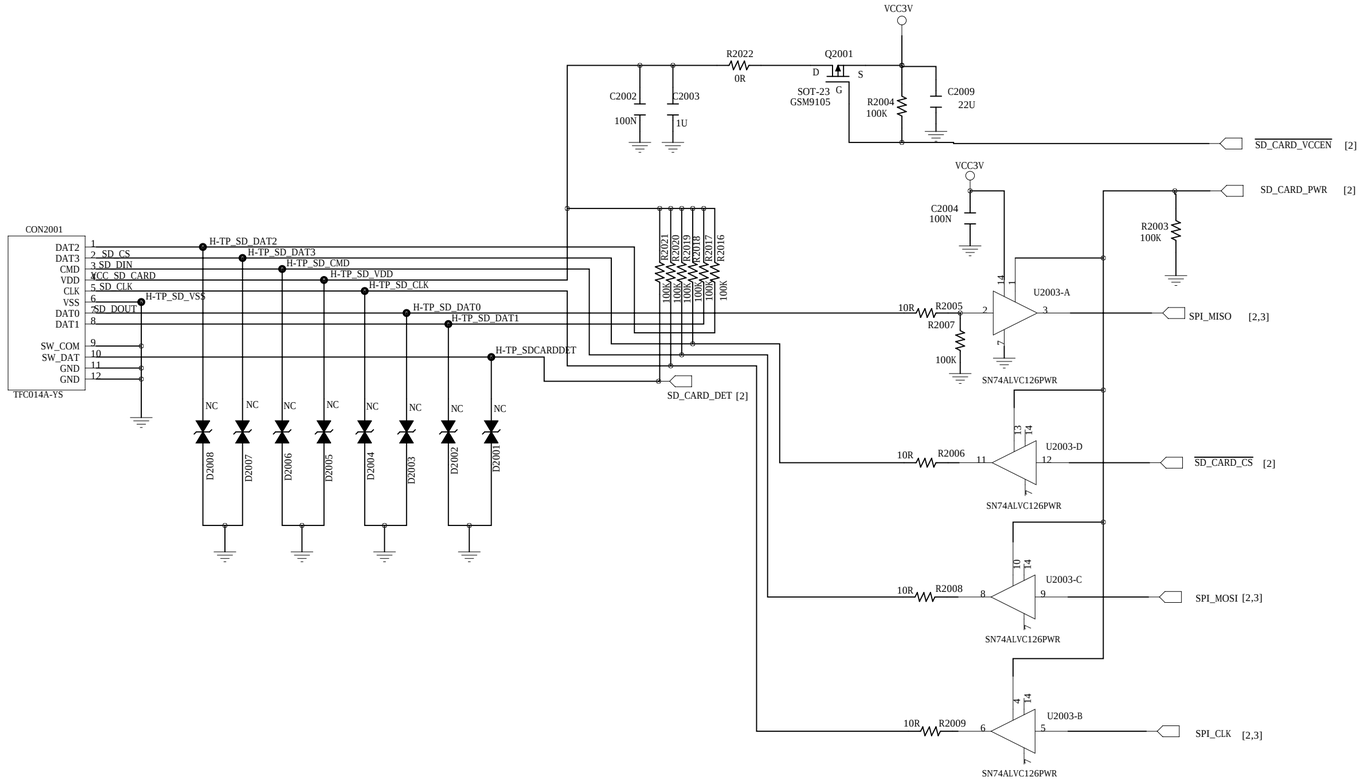
SPI FLASH



RAM

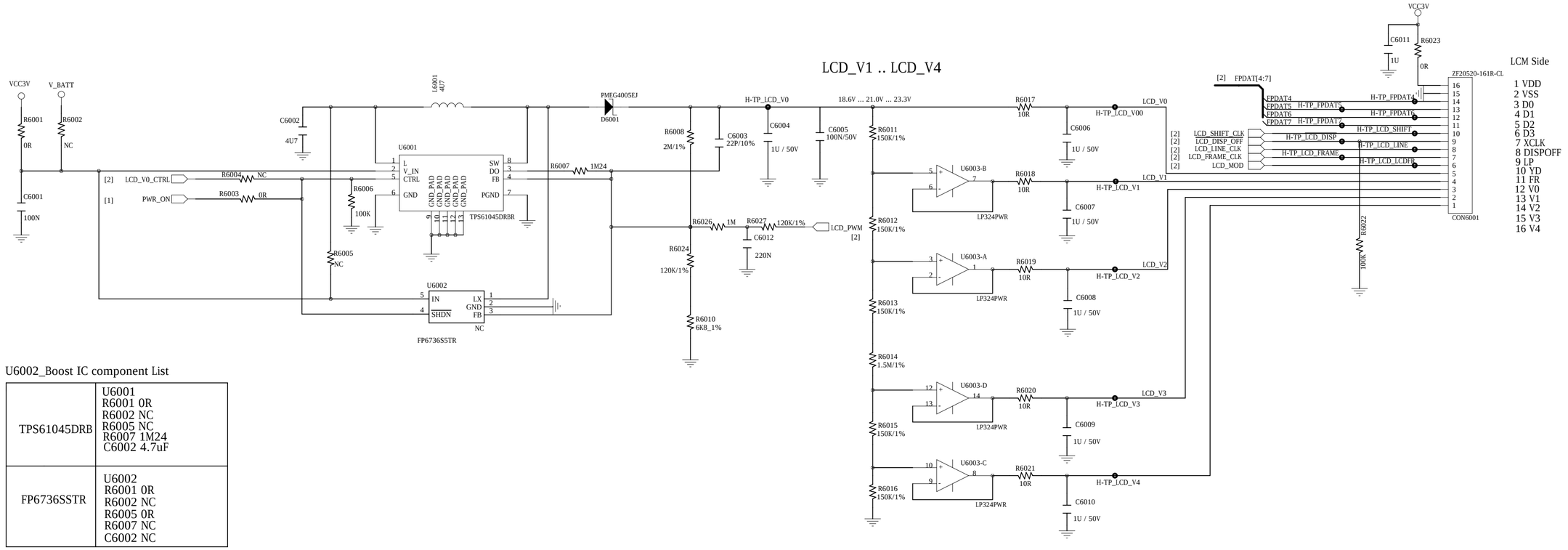


SD CARD INTERFACE



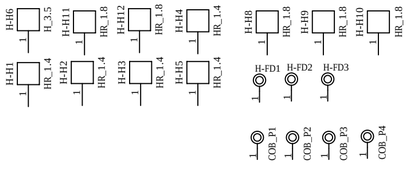
OPENMOKO			
LICENSE	CC-BY-SA		
MODEL	SAMO		
TITLE	MEMORY		
NUMBER	V3		
DWG.	Tim Lee	SHEET	3
		DATE	2009/10/26

LCD
Location : 60xx



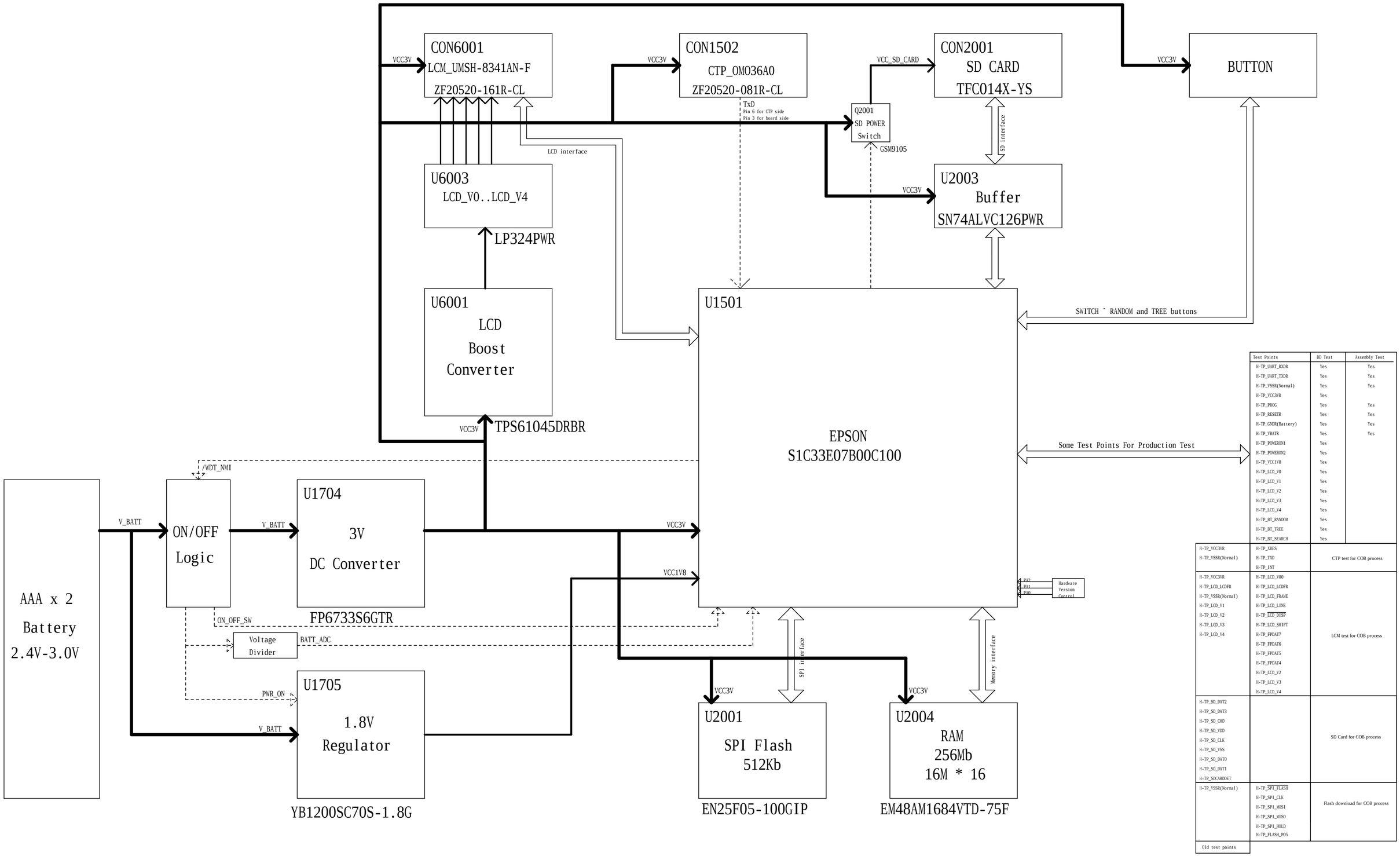
U6002_Boost IC component List

TPS61045DRB	U6001 R6001 0R R6002 NC R6005 NC R6007 1M24 C6002 4.7uF
FP6736SSTR	U6002 R6001 0R R6002 NC R6005 0R R6007 NC C6002 NC



OPENMOKO			
LICENSE	CC-BY-SA		
MODEL	SAMO		
TITLE	LCD		
NUMBER	V3		
DWG.	Tim Lee	SHEET	4
		DATE	2009/10/26

BLOCK DIAGRAM



Test Points	BD Test	Assembly Test
H-TP_UART_RXDR	Yes	Yes
H-TP_UART_TXDR	Yes	Yes
H-TP_VSSR(Nornal)	Yes	Yes
H-TP_VCC3VR	Yes	
H-TP_PROG	Yes	Yes
H-TP_RESETR	Yes	Yes
H-TP_GNDR(Battery)	Yes	Yes
H-TP_VBATR	Yes	Yes
H-TP_POWERON1	Yes	
H-TP_POWERON2	Yes	
H-TP_VCC1V8	Yes	
H-TP_LCD_V0	Yes	
H-TP_LCD_V1	Yes	
H-TP_LCD_V2	Yes	
H-TP_LCD_V3	Yes	
H-TP_LCD_V4	Yes	
H-TP_BT_RANDOM	Yes	
H-TP_BT_TREE	Yes	
H-TP_BT_SEARCH	Yes	

H-TP_VCC3VR H-TP_VSSR(Nornal)	H-TP_XRES H-TP_TXD H-TP_INT	CTP test for COB process
H-TP_VCC3VR H-TP_LCD_LCDFR H-TP_VSSR(Nornal) H-TP_LCD_V1 H-TP_LCD_V2 H-TP_LCD_V3 H-TP_LCD_V4	H-TP_LCD_VDD H-TP_LCD_LCDFR H-TP_LCD_FRAME H-TP_LCD_LINE H-TP_LCD_DISP H-TP_LCD_SHIFT H-TP_FPDAT7 H-TP_FPDAT6 H-TP_FPDAT5 H-TP_FPDAT4 H-TP_LCD_V2 H-TP_LCD_V3 H-TP_LCD_V4	LCM test for COB process
H-TP_SD_DAT2 H-TP_SD_DAT3 H-TP_SD_CMD H-TP_SD_VDD H-TP_SD_CLK H-TP_SD_VSS H-TP_SD_DAT0 H-TP_SD_DAT1 H-TP_SDCARDDET		SD Card for COB process
H-TP_VSSR(Nornal)	H-TP_SPI_FLASH H-TP_SPI_CLK H-TP_SPI_MOSI H-TP_SPI_MISO H-TP_SPI_HOLD H-TP_FLASH_POS	Flash download for COB process
Old test points		

OPENMOKO			
LICENSE	CC-BY-SA		
MODEL	SAMO		
TITTLE	BLOCK DIAGRAM		
NUMBER	V3		
DWG.	Tim Lee	SHEET	5
		DATE	2009/10/26