



DVI-to-RGB (Sink) 1.4 IP Core User Guide

Revised April 23, 2015; Author Elod Gyorgy

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1 Introduction

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CAw	Α	Α	Α	A A	у А	Α	Α		
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2 Features

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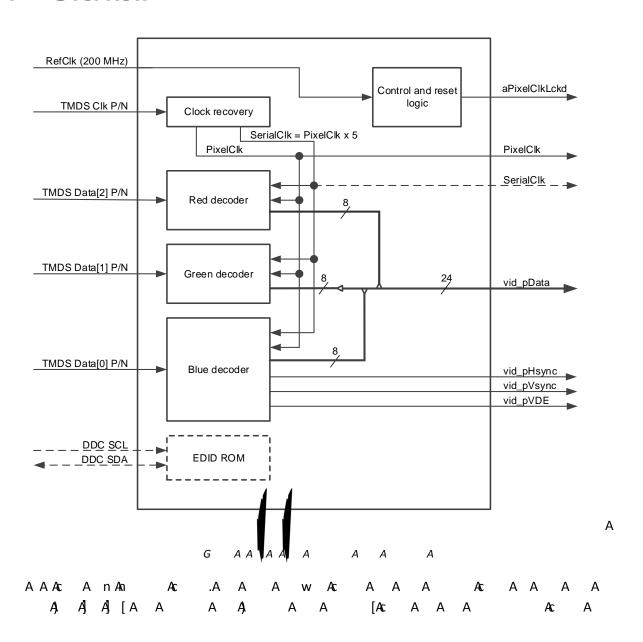
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3 Performance



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4 Overview

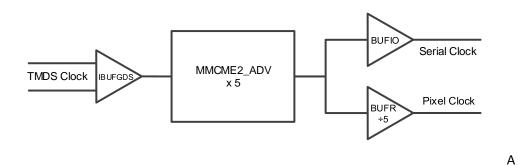


4.1 Clock recovery

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G A A A A

4.2 Data Decoder

4.2.1 Synchronization



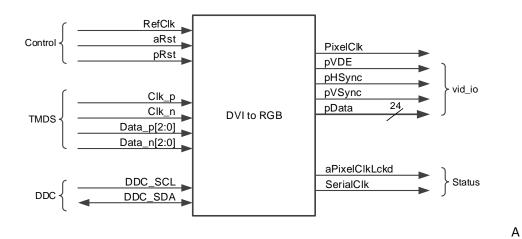
4.2.2 Decoding

4.3 EDID ROM (Display Data Channel)



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Port Descriptions



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6 Designing with the core

6.1 Customization

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6.2 Using SerialClk

6.3 Default EDID



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Anc A	Α
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G A w A	Α
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Α	

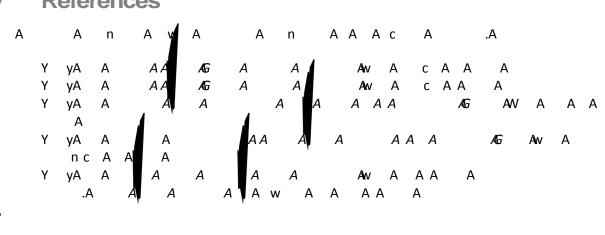
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7 References



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