# AC6379B Datasheet

# Zhuhai Jieli Technology Co.,LTD

Version: 1.0

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## **AC6379B Features**

#### **CPU**

- 32-bit DSP supports hardware Float Point Unit(FPU)
- Up to 160MHz programmable processor
- 64Vectored interrupts
- 4 Levels interrupt priority

#### **Bluetooth**

- Compliant with Bluetooth
  V5.1+BR+EDR+BLE specification
- Meet class1 class2 and class3 transmitting power requirement
- Support GFSK and π/4 DQPSK all packet types
- Provides amaximum+8dbm transmitting power
- receiver with -94dBm sensitivity
- Fast AGC for enhanced dynamic range
- Supports

  a2dp\avctp\avdtp\avrcp\hfp\spp\smp\att\gapga

  tt\rfcomm\sdp\l2cap profile

#### **Peripherals**

- One full speed USB 2.0 OTG controller
- Six multi-function 32-bit timers, support capture and PWM mode

- Three full-duplex basic UART, support DMA mode
- One hardware IIC interface supports host and device mode
- One Built-in low power Cap Sense Keys
- Built-in Cap Sense Key controller
- 10-bit ADC for analog sampling
- External wake up/interrupt on all GPIOs

#### **PMU**

- Low voltage LDO and DC-DC for internal digital and analog circuit supply
- 2uA current consumption in the soft-off mode
- Built-in LDO and DC-DC for the core, I/O, Bluetooth and flash
- **VDDIO** is 1.8V to 3.4V

#### **Packages**

SOP16

#### **Temperature**

- Operating temperature: -40°Cto+85°C
- Storage temperature: -65°C to +150°C

#### **Applications**

Bluetooth IOT

# 1. Pin Definition

## 1.1 Pin Assignment

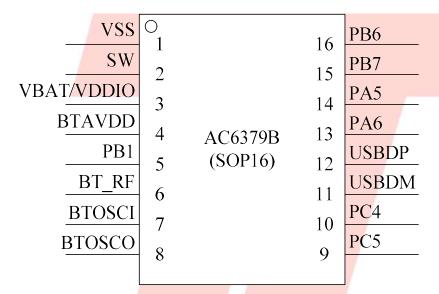


Figure 1-1 AC6379B Package Diagram



# 1.2 Pin Description

Table 1-1 AC6379B Pin Description

Name	DD.		1/0	ъ.		
1	PIN	Name	I/O	Drive	Function	Other Function
2   SW	NO.		Туре	(mA)		
VBAT	1	VSS	P	/		
VDDIO	2	SW	P	/	DCDC output	DCDC switch output, connected to inductor
VDDIO	2	VBAT	P	/		connect to battery
Solution	3	VDDIO	P	/		IO Power 3.3v
Section   PBI	4	BTAVDD	P	/		BT Power
S						Long Press Reset
Page   Page	5	PB1	I/O	8/24	GPIO	UART2TXC: Uart2 Data Output(C)
BT_RF				0/21	(pull up)	ADC6: ADC Input Channel 6
PCS						LP_TH0: Low Power Touch Channel 0
BTOSCO	6	BT_RF	/	1	7.4	BT Antenna
9 PC5 1/0 8/24 GPIO UART2RXD: Uart2 Data Input(D) SPI1DOB: SPI1 Data Out(B) IIC_SDA_B: IIC SDA(B) ADC5: ADC Input Channel 5  UART2TXD: Uart2 Data Output(D) SPI1CLKB: SPI1 Clk(B) IIC_SCL_B: IIC SCL(B) ADC4: ADC Input Channel 4 PWM4: Timer4 PWM Output:  USB Negative Data (pull down)  12 USB Positive Data (pull down)  IIC_SCL_A: IIC SCL(A) ADC11: ADC Input Channel 10  IIC_SCL_A: IIC SDA(D) UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART1ORXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	7	BTOSCI	I	/		BTOSC In
9 PC5 I/O 8/24 GPIO SPI1DOB: SPI1 Data Out(B) IIC_SDA_B: IIC SDA(B) ADC5: ADC Input Channel 5  UART2TXD: Uart2 Data Output(D) SPI1CLKB: SPI1 Clk(B) IIC_SCL_B: IIC SCL(B) ADC4: ADC Input Channel 4 PWM4: Timer4 PWM Output:  USB Negative Data (pull down) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  USBDP I/O 4 USB Positive Data (pull down) IIC_SCL_A: IIC SDA(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	8	BTOSCO	О	/	7 /	BTOSC Out
9 PC5 I/O 8/24 GPIO IIC_SDA_B: IIC SDA(B) ADC5: ADC Input Channel 5  UART2TXD: Uart2 Data Output(D) SPI1CLKB: SPI1 Clk(B) IIC_SCL_B: IIC SCL(B) ADC4: ADC Input Channel 4 PWM4: Timer4 PWM Output;  USBDM I/O 4 USB Negative Data (pull down) SPI2DOB: SPI2 Data Out(B) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UARTORXA: Uar0 Data Input(A) SPI2DOA: SPI2DOA: SPI2Data Out(A) ADC2: ADC Input Channel 2					/ /	UART2RXD: Uart2 Data Input(D)
IIC_SDA_B: IIC SDA(B)   ADC5: ADC Input Channel 5   UART2TXD: Uart2 Data Output(D)   SPI1CLKB: SPI1 Clk(B)   IIC_SCL_B: IIC SCL(B)   ADC4: ADC Input Channel 4   PWM4: Timer4 PWM Output:   UART1RXD: Uart1 Data Input(D)   SPI2DOB: SPI2 Data Out(B)   IIC_SDA_A: IIC SDA(A)   ADC11: ADC Input Channel 11   UART1TXD: Uart1 Data Output(D)   SPI2CLKB: SPI2 Clk(B)   IIC_SCL_A: IIC SDA(A)   ADC11: ADC Input Channel 10   IIC_SCL_A: IIC SCL(A)   ADC10: ADC Input Channel 10   IIC_SDA_D: IIC SDA(D)   UART0RXA: Uar0 Data Input(A)   SPI2DOA: SPI2 Data Out(A)   ADC2: ADC Input Channel 2		DC5	I/O	0/24	CDIO	SPI1DOB: SPI1 Data Out(B)
USBDM  1/O  8/24  GPIO  UART2TXD: Uart2 Data Output(D) SPI1CLKB: SPI1 Clk(B)  IIC_SCL_B: IIC SCL(B) ADC4: ADC Input Channel 4 PWM4: Timer4 PWM Output:  UART1RXD: Uart1 Data Input(D) SPI2DOB: SPI2 Data Out(B) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC_SDA(D) UART0RXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	9	PCS	1/0	8/24	GPIO	IIC_SDA_B: IIC SDA(B)
SPI1CLKB: SPI1 Clk(B) IIC_SCL_B: IIC SCL(B) ADC4: ADC Input Channel 4 PWM4: Timer4 PWM Output;  USBDM I/O  4  USB Negative Data (pull down)  USB Positive Data (pull down)  UARTITXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  UARTORXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2						ADC5: ADC Input Channel 5
10 PC4 I/O 8/24 GPIO IIC_SCL_B: IIC SCL(B) ADC4: ADC Input Channel 4 PWM4: Timer4 PWM Output:  USBDM I/O 4 USB Negative Data (pull down) SPI2DOB: SPI2 Data Out(B) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UARTITXD: Uart1 Data Input(D) SPI2COB: SPI2 Data Out(B) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UARTITXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UARTORXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	1				- V - /	UART2TXD: Uart2 Data Output(D)
ADC4: ADC Input Channel 4 PWM4: Timer4 PWM Output;  UARTIRXD: Uart1 Data Input(D) SPI2DOB: SPI2 Data Out(B) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UARTITXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC_SDA(D) UARTORXA: Uar0 Data Input(A) SPI2DOA: SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2						SPI1CLKB: SPI1 Clk(B)
PWM4: Timer4 PWM Output;  USB Negative Data (pull down)  I/O  USB Negative Data (pull down)  IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Output(D) SP12CLKB: SP12 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A) SP12DOB: SP12 Data Out(B) IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A) SP12DOA: SP12 Data Out(A) ADC2: ADC Input Channel 2	10	PC4	I/O	8/24	GPIO	IIC_SCL_B: IIC SCL(B)
USB Negative Data (pull down)  12  USB Negative Data (pull down)  4  USB Negative Data (pull down)  IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Out(B) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART1TXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	V					ADC4: ADC Input Channel 4
USBDM  I/O  4  USB Negative Data (pull down)  IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	1/2					PWM4: Timer4 PWM Output;
11 USBDM I/O 4 (pull down) IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A) SPI2DOA: SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2					A	UART1RXD: Uart1 Data Input(D)
(pull down)  IIC_SDA_A: IIC SDA(A) ADC11: ADC Input Channel 11  UART1TXD: Uart1 Data Output(D)  SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A)  SPI2DOA: SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	11	LICDDM	I/O		USB Negative Data	SPI2DOB: SPI2 Data Out(B)
USBDP  I/O  4  USB Positive Data (pull down)  IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  UART1TXD: Uart1 Data Output(D) SPI2CLKB: SPI2 Clk(B) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  SPI2DOA: SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	11	USBDM	1/0	4	(pull down)	IIC_SDA_A: IIC SDA(A)
USBDP  I/O  4  USB Positive Data (pull down)  IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A) SPI2DOA: SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	1	0				ADC11: ADC Input Channel 11
12 USBDP I/O 4 (pull down) IIC_SCL_A: IIC SCL(A) ADC10: ADC Input Channel 10 IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2		7				UART1TXD: Uart1 Data Output(D)
(pull down)  IIC_SCL_A: IIC SCL(A)  ADC10: ADC Input Channel 10  IIC_SDA_D: IIC SDA(D)  UART0RXA: Uar0 Data Input(A)  SPI2DOA: SPI2 Data Out(A)  ADC2: ADC Input Channel 2	12	LICDED	1/0		USB Positive Data	SPI2CLKB: SPI2 Clk(B)
IIC_SDA_D: IIC SDA(D) UART0RXA: Uar0 Data Input(A) SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2	12	USBDP	1/O	4	(pull down)	IIC_SCL_A: IIC SCL(A)
UARTORXA: Uar0 Data Input(A)  13 PA6 I/O 8/24 GPIO SPI2DOA: SPI2 Data Out(A)  ADC2: ADC Input Channel 2						ADC10: ADC Input Channel 10
13 PA6 I/O 8/24 GPIO SPI2DOA: SPI2 Data Out(A) ADC2: ADC Input Channel 2						IIC_SDA_D: IIC SDA(D)
ADC2: ADC Input Channel 2						UART0RXA: Uar0 Data Input(A)
	13	PA6	I/O	8/24	GPIO	SPI2DOA: SPI2 Data Out(A)
						ADC2: ADC Input Channel 2

### 3

14	PA5	I/O	8/24	GPIO	IIC_SCL_D: IIC SCL(D)  UART0TXA: Uart0 Data Output(A)  SPI2CLKA: SPI2 Clk(A  ADC1: ADC Input Channel 1  Touch2: Touch Input Channel2
15	PB7	I/O	8/24	GPIO	UARTOTXB: Uart0 Data Output(B) SPI1DOA: SPI1 Data Out(A) Q-decoder1
16	PB6	I/O	8/24	GPIO	UART1RXA: Uart1 Data Input(A)  SPI1CLKA: SPI1 Clk(A)  PWM2: Timer2 PWM Output  ADC9: ADC Input Channel 9  Touch7: Touch Input Channel 7  Q-decoder0



# 2. Electrical Characteristics

### 2.1 Absolute Maximum Ratings

Table 2-1

Symbol	Parameter	Min	Max	Unit
Tamb	Ambient Temperature	-40	+85	°C
Tstg	Storage temperature	-65	+150	°C
VBAT	Supply Voltage	-0.3	4.5	V
LDO_IN	Charger Voltage	-0.3	6	V
V <sub>3.3IO</sub>	3.3V IO Input Voltage	-0.3	3.6	V

Note: The chip can be damaged by any stress in excess of the absolute maximum ratings listed below

# 2.2 Recommended Operating Conditions

Table 2-2

Symbol	Parameter	Min	Тур	Max	Unit		<b>Test Conditions</b>
VBAT	Voltage Input	1.8	3.0	3.4	V	1	
$V_{3.3}$	Voltage output	1.8	3.0	3.4	V	1	
V <sub>BT_AVDD</sub>	Voltage output	1.2	1.25	1.35	V	7	

# 2.3 IO Input/Output Electrical Logical Characteristics

Table 2-3

IO input ch	aracteristics	1				
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
$V_{IL}$	Low-Level Input Voltage	-0.3	_	0.3* VDDIO	V	VDDIO = 3.3V
$V_{ m IH}$	High-Level Input Voltage	0.7* VDDIO	_	VDDIO+0.3	V	VDDIO = 3.3V
IO output c	haracteristics					
V <sub>OL</sub>	Low-Level Output Voltage	_	_	0.33	V	VDDIO = 3.3V
V <sub>OH</sub>	High-Level Output Voltage	2.7	_	-	V	VDDIO = 3.3V

### 2.4 Internal Resistor Characteristics

Table 2-4

Port	General Output	High Drive	Internal Pull-Up Resistor	Internal Pull-Down Resistor	Comment
PA3, PB1,PB4, PB6~PB8, PC4,PC5, PD5,PD6,	8mA	24mA	10K	10K	1、PB1 default pull up 2、USBDM & USBDP default pull down
USBDP	4mA	_	1.5K	15K	3 internal pull-up/pull-down resistance   accuracy ±20%
USBDM	4mA	_	180K	15K	

### 2.5 BT Characteristics

### 2.5.1 Transmitter

**Basic Data Rate** 

Table 2-5

Paran	eter	Min	Тур	Max	Unit	Test Conditions
RF Transm	it Power		6	8	dBm	
RF Power Co	ntrol Range	A	20		dB	25℃,
20dB Bar	20dB Bandwidth		950		KHz	Power Supply
	+2MHz		-40		dBm	
Adjacent Chann	el -2MHz		-38		dBm	VBAT=5V
Transmit Powe	+3MHz	y	-44	1	dBm	2441MHz
	-3MHz	9	-35		dBm	

**Enhanced Data Rate** 

Table 2-6

Paramete	Parameter			Max	Unit	Test Conditions
Relative Po	wer		-1		dB	
π/4 DQPSK	DEVM RMS		6		%	
	DEVM 99%		10		%	25℃,
Modulation Accuracy	DEVM Peak		15		%	Power Supply
	+2MHz		-40		dBm	VBAT=5V
Adjacent Channel	-2MHz		-38		dBm	2441MHz
Transmit Power	+3MHz		-44		dBm	
	-3MHz		-35		dBm	

### 2.6.2 Receiver

### **Basic Data Rate**

**Table 2-7** 

Paramete	Parameter			Max	Unit	Test Conditions
Sensitivit	y		-94		dBm	
Co-channel Interferer	nce Rejection		-13		dB	
	+1MHz		+5		dB	25℃,
	-1MHz		+2		dB	Power Supply
Adjacent Channel	+2MHz		+37		dB	VBAT=5V
Interference Rejection	-2MHz		+36		dB	2441MHz
	+3MHz		+40		dB	7
	-3MHz		+35		dB	

### **Enhanced Data Rate**

Table 2-8

	_	part and a second				
Paramete	Min	Тур	Max	Unit	<b>Test Conditions</b>	
Sensitivit	y		-94		dBm	
Co-channel Interferer	nce Rejection		-13		dB	
	+1MHz		+5		dB	25℃,
	-1MHz		+2		dB	Power Supply
Adjacent Channel	+2MHz		+37		dB	VBAT=5V
Interference Rejection	-2MHz		+36		dB	2441MHz
	+3MHz		+40		dB	
	-3MHz		+35		dB	

# 3. Package Information

### 3.1 SOP16

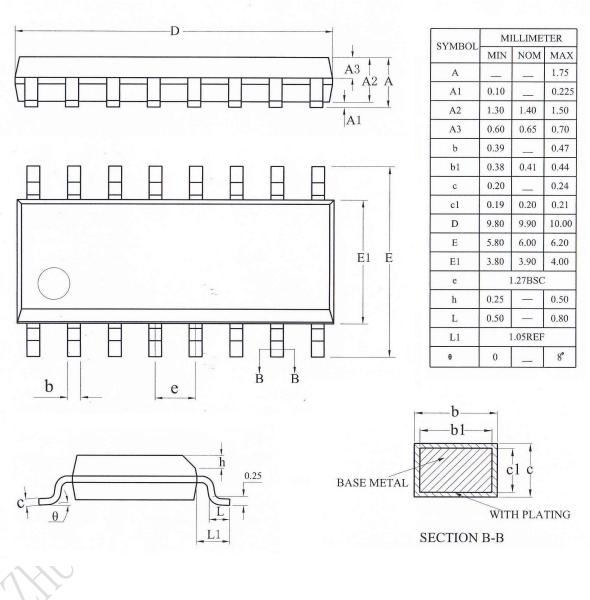


Figure 3-1 AC6379B Package

# 4. Revision History

Date	Revision	Description
2020.10.12	V1.0	Initial Release

