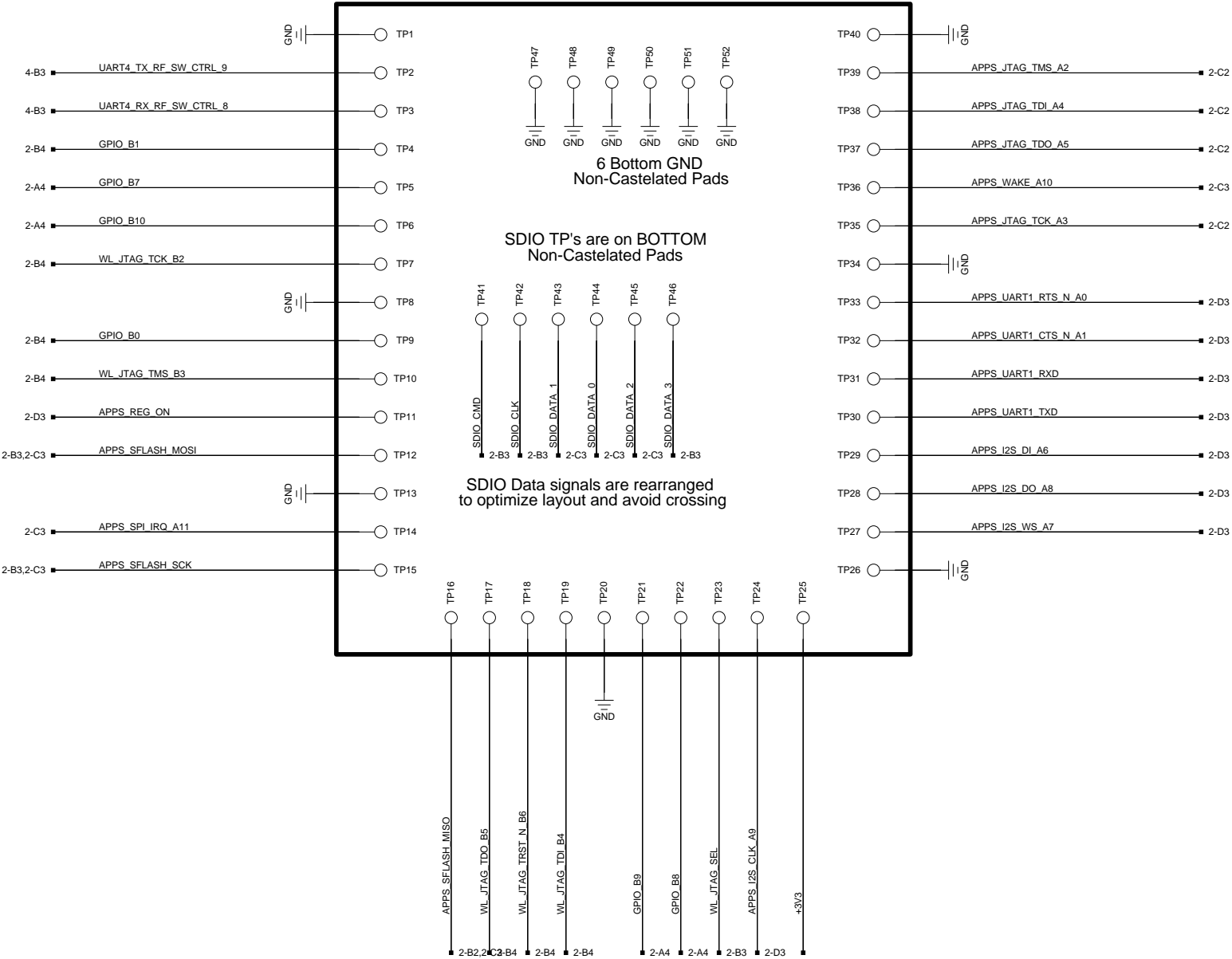


INDEX

- 1. TITLE
- 2. SYSTEM INTERFACE
- 3. PMU & POWER
- 4. RFFE & RF CONTROL SIGNALS

NON PCB MOUNT PART	
SHIELD FRAME 210-125274-0002	
MANUPARTNUM	
NON PCB MOUNT PART	
SHIELD LID 210-125274-0003	
DOCUMENT	
REWORK PROCEDURE 812-125274-0001	
DOCUMENT	
SCHEMATIC DIAGRAM 824-125274-0010	
PRINTED CIRCUIT BOARD	
200-125274-0010	

Approvals		<div>Broadcom</div> <div>Company Confidential</div>		
Designer	Date			
Reviewer	Date	SIZE B	TITLE BCM94390WCD1_1	
Reviewer	Date	Designer JOVICA JOVANOVSKI		Drawing Number 824-125274-0010
Reviewer	Date	DATE 09/08/2013:13:49		Version 11
		SHEET 1 OF 4		



ECO-1: Connect  
"WL\_REG\_ON" to GND

Power pins for 3.3V

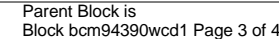
## SYSTEM INTERFACE

Pin	Strap Function	Default Strap Pull	On This Board
SDIO_SEL	SDIO_SEL	1	1 => R2 = Depop
SDIO_PADVDDIO	SDIO_PADVDDIO	0	1 => R8 = 10k
SDIO_CLK	CPULESS or SPROM_DISABLE	1	1 => R4 = Depop
SDIO_DATA_2	SPI_SEL	1	1 => R3 = Depop
JTAG_SEL	JTAG_ENABLE	External Pull High to Enable JTAG is DISABLED by Default	Pull-up located on Eval brd.

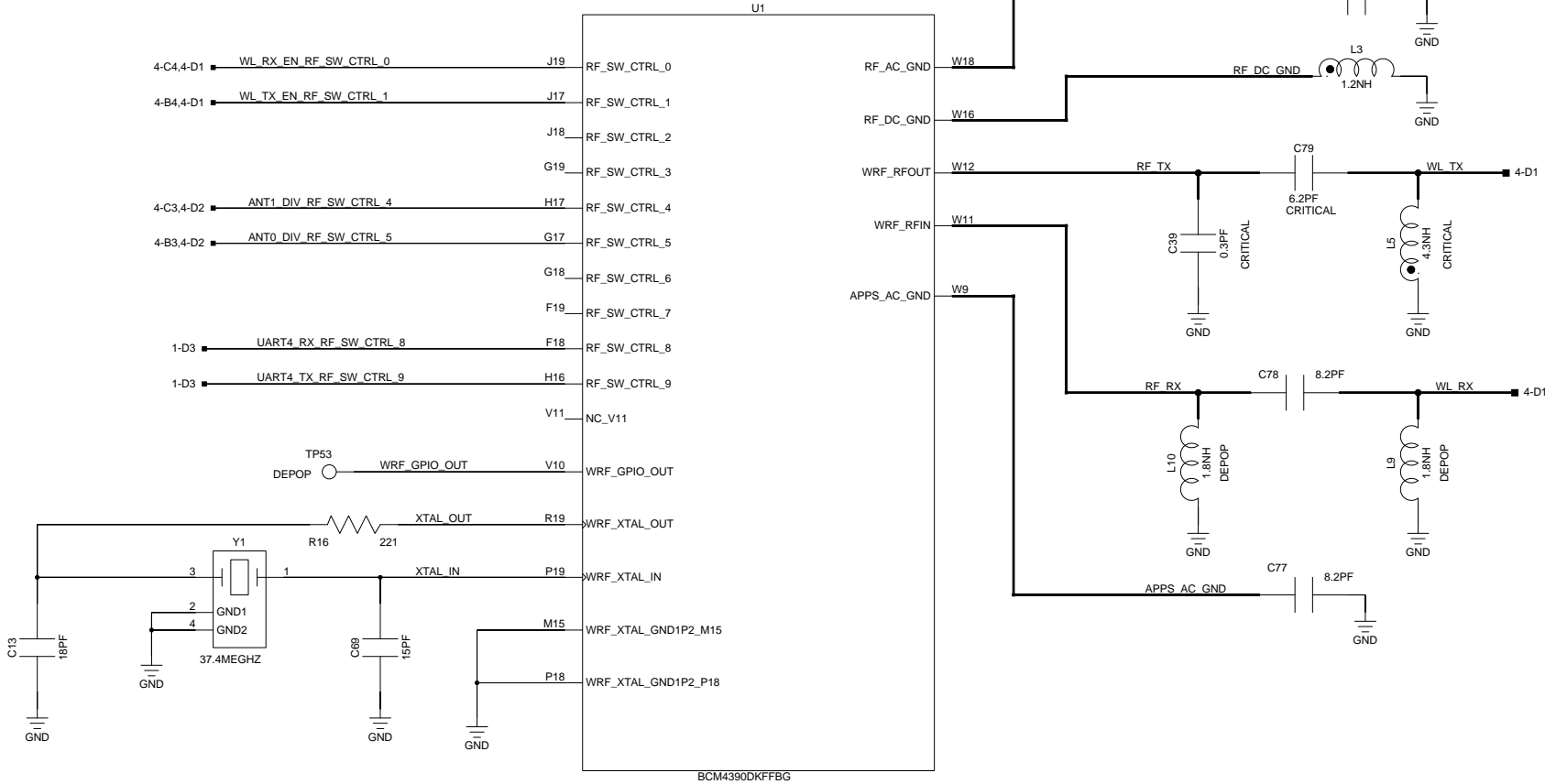
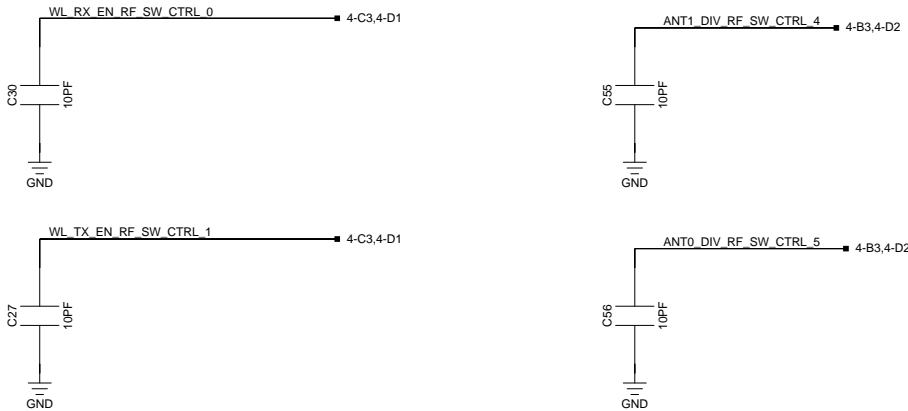
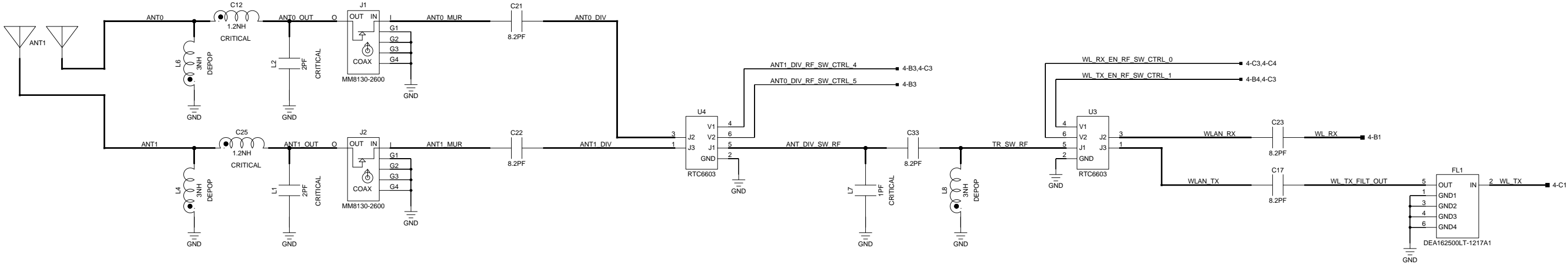
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SIZE	TITLE
B	BCM94390WCD1_1
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Even though ref. des is ANT1 only, this symbol is Two Antennas that go as a pair



MODE OF OPERATION	WL_RX_EN_RF_SW_CTRL_0	WL_TX_EN_RF_SW_CTRL_1
TR SWITCH (U3)	TR SW (U3) Pin 6 = V2	TR SW (U3) Pin 4 = V1
pin 3 U3 - pin 5 U3 = WLAN_RX to TR_SW_RF	1	0
pin 1 U3 - pin 5 U3 = WLAN_TX to TR_SW_RF	0	1
UNDEFINED STATE 1	1	1
UNDEFINED STATE 2	0	0

MODE OF OPERATION	ANT1_DIV_RF_SW_CTRL_4	ANT0_DIV_RF_SW_CTRL_5
DIVERSITY SWITCH (U4)	DIV SW (U4) Pin 4 = V1	DIV SW (U4) Pin 6 = V2
pin 5 U4 - pin 1 U4 = ANT_DIV_SW_RF to ANT1_DIV	1	0
pin 5 U4 - pin 3 U4 = ANT_DIV_SW_RF to ANT0_DIV	0	1
UNDEFINED STATE 1	1	1
UNDEFINED STATE 2	0	0

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B	BCM94390WCD1_1
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