

Bachelor's Thesis

Collisions in Hash-Functions

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Chapter 1

Introduction

1.1 May Contains

1. about hashfunctions
2. about md5
3. Definition of md5
4. impelmentation of md5
 - (a) padding (missing in Stevens code)
 - (b) md5compress
 - (c) potential for improvement
5. collisions for md5
 - (a) Blocks N & M
 - (b) 00, 01, 10, 11
 - (c) MMM
6. conclusion
7. SHA1
8. why not in SHA1
9. notation helper

Notation

Stevens	Wang	Definition
$h(m) = H$		the hash H of some message m
$RL(X, Y)$	$ROTL^Y(X)$	cyclic left shift X by $Y \pmod{31}$
$RR(X, Y)$	-	cyclic right shift X by $Y \pmod{31}$
$RC(t)$	$S(t)$	rotation Constant of t
Block 1, Block 2	Block N, Block M	pair of first blocks for collisions finding same pair but im Code
Block 0, Block 1	Block N, Block M	

Motivation

This thesis is about a deeper look on MD5. We take a closer look at the Master thesis of M. Stevens: a fast collisions finding algorithm [1]. The goal is to work out a more clear and understandable code, which is not necessarily faster, to reevaluate the code on modern systems and the difference to SHA1.

Notes

Example for Stevens bit manipulation for Q_t mit $t = 3$:

Bit condition for $Q_t|t = 3$:

1. the val to set the zeros (and with 0xfe87bc3f)
2. the val to set the ones (or with 0x017841c0)
3. the new bit Conds
4. the old bit Conds

1.&	11111110	10000111	10111100	00111111	0xfe87bc3f
2.	00000001	01111000	01000001	11000000	0x017841c0
3.1111...	.1....01	11.....	
4.0...0...	.0.....	

the & flips the 0 correct, the || flips the 1 correct

1.2 Notation Helper and Ideas

$$RR(X, Y) \equiv X \otimes Y$$

$$RL(X, Y) \equiv X \circledast Y$$

[illegible]

Chapter 2

Implementation of MD5

2.1 MD5

MD5 stands for *Message-Digest Algorithm 5* since it generates a digest for any given text. MD5's output length is 128 Bit, represented in hexadecimal. Since the amount of possible text is close to and the amount of MD5s is limited by 32^{16} , some text may have the MD5. If two different inputs create have the same hash value, that is what we call a collision. MD5 is usually seen as 4 steps as seen in fig md5

1. padding
2. processing
3. md5 sum
4. output

Figure 2.1: MD5 algo

Chapter 3

Collision Finding

3.1 About collisions

Hash functions have resistance:

1. First Preimage Resistance: for a hash function $h(m) = H$ the message m is hard to find.
2. Second Preimage Resistance: for a given messages m_1 it is hard to find an m_2 with $m_1 \neq m_2$ and $h(m_1) = h(m_2)$.
3. Collision Resistance: two arbitrary messages m_1 and m_2 with $\neq m_2$ and $h(m_1) = h(m_2)$ are hard to find.

3.2 Differential Path

Stevens starts with Wang's attack, which tries to find two pairs of blocks: (B_0, B'_0) and (B_1, B'_1) that $IHV = IHV'$, with the goal to create two messages M and M' , with the same hash value:

$$\begin{array}{cccccccccccccccc}
 IHV_0 & \xrightarrow{M_{(1)}} & \cdots & \xrightarrow{M_k} & IHV_k & \xrightarrow{B_0} & IHV_{k+1} & \xrightarrow{B_1} & IHV_{k+2} & \xrightarrow{M_{k+1}} & \cdots & \xrightarrow{M_N} & IHV_N \\
 = & & & = & & \neq & & = & & & & = & \\
 IHV_0 & \xrightarrow{M_{(1)}} & \cdots & \xrightarrow{M_k} & IHV_k & \xrightarrow{B_0} & IHV'_{k+1} & \xrightarrow{B_1} & IHV'_{k+2} & \xrightarrow{M_{k+1}} & \cdots & \xrightarrow{M_N} & IHV_N
 \end{array}$$

The idea to manipulate a block B such that $Q_1 \dots Q_{16}$ maintain their conditions and that Q_1 to some Q_k do not change at all. We try to make k as large as possible.

3.3 Bit Conditions

Bit conditions describe the differential path on bit conditions. We need the bit conditions to avoid a carry, so a manipulation in step t stays in step t and does not propagate beyond the 31st bit. We look at conditions and restrictions. The restrictions leads to conditions, which we calculate in the following. A restriction e.g. $\Delta T_2[31] = +1$ leads to conditions $Q_1[16] Q_2[16] = Q_3[15] = 0$ and $Q_2[15] = 1$. Notice, conditions are on $\Delta T_t[i]$ a state in md5-algorithm before the rotation and restrictions are on $Q_t[i]$ states of the md5-algorithm after the rotation.

We calculate the bit conditions by using the Add difference. We calculate an δ for each f_t , Q_t , T_t and R_t to calculate the Add-Difference for Q_{t+1} . Additional we need the rotation constant RC for each t . In general we begin with the f_t :

fix writing

1. $t \in \{0, 1, 2, 3\}$:

$Q_t = 0$ since here is no influence by an message and no calculation of f , there is nothing to change:

2. $t = 4$

$\Delta T_4 = -2^{31}$, because we must not have a carry, we *lock* the last bit. Since $RL(T_4, RC_4) = RL(-2^{31}, 7) = -2^6$ and $\delta Q_4 = 0 \Rightarrow \delta Q_5 = -2^6$

t	$RC(t)$
0	7
1	12
2	17
3	22
4	7
5	12
6	17
7	22
8	7
9	12
10	17
11	22
12	7
13	12
14	17
15	22

$$m_t = RR(Q_{t+1} - Q_t, RC_t) - f_t(Q_t, Q_{t-1}, Q_{t-2}) - Q_{t-3} - AC$$

Appendix A

Tables

+—+
abc
+—+

Appendix B

Code

```
a +=a;
```

List of Figures

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Algorithmenverzeichnis

Bibliography

Hiermit versichere ich, dass ich die vorliegende Arbeit selbstständig verfasst habe und keine anderen als die angegebenen Quellen und Hilfsmittel verwendet sowie Zitate kenntlich gemacht habe.

Dortmund, den May 30, 2023

Muster Mustermann

