

CSARCH Lecture Series: Introduction to Computer Organization and Architecture

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Overview

Reflect on the following questions:

- Is there a difference between Computer Organization and Computer Architecture?
- What is Von Neumann Architecture?
- What are some technologies that lead to the growth of computer system?

Overview

- This sub-module presents a general introduction to the course Computer Organization and Architecture
- The objective are as follows:
 - ✓ Explain the difference between Computer Organization and Computer Architecture
 - ✓ Explain Von Neumann Architecture
 - ✓ Describe the brief history of computer technology

Computer Architecture

Computer Architecture involves computer design based on these aspects:

- Instruction Set Architecture
- Organization or microarchitecture
- Hardware implementation

CSARCH2+CEPARCO/CSC612M

- LBYARCH
- CSARCH2
- CSARCH1

Computer Architecture

Issues involving Computer Architecture (i.e., Architectural attributes):

- Representation of data types is integer defined as 32-bit or 64-bit?
- Class of ISA load-store or register-memory architecture?
- Instruction set whether to support MUL instruction or not?
- Memory addressing byte-size? Endianness?
- Addressing modes support complicated addressing such as autoincrement?

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Slide 6

Instruction Set Architecture

- Refers to those attributes of a system visible to a programmer
- Attributes that have a direct impact on the logical execution of a program
- "Assembly language"

Computer Architecture involves computer design based on these aspects:

- Instruction Set Architecture
- Organization or microarchitecture
- Hardware implementation

Computer Organization

- Also refers to the term "microarchitecture"
- Refers to the operational units and their interconnections that realize the architectural specifications.

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- Instruction Set Architecture
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- Hardware implementation

Computer Organization

Issues involving Computer Organization (i.e., Organizational attributes):

- Arithmetic unit Sequential circuit binary multiplier
- Control signals hardwired vs. microprogramming
- Cache memory

Example:

If MUL instruction is to be used, the question is whether to use a special multiply unit or use existing add unit?

Hardware

- Refers to the specific of a computer:
 - logic design
 - Combinational circuit
 - sequential circuit
 - Packaging technology of the computer

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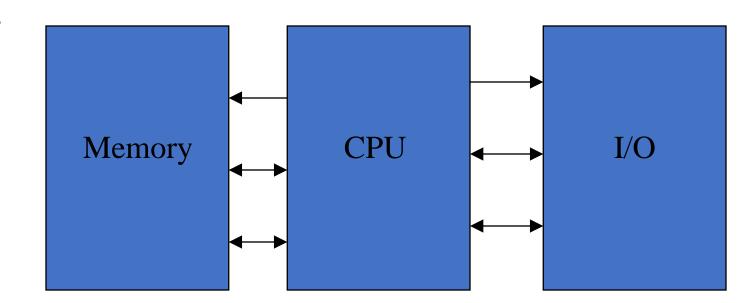
Von Neumann Architecture

Whether it is a desktop computer (old or new), laptop computer, or tablet, all uses Von Neumann Architecture



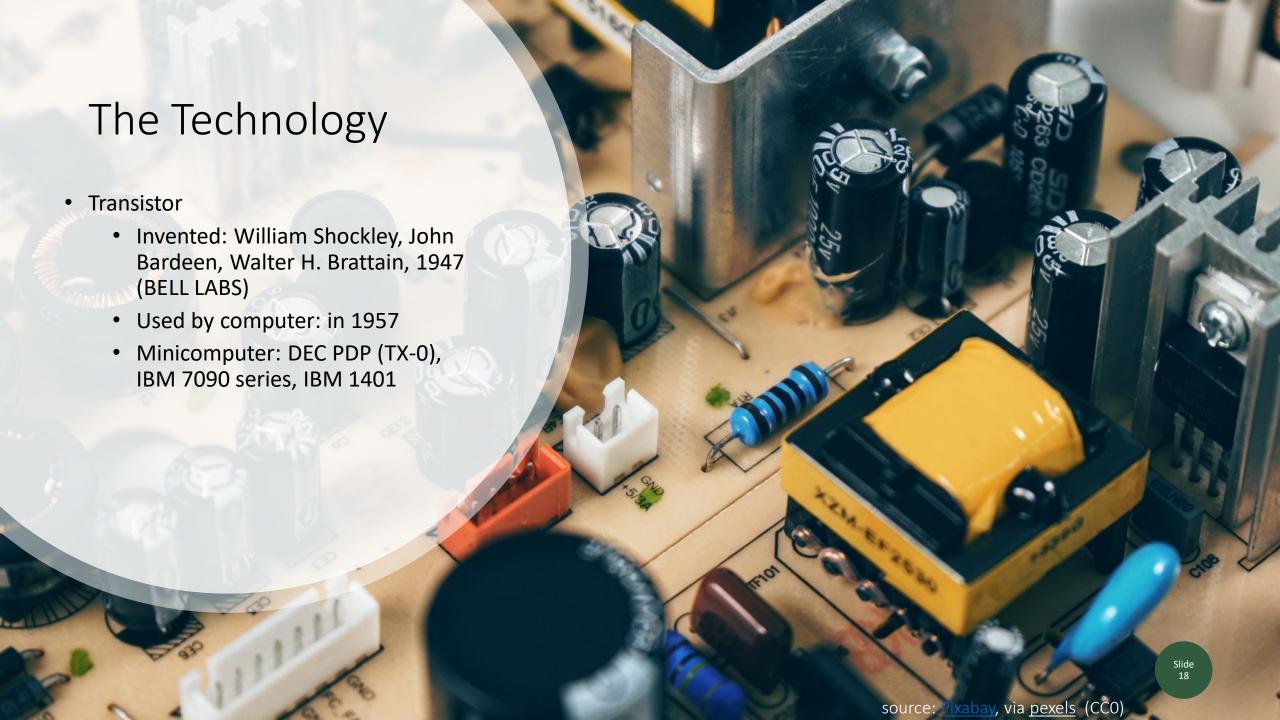
Von Neumann Architecture

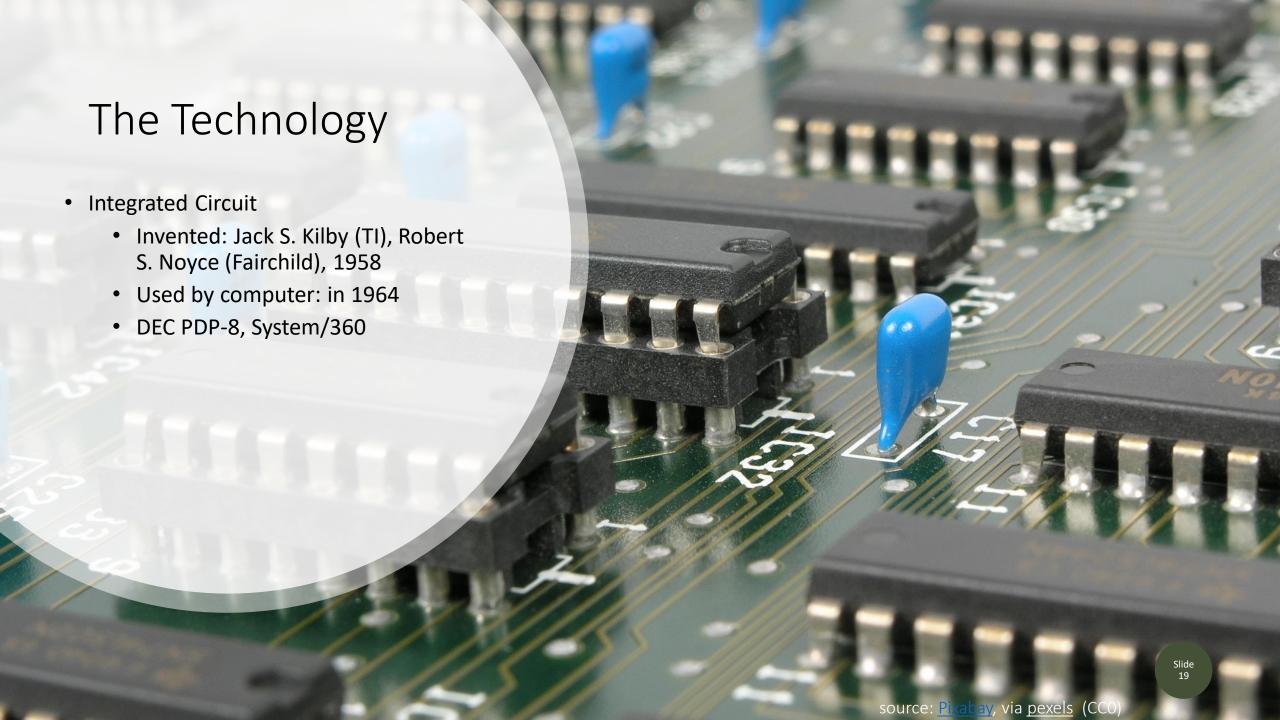
- Store Program Architecture program & data are stored in the main memory and **not** in the CPU
- Instructions in the main memory are Fetched,
 Decoded and Executed sequentially (or at least it appears to be)

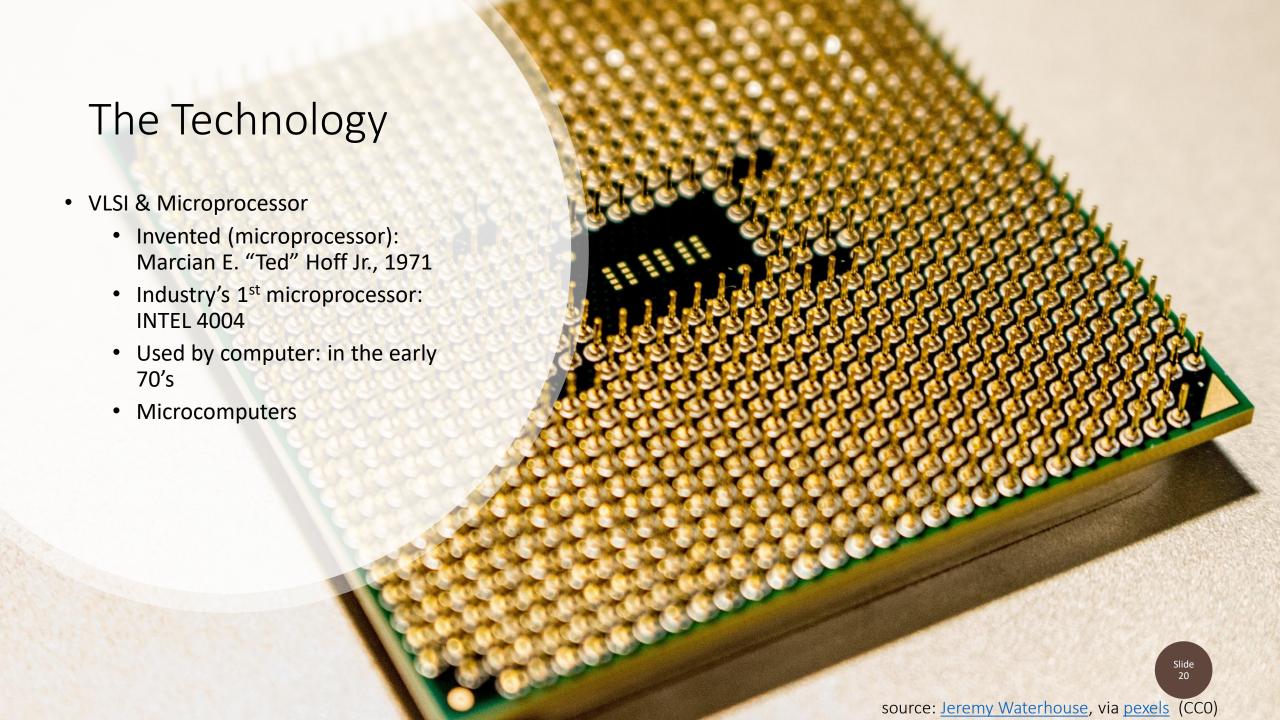


- Vacuum Tubes
 - Invented: Dr. Lee De Forest, 1906
 - Used by computer: in the late 30's, early 40's
 - Mainframe: ENIAC, EDVAC, UNIVAC1









TeraHertz Transistor

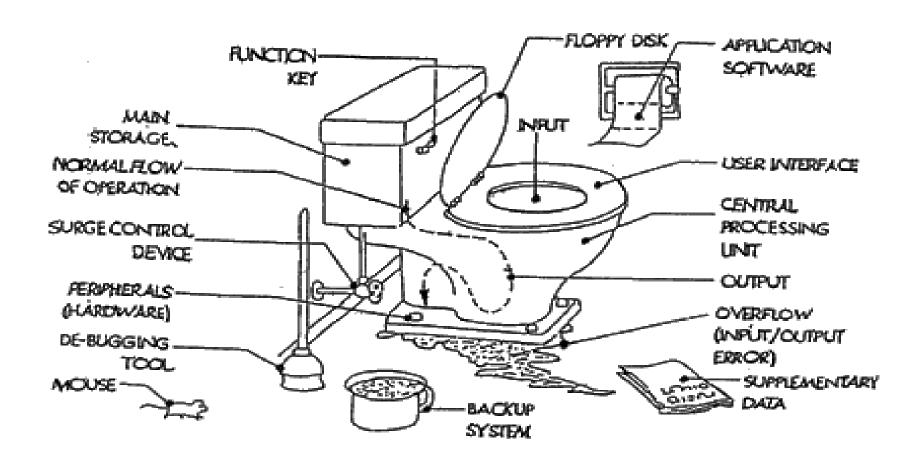
- Transistor size to shrink from 10um (10000*10⁻⁹m) to 2nm (internally: 45nm)
- Marketing term: 2nm (internally: 45nm).
- Currently: Intel desktop "Raptor Lake" (13th gen Intel core processor). Based on Intel7 process ("7nm", internally: 54nm gate pitch using FINFet). Example: Core i7-13700 (16-core, 1.5GHz→5.2GHz) / Core i5-13600 (2.0GHz→5.0GHz)
- Currently: Intel mobile "Raptor Lake-P" (13th gen Intel core processor). Based on Intel7 process (54nm gate pitch using FINFet). Example: Core i7-1370P (16-core, 1.4GHz→5.2GHz)
- Future works: Intel Meteor Lake (Intel4 process, or "5nm", internally: 50nm, 2024/2025). Mobile only. Intel Core 5 processor (letter "i" will be dropped).
- Future future works: Intel 20a processor (or "2nm", internally: 45nm based on Gate-All-Around Field Effect Transistor (GAAFET).

- Micron (μ)
 - Micrometer or one millionth of a meter (1x10⁻⁶m)
 - Strand of human hair is about 100 μ wide
 - Red blood cells ~8 μ in diameter
 - Upper size for airborne virus particle \sim 50nm (0.05 μ)

- Angstrom (Å)
 - Equal to 0.1nanometer (0.1x10⁻⁹ meter)
 - Visible light ranges from 4000 to 7000 Å
 - Some atom radii is ~1 to 2 Å

Another look at a computer system

Understanding Computer Technology



Quote ...

- Where... the ENIAC is equipped with 18000 vacuum tubes and weighs 30 tons, computers in the future may have 1000 vacuum tubes and perhaps weigh just 1.5 tons.
 - Popular Mechanics, 1949

Quote ...

- I though computers would be a universally applicable idea, like a book is. But I didn't think it would develop as fast as it did, because I didn't envision we'd be able to get as many parts on a chip as we finally got. The transistor came along unexpectedly. It all happened much faster than we expected.
 - J. Presper Eckert, co-inventor of ENIAC, speaking in 1991

Quote ...

- Science must begin with myths, and the criticism of myths
 - Sir Karl Popper, The Philosophy of Science, 1957

To recall ...

- What have we learned:
 - ✓ Explain the difference between Computer Organization and Computer Architecture
 - ✓ Explain Von Neumann Architecture
 - ✓ Describe the brief history of computer technology