



CSARCH Lecture Series: Single-precision floating-point format for decimal (decimal-32)



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Overview

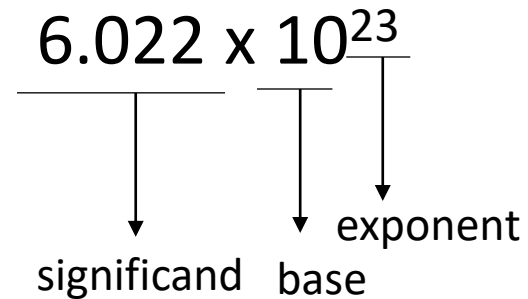
- This sub-module introduces the IEEE-754 decimal-32 floating-point format
- The objective is as follows:
 - ✓ Describe the process of representing decimal-32 floating-point data using IEEE-754 standard

Floating Point

- Scientists and engineers use scientific notation where a number is expressed as

$$+/- S \times 10^{\pm E}$$

- Where S is the significand (also known as mantissa), E is the exponent and 10 is the base.



The diagram shows the expression 6.022×10^{23} with three horizontal lines underneath it. The first line is under '6.022', the second is under '10', and the third is under '23'. Three vertical arrows point downwards from these lines to the labels 'significand', 'base', and 'exponent' respectively.

$$\begin{array}{c} 6.022 \times 10^{23} \\ \hline \downarrow \quad \downarrow \quad \downarrow \\ \text{significand} \quad \text{base} \quad \text{exponent} \end{array}$$

Floating Point

- Floating point standard for floating-point numbers in computer is the IEEE-754 (Institute of Electrical and Electronics Engineers Standard 754). Originally 1985, revised 2008, current version 2019
- Decimal floating point is introduced in 2008.
- The representation is to be used in applications that need to emulate decimal rounding exactly (i.e., financial and tax computations)

IEEE-754 decimal floating-point

- Decimal32 precision
- Decimal64 precision
- Decimal128 precision

Length of field

Format	Decimal32	Decimal64	Decimal128
Format length	32	64	128
Sign bit	1	1	1
Combination bit	5	5	5
Exponent continuation bit	6	8	12
coefficient continuation bit	20	50	110
Total coefficient in digits	7	16	34
E_{\max} (denormalized/normalized)	96/90	384/369	6144/6111
E_{\min} (denormalized/normalized)	-95/-101	-383/-398	-6143/-6176
Bias	101	398	6176
E_{limit}	191	767	12287

IEEE-754 decimal-32 floating-point format

Sign	Combination field	Exponent continuation	Coefficient continuation
1	5	6	20

normalized to this
format before
representation:

ddddddd × 10^{*e*}

- IEEE-754 decimal32 floating-point format is 32-bit in width
- The 32-bit is partitioned as 1 bit for sign bit; 5 bits for combination field, 6 bits for exponent continuation and 20 bits for coefficient continuation
 - Significand in decimal
 - Base 10
 - sign bit: 0 → positive; 1 → negative
 - $e' = e + 101$
 - significand normalized to 7 whole decimal digits

Combination field

Combination Field	Type	Exp MSbs	coefficient MSD
a b c d e	Finite	a b	0 c d e
1 1 c d e	Finite	c d	1 0 0 e
1 1 1 1 0	Infinity	- -	- - - -
1 1 1 1 1	NaN	- -	- - - -

- 5-bit combination field is composed of:
 - two most significant bits of the exponent representation (valid bits: 00, 01 and 10 only)
 - 1 or 3 bits of the most significant digit of the significand

Exponent continuation field

- Exponent representation is $e+101$
 - two most significant bits of the exponent representation (valid bits: 00, 01 and 10 only) in the **combination field**
 - The rest of the 6 bits in the **exponent continuation field**
 - Largest exponent (normalized) value that can be represented is **90**
 - smallest exponent (normalized) value that can be represented is **-101**

Coefficient continuation field

- 7 whole decimal digits
- most significant digit is stored in the **combination field**
- remaining 6 digits are represented as **densely-packed BCD** and stored in the **coefficient continuation field**

Redundant encoding

- 7.5
 - 75×10^{-1}
 - 750×10^{-2}
 - 7500×10^{-3}
 - 75000×10^{-4}
 - 750000×10^{-5}
 - 7500000×10^{-6}

All are the representation of 7.5 (they are all called “cohorts”)

Example

- 7123456×10^{20}

Significand in decimal?	yes
Base-10?	yes
Normalized?	Yes, 7 whole digits
	MSD = 7 (0 111)
Sign bit	0 (+)
$e' = e+101$	$20+101 = 121$ (01 111001)

Combination Field	Type	Exp MSBs	coefficient MSD
a b c d e	Finite	a b	0 c d e
1 1 c d e	Finite	c d	1 0 0 e
1 1 1 1 0	Infinity	- -	- - - -
1 1 1 1 1	NaN	- -	- - - -

Sign	Combination field	Exponent continuation	Coefficient continuation
0	01 111	111001	0010100011 1001010110

Example

- 8765432 x 10⁻²⁰

Combination Field	Type	Exp MSBs	coefficient MSD
a b c d e	Finite	a b	0 c d e
1 1 c d e	Finite	c d	1 0 0 e
1 1 1 1 0	Infinity	- -	- - - -
1 1 1 1 1	NaN	- -	- - - -

Significand in decimal?	yes
Base-10?	yes
Normalized?	Yes, 7 whole digits
	MSD = 8 (1000)
Sign bit	1 (-)
e' = e+101	-20+101 = 81 (01 010001)

Sign	Combination field	Exponent continuation	Coefficient continuation
1	11 010	010001	1111100101 1000110010



- -1.234567×10^{15}

Significand in decimal?	
Base-10?	
Normalized?	
Sign bit	
$e' = e+101$	

Combination Field	Type	Exp MSbs	coefficient MSD
a b c d e	Finite	a b	0 c d e
1 1 c d e	Finite	c d	1 0 0 e
1 1 1 1 0	Infinity	- -	- - - -
1 1 1 1 1	NaN	- -	- - - -

Sign	Combination field	Exponent continuation	Coefficient continuation



- -1.234567×10^{15}

Significand in decimal?	yes
Base-10?	yes
Normalized?	No, -1234567×10^9
	MSD = 1 (0001)
Sign bit	1 (-)
$e' = e+101$	$9+101 = 110$ (01 101110)

Combination Field	Type	Exp MSBs	coefficient MSD
a b c d e	Finite	a b	0 c d e
1 1 c d e	Finite	c d	1 0 0 e
1 1 1 1 0	Infinity	- -	- - - -
1 1 1 1 1	NaN	- -	- - - -

Sign	Combination field	Exponent continuation	Coefficient continuation
1	01 001	101110	0100110100 1011100111

To recall ...

- What have we learned:
 - ✓ Describe the process of representing decimal-32 floating-point data using IEEE-754 standard