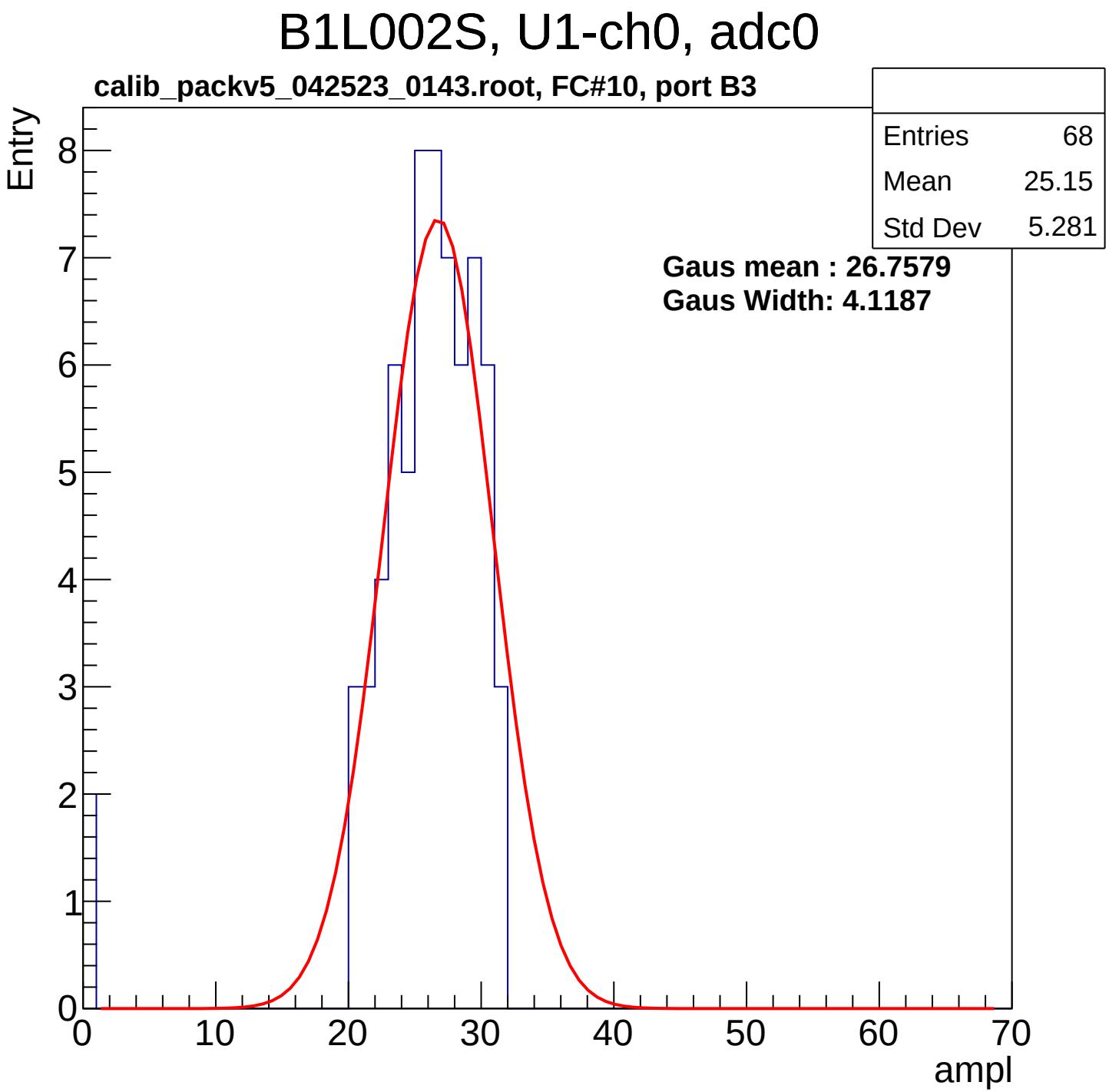
B1L002S, U1-ch0, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 25.15
Std Dev 5.281
Gaus mean : 26.7579
Gaus Width: 4.1187
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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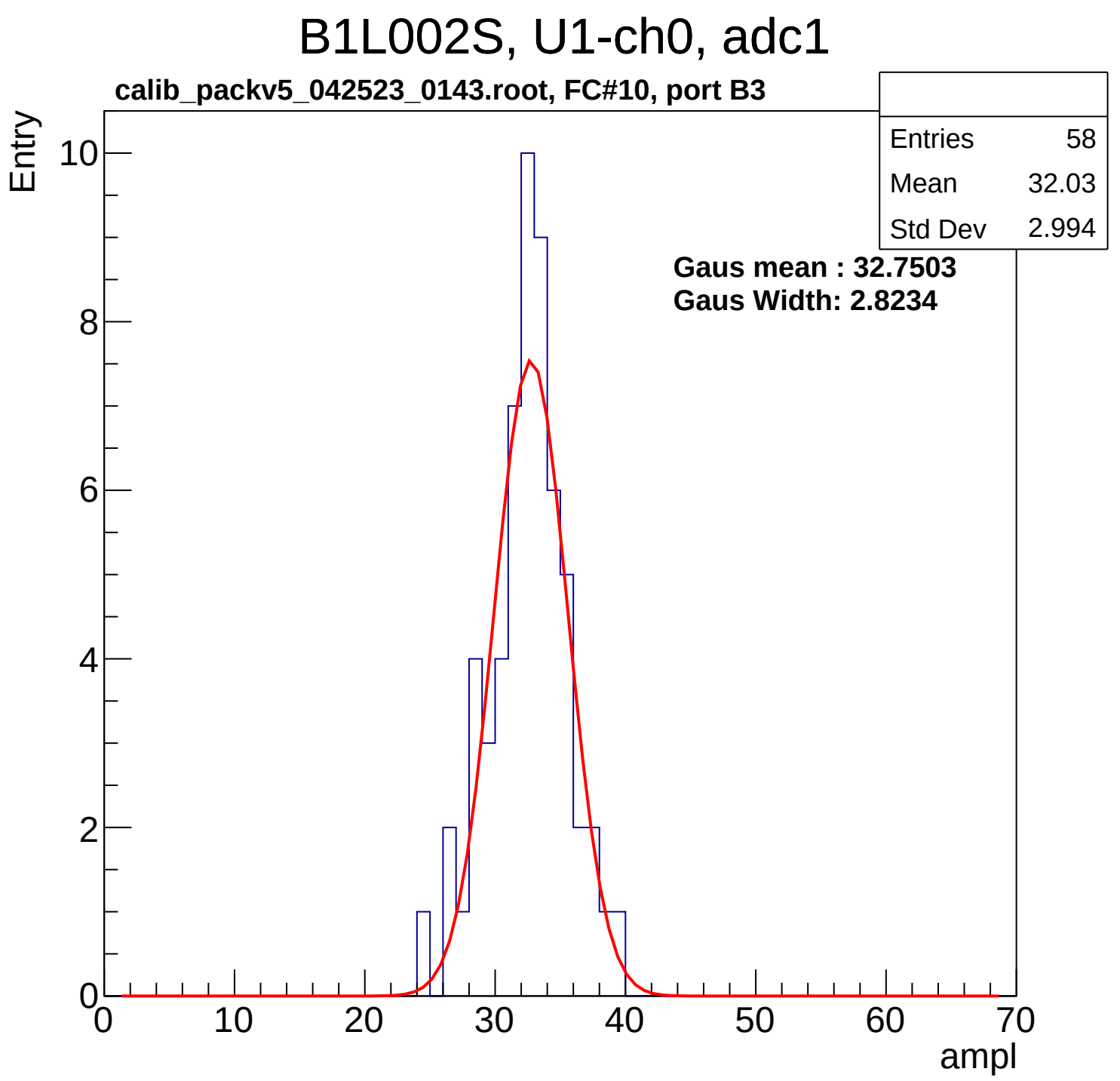
B1L002S, U1-ch0, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 32.03
Std Dev 2.994
Gaus mean : 32.7503
Gaus Width: 2.8234
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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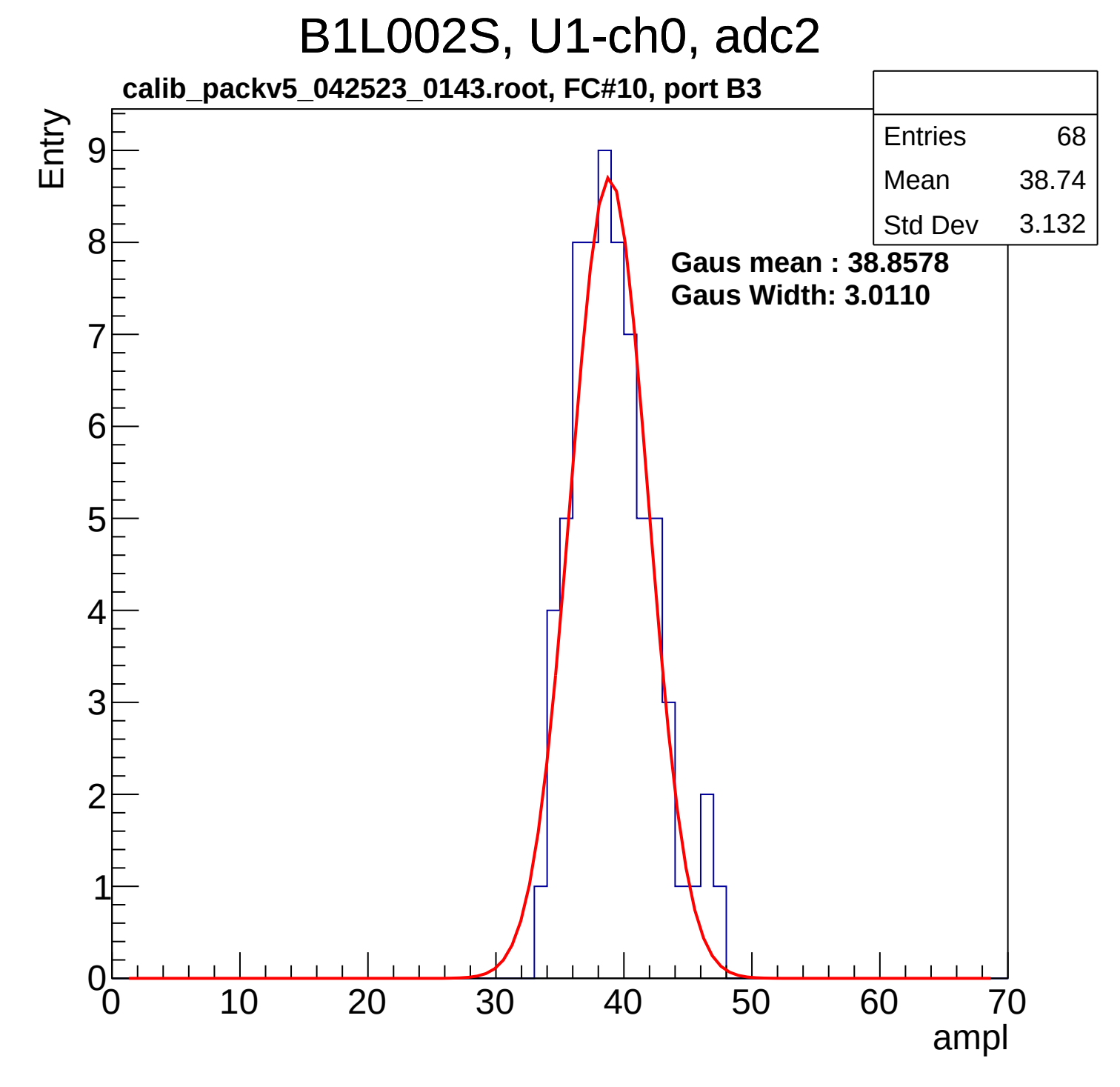
B1L002S, U1-ch0, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 38.74
Std Dev 3.132
Gaus mean : 38.8578
Gaus Width: 3.0110
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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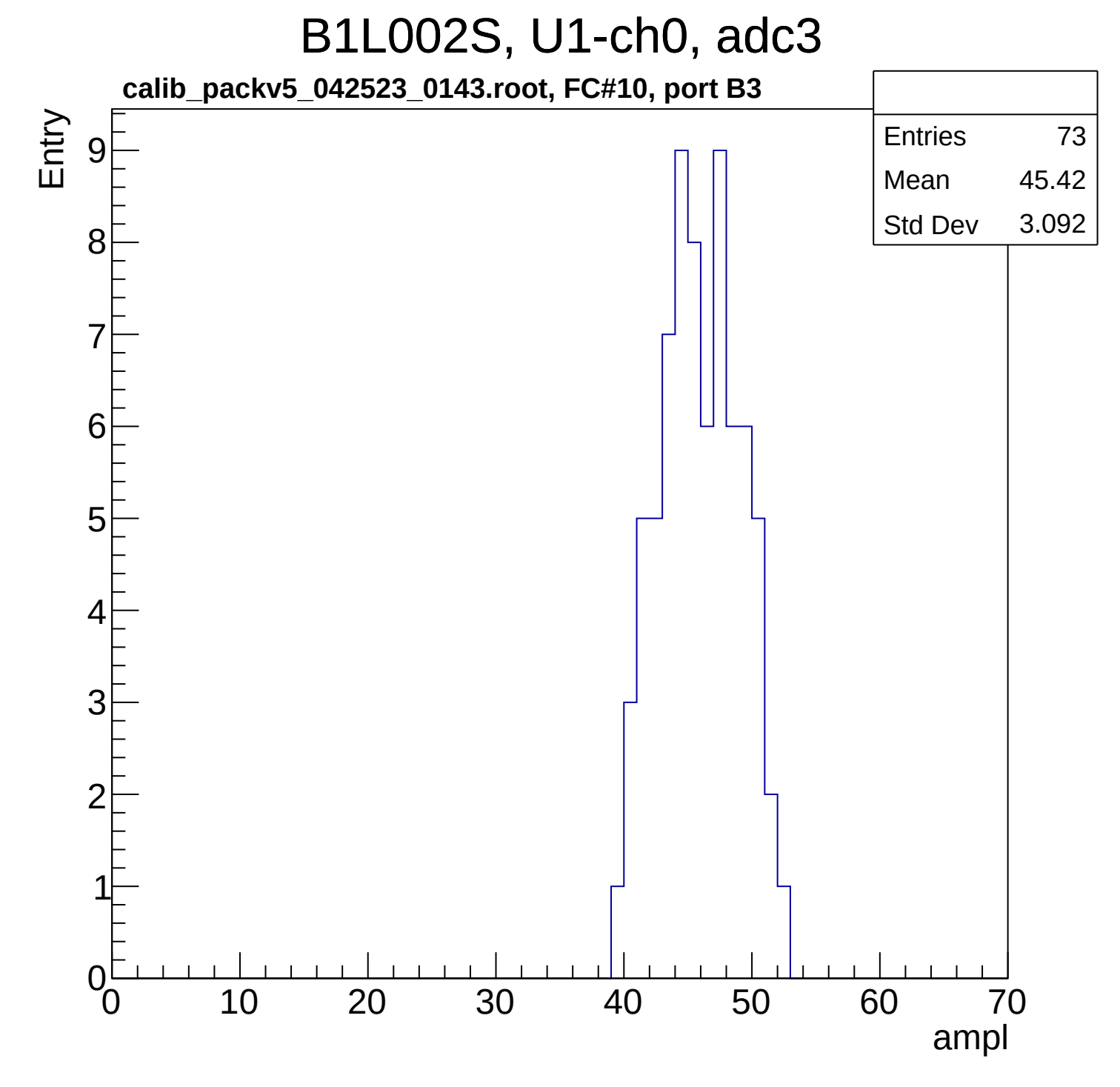
B1L002S, U1-ch0, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 45.42
Std Dev 3.092
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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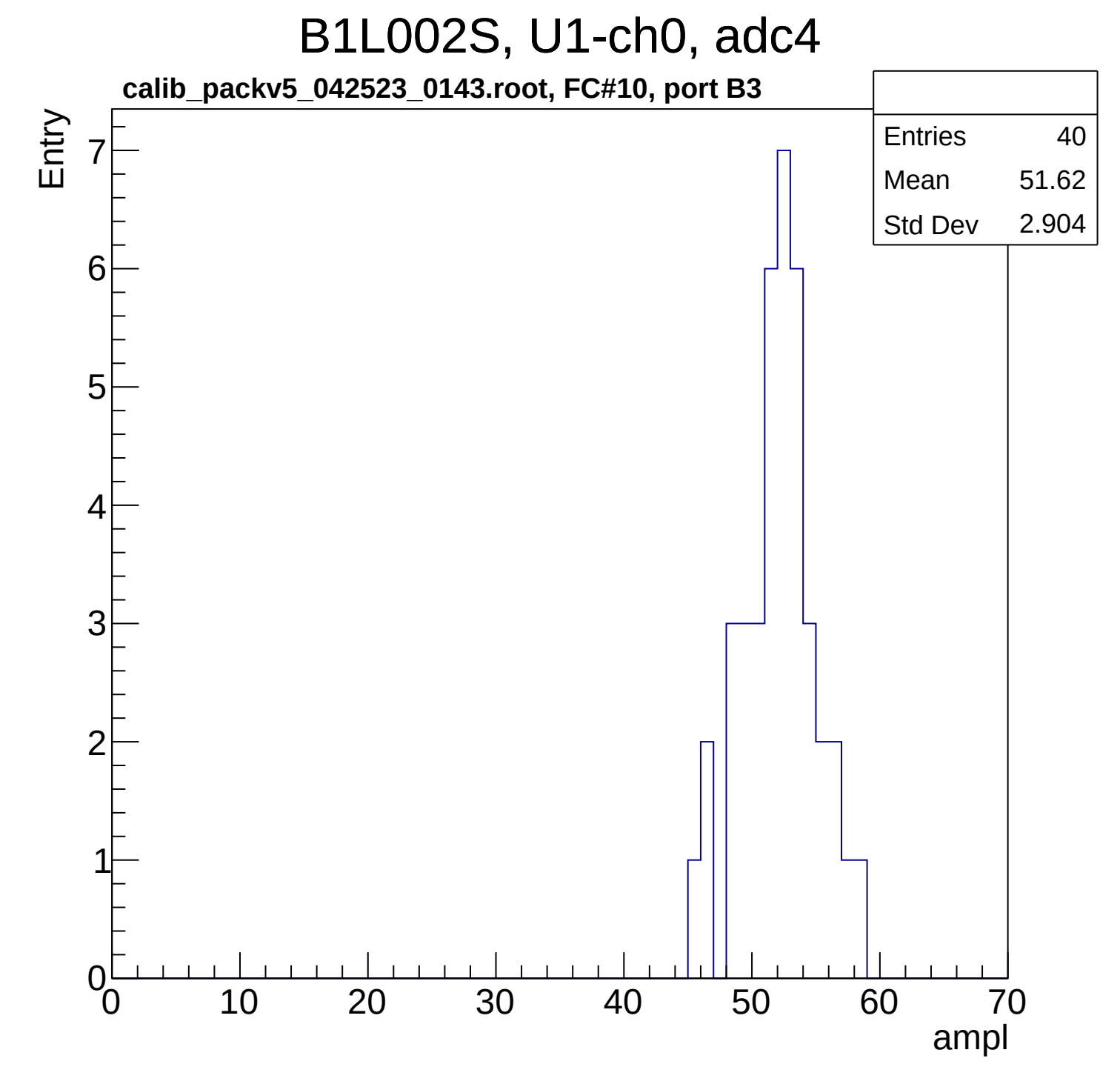
B1L002S, U1-ch0, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 51.62
Std Dev 2.904
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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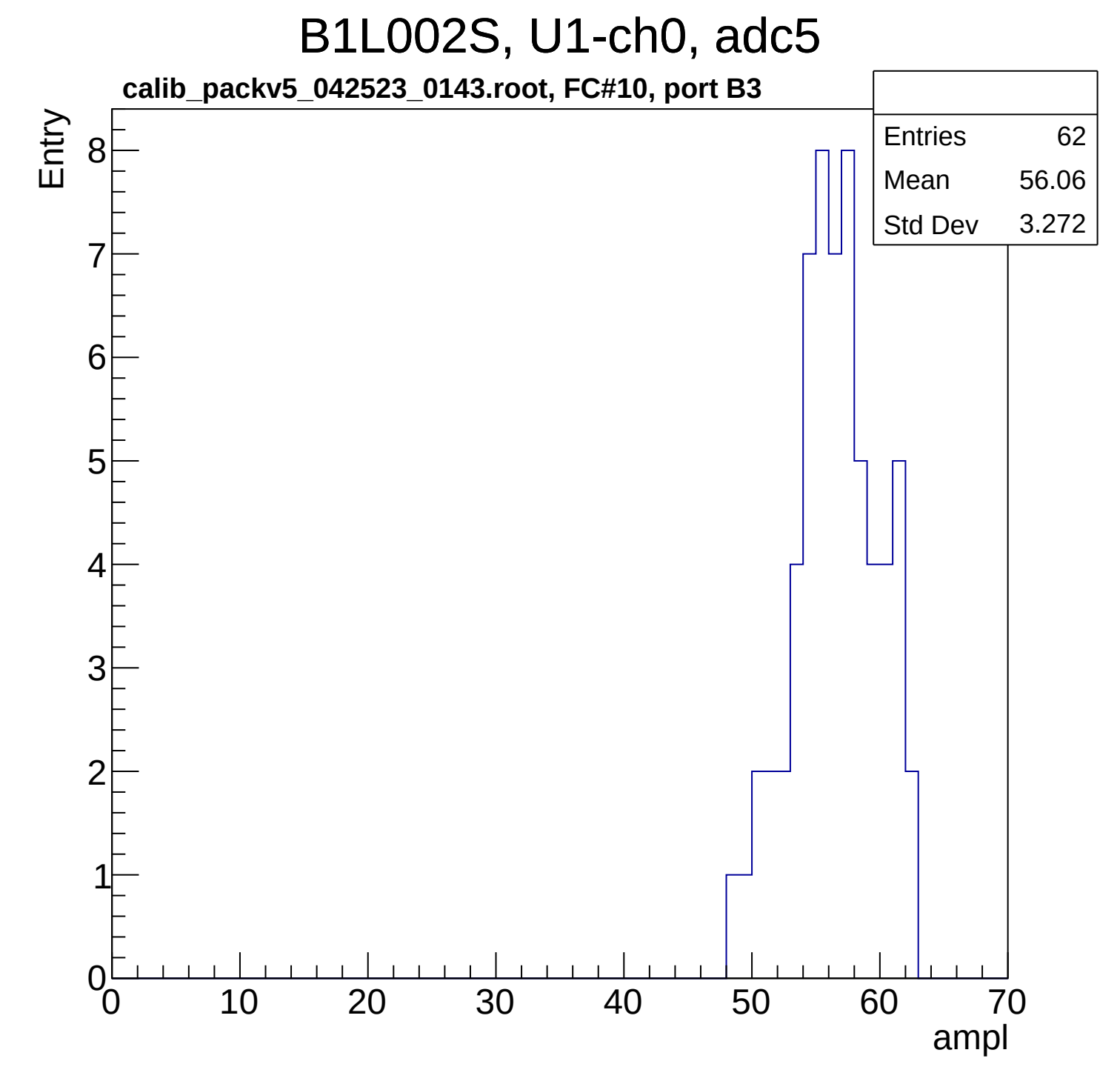
B1L002S, U1-ch0, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 56.06
Std Dev 3.272
Entry
ampl
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1
2
3
4
5
6
7
8
0
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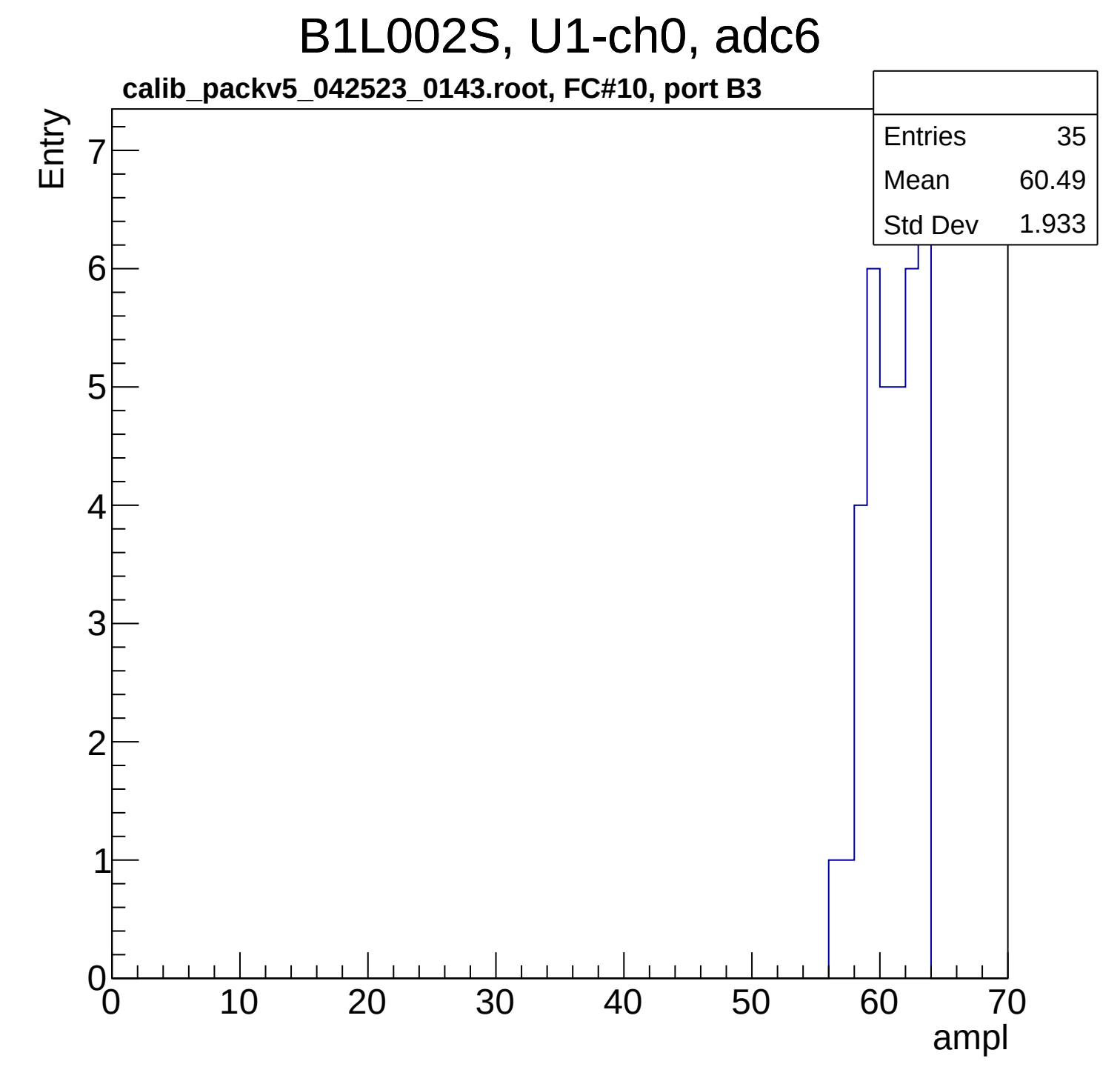
B1L002S, U1-ch0, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 60.49
Std Dev 1.933
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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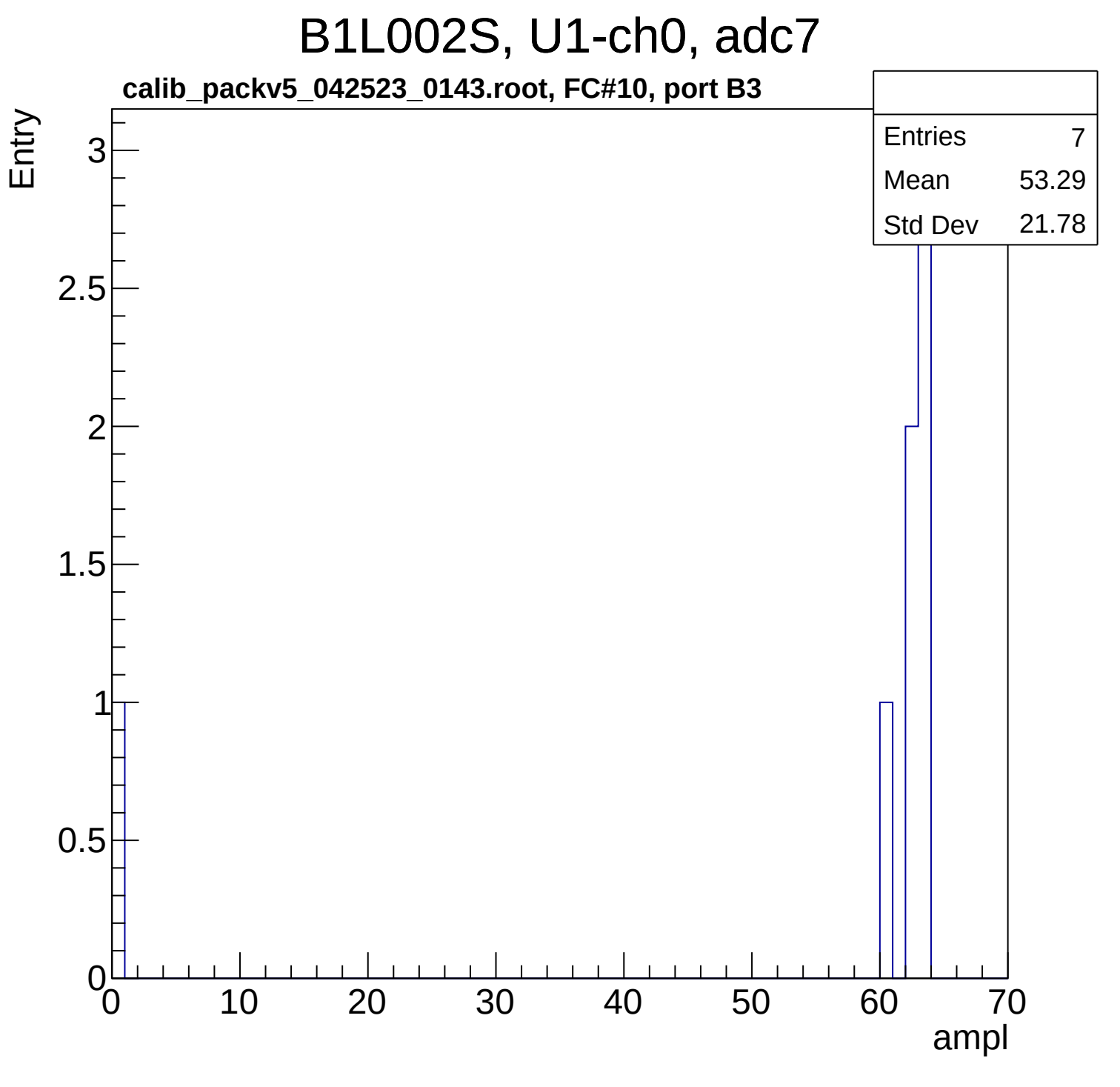
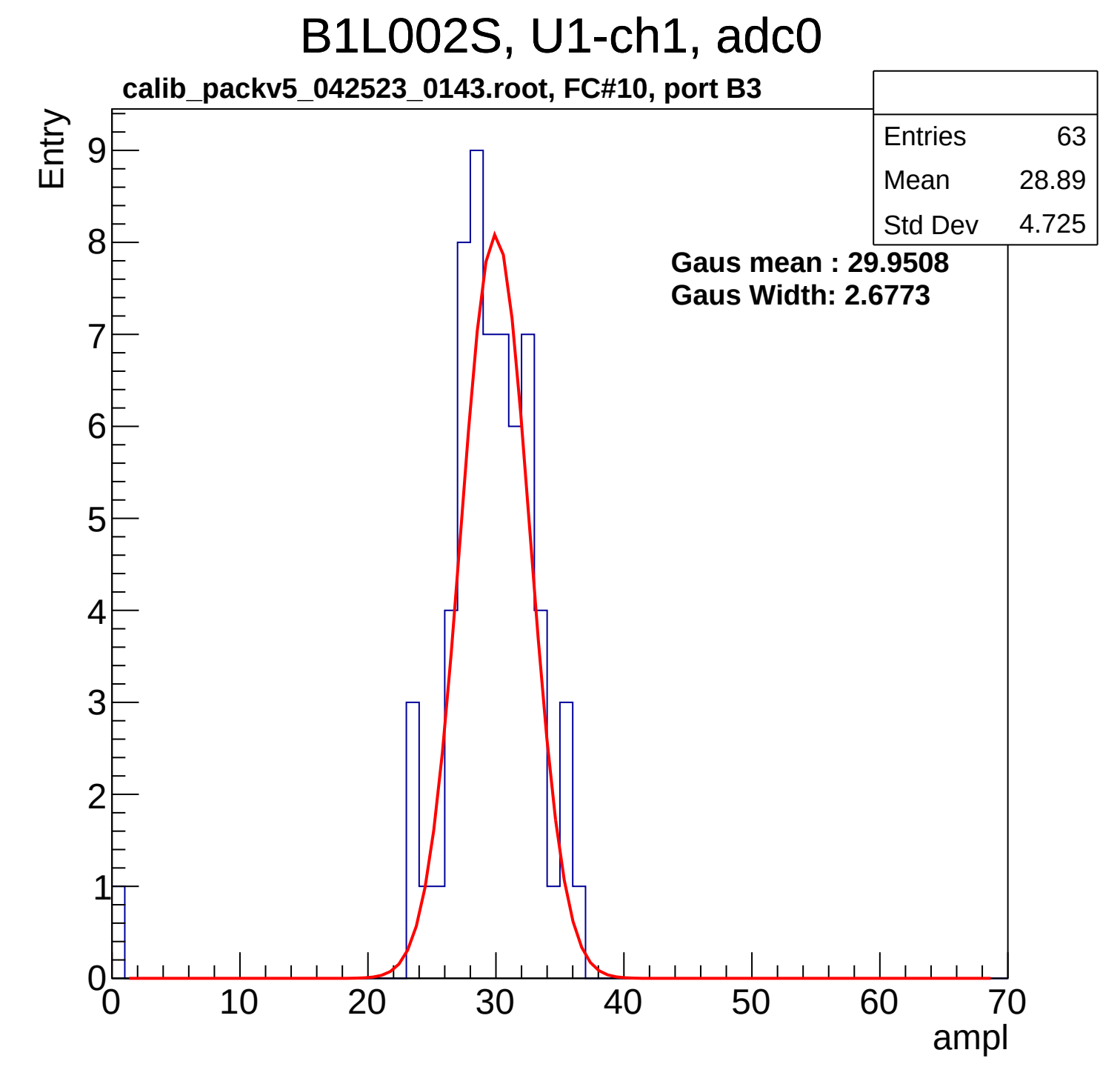

B1L002S, U1-ch0, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53.29
Std Dev 21.78
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch1, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.89
Std Dev 4.725
Gaus mean : 29.9508
Gaus Width: 2.6773
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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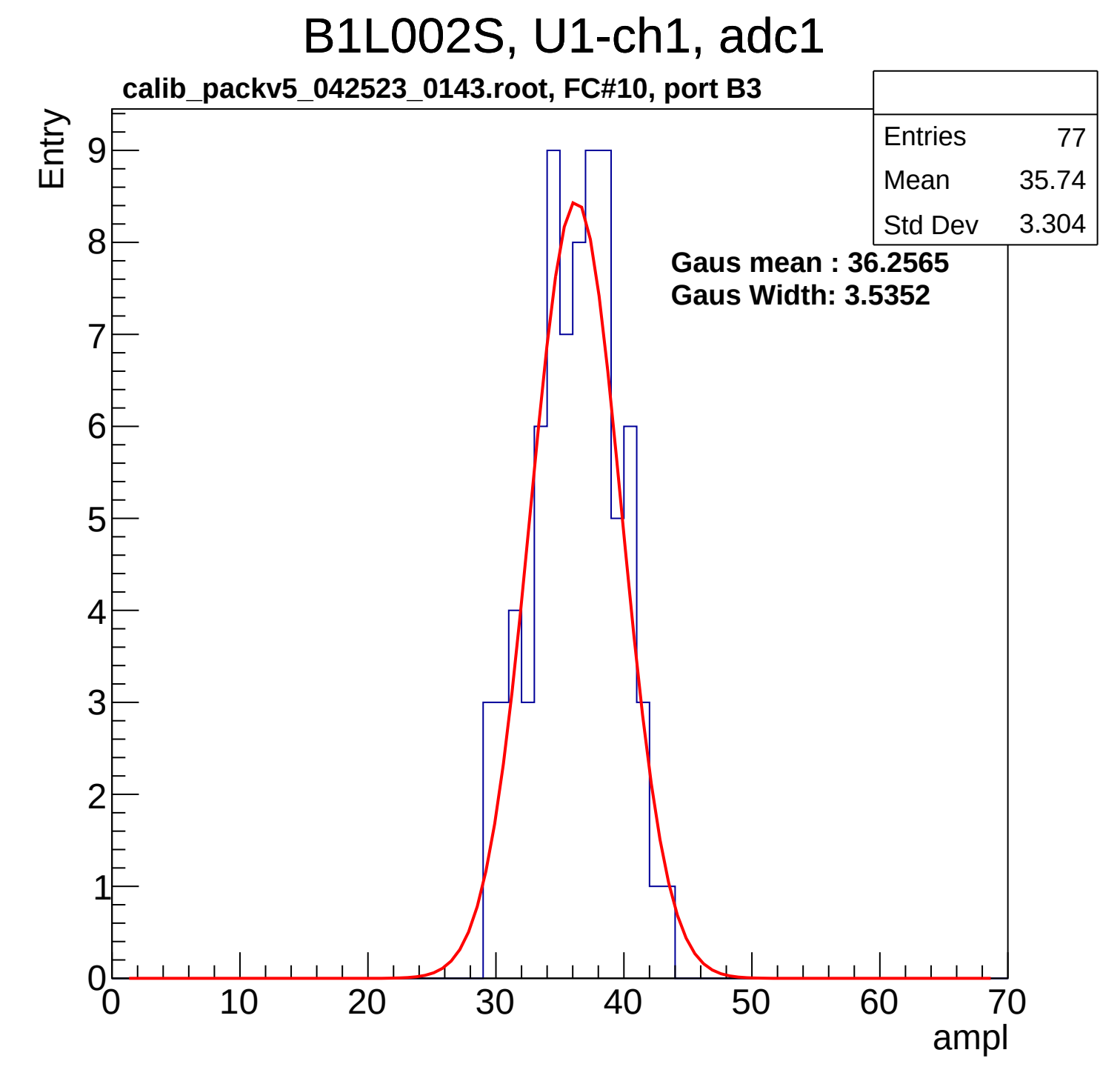
B1L002S, U1-ch1, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.74
Std Dev 3.304
Gaus mean : 36.2565
Gaus Width: 3.5352
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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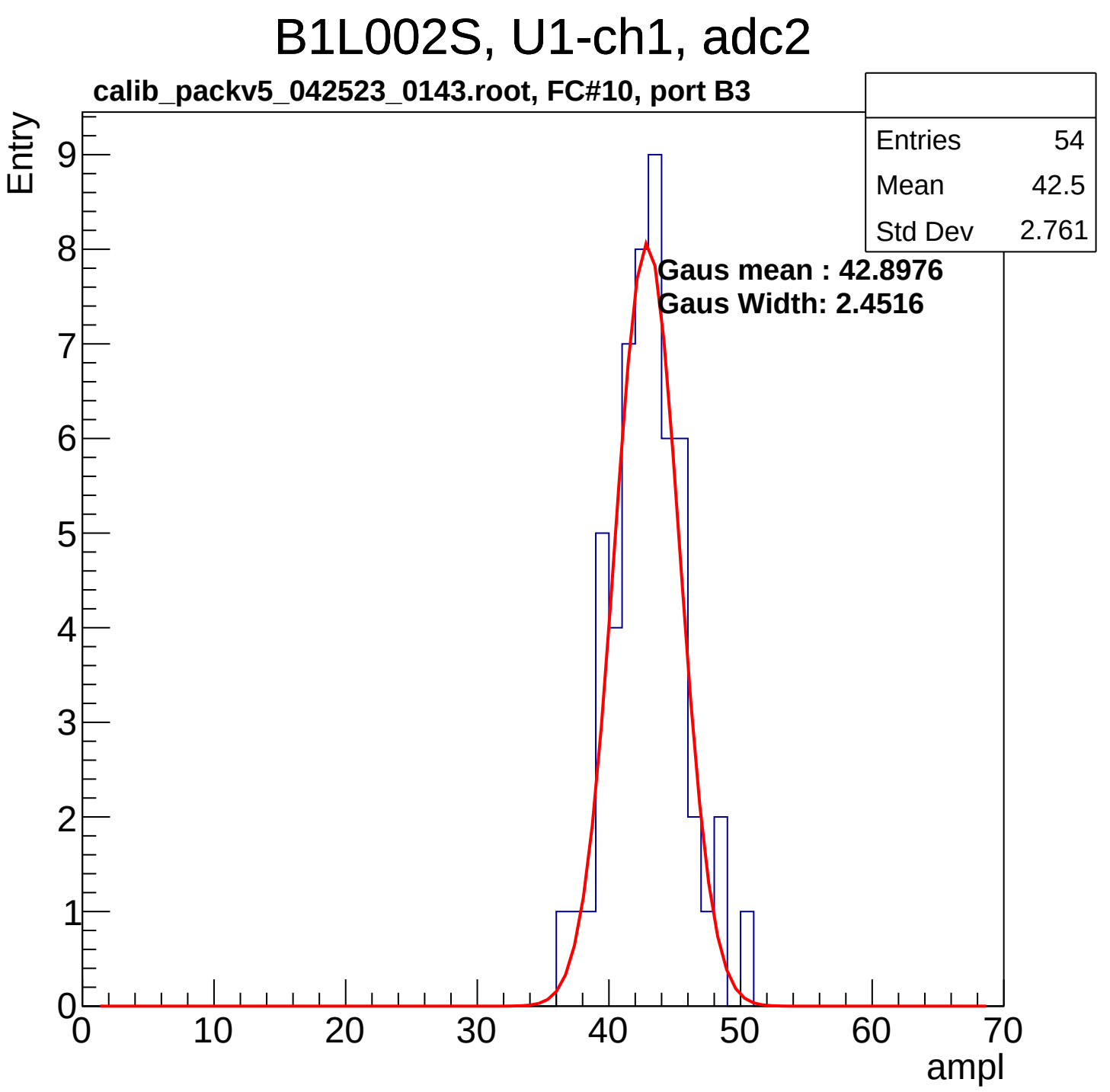
B1L002S, U1-ch1, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 42.5
Std Dev 2.761
Gaus mean : 42.8976
Gaus Width: 2.4516
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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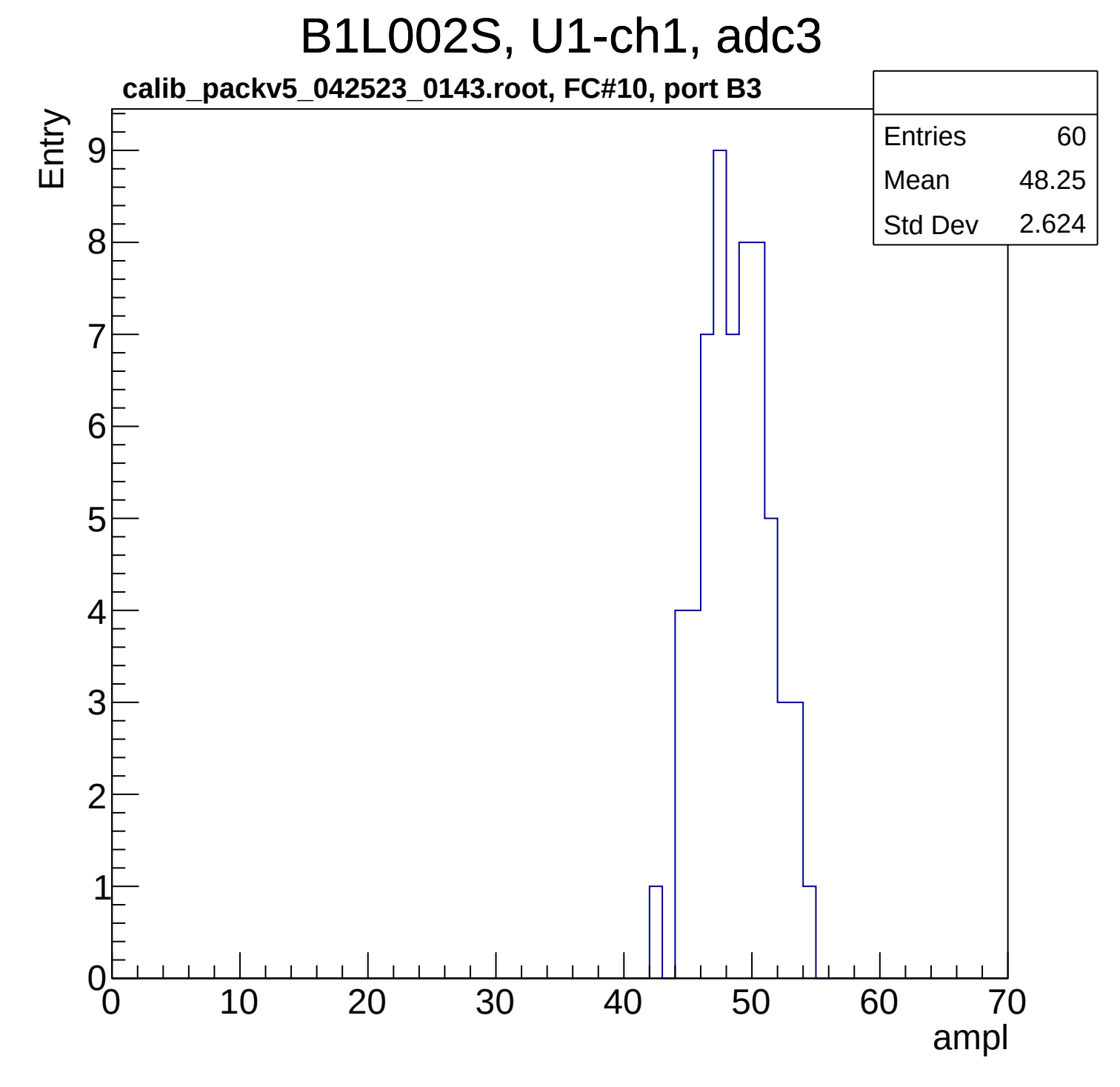
B1L002S, U1-ch1, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 48.25
Std Dev 2.624
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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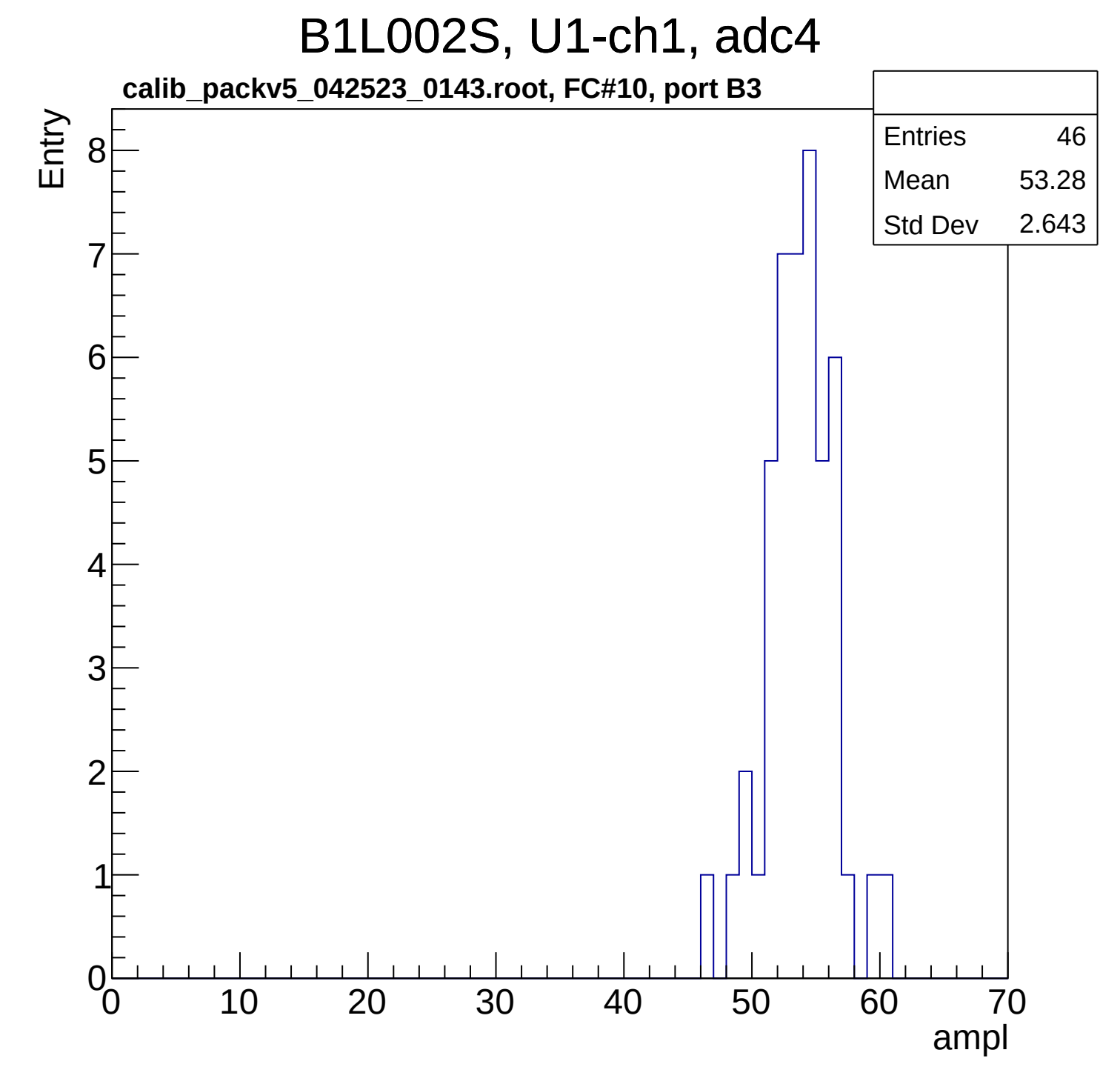
B1L002S, U1-ch1, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 53.28
Std Dev 2.643
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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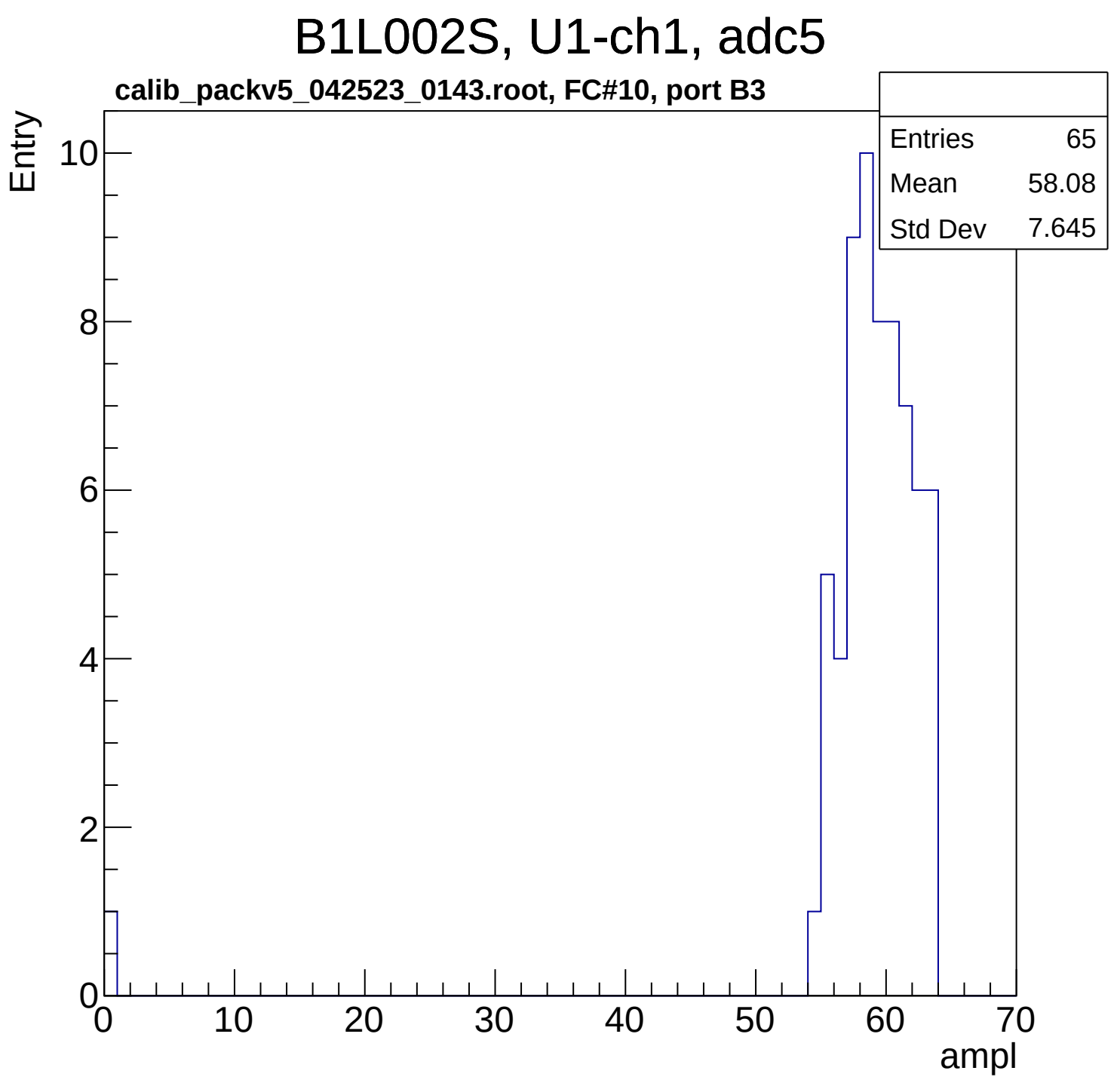
B1L002S, U1-ch1, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.08
Std Dev 7.645
Entry
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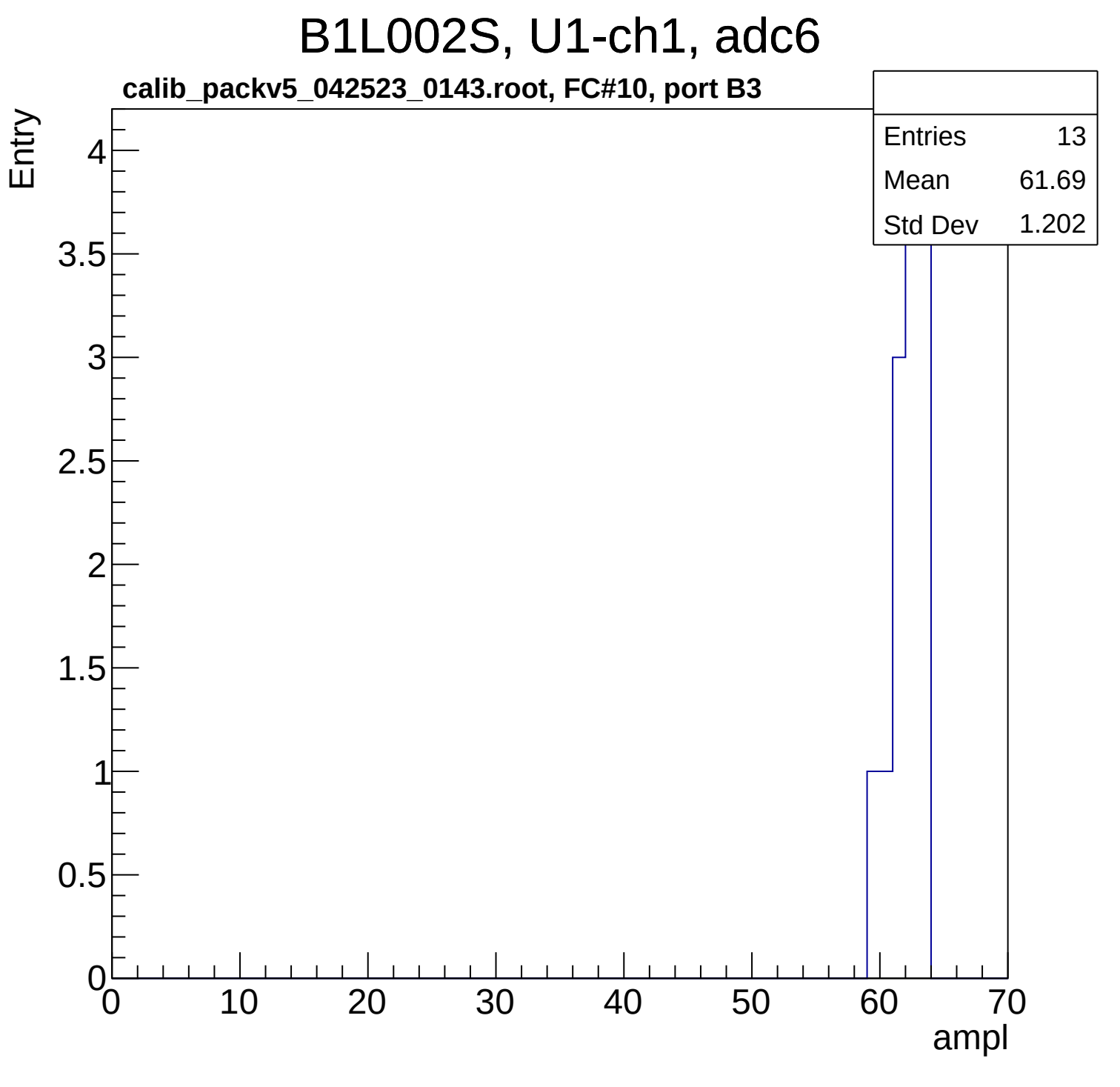
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch1, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.69
Std Dev 1.202
Entry
ampl
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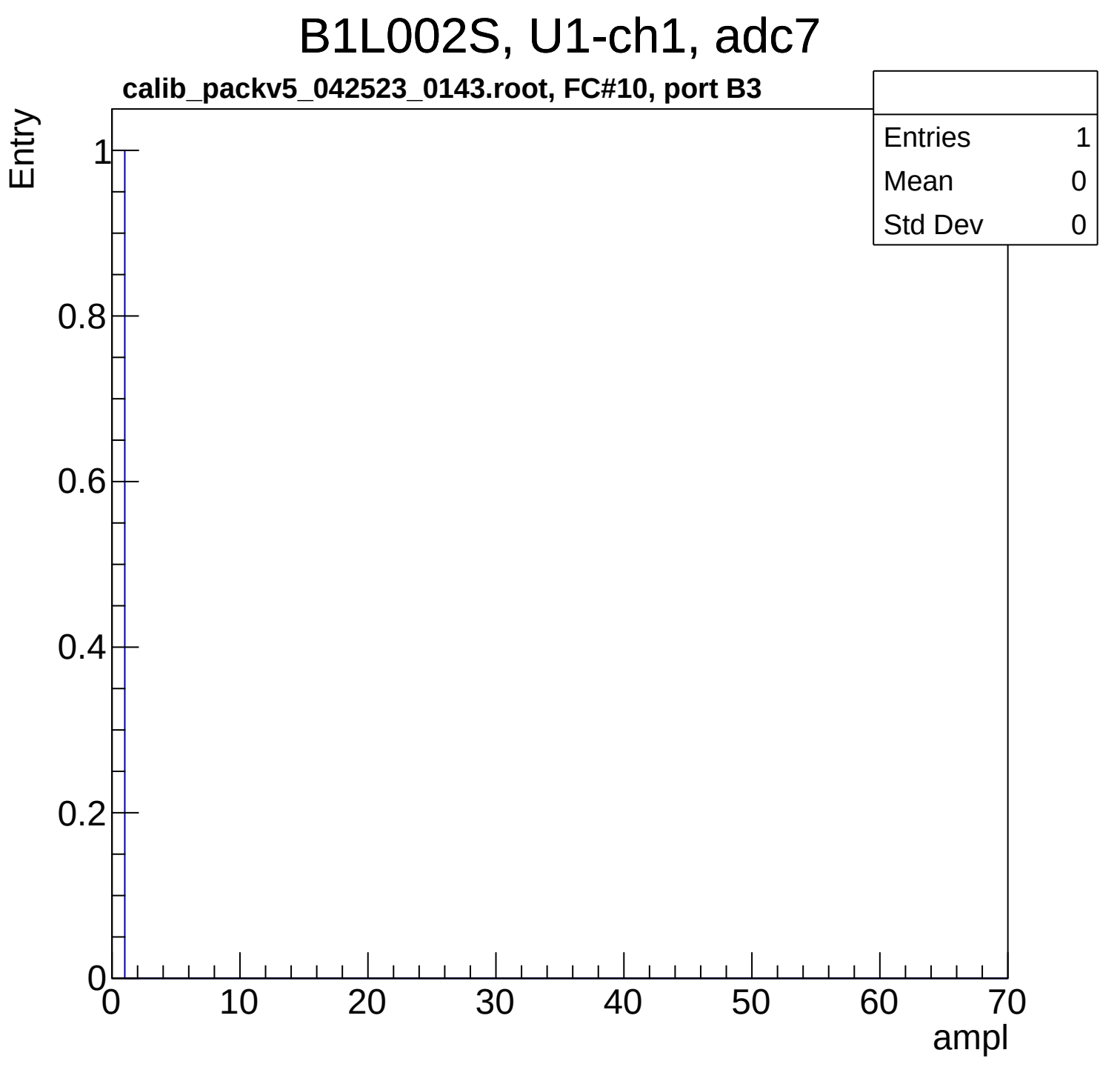
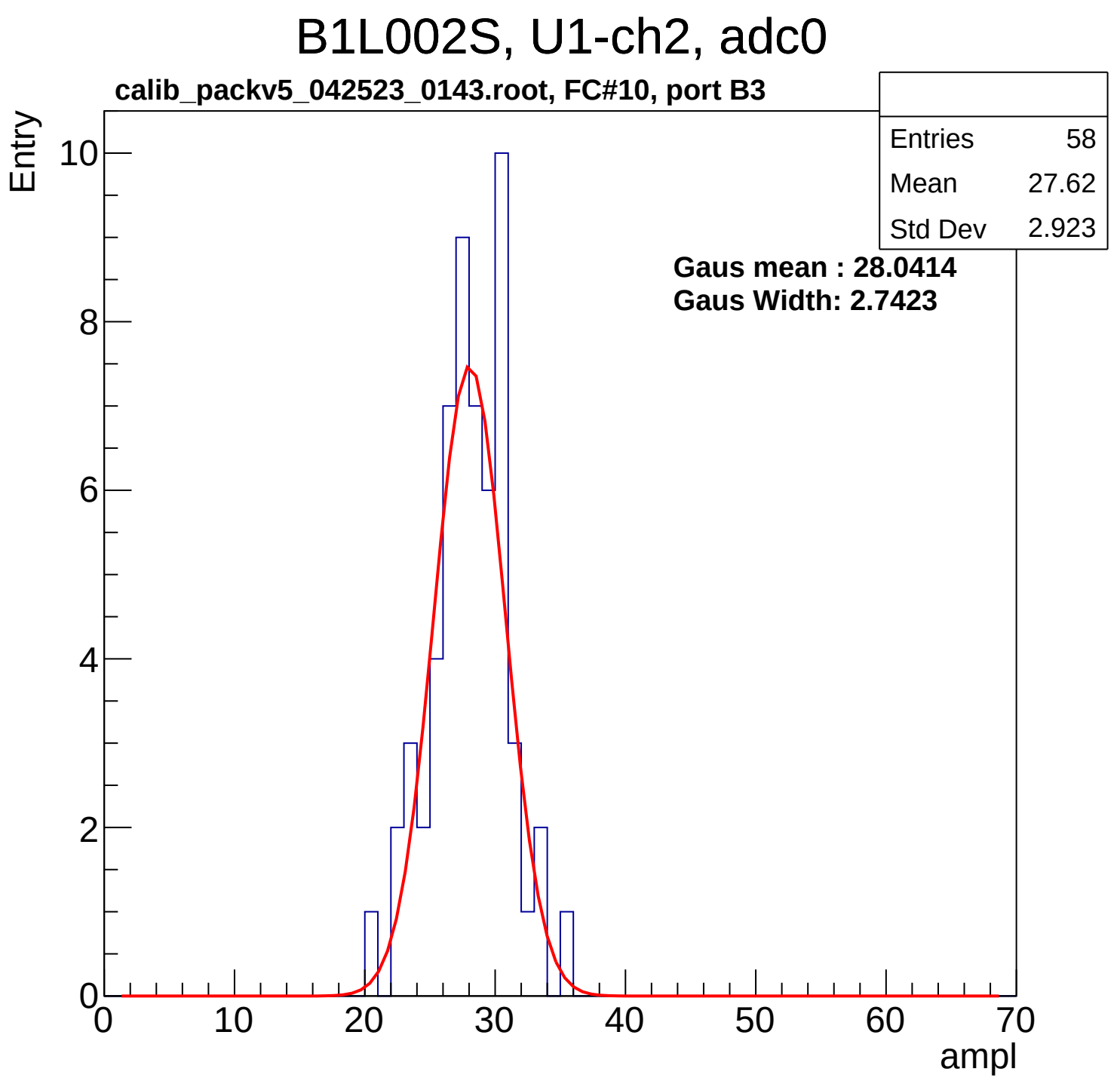
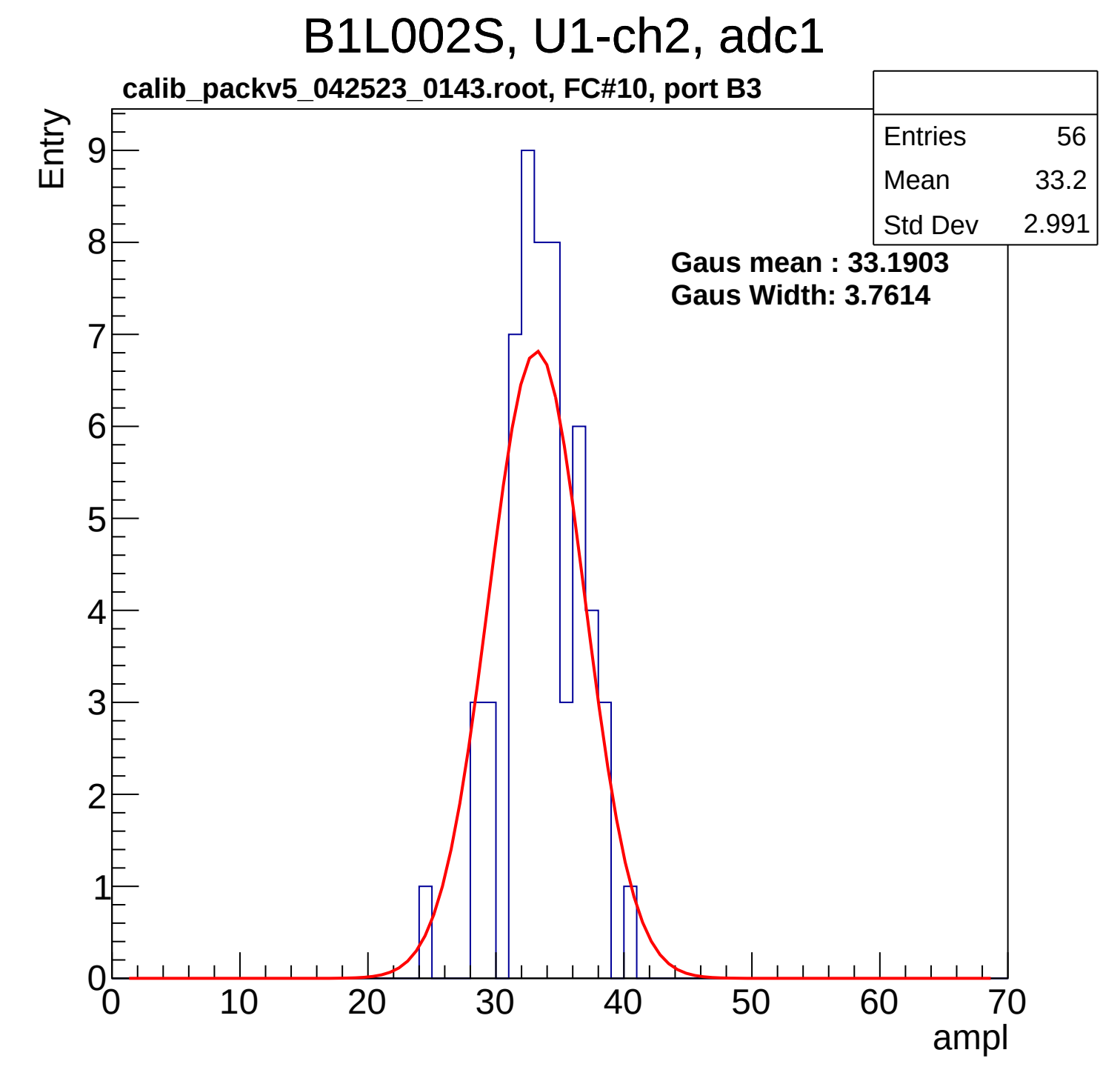
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U1-ch1, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch2, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.62
Std Dev 2.923
Gaus mean : 28.0414
Gaus Width: 2.7423
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch2, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 33.2
Std Dev 2.991
Gaus mean : 33.1903
Gaus Width: 3.7614
Entry
ampl
0
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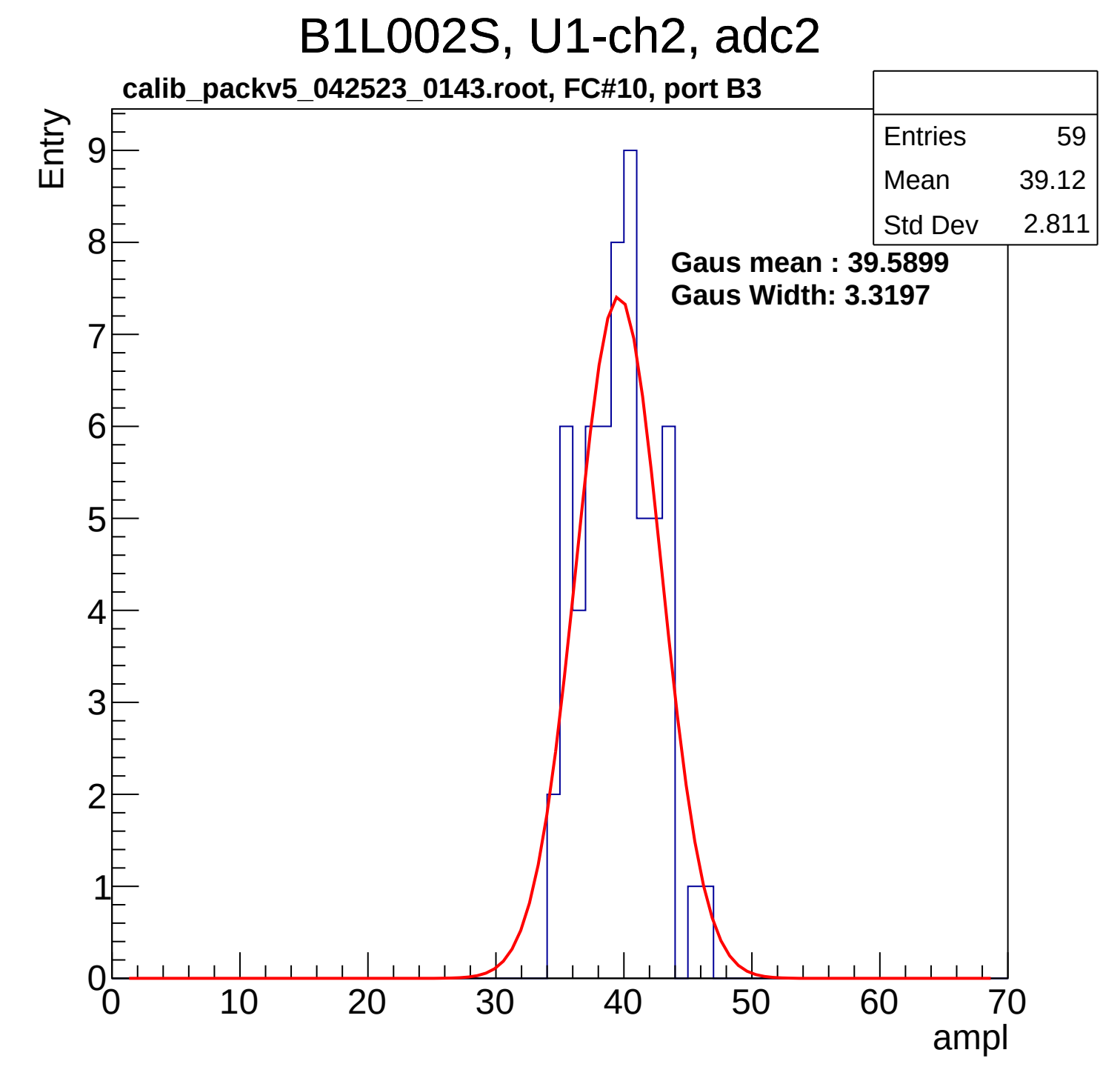
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch2, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 39.12
Std Dev 2.811
Gaus mean : 39.5899
Gaus Width: 3.3197
Entry
ampl
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2
3
4
5
6
7
8
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0
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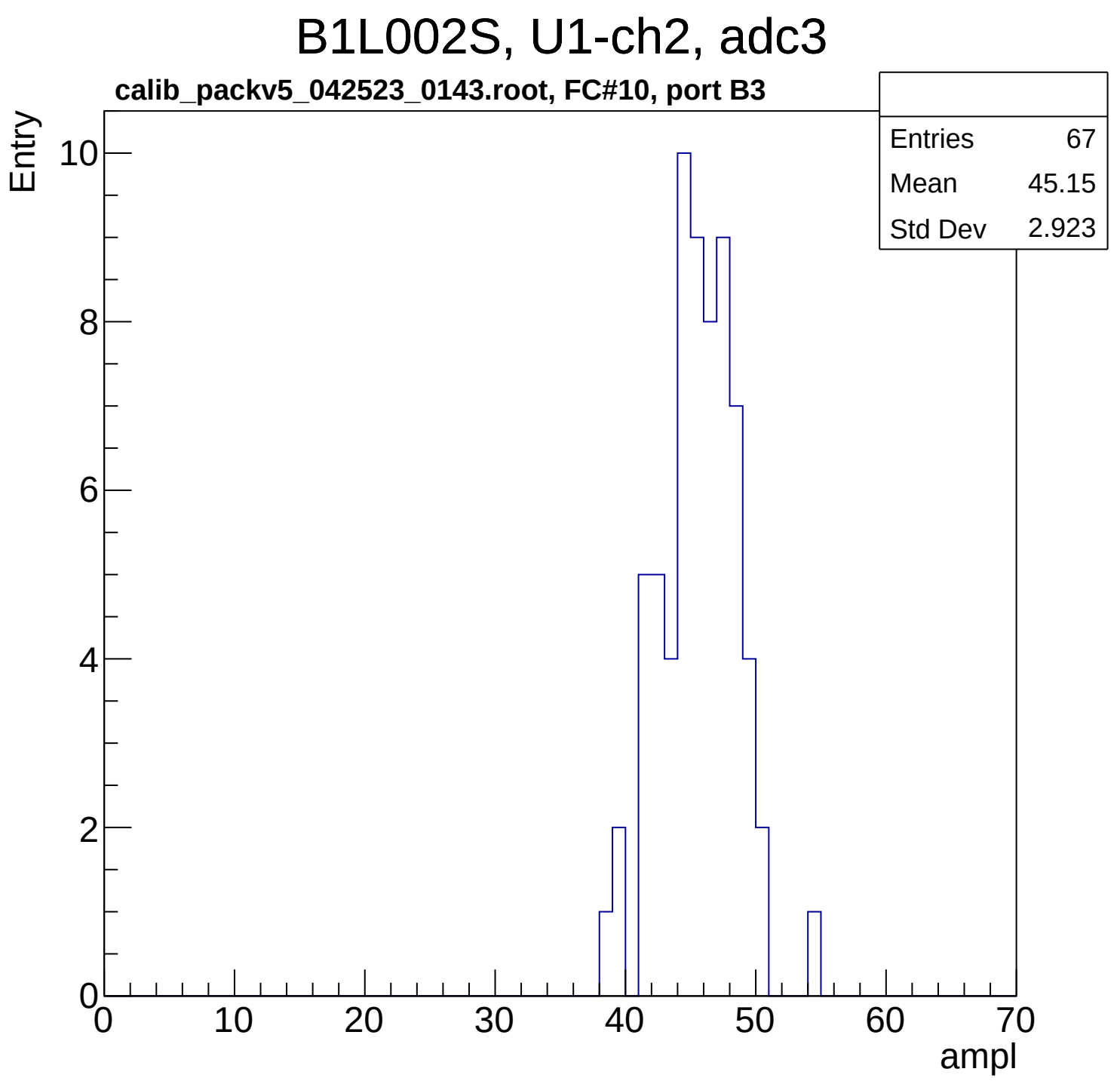
B1L002S, U1-ch2, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 45.15
Std Dev 2.923
Entry
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2
4
6
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10
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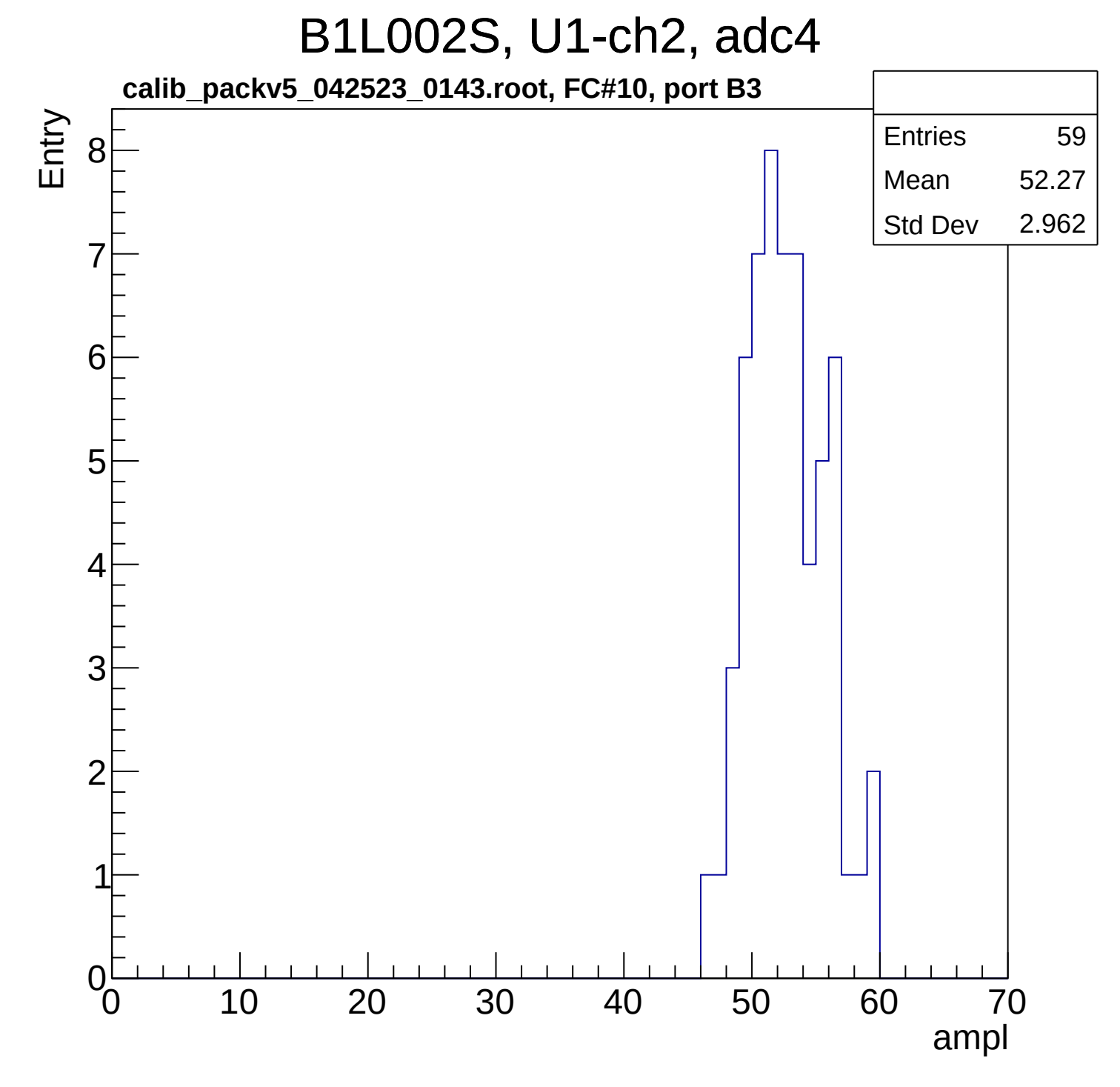
70
ampl


B1L002S, U1-ch2, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.27
Std Dev 2.962
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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60
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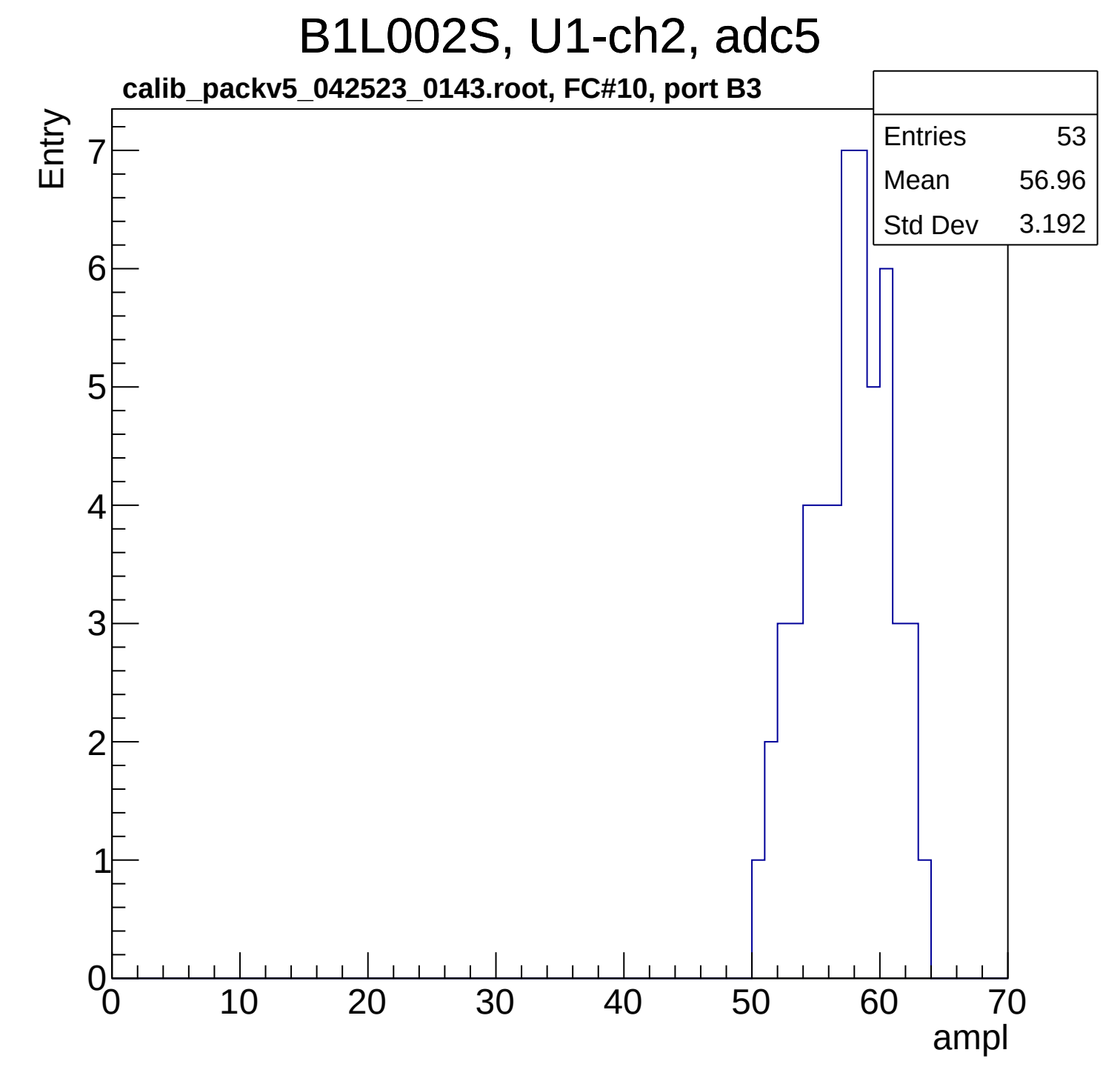
B1L002S, U1-ch2, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 56.96
Std Dev 3.192
Entry
ampl
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2
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0
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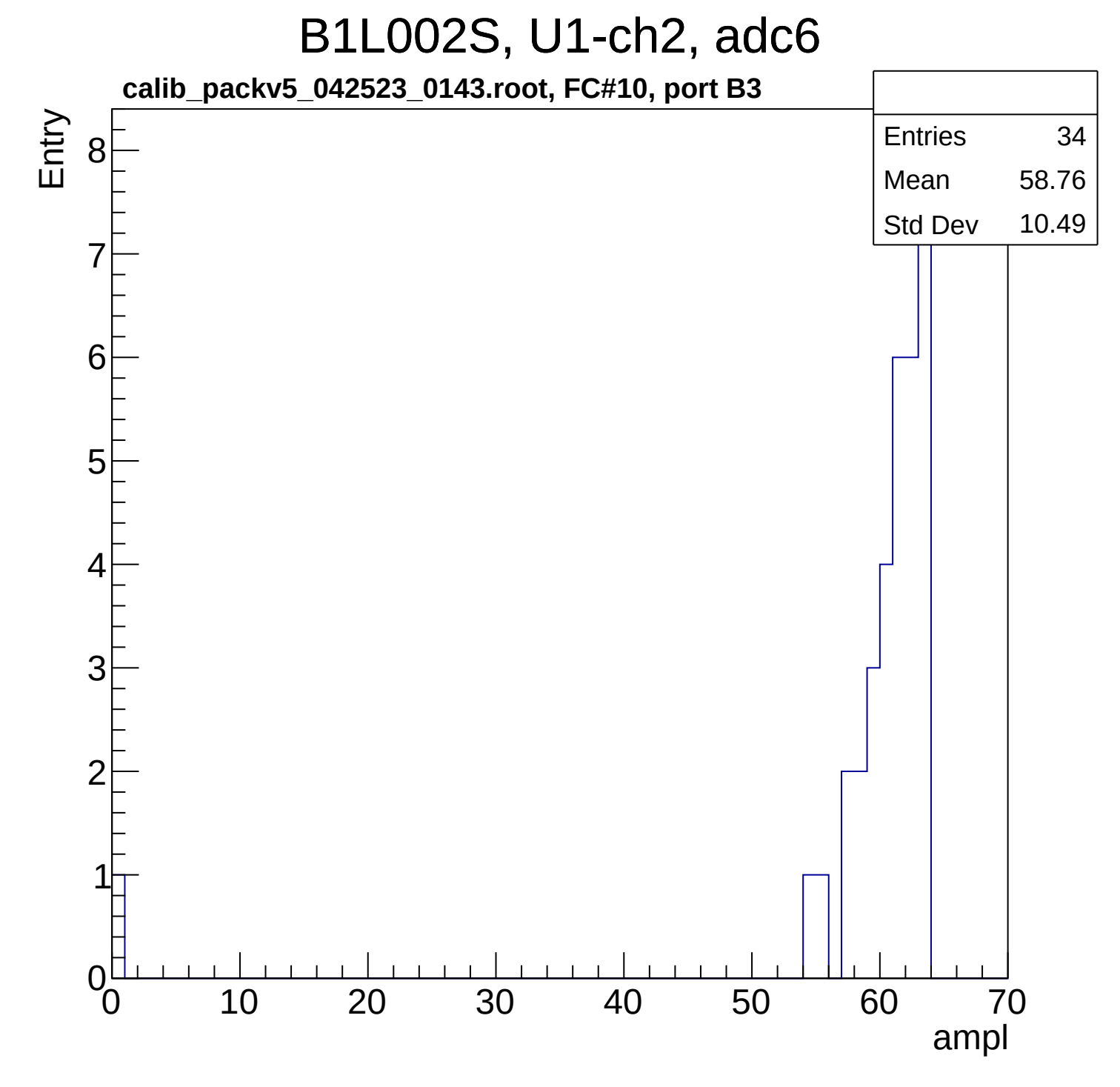
B1L002S, U1-ch2, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.76
Std Dev 10.49
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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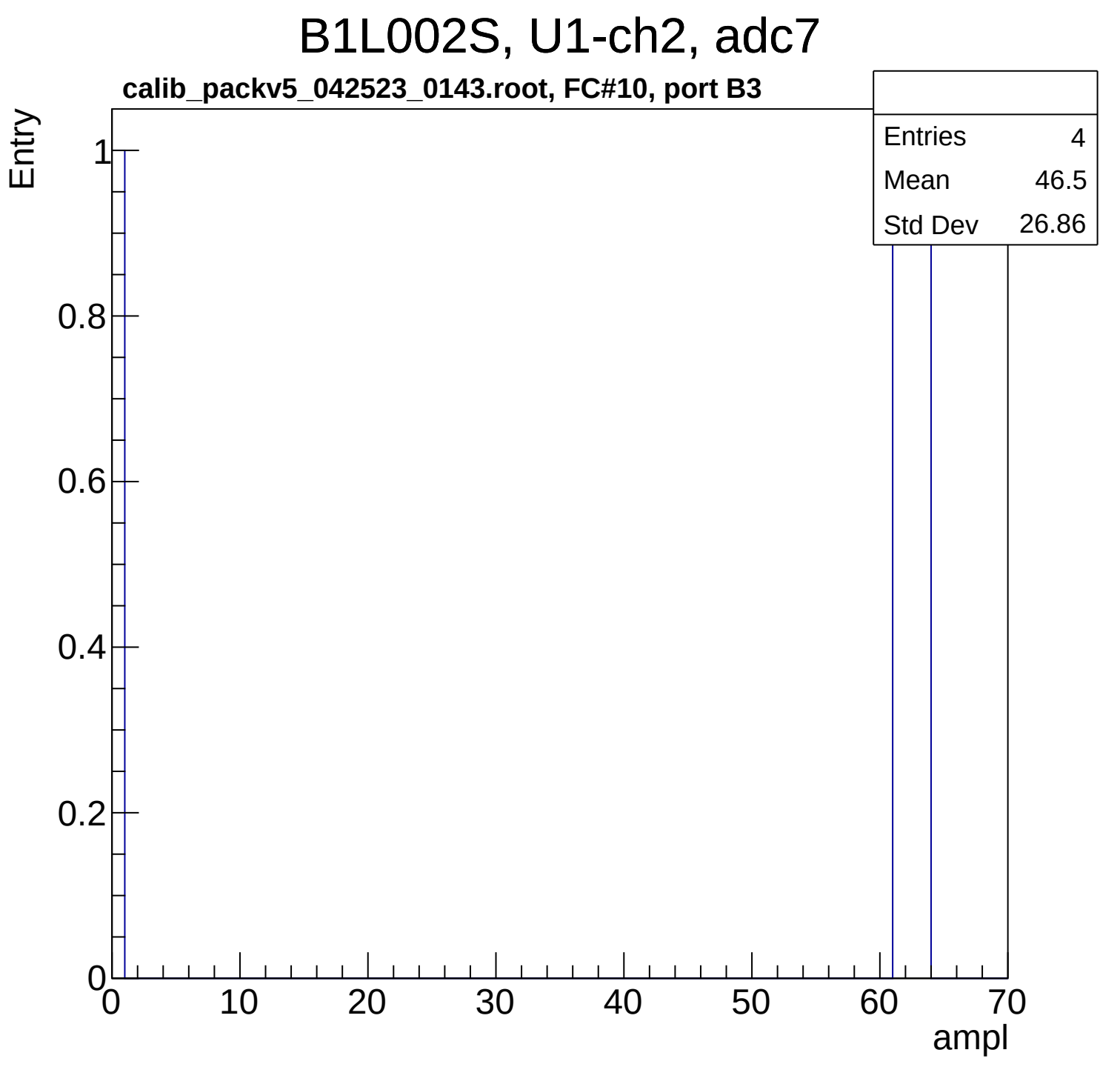
B1L002S, U1-ch2, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 46.5
Std Dev 26.86
Entry
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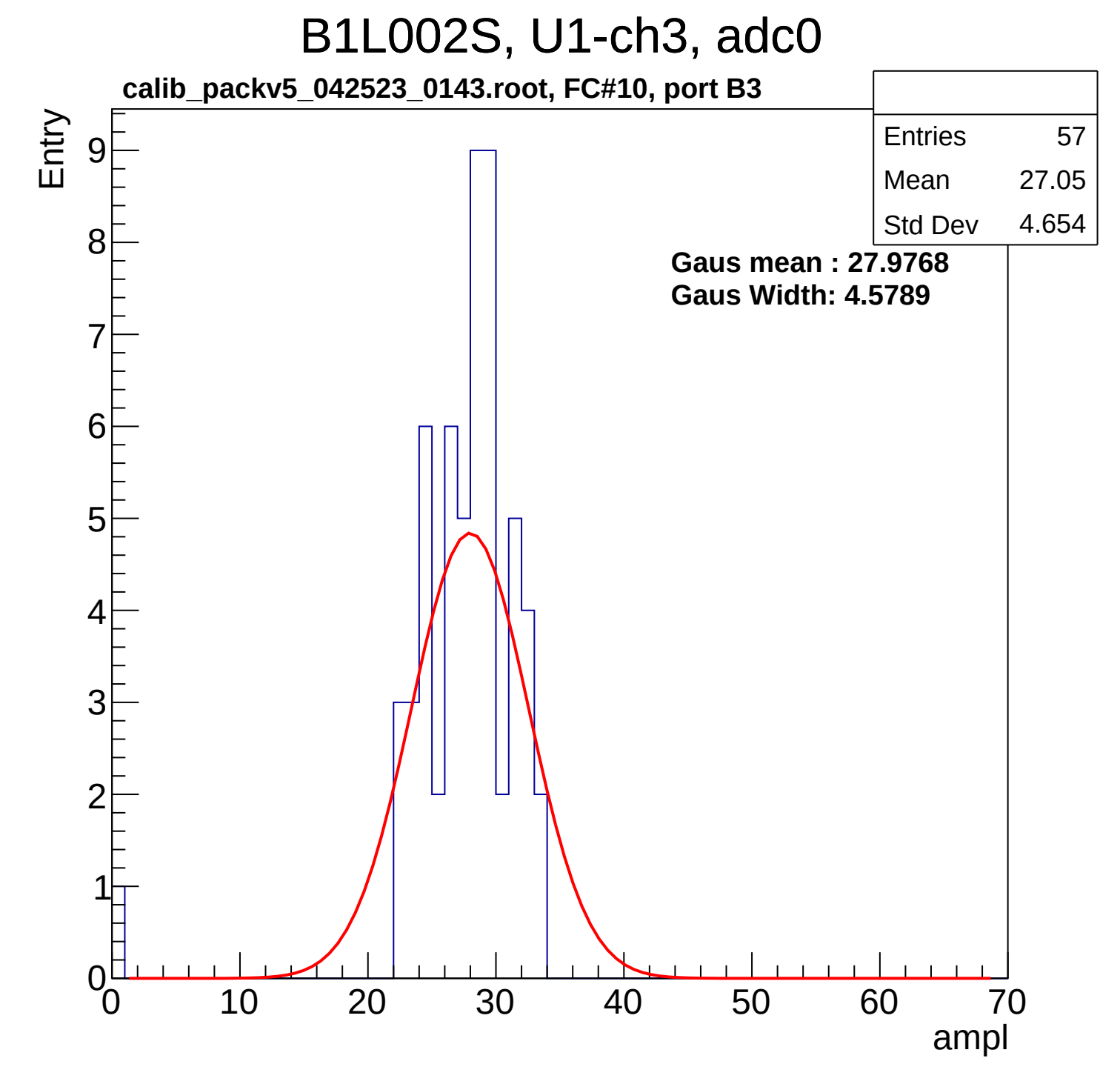
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch3, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.05
Std Dev 4.654
Gaus mean : 27.9768
Gaus Width: 4.5789
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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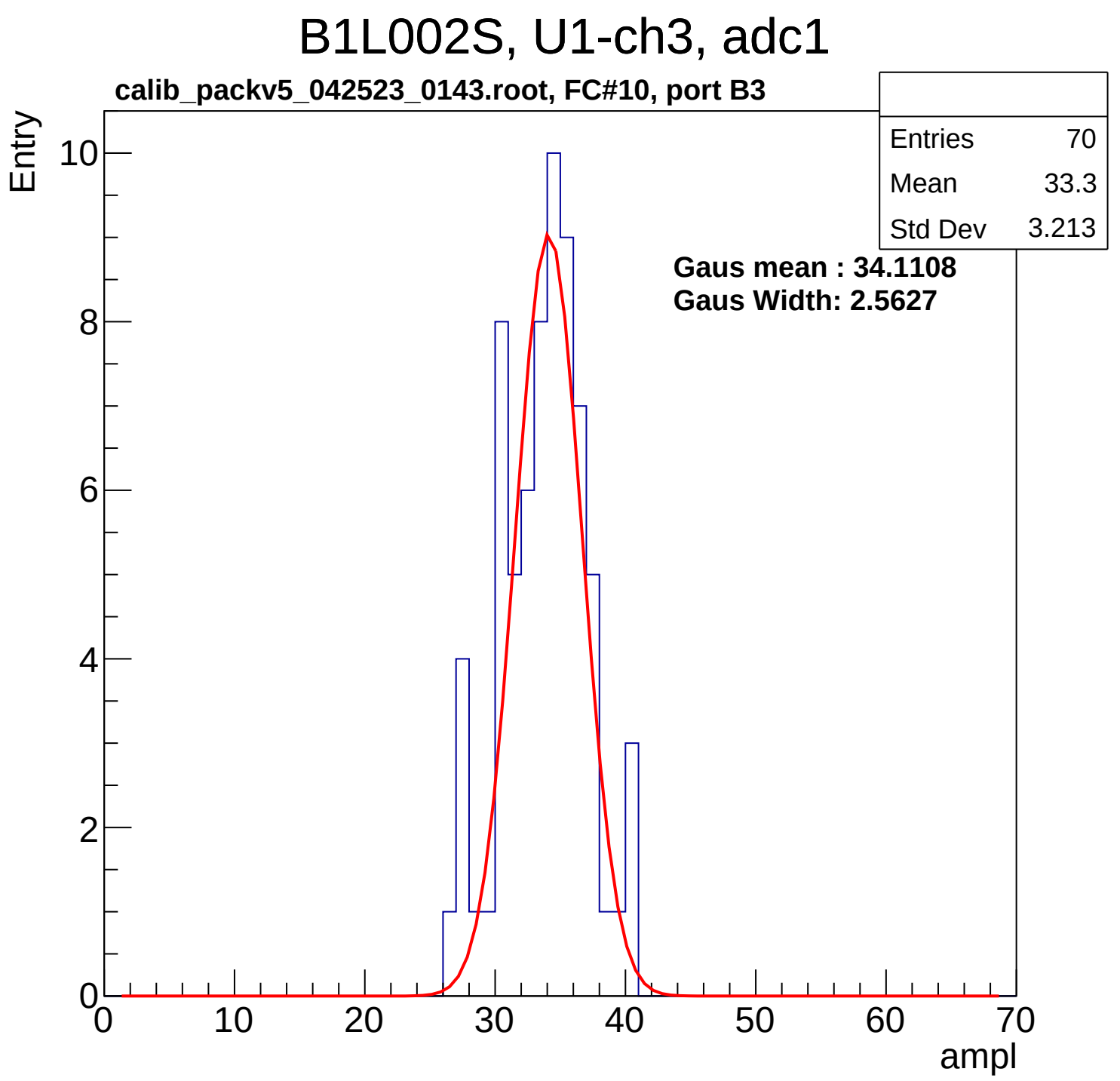
B1L002S, U1-ch3, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 33.3
Std Dev 3.213
Gaus mean : 34.1108
Gaus Width: 2.5627
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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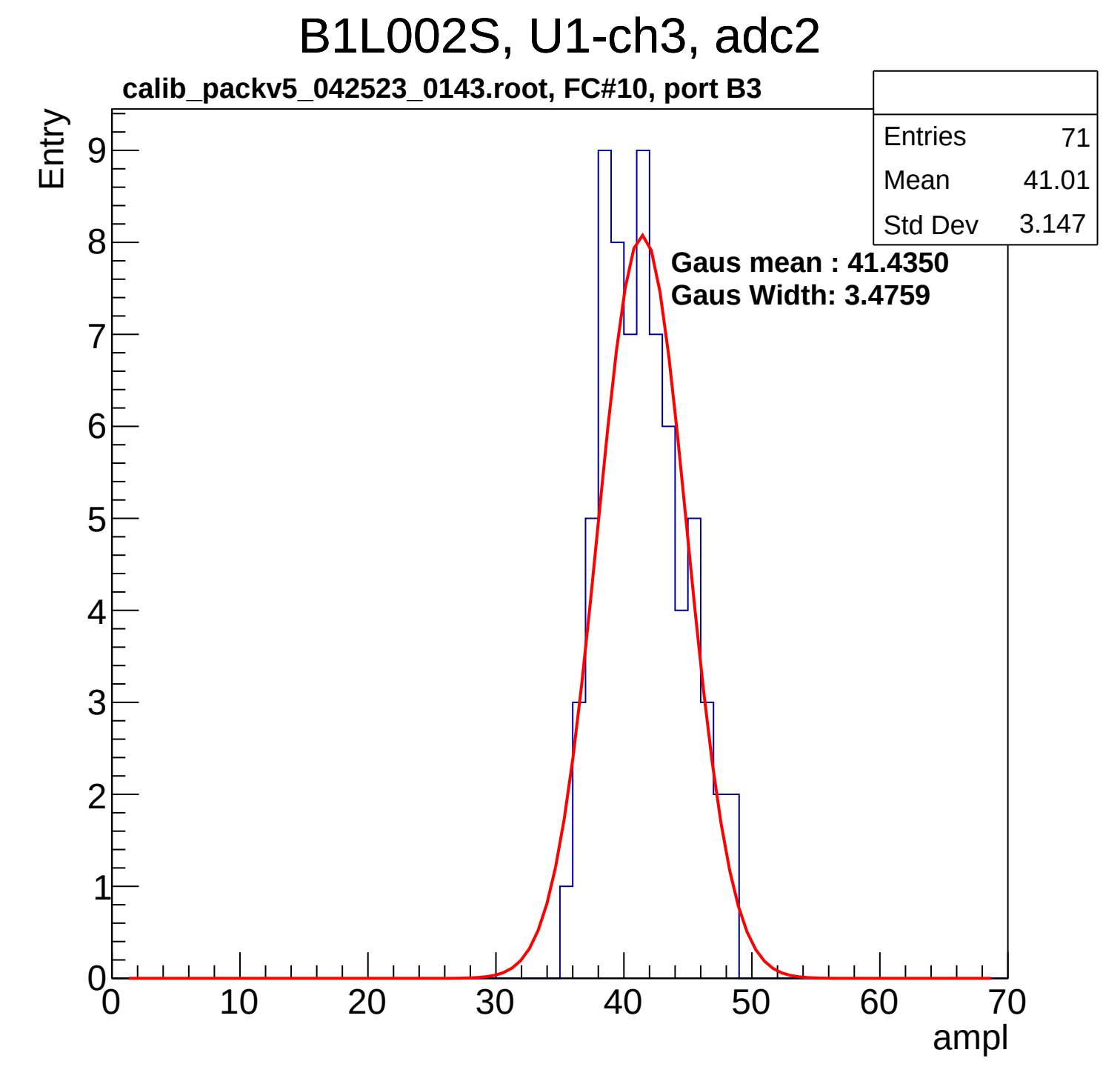
B1L002S, U1-ch3, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 41.01
Std Dev 3.147
Gaus mean : 41.4350
Gaus Width: 3.4759
Entry
ampl
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2
3
4
5
6
7
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0
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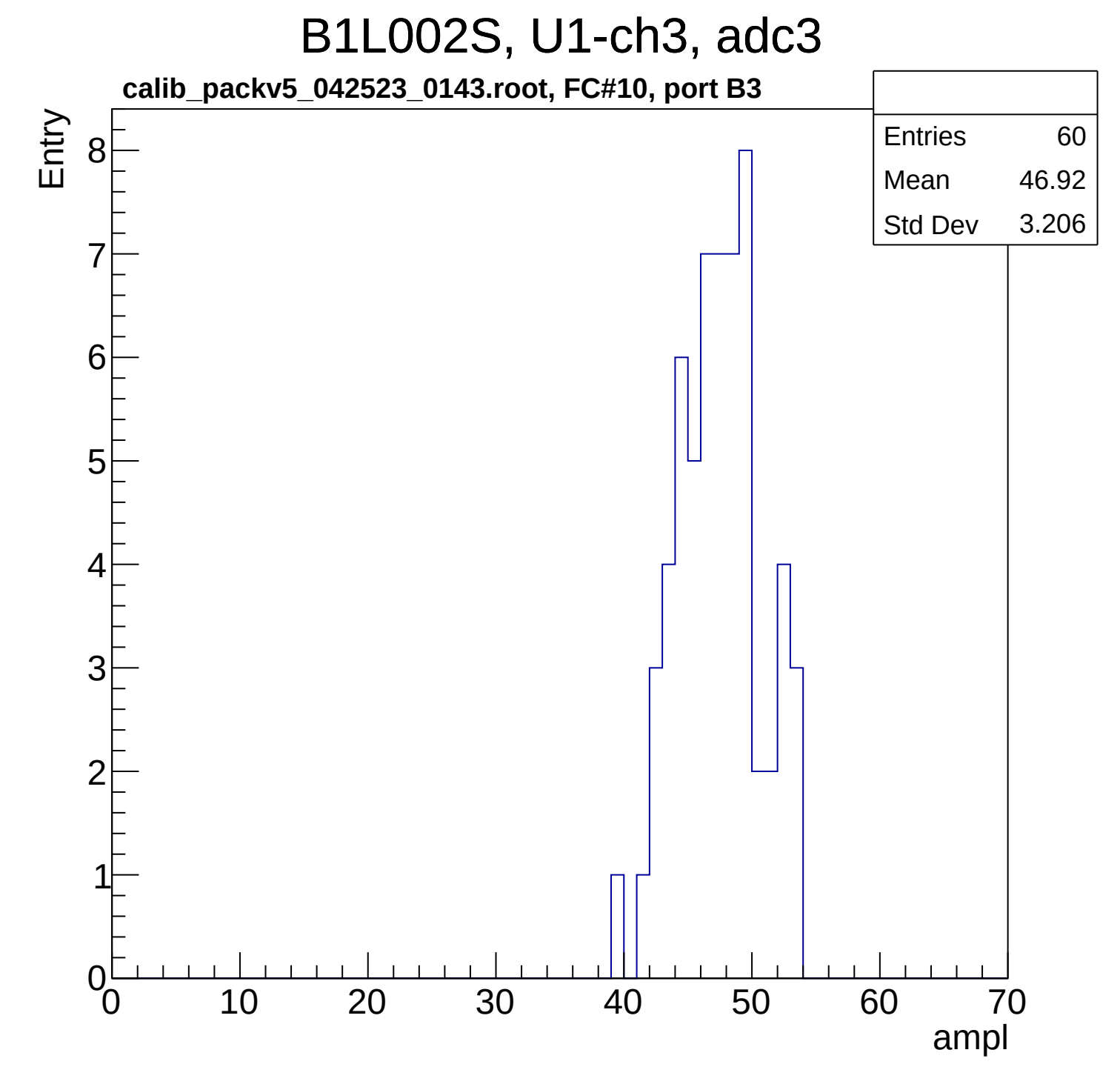
B1L002S, U1-ch3, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.92
Std Dev 3.206
Entry
ampl
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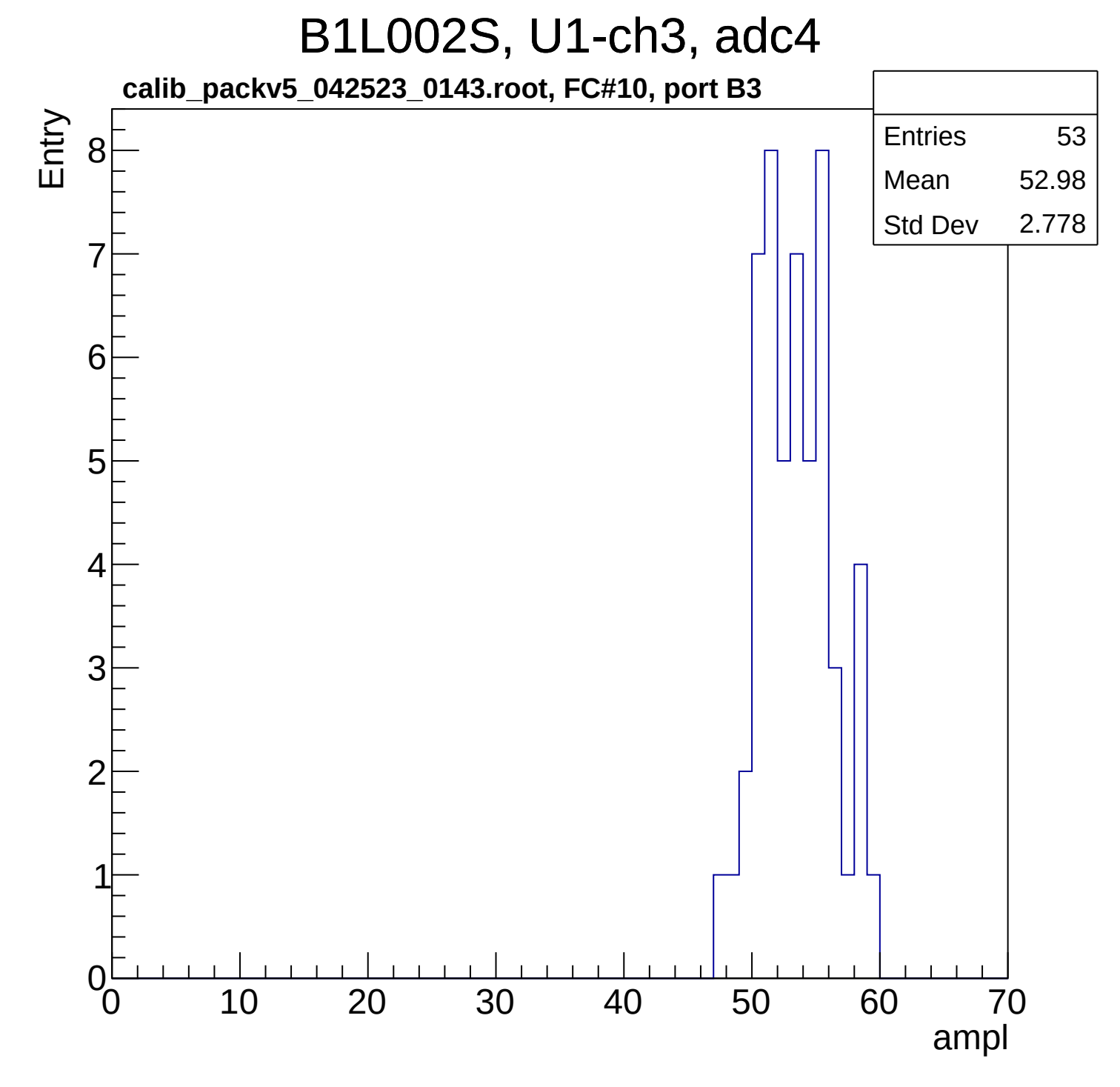
B1L002S, U1-ch3, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.98
Std Dev 2.778
Entry
ampl
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2
3
4
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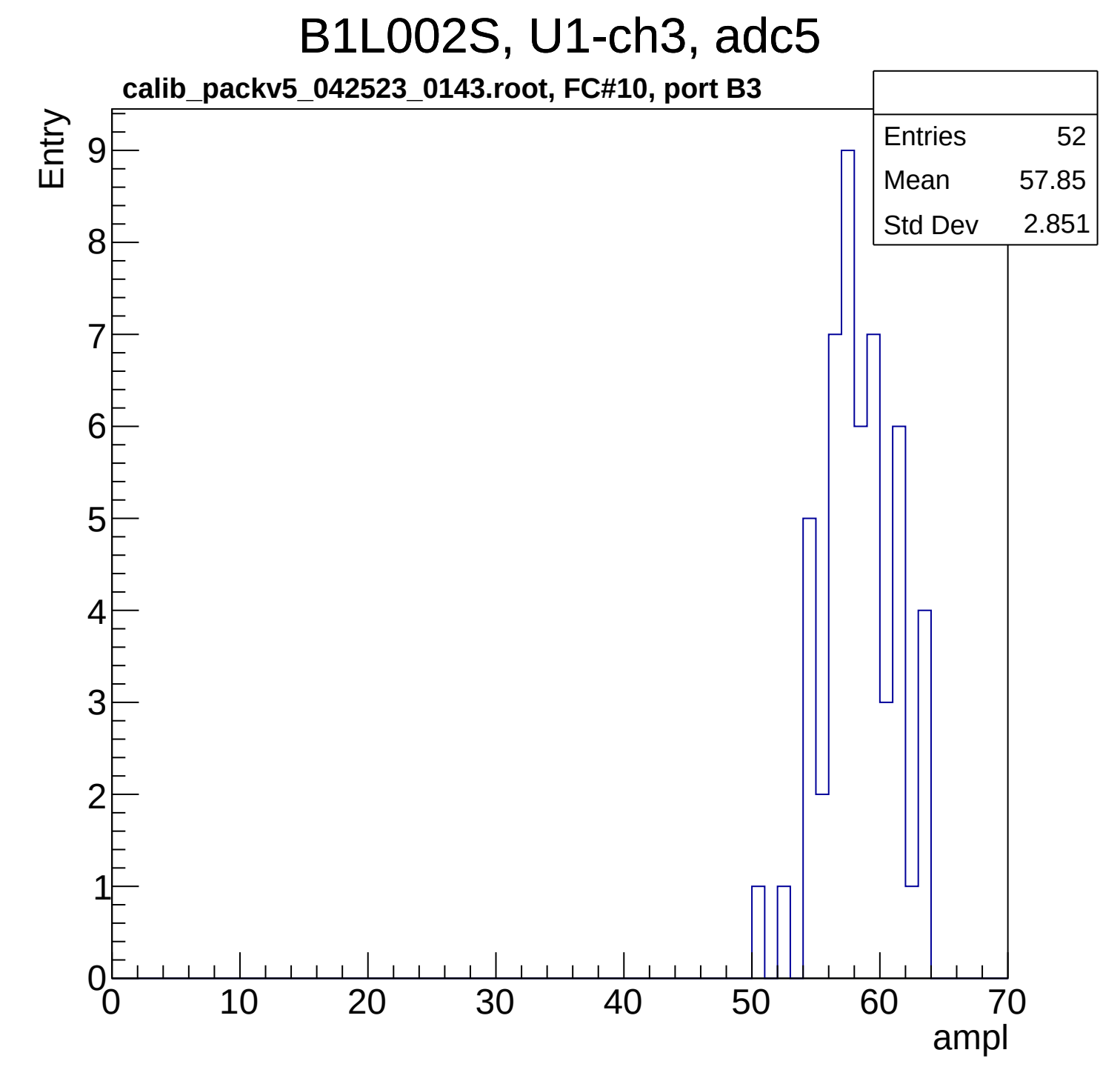
B1L002S, U1-ch3, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.85
Std Dev 2.851
Entry
ampl
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4
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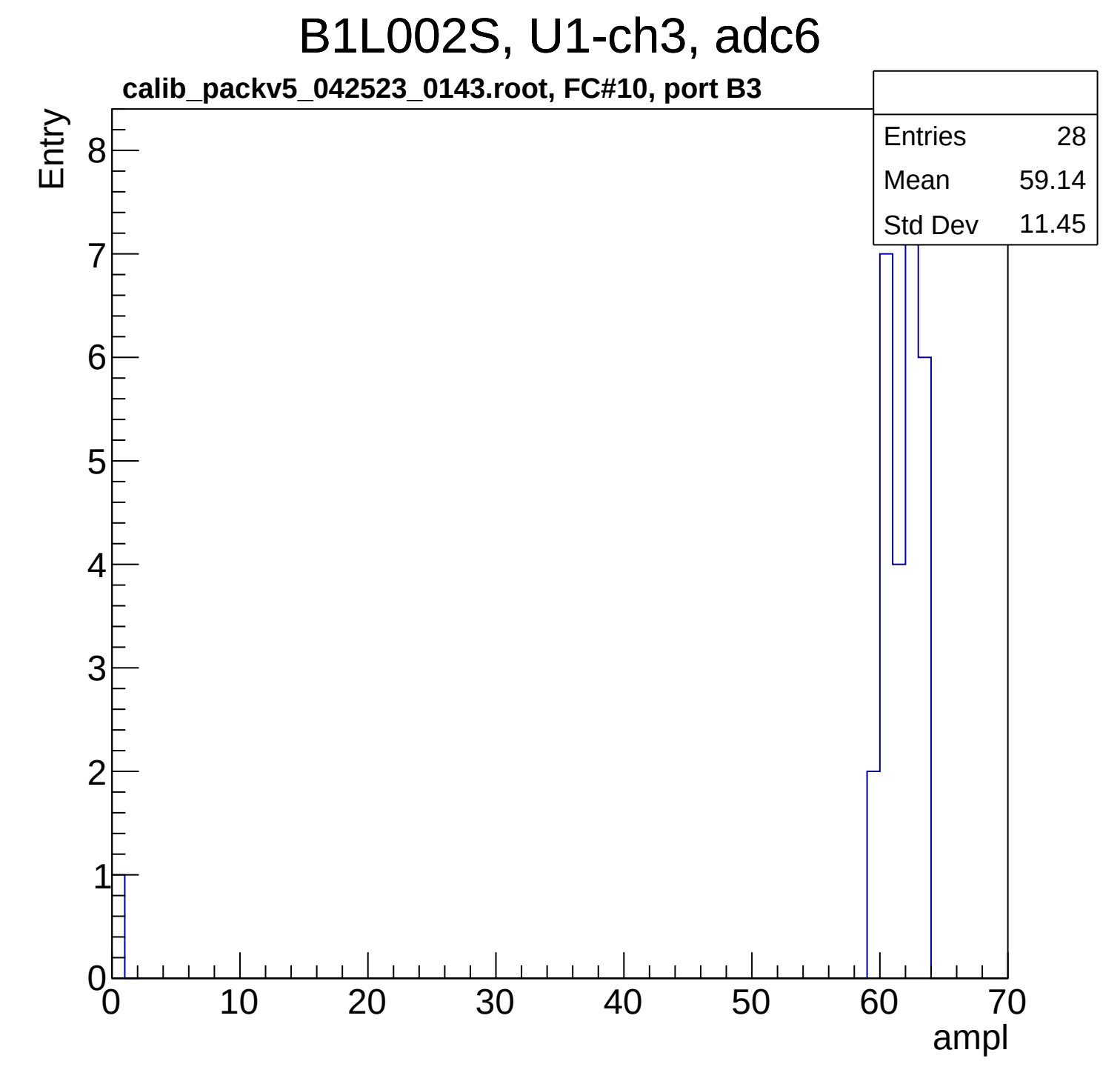
B1L002S, U1-ch3, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 59.14
Std Dev 11.45
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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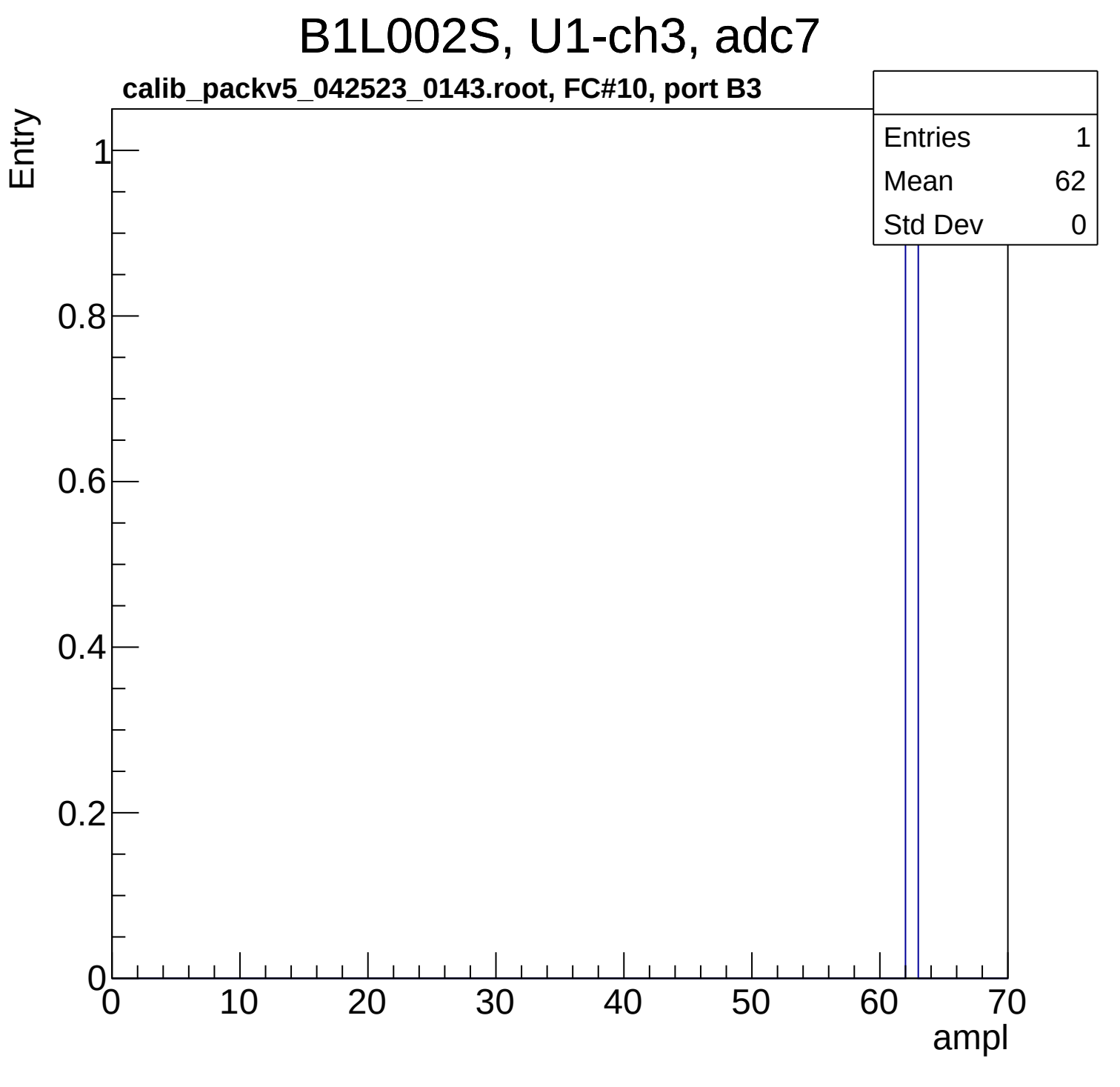
B1L002S, U1-ch3, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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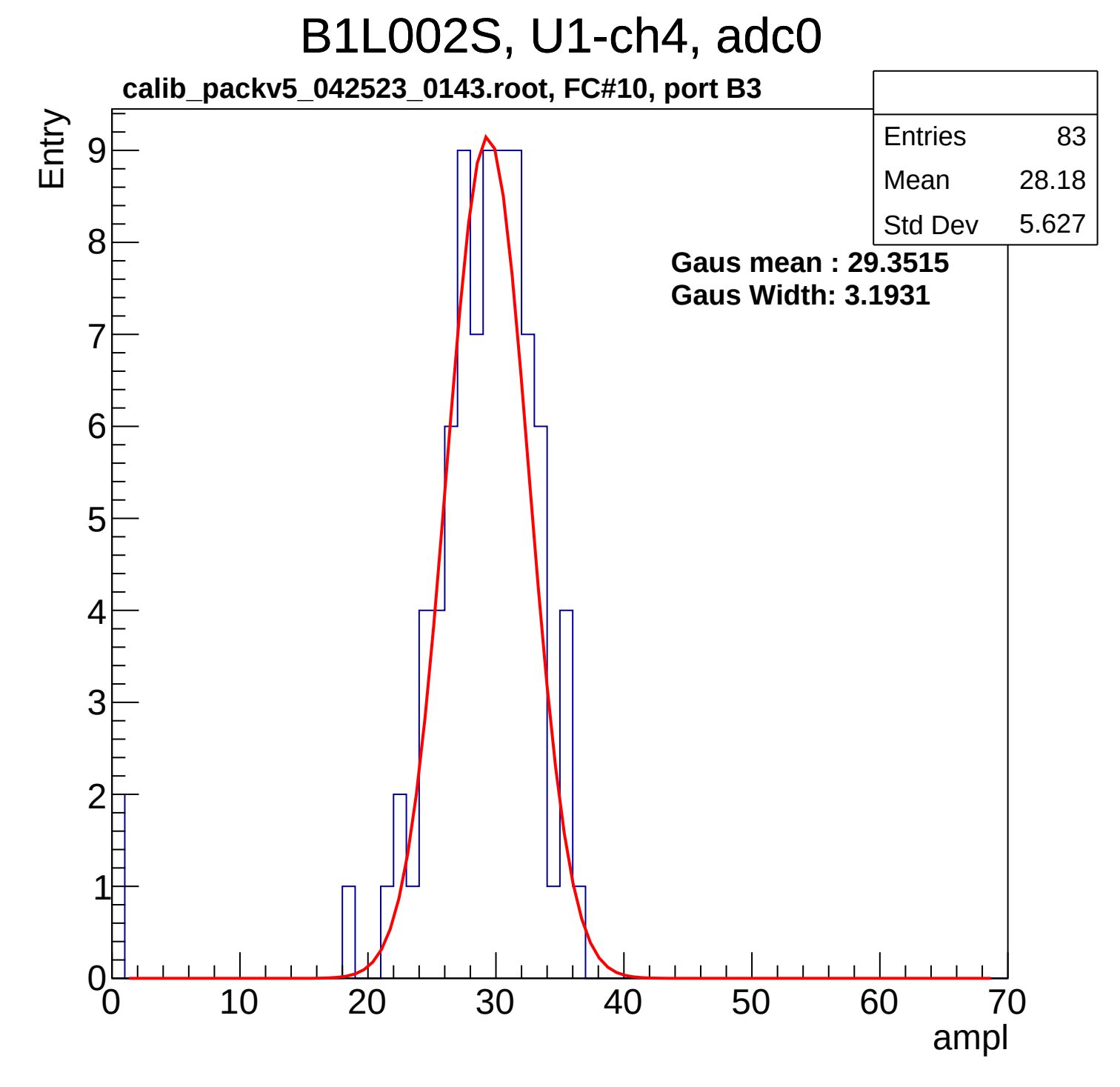
40
50
60
70
ampl


B1L002S, U1-ch4, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 28.18
Std Dev 5.627
Gaus mean : 29.3515
Gaus Width: 3.1931
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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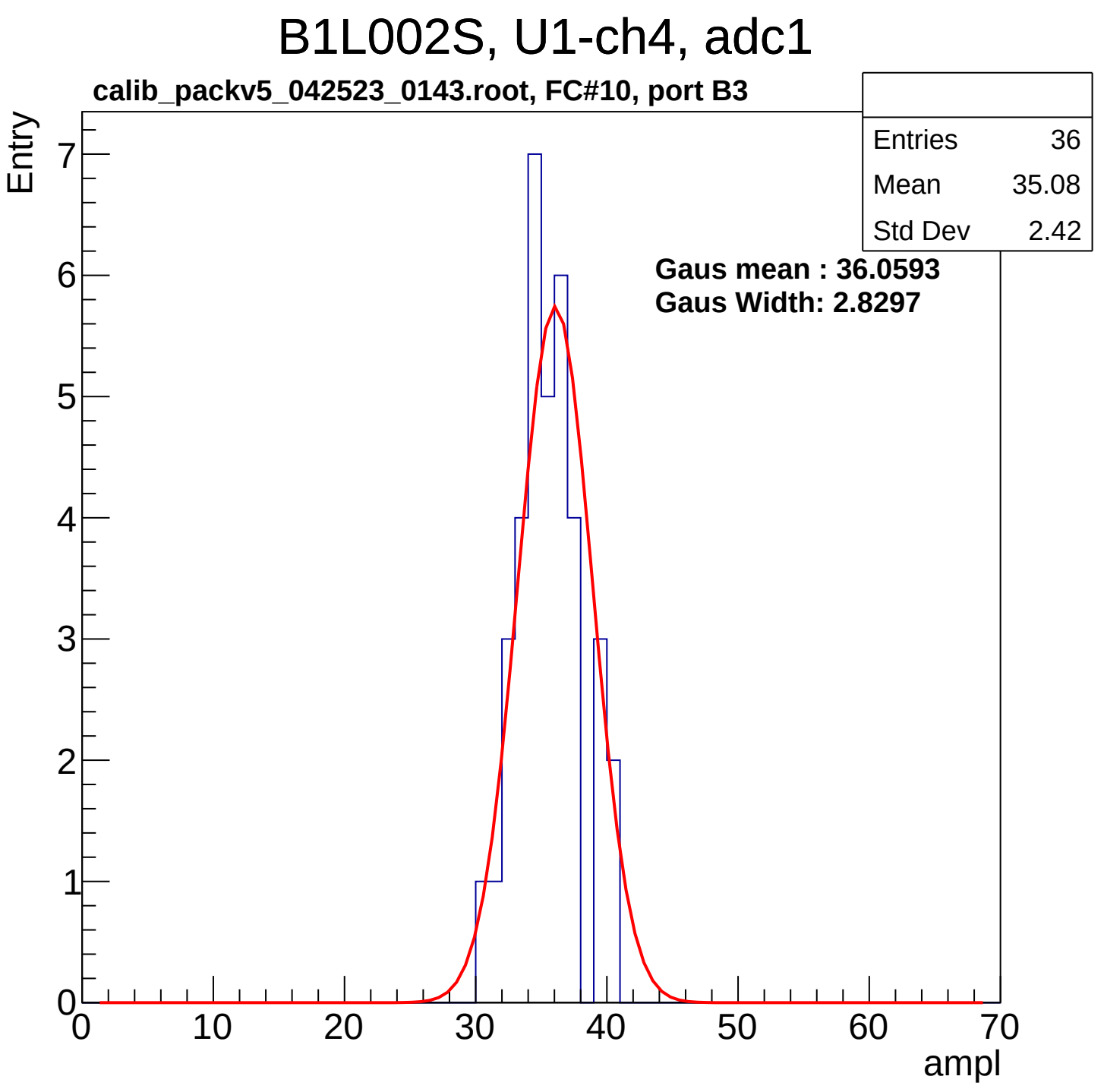
B1L002S, U1-ch4, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 35.08
Std Dev 2.42
Gaus mean : 36.0593
Gaus Width: 2.8297
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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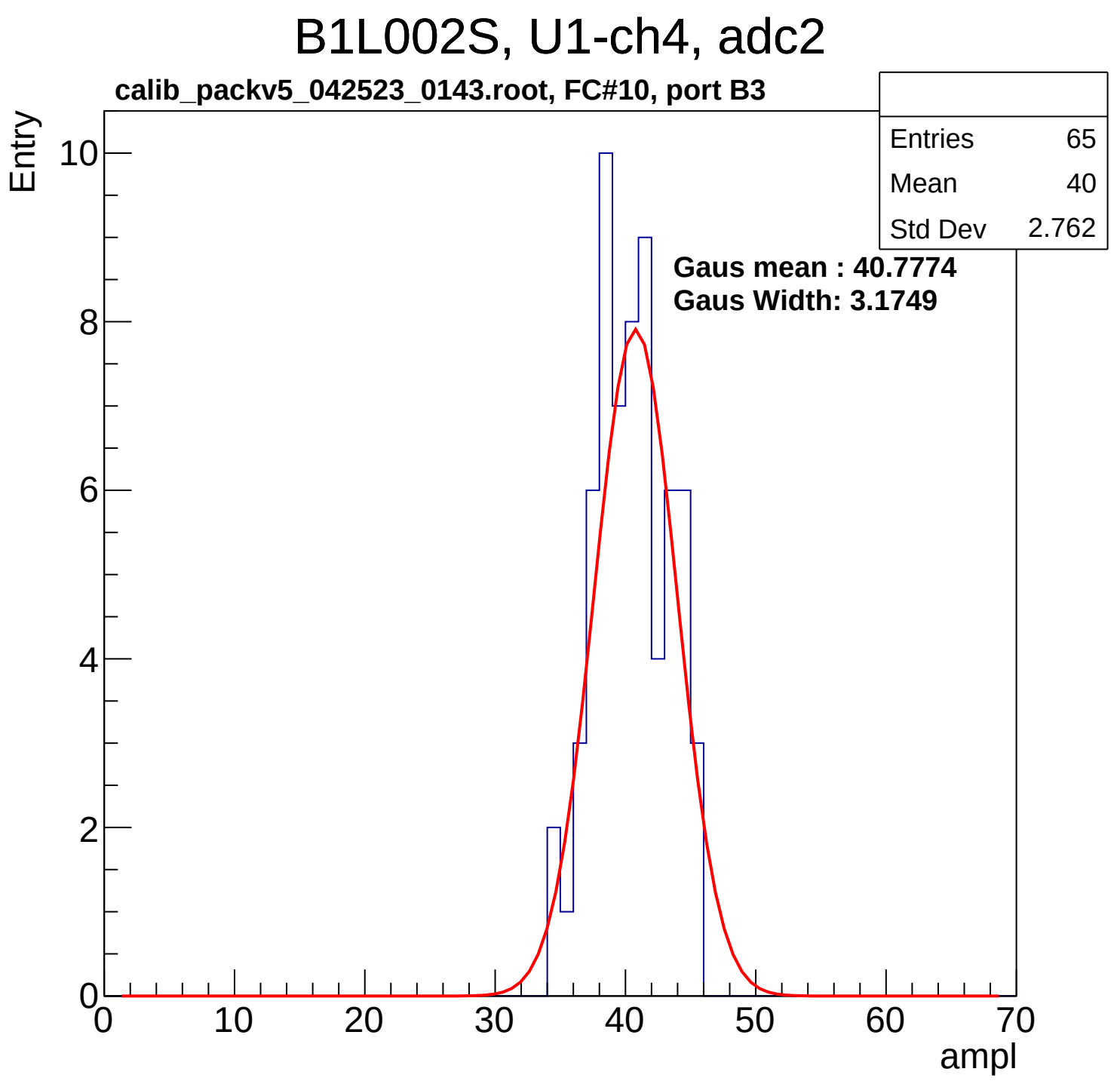
B1L002S, U1-ch4, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40
Std Dev 2.762
Gaus mean : 40.7774
Gaus Width: 3.1749
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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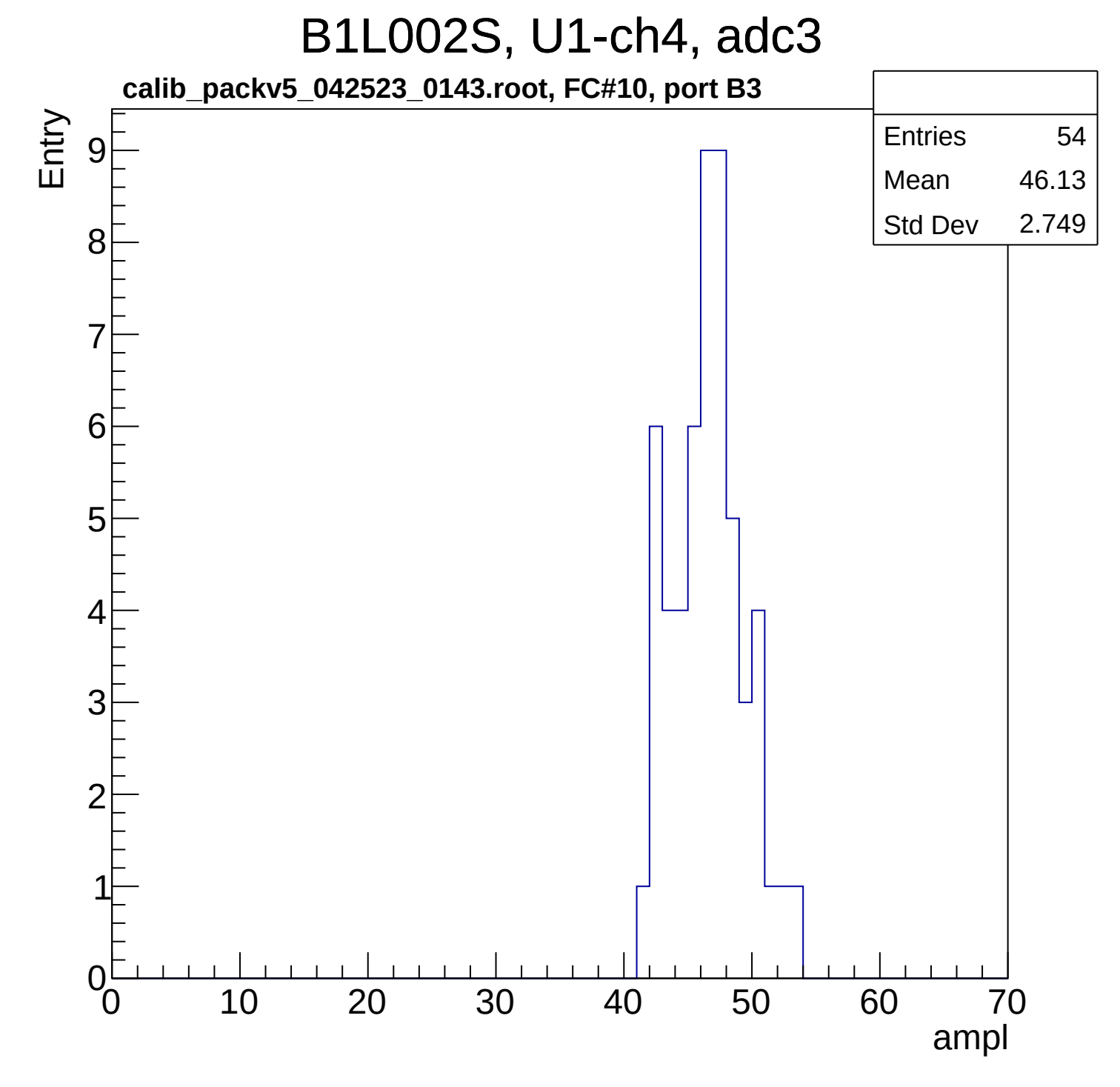
B1L002S, U1-ch4, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.13
Std Dev 2.749
Entry
ampl
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2
3
4
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6
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0
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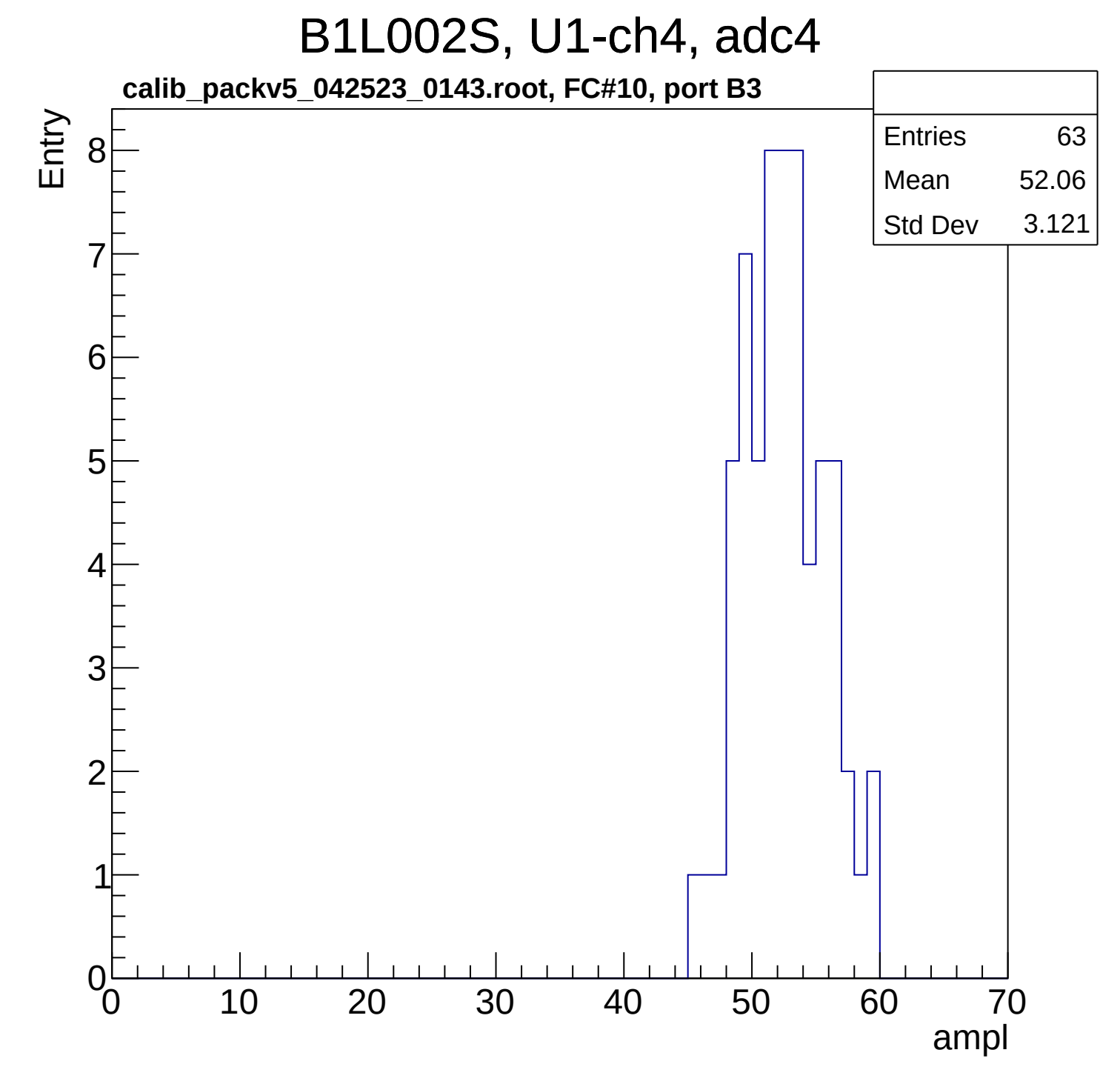
B1L002S, U1-ch4, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 52.06
Std Dev 3.121
Entry
ampl
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1
2
3
4
5
6
7
8
0
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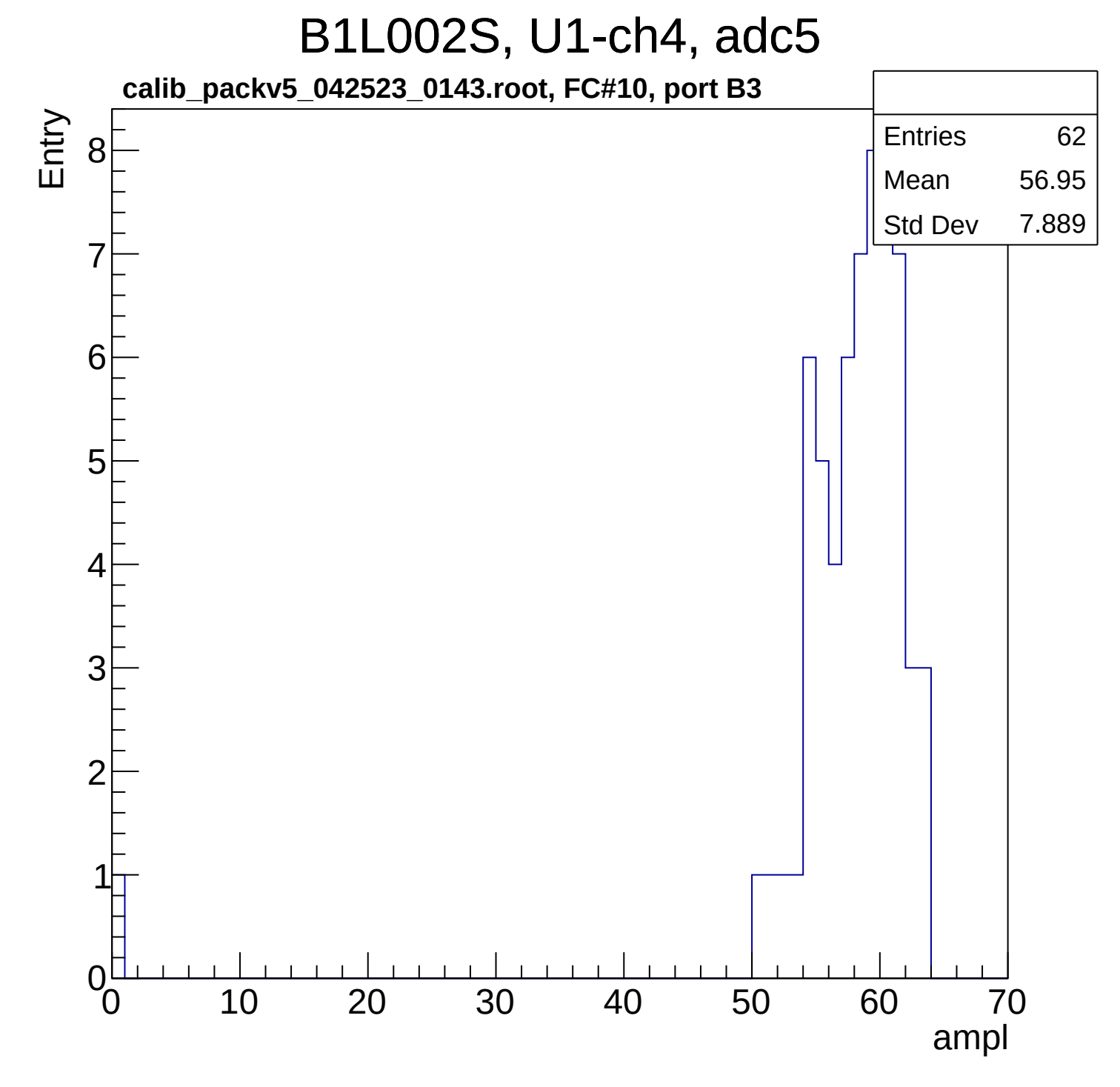
B1L002S, U1-ch4, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 56.95
Std Dev 7.889
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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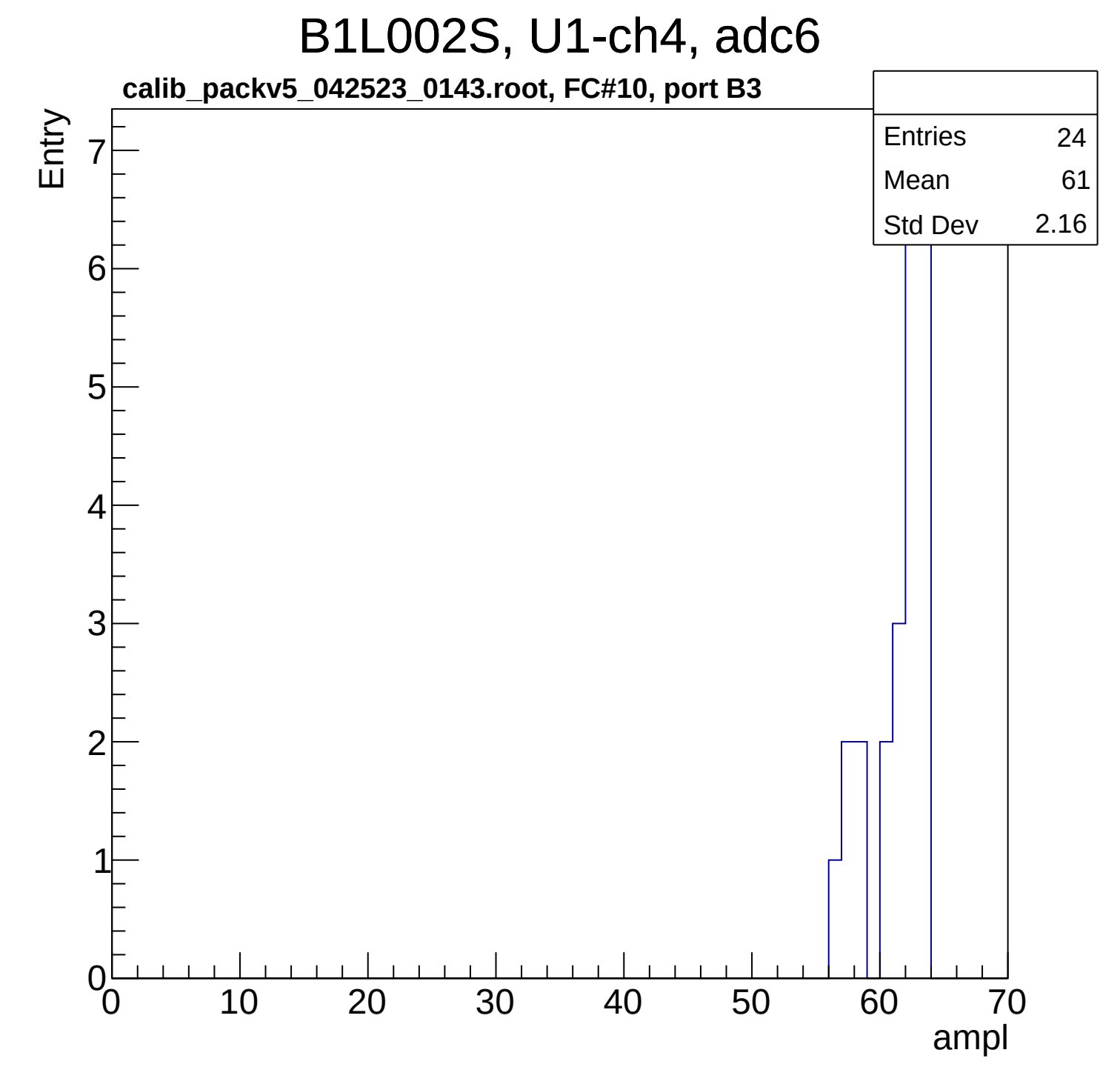
B1L002S, U1-ch4, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61
Std Dev 2.16
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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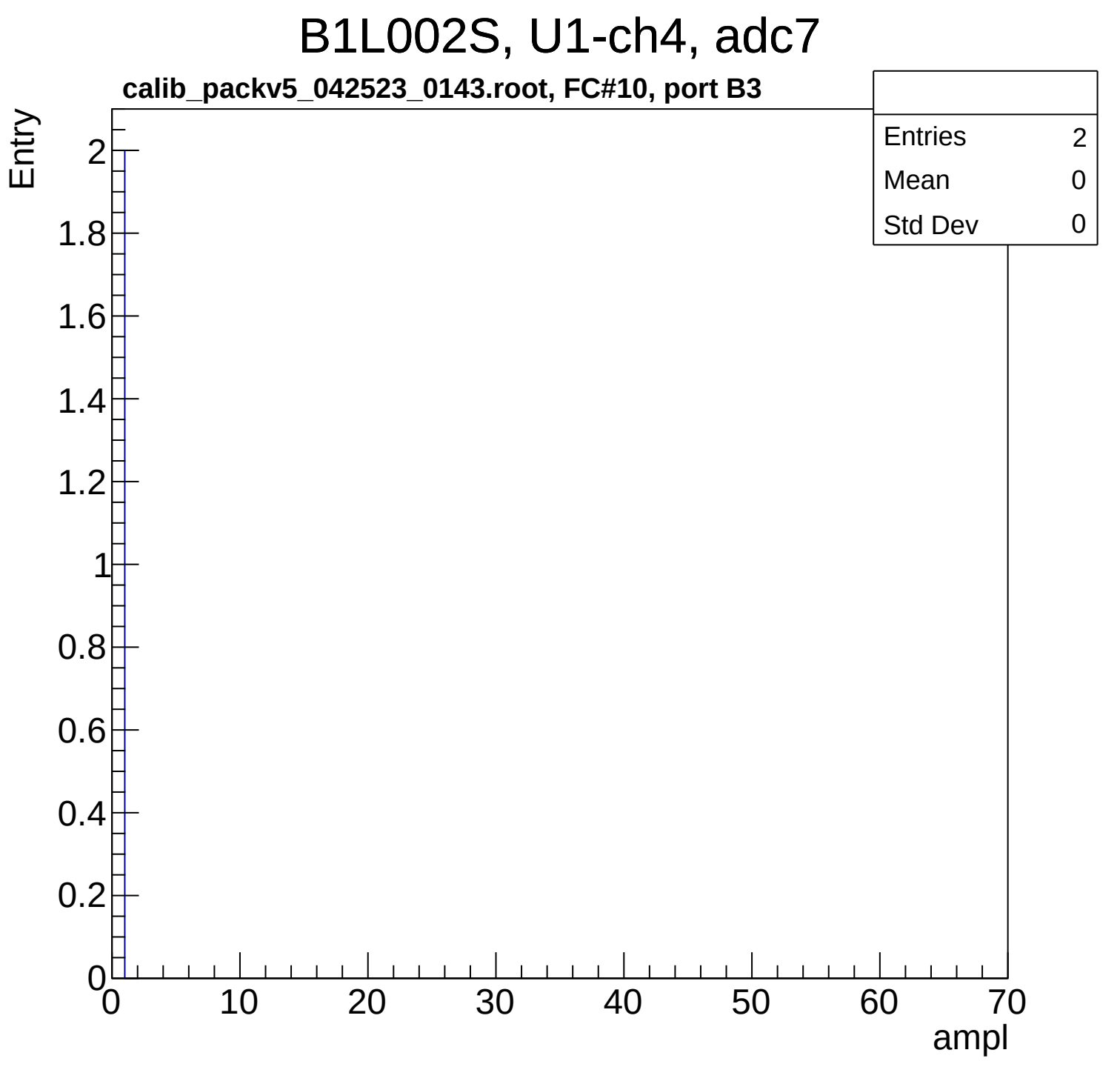
B1L002S, U1-ch4, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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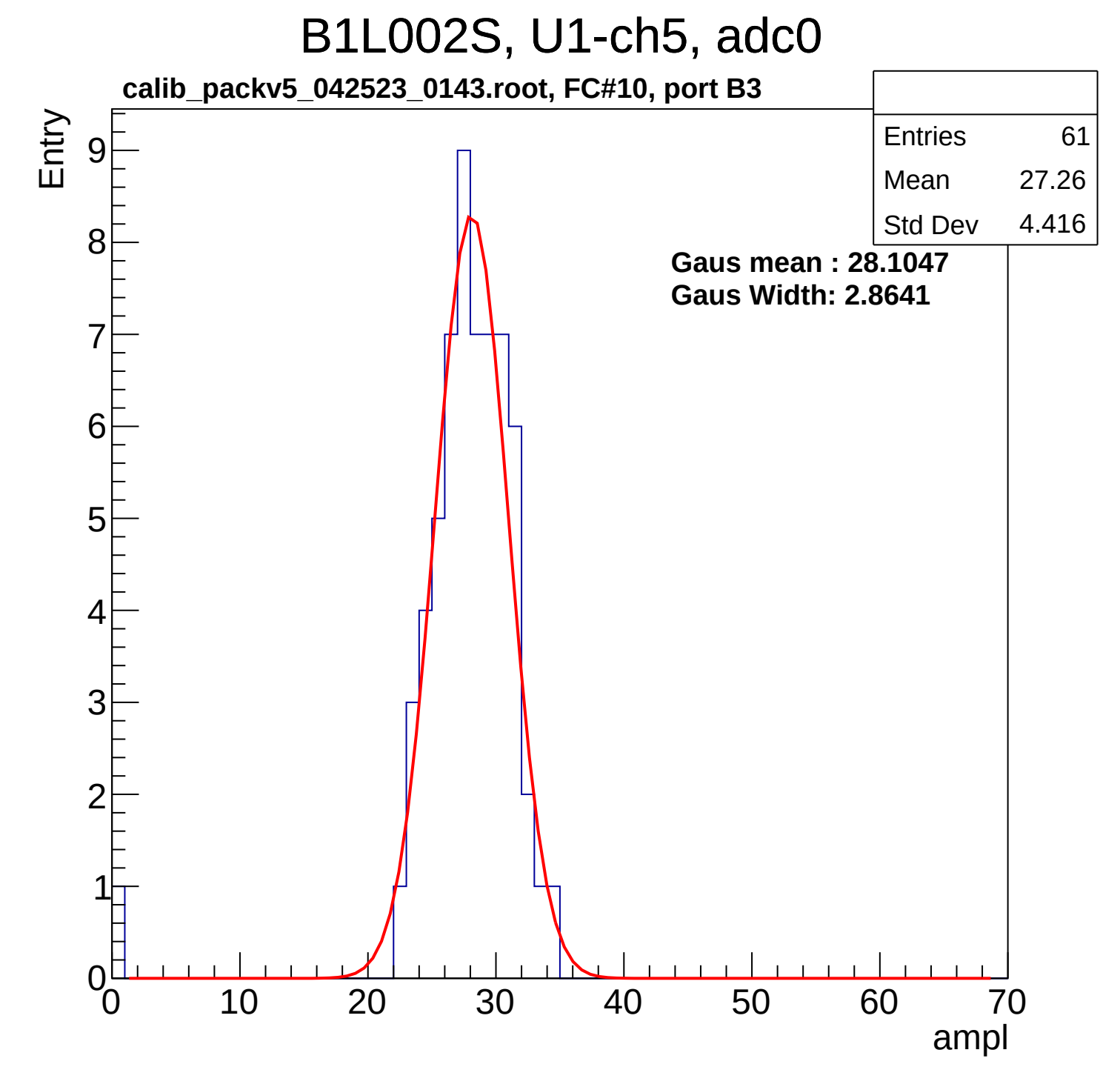
B1L002S, U1-ch5, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.26
Std Dev 4.416
Gaus mean : 28.1047
Gaus Width: 2.8641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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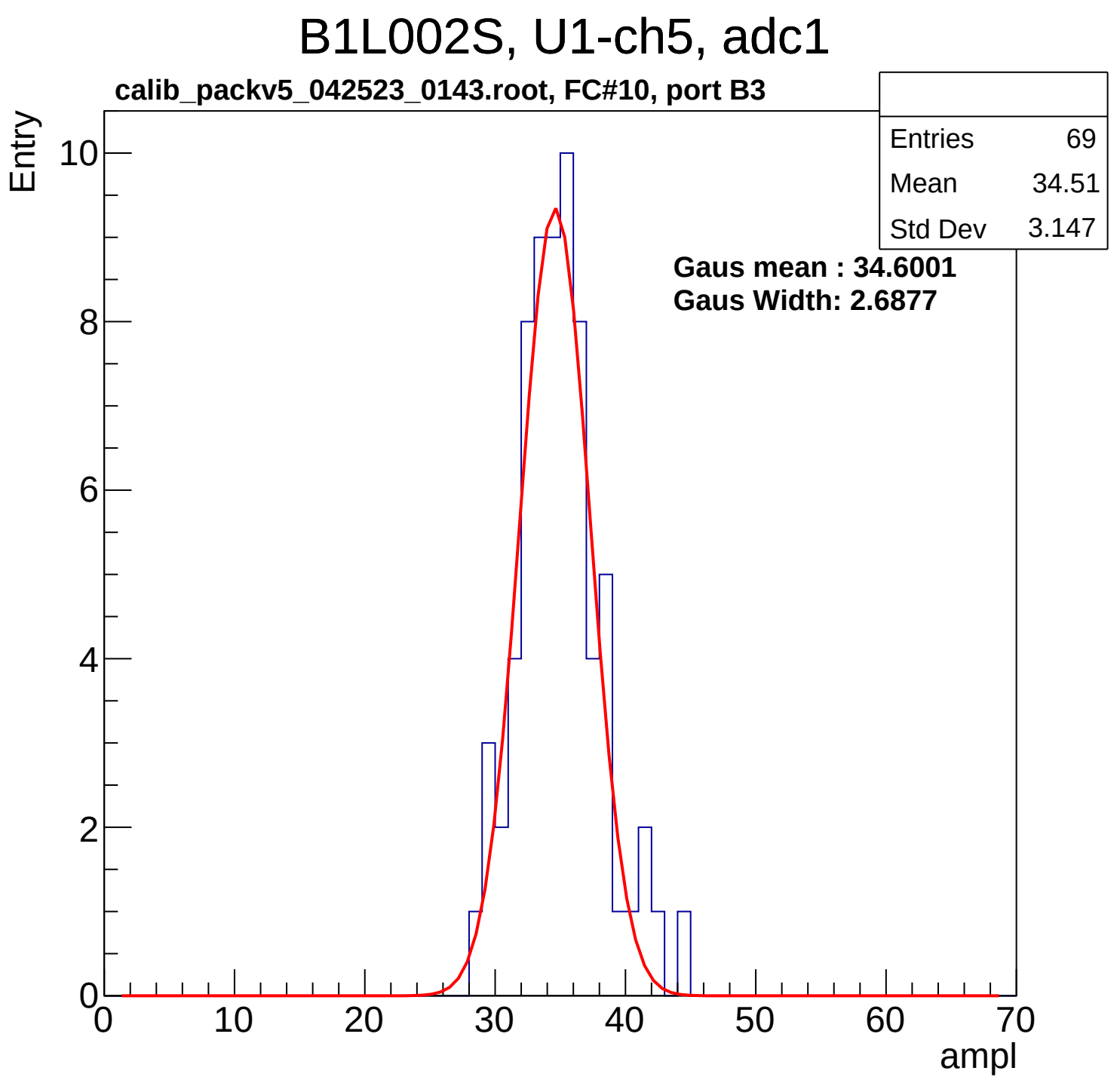
B1L002S, U1-ch5, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.51
Std Dev 3.147
Gaus mean : 34.6001
Gaus Width: 2.6877
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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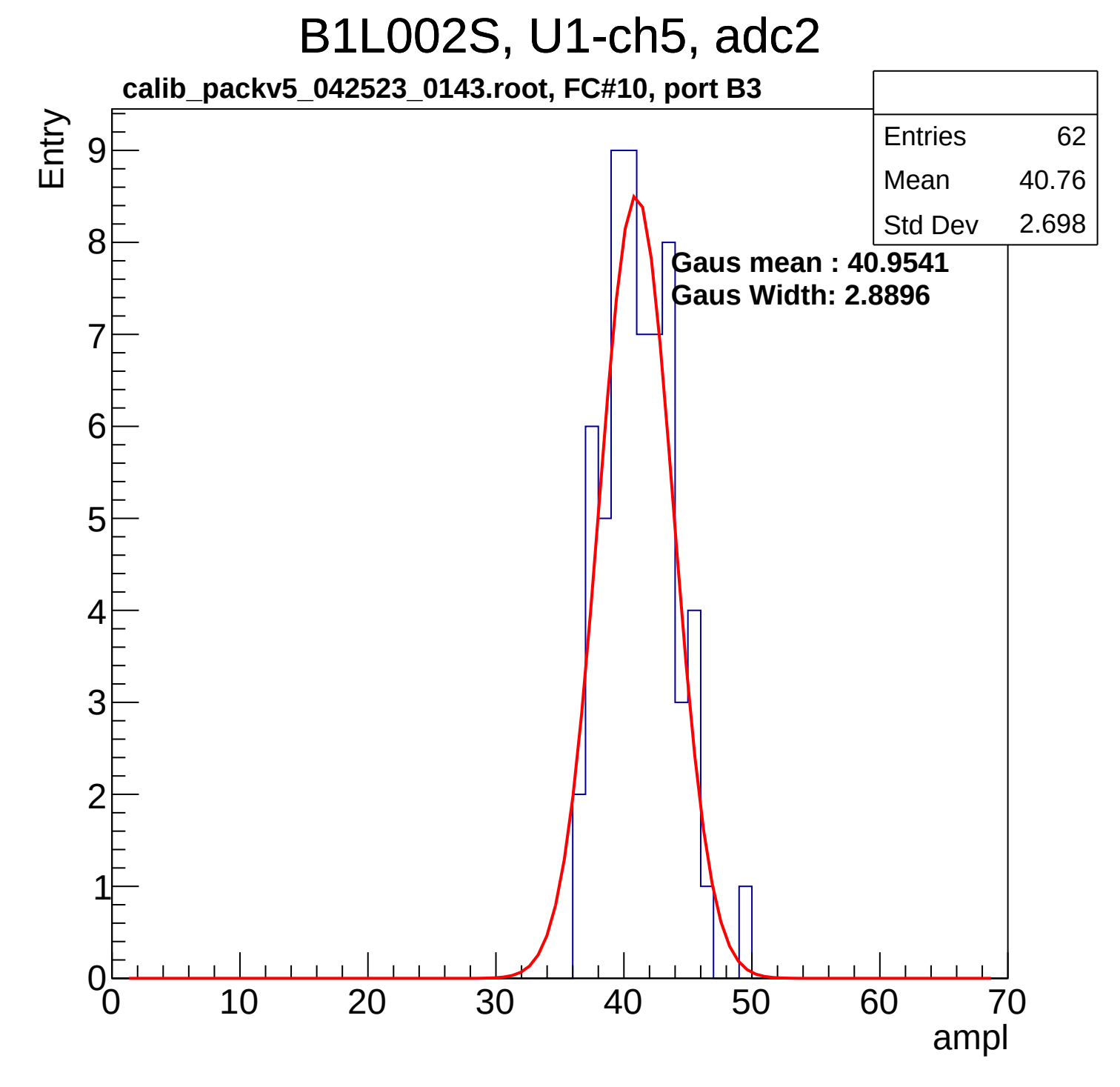
B1L002S, U1-ch5, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.76
Std Dev 2.698
Gaus mean : 40.9541
Gaus Width: 2.8896
Entry
ampl
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10
20
30
40
50
60
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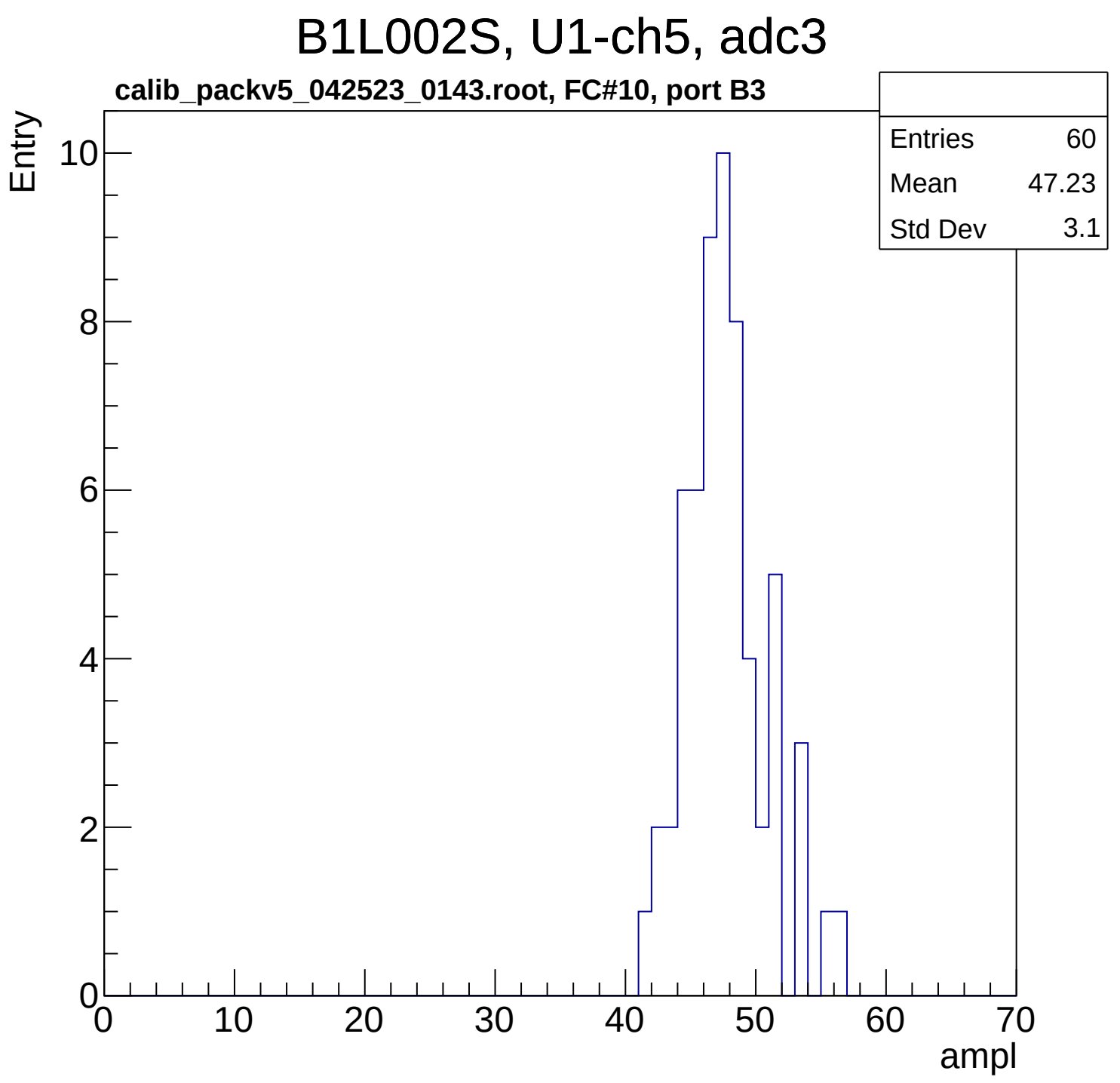
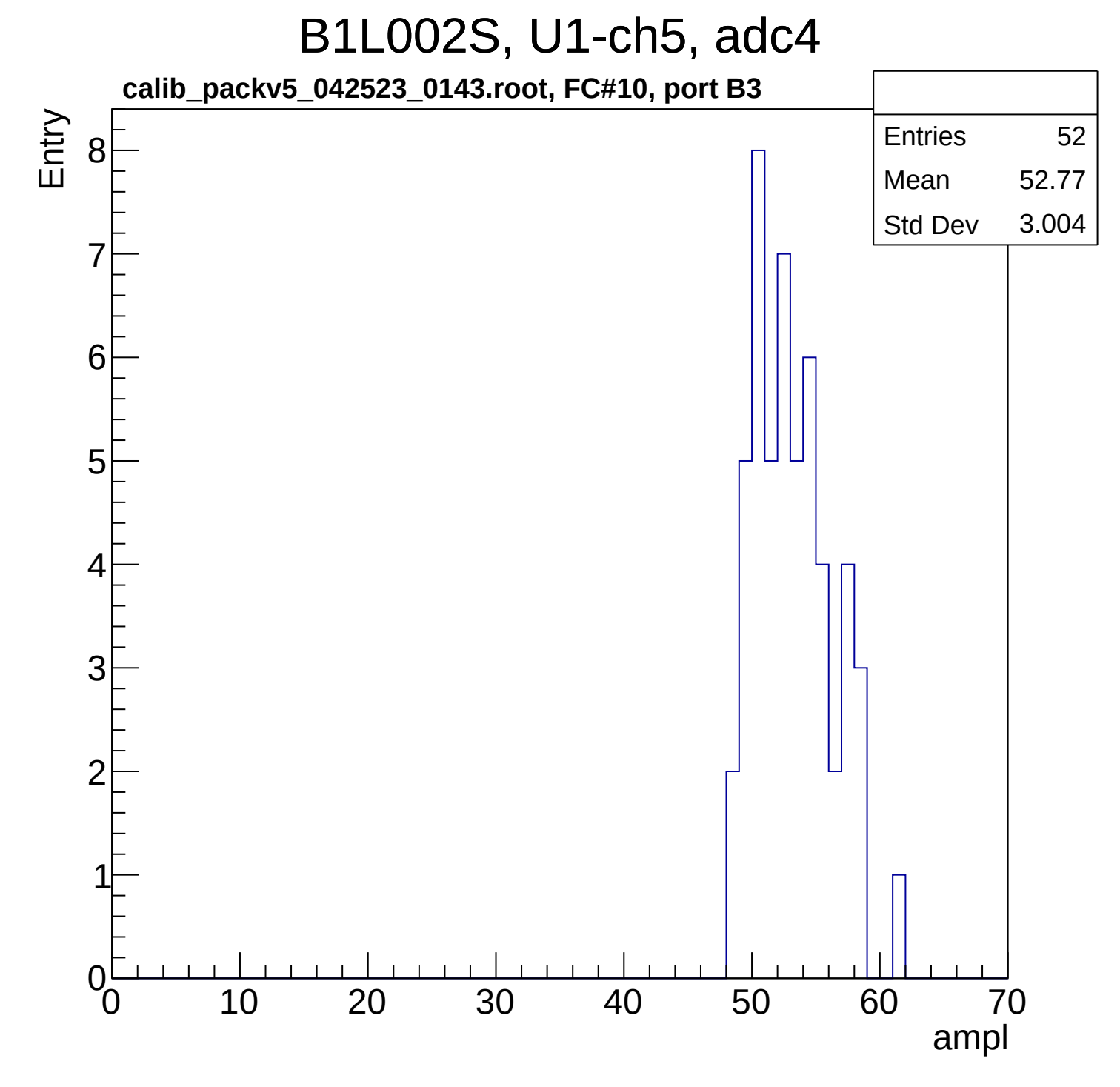
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch5, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.23
Std Dev 3.1
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch5, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.77
Std Dev 3.004
Entry
ampl
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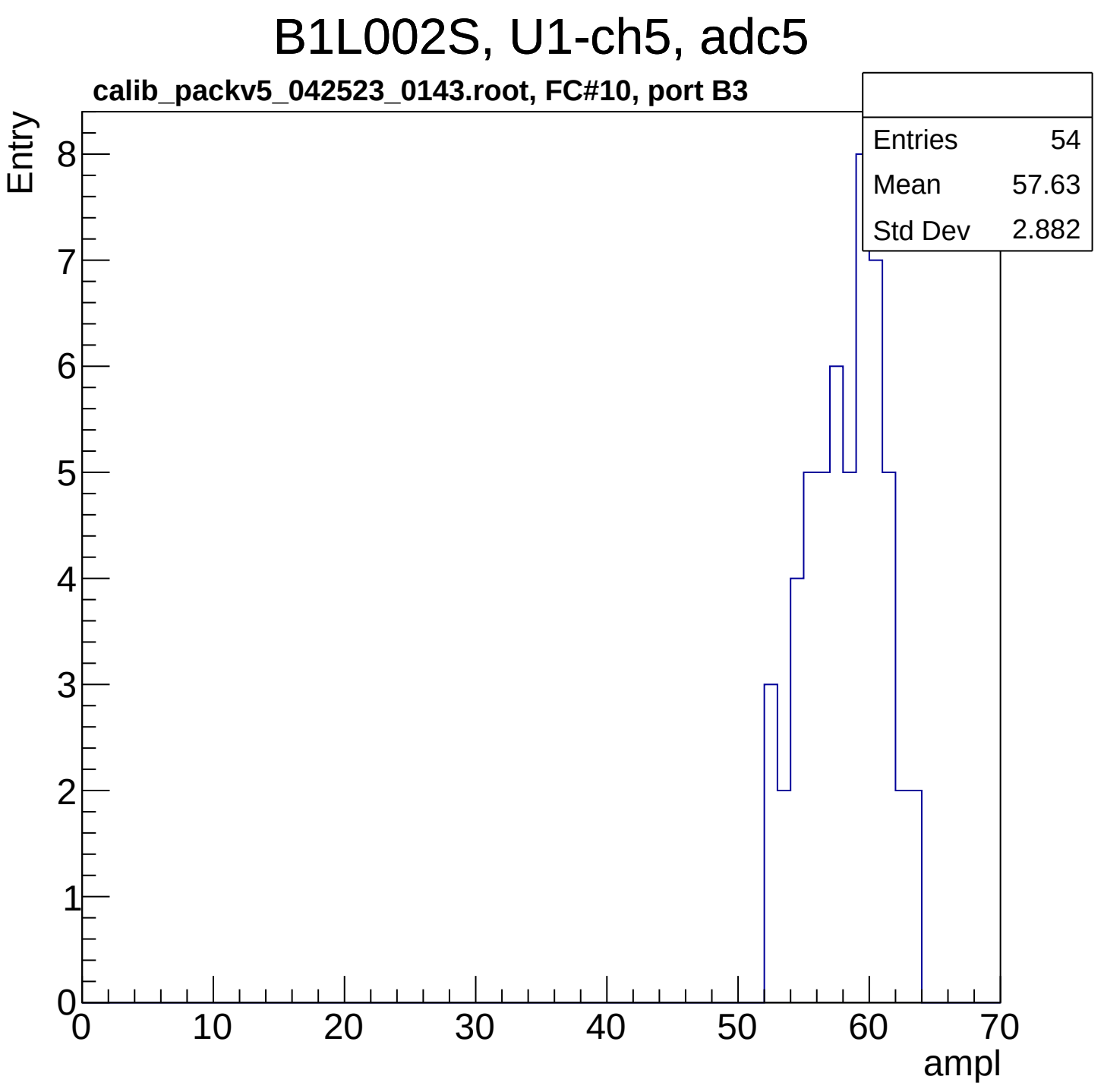
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch5, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.63
Std Dev 2.882
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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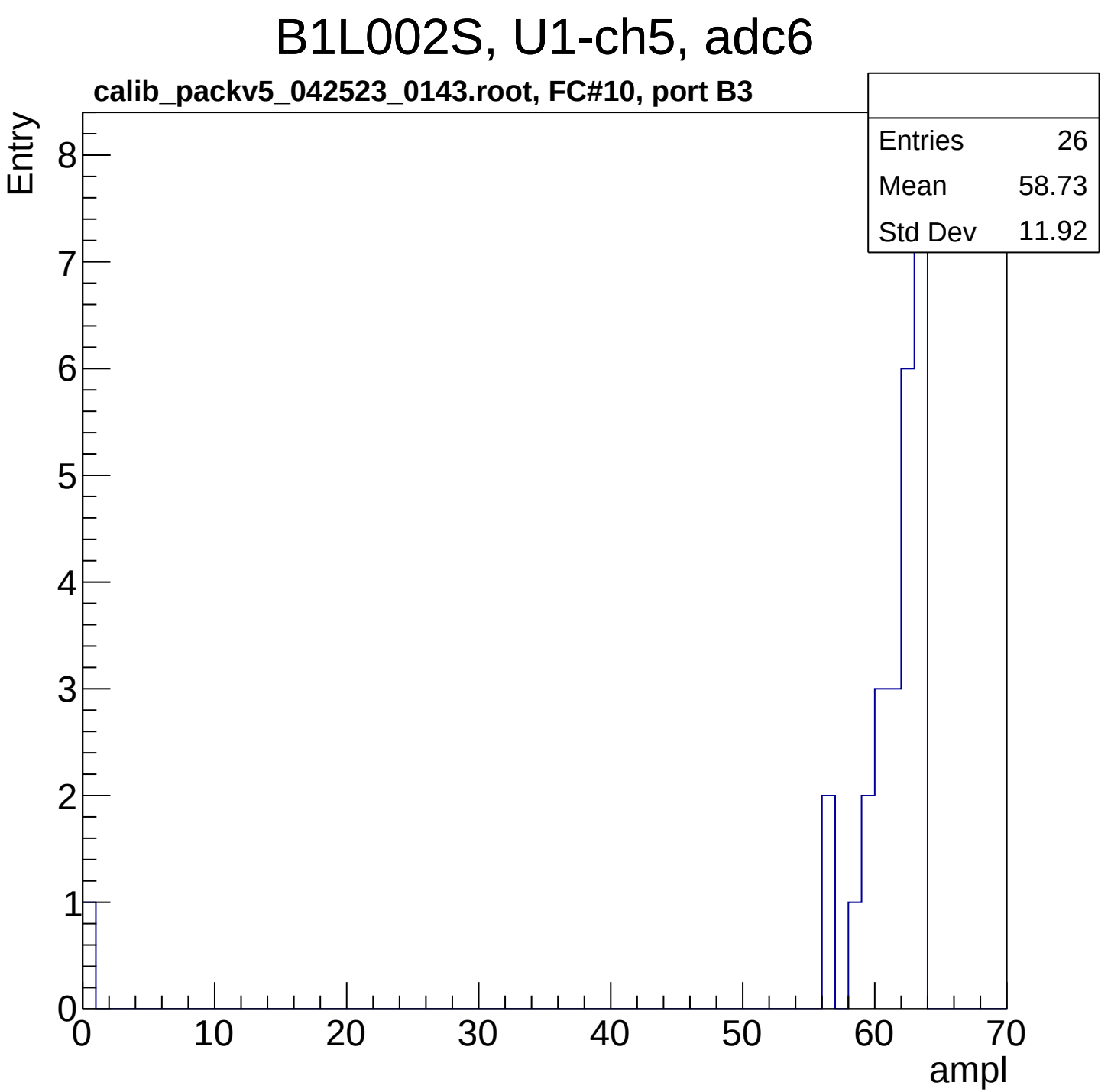
B1L002S, U1-ch5, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.73
Std Dev 11.92
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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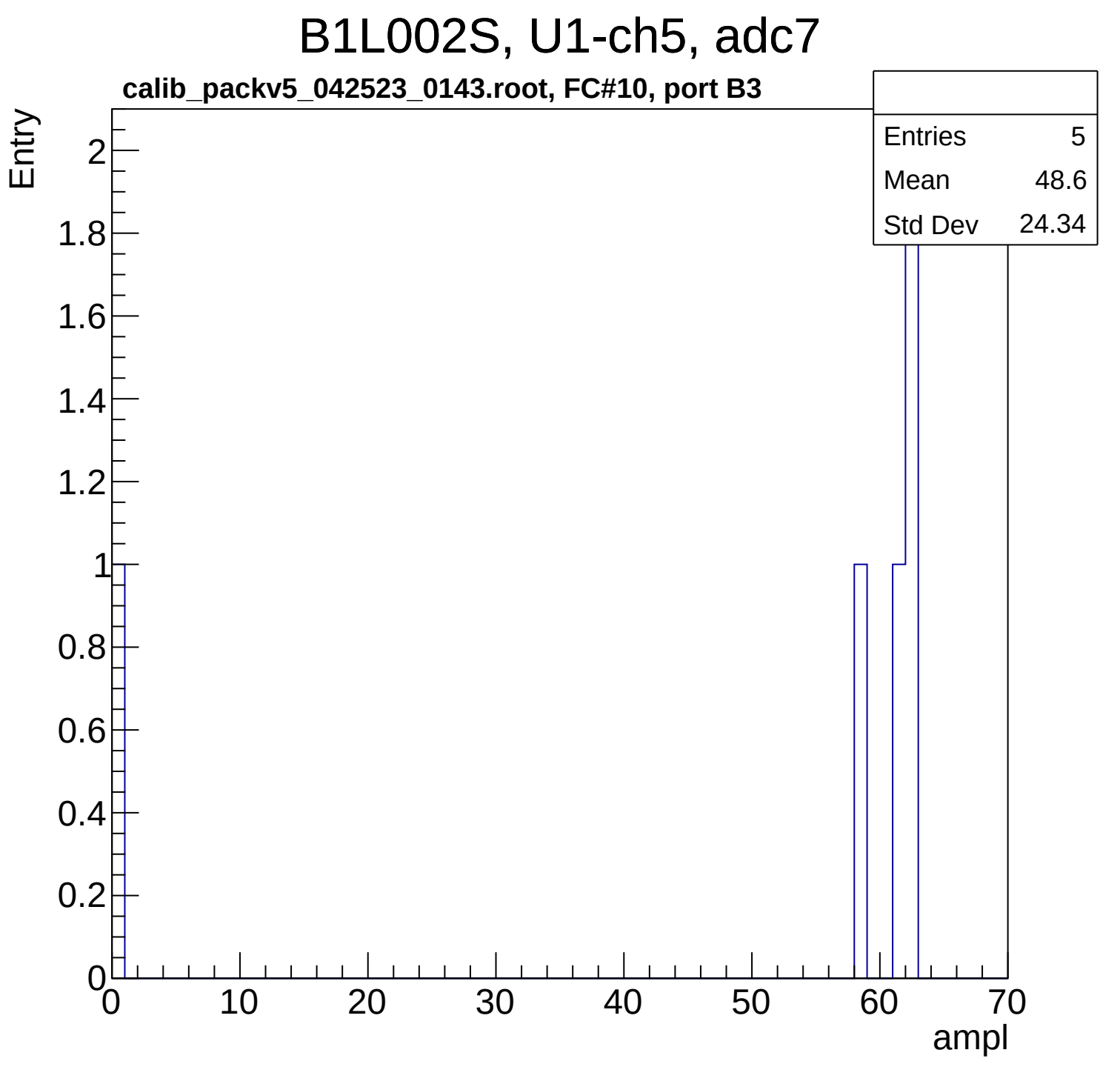
B1L002S, U1-ch5, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 48.6
Std Dev 24.34
Entry
ampl
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0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
50
60
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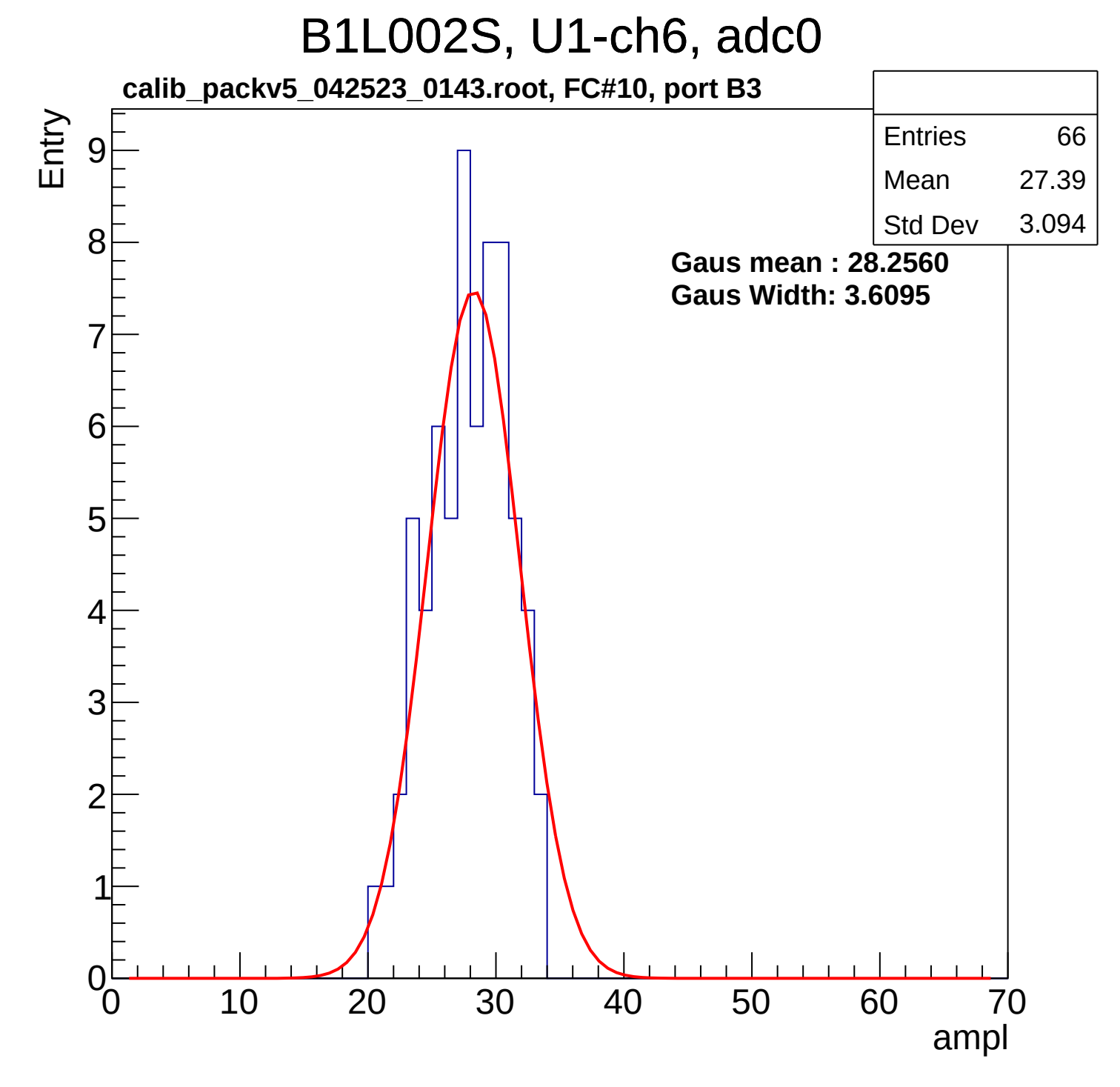
B1L002S, U1-ch6, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 27.39
Std Dev 3.094
Gaus mean : 28.2560
Gaus Width: 3.6095
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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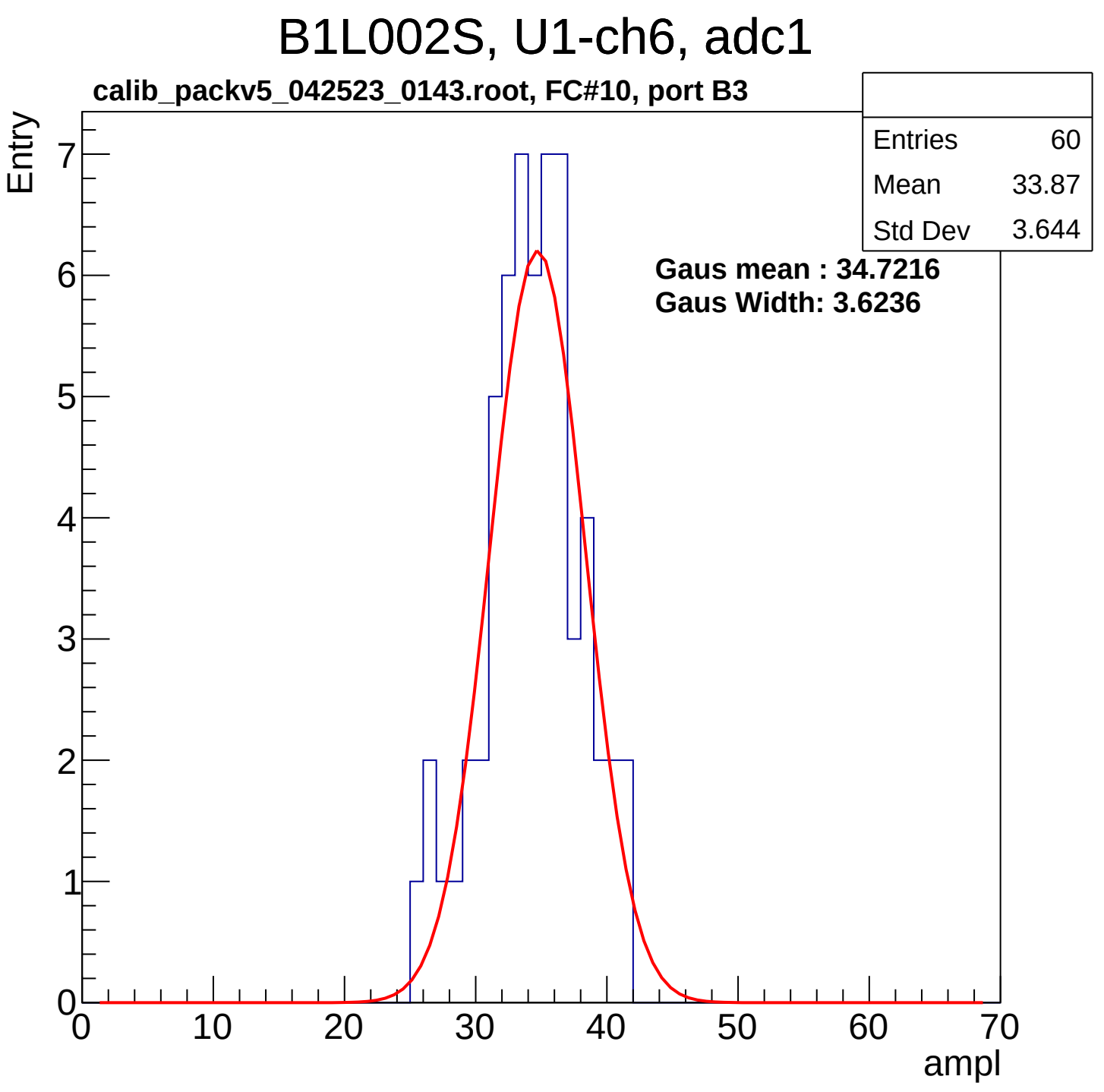
B1L002S, U1-ch6, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 33.87
Std Dev 3.644
Gaus mean : 34.7216
Gaus Width: 3.6236
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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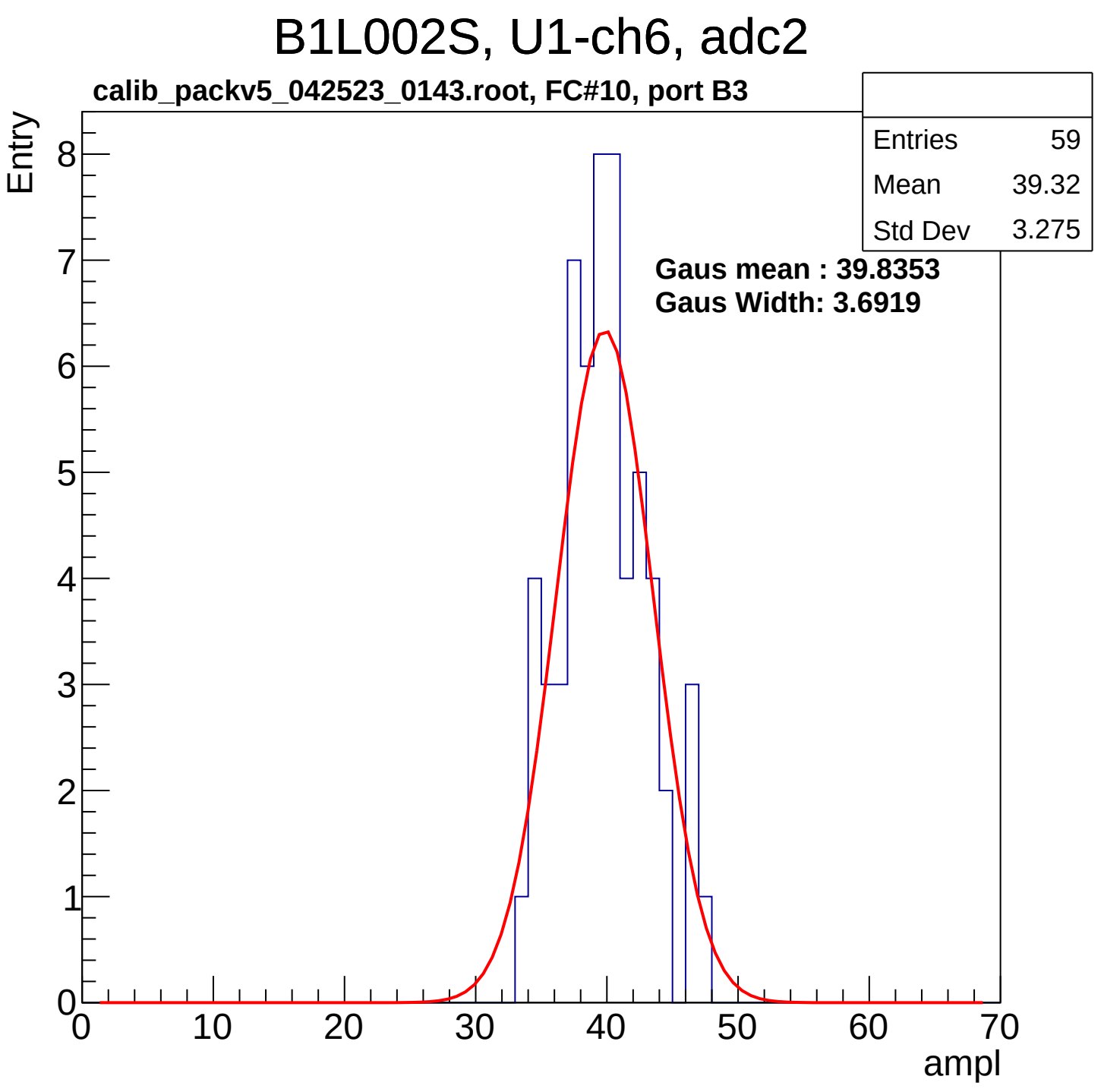
B1L002S, U1-ch6, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 39.32
Std Dev 3.275
Gaus mean : 39.8353
Gaus Width: 3.6919
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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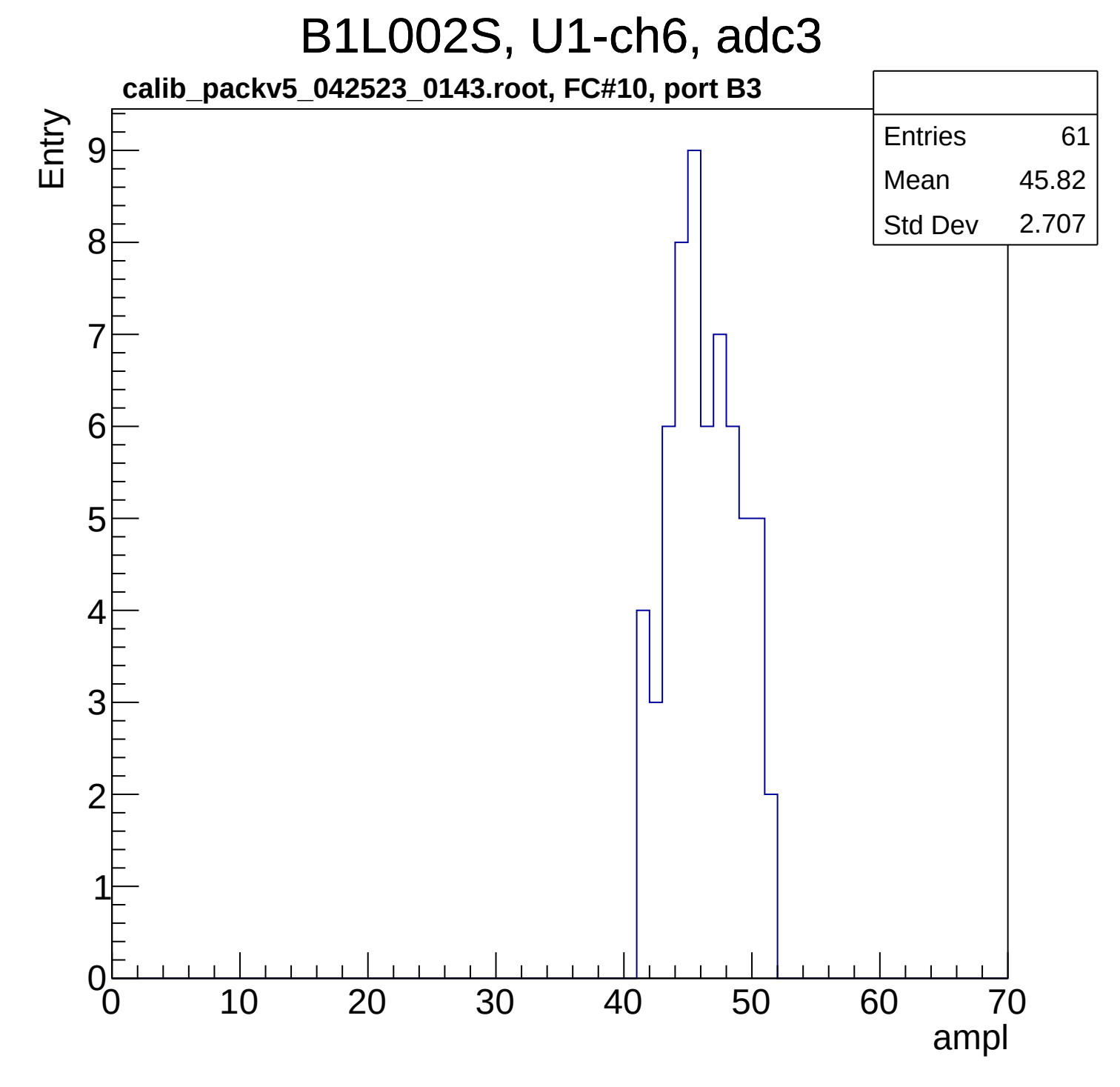
B1L002S, U1-ch6, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 45.82
Std Dev 2.707
Entry
ampl
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2
3
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5
6
7
8
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0
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20
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60
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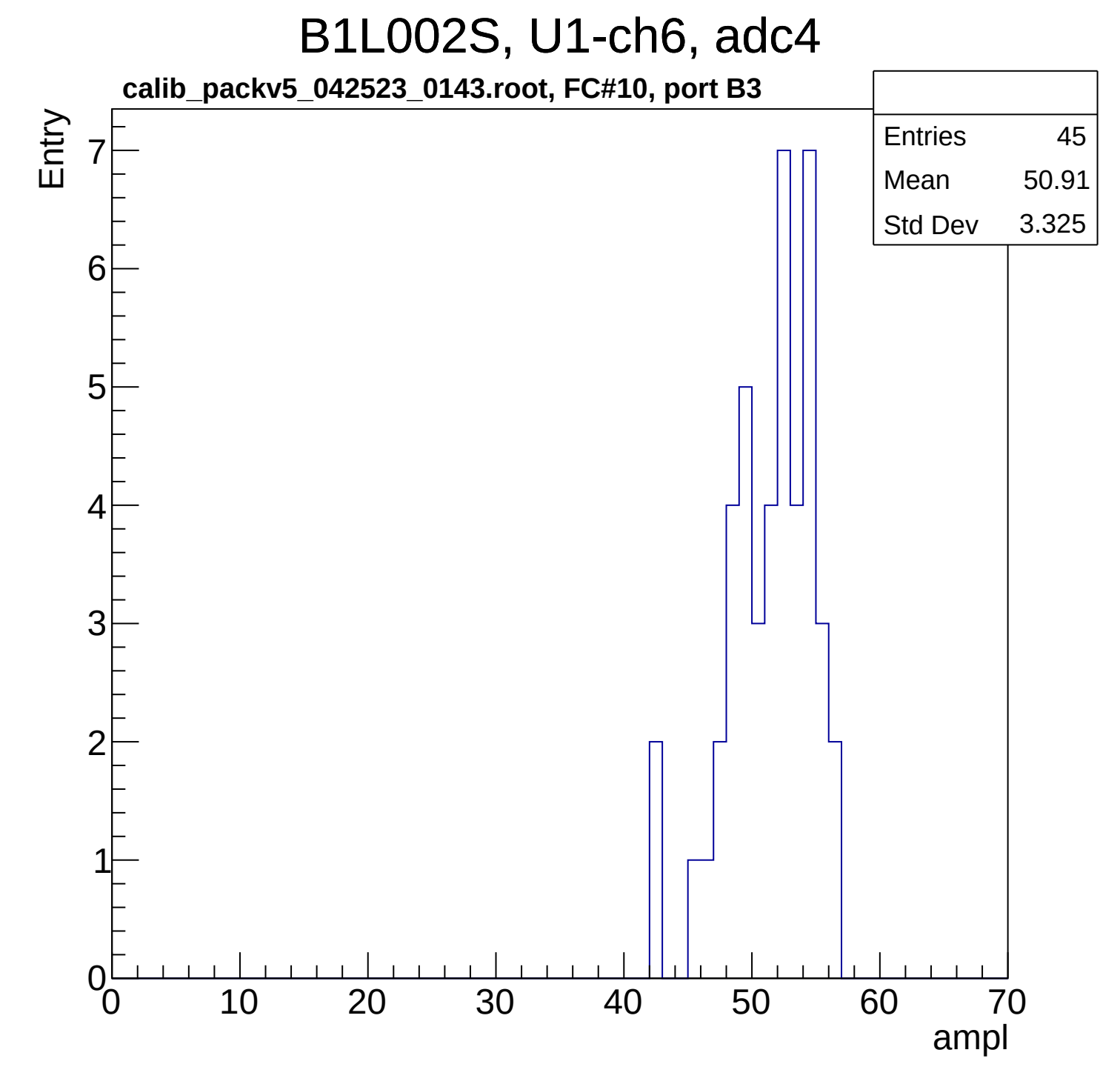
B1L002S, U1-ch6, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 50.91
Std Dev 3.325
Entry
ampl
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3
4
5
6
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0
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40
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60
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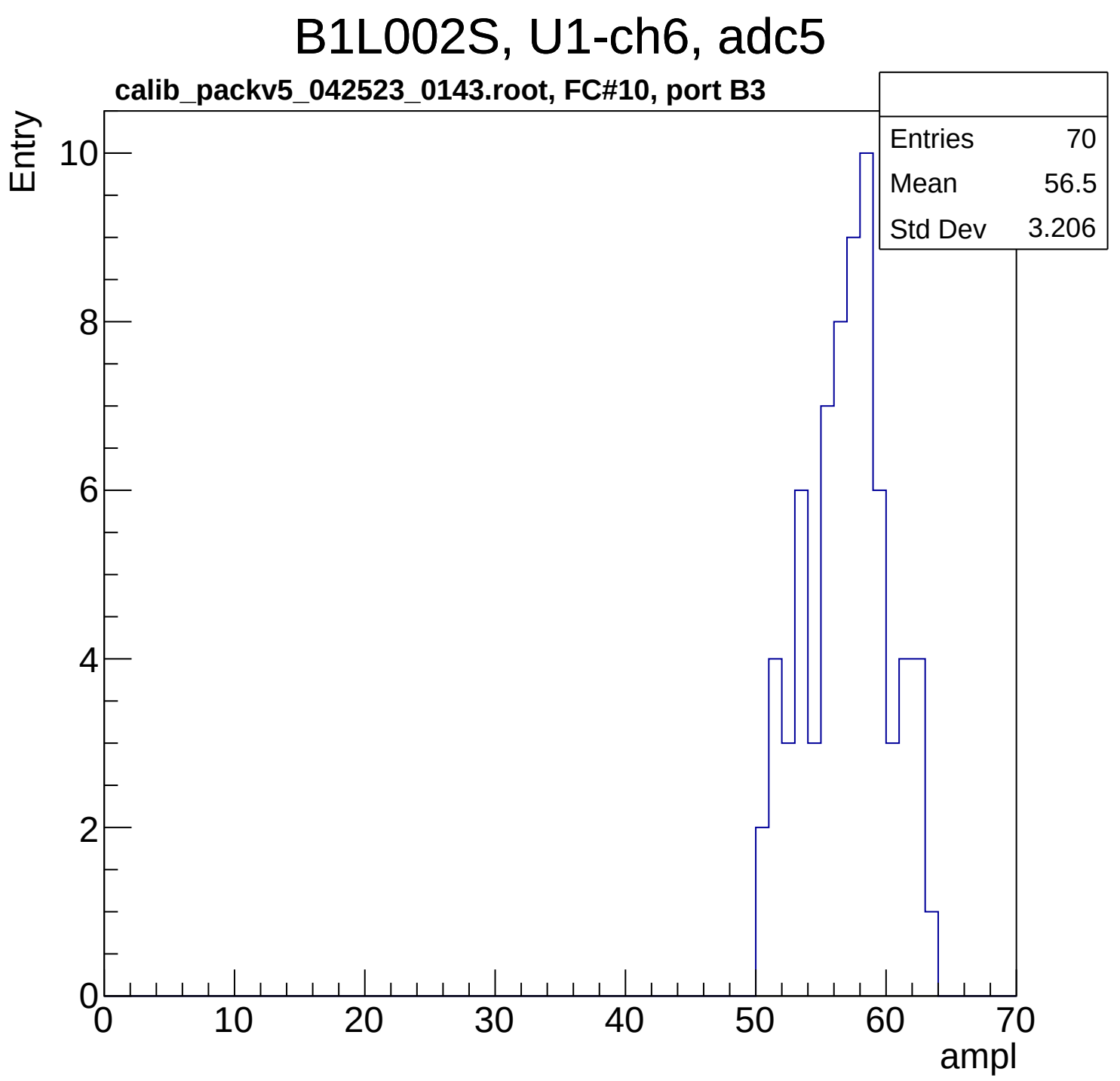
B1L002S, U1-ch6, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 56.5
Std Dev 3.206
Entry
0
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4
6
8
10
0
10
20
30
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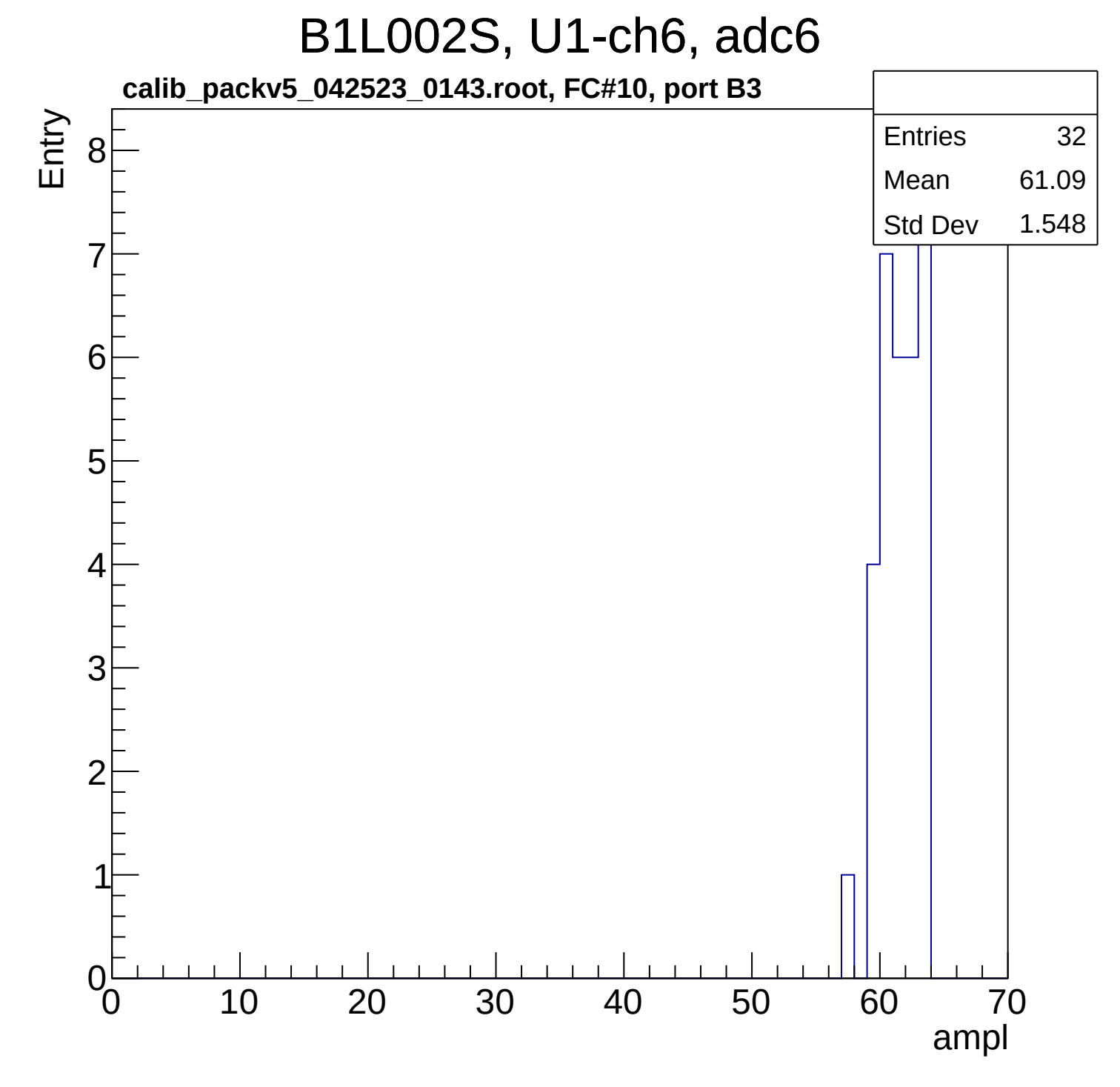
50
60
70
ampl


B1L002S, U1-ch6, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 61.09
Std Dev 1.548
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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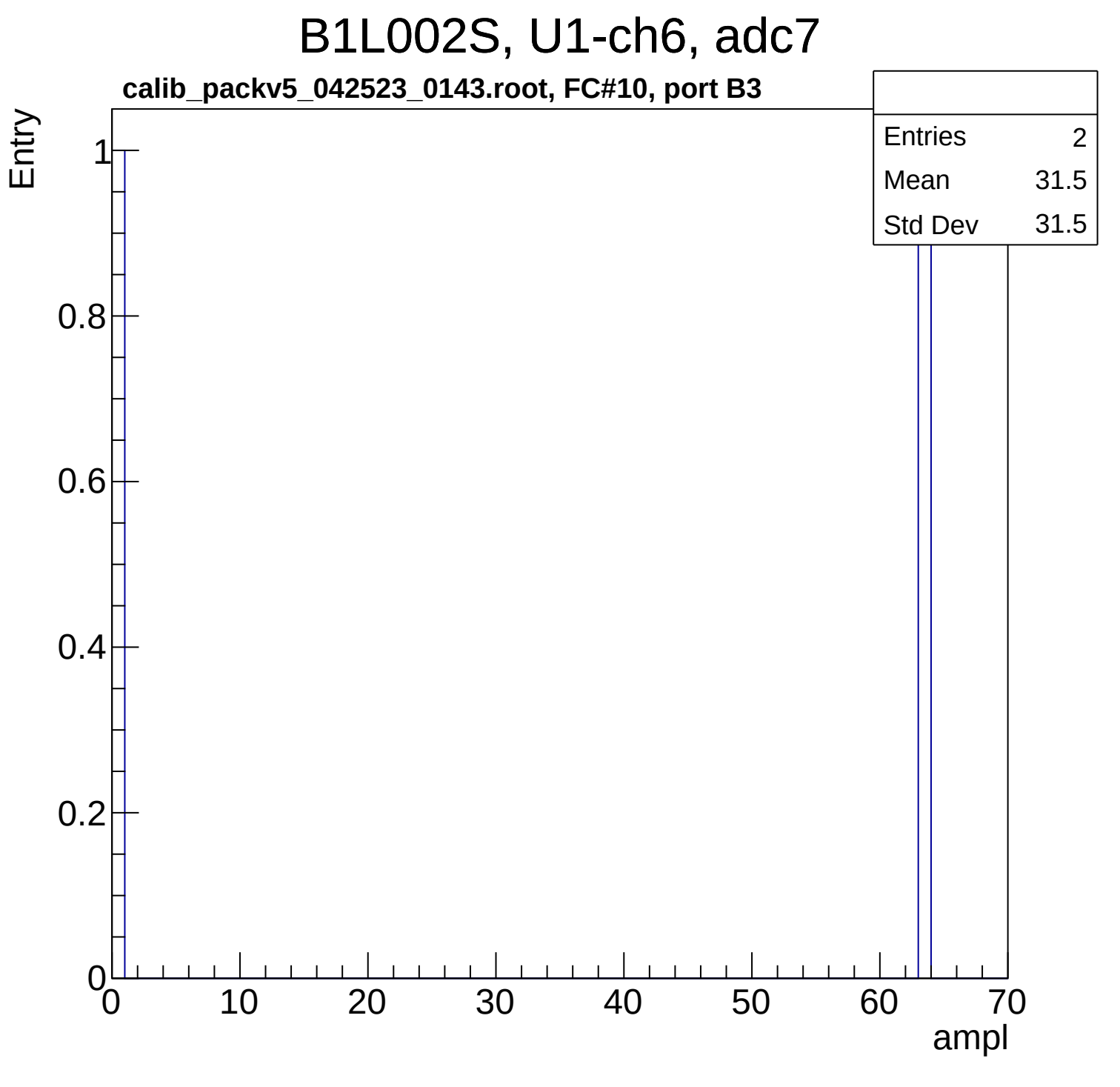
B1L002S, U1-ch6, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
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0
10
20
30
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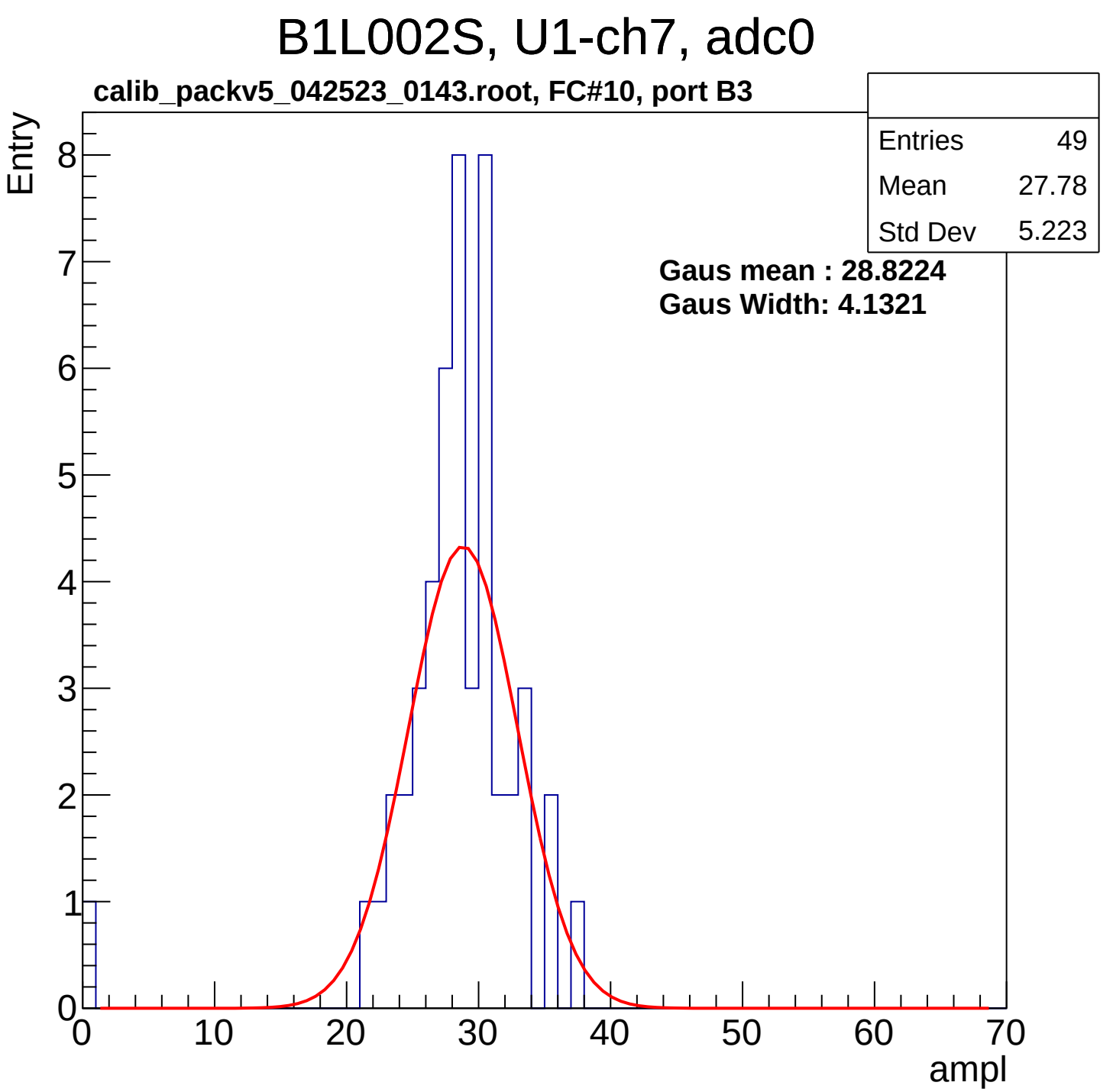
50
60
70
ampl


B1L002S, U1-ch7, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 27.78
Std Dev 5.223
Gaus mean : 28.8224
Gaus Width: 4.1321
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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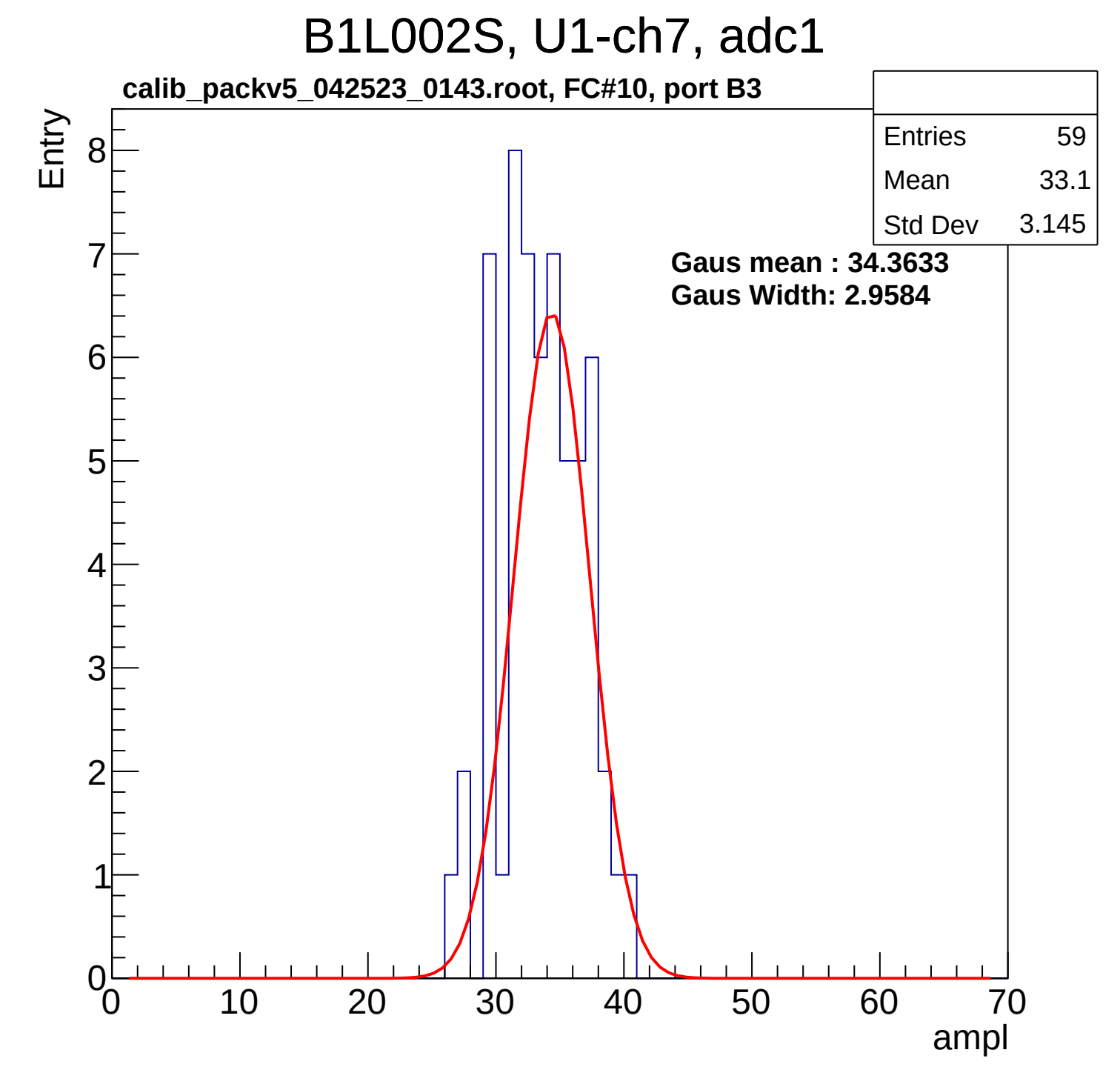
B1L002S, U1-ch7, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 33.1
Std Dev 3.145
Gaus mean : 34.3633
Gaus Width: 2.9584
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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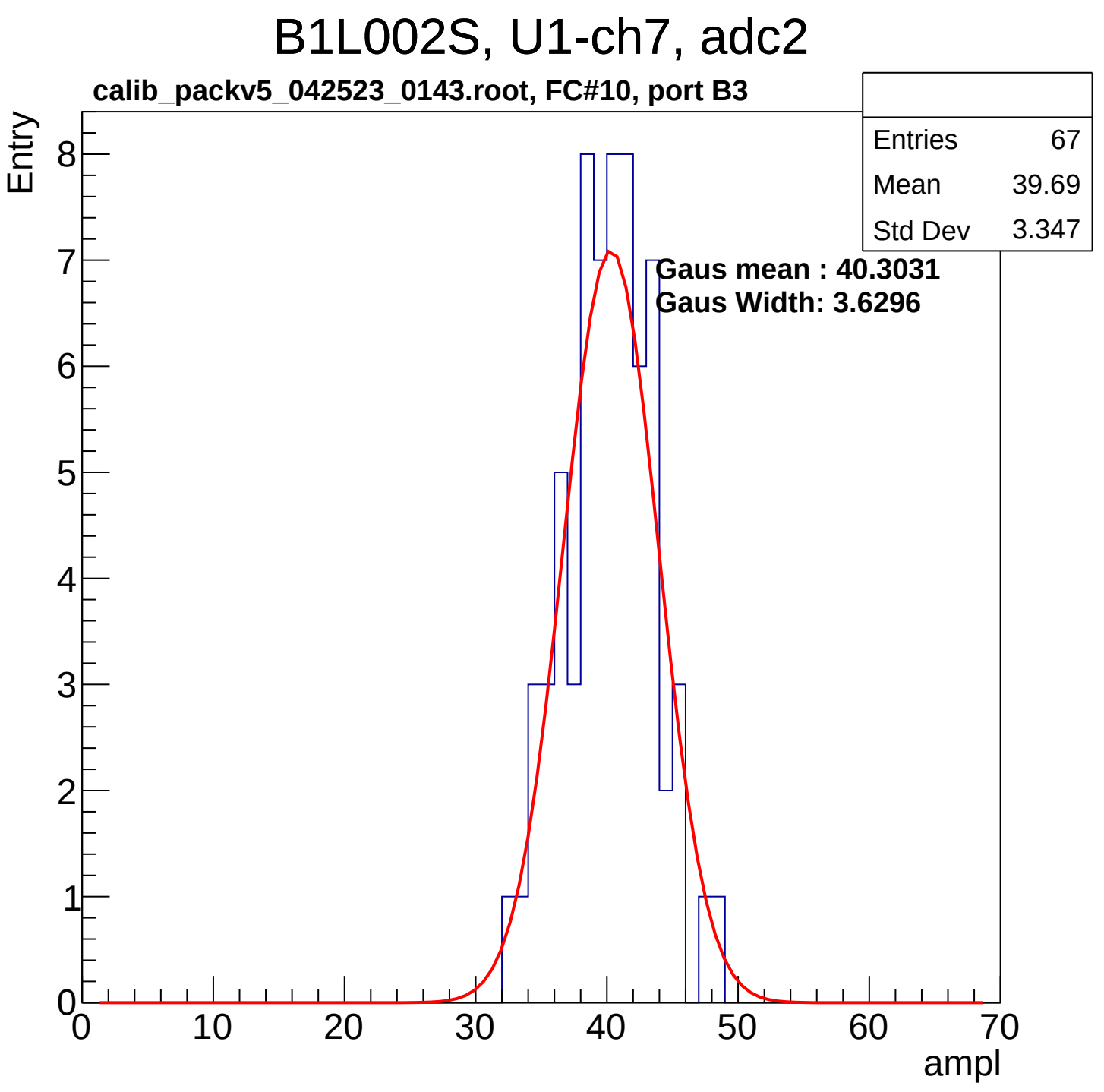
B1L002S, U1-ch7, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 39.69
Std Dev 3.347
Gaus mean : 40.3031
Gaus Width: 3.6296
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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50
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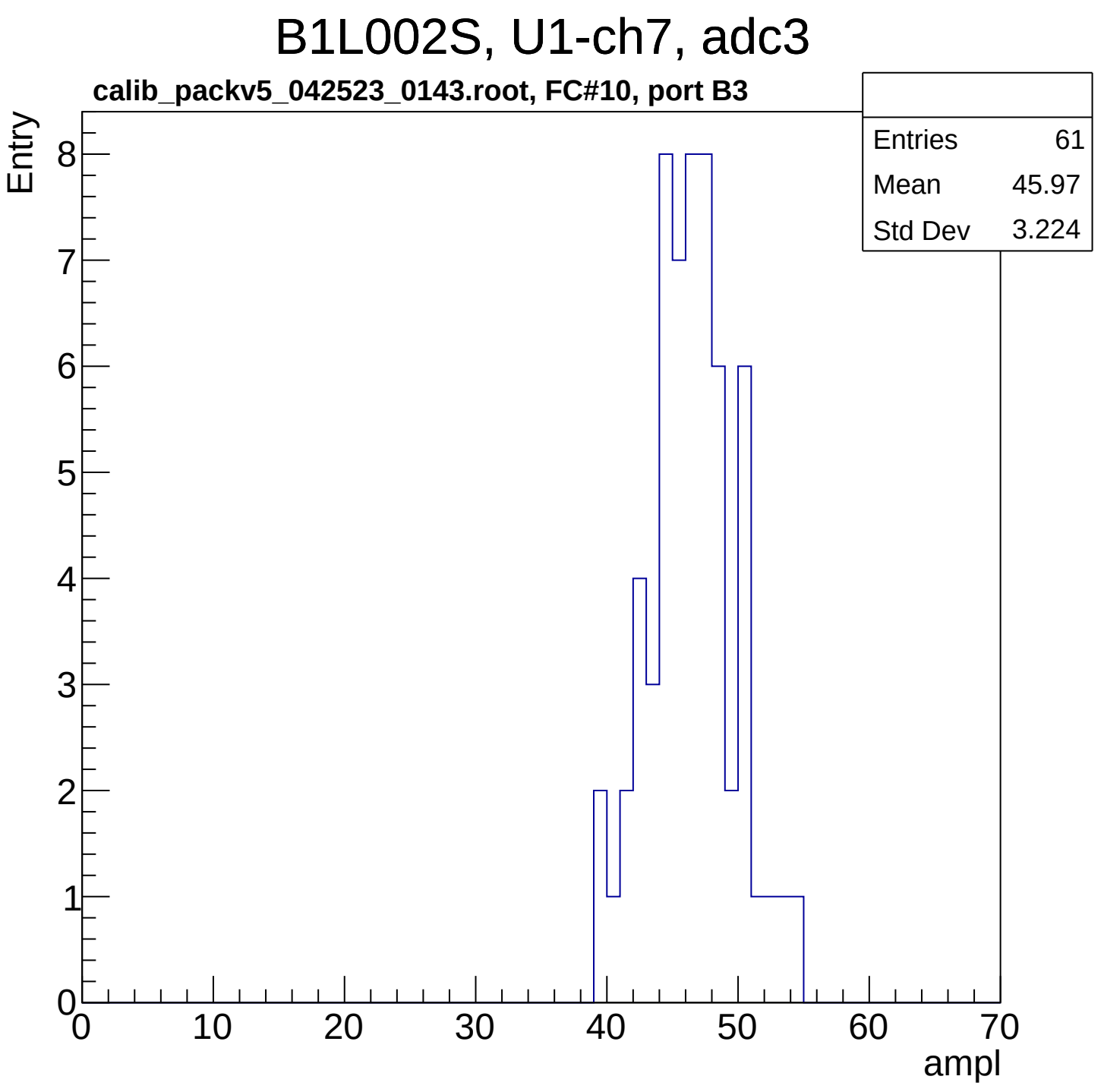
B1L002S, U1-ch7, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 45.97
Std Dev 3.224
Entry
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6
7
8
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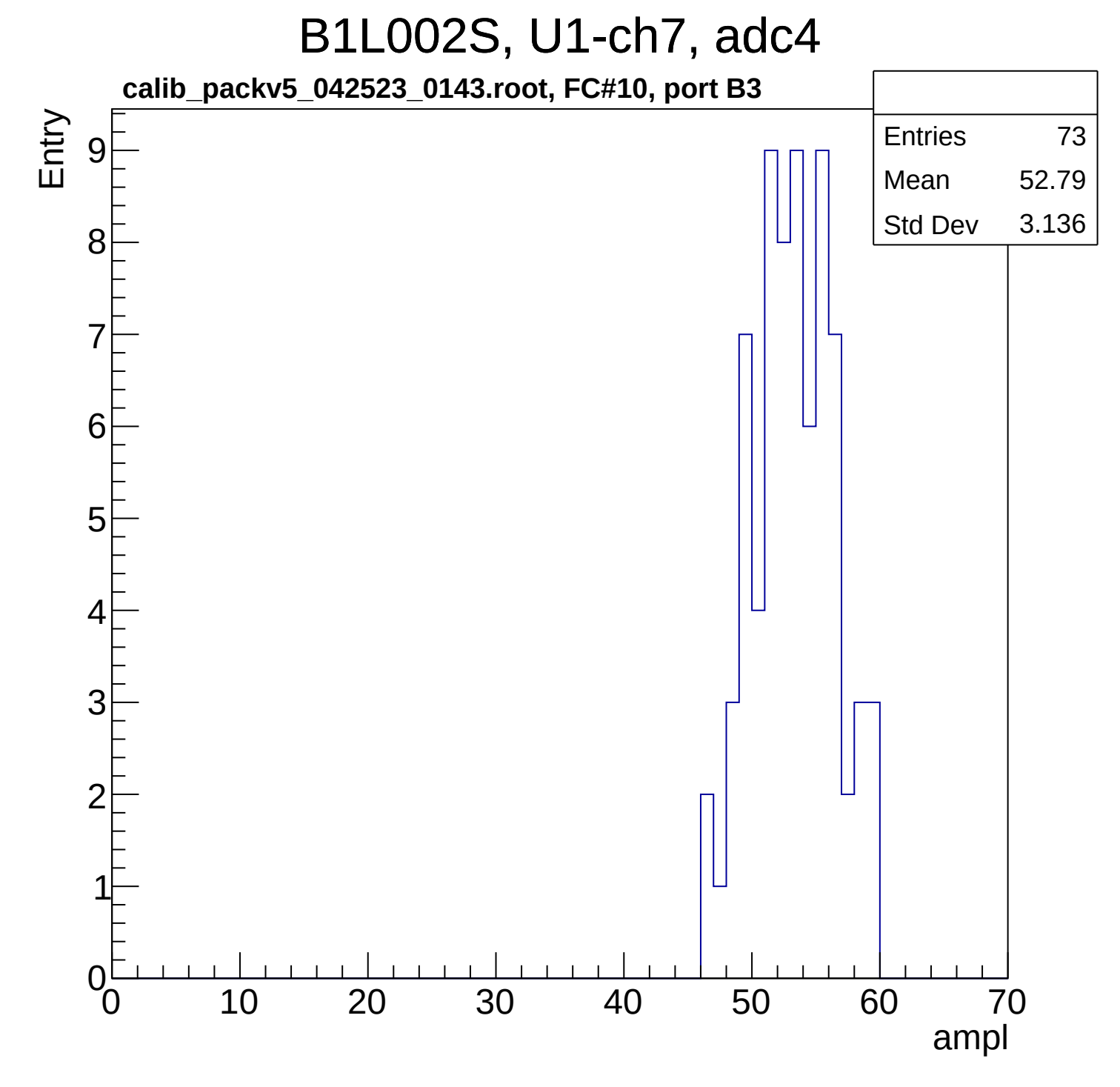
70
ampl


B1L002S, U1-ch7, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 52.79
Std Dev 3.136
Entry
ampl
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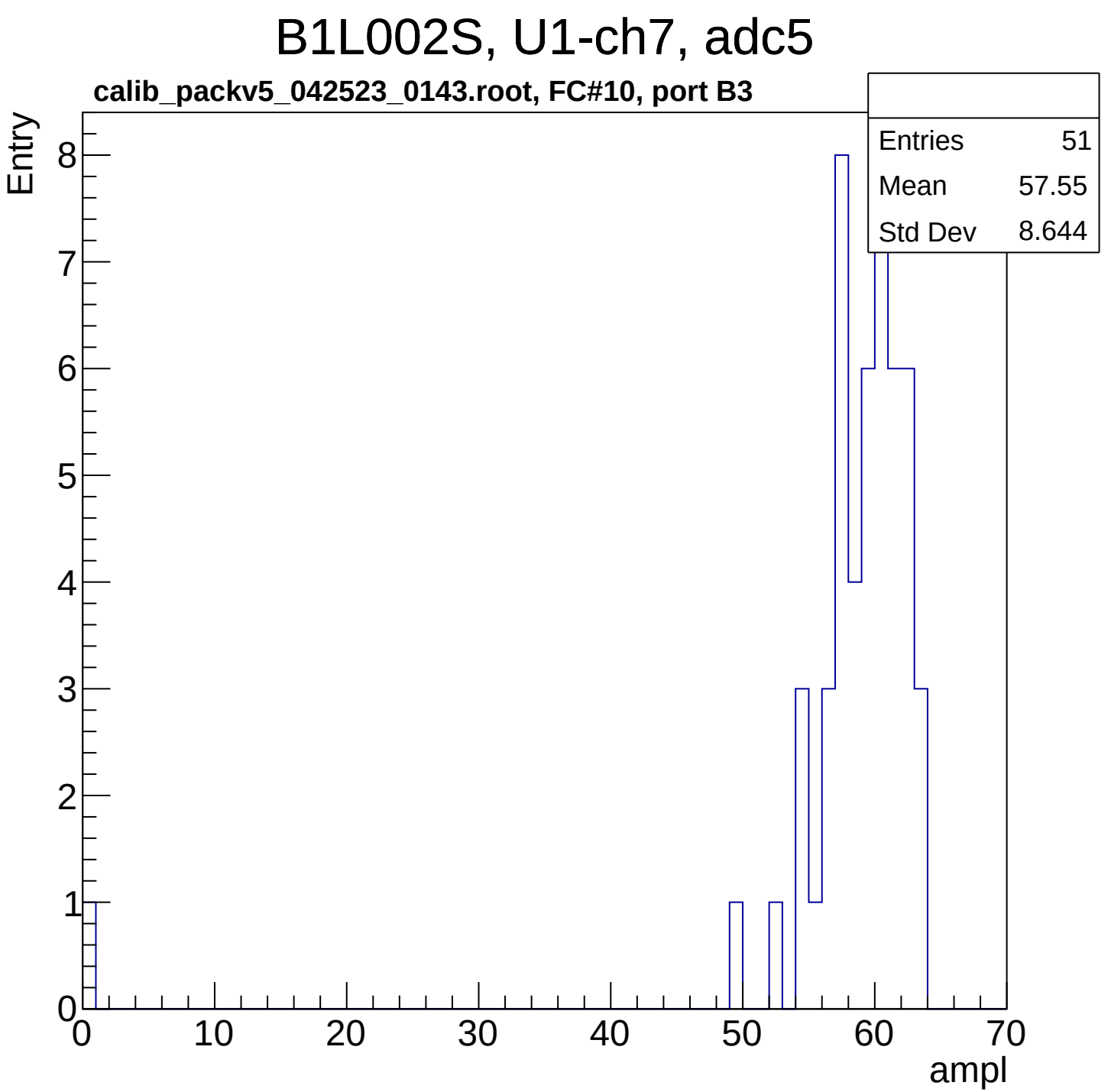
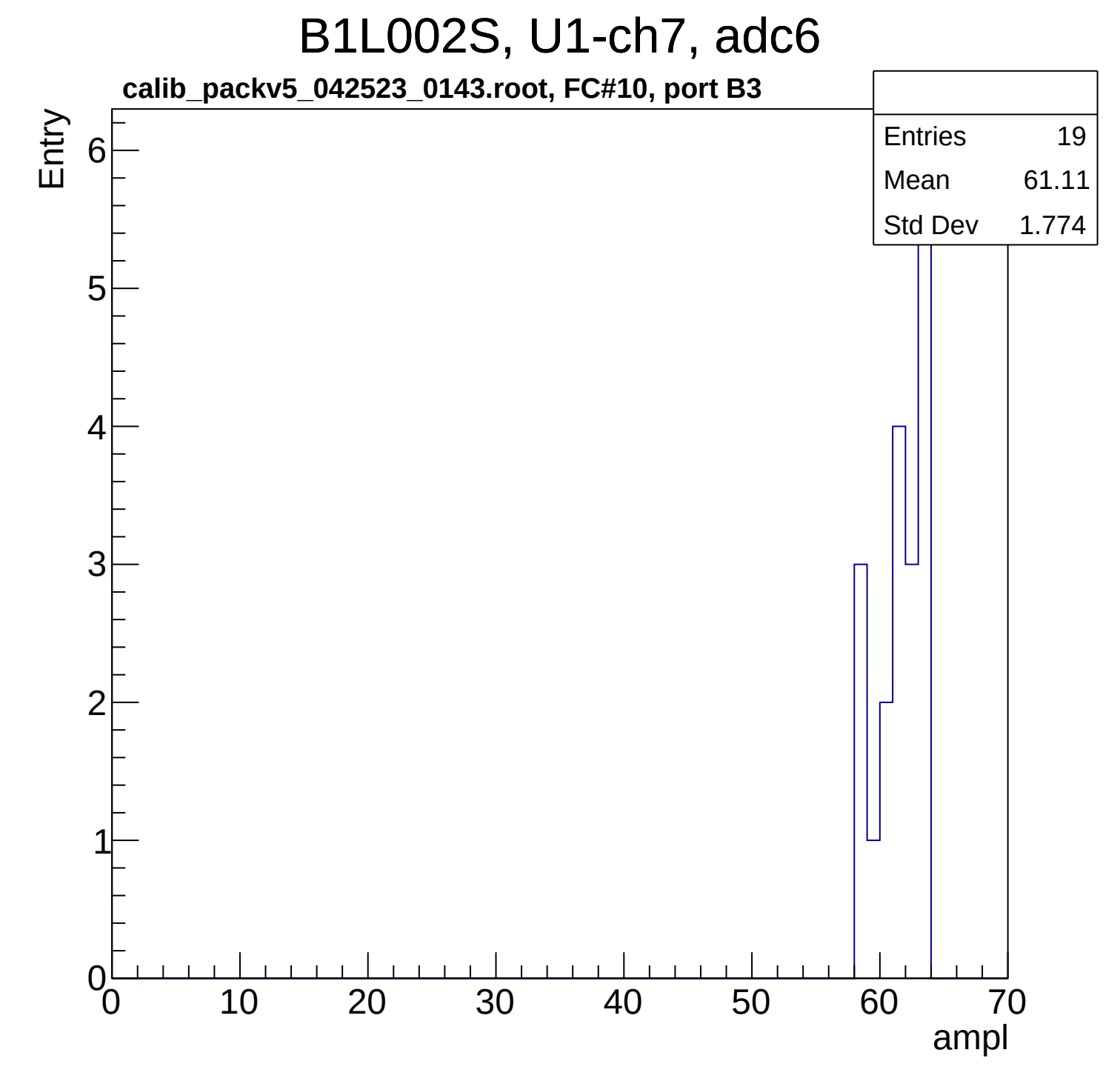

B1L002S, U1-ch7, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.55
Std Dev 8.644
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch7, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.11
Std Dev 1.774
Entry
0
1
2
3
4
5
6
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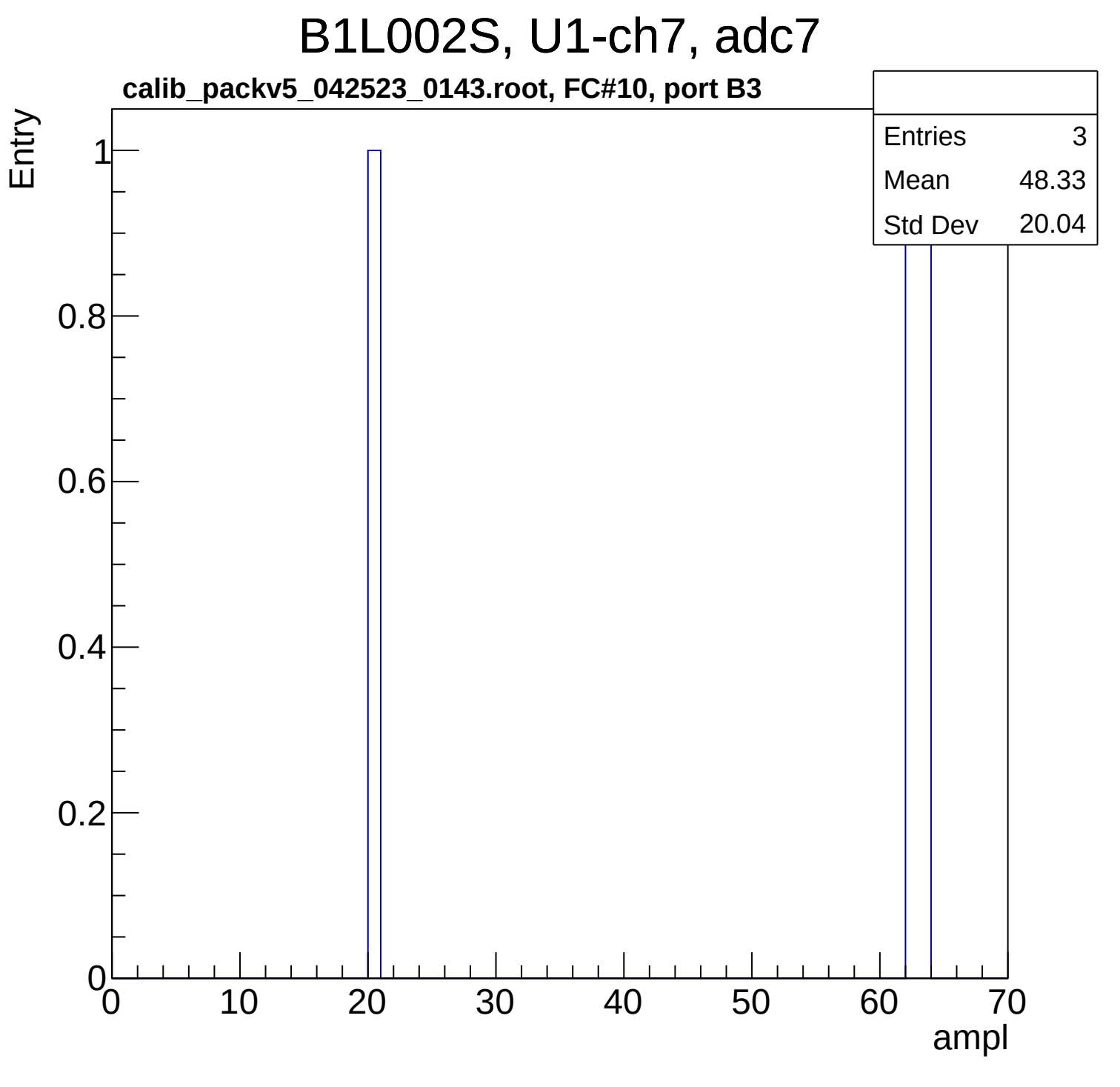
70
ampl


B1L002S, U1-ch7, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48.33
Std Dev 20.04
Entry
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0.2
0.4
0.6
0.8
1
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10
20
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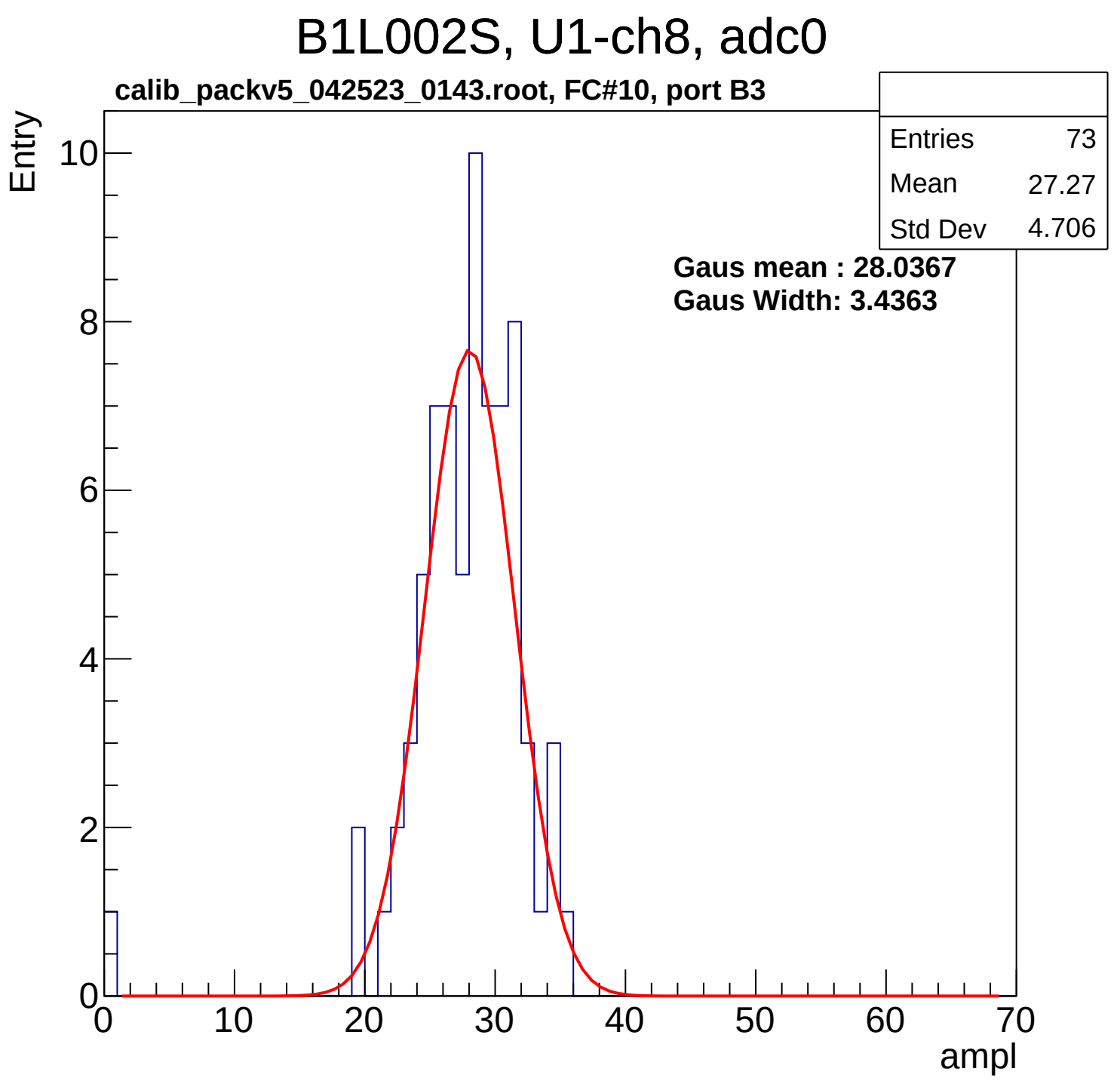
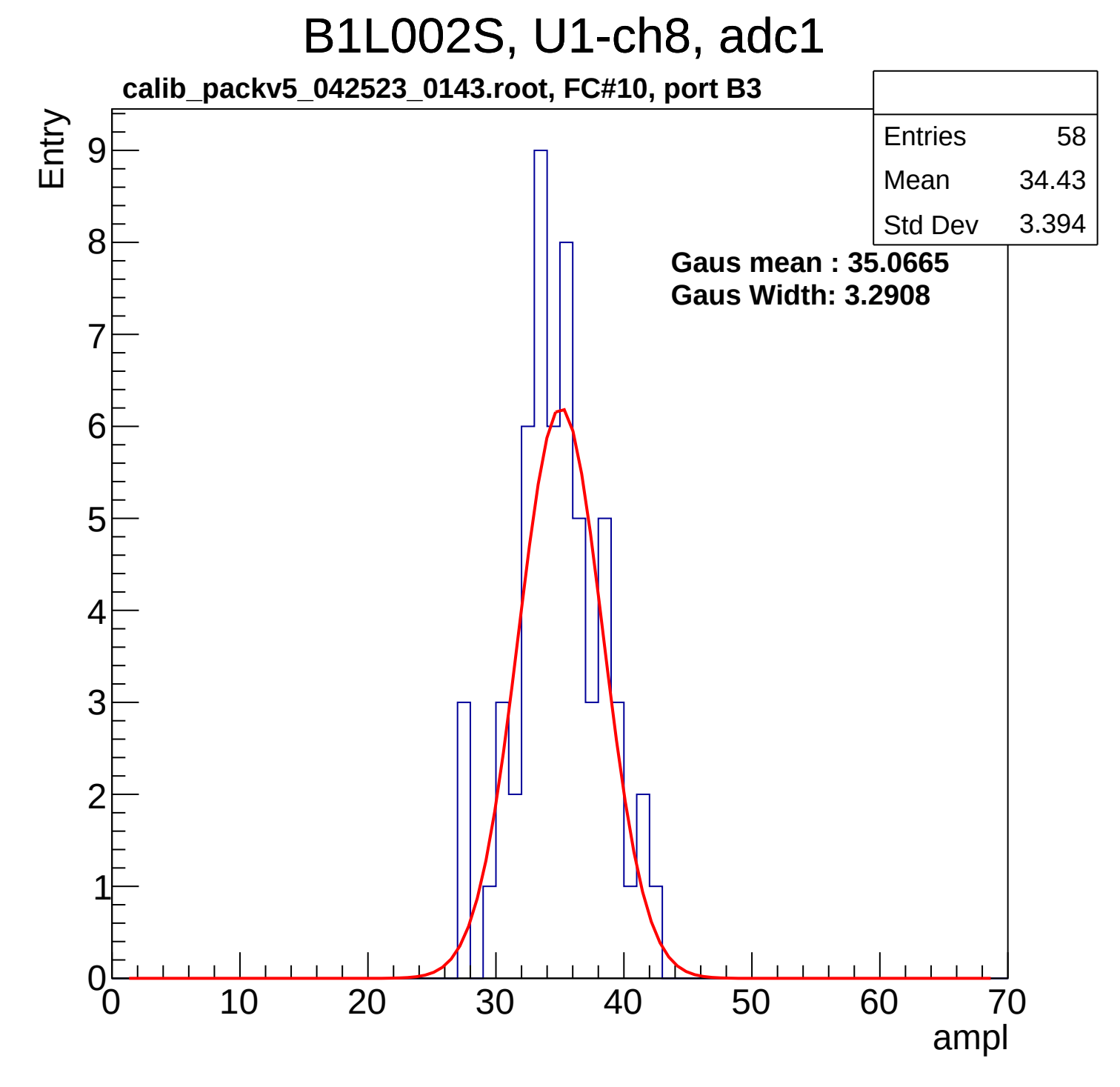
50
60
70
ampl


B1L002S, U1-ch8, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 27.27
Std Dev 4.706
Gaus mean : 28.0367
Gaus Width: 3.4363
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch8, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.43
Std Dev 3.394
Gaus mean : 35.0665
Gaus Width: 3.2908
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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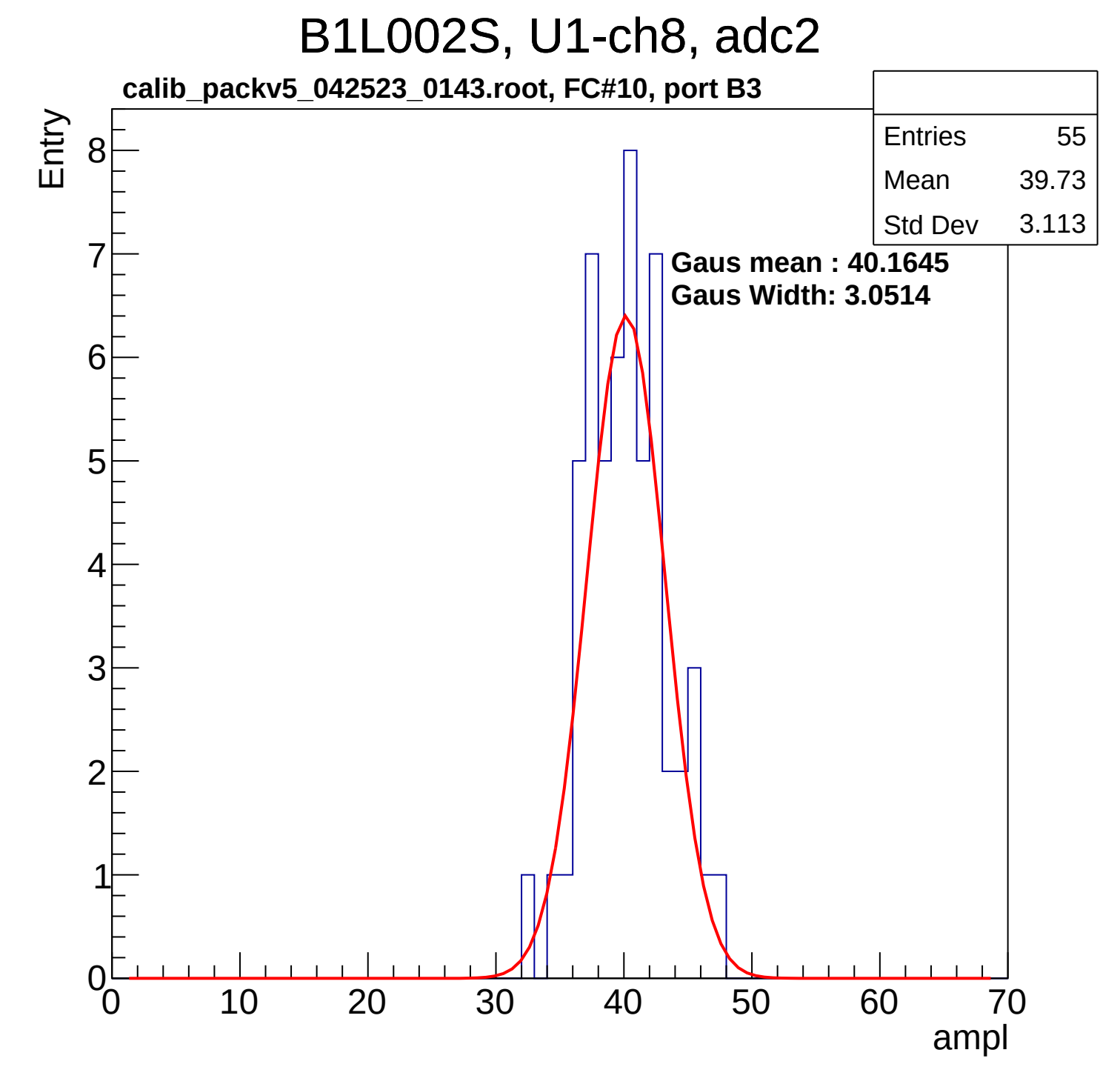
B1L002S, U1-ch8, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 39.73
Std Dev 3.113
Gaus mean : 40.1645
Gaus Width: 3.0514
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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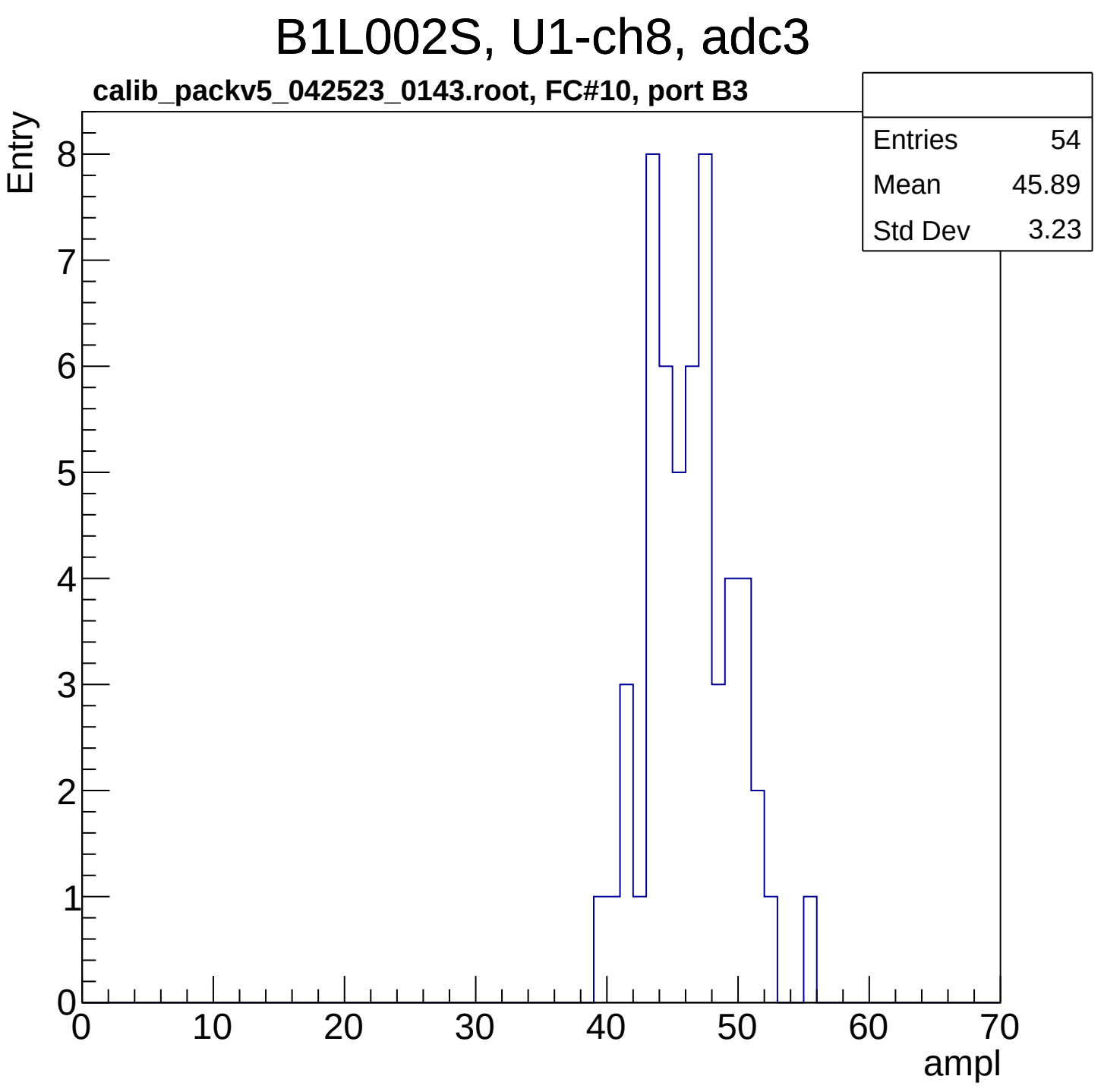
B1L002S, U1-ch8, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 45.89
Std Dev 3.23
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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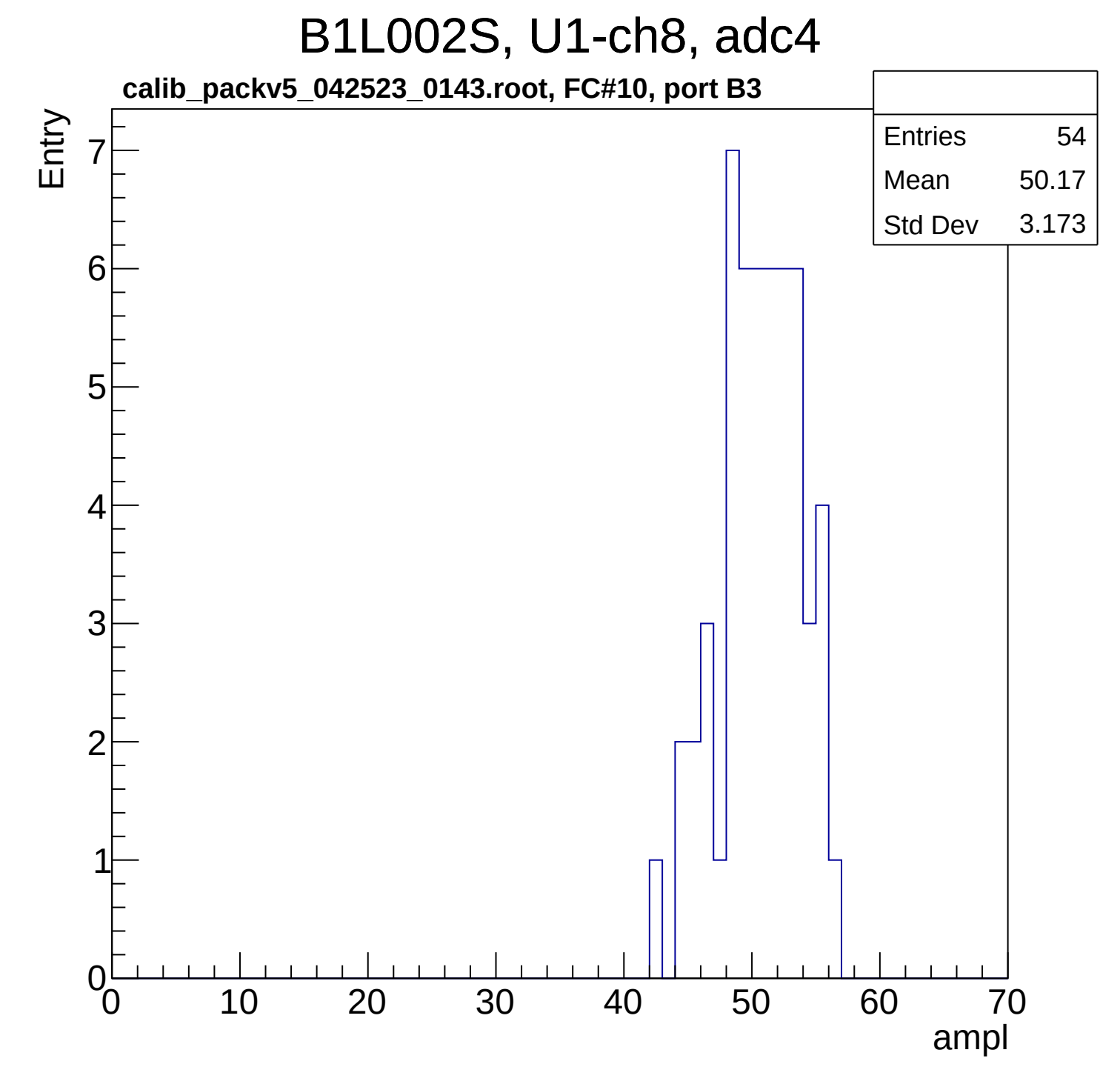
B1L002S, U1-ch8, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 50.17
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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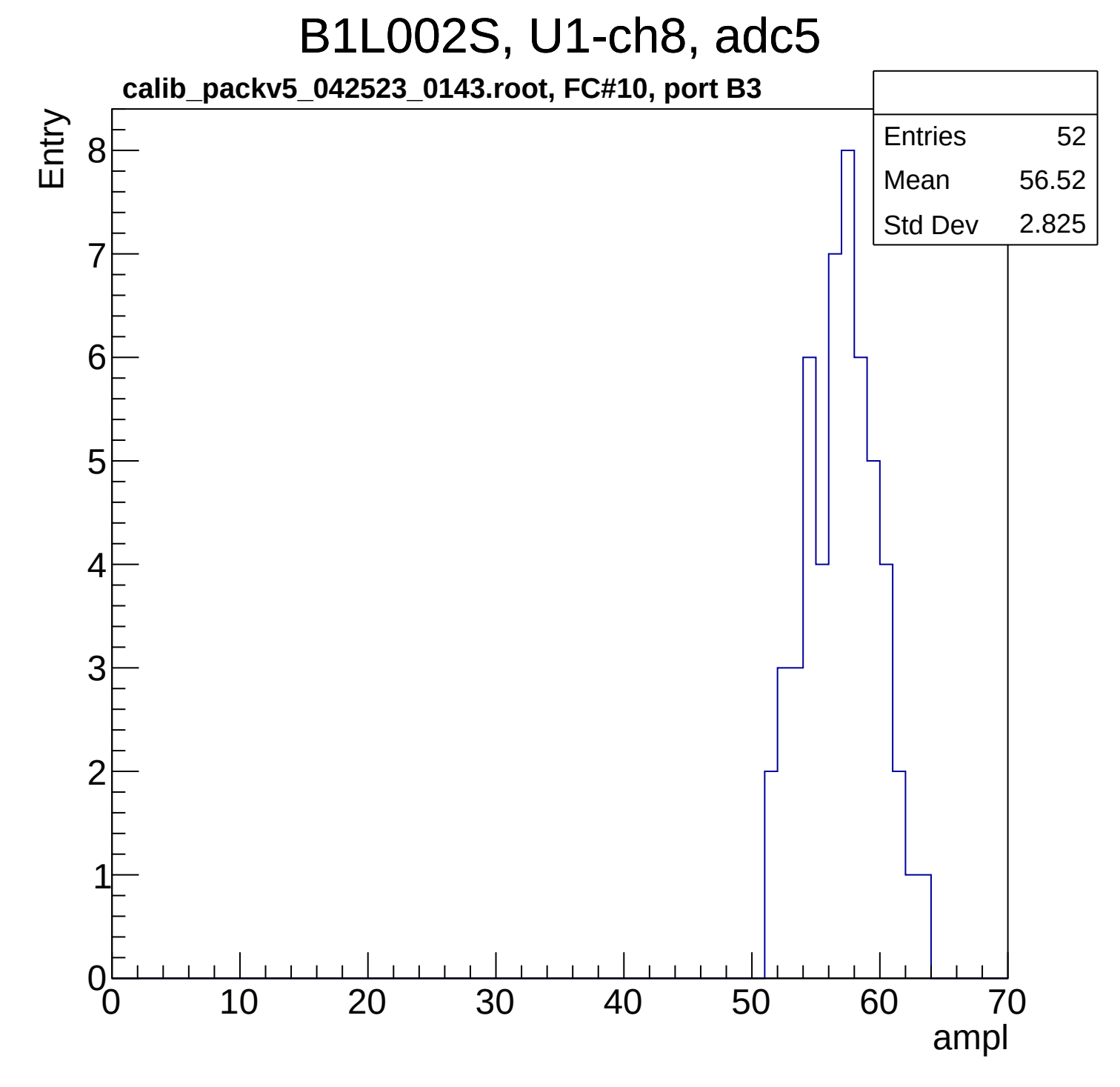
B1L002S, U1-ch8, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 56.52
Std Dev 2.825
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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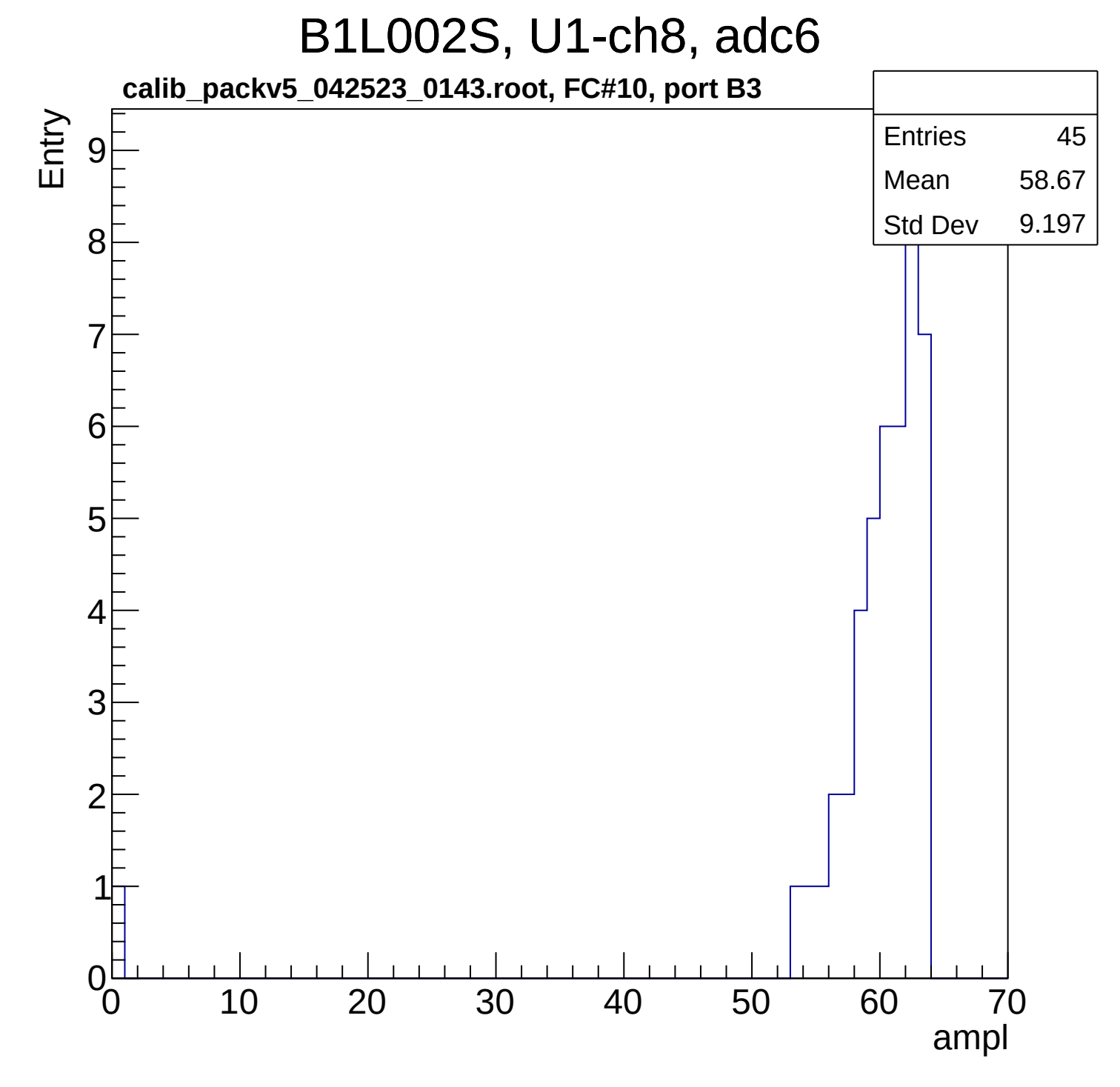
B1L002S, U1-ch8, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.67
Std Dev 9.197
Entry
ampl
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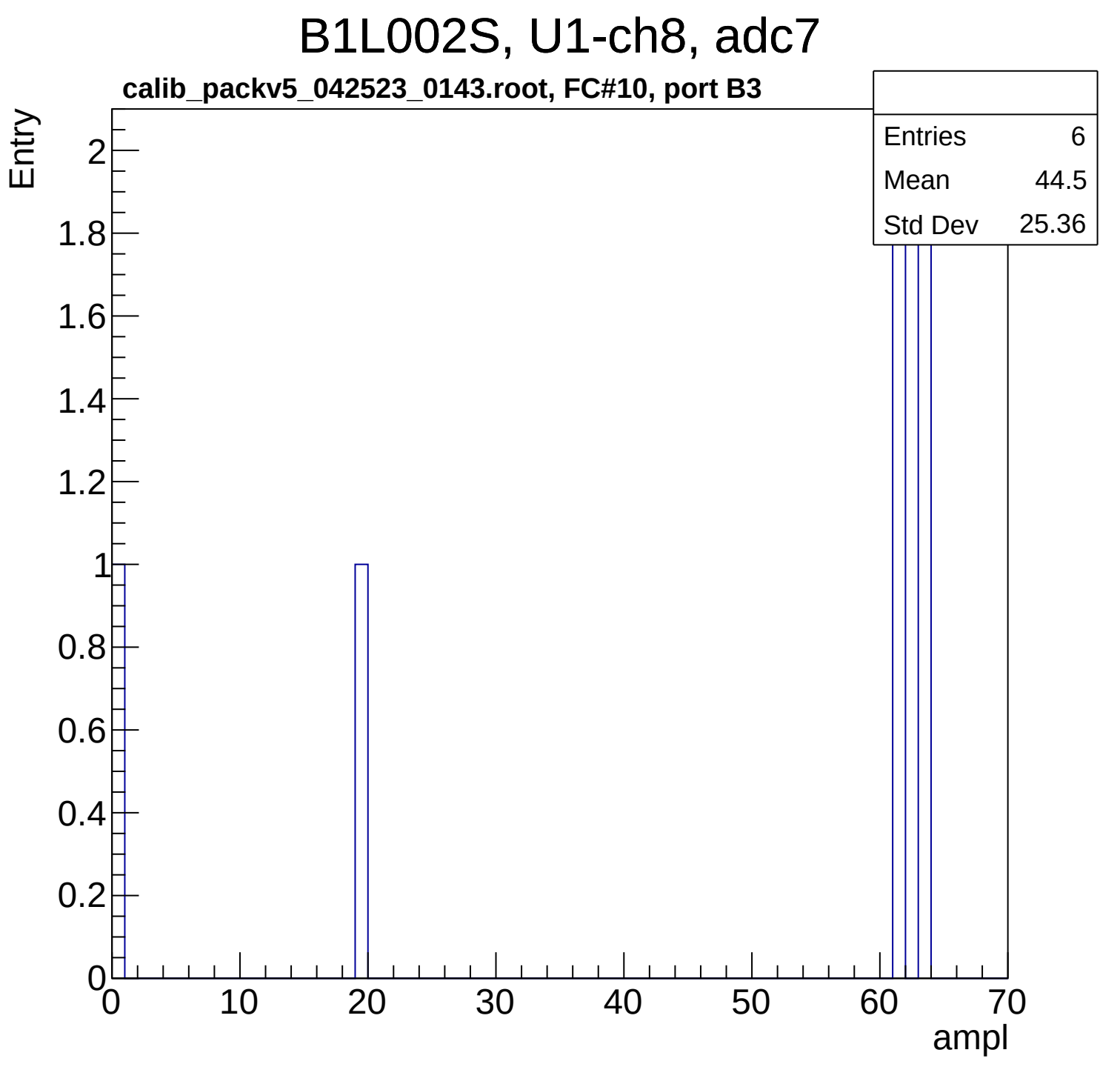
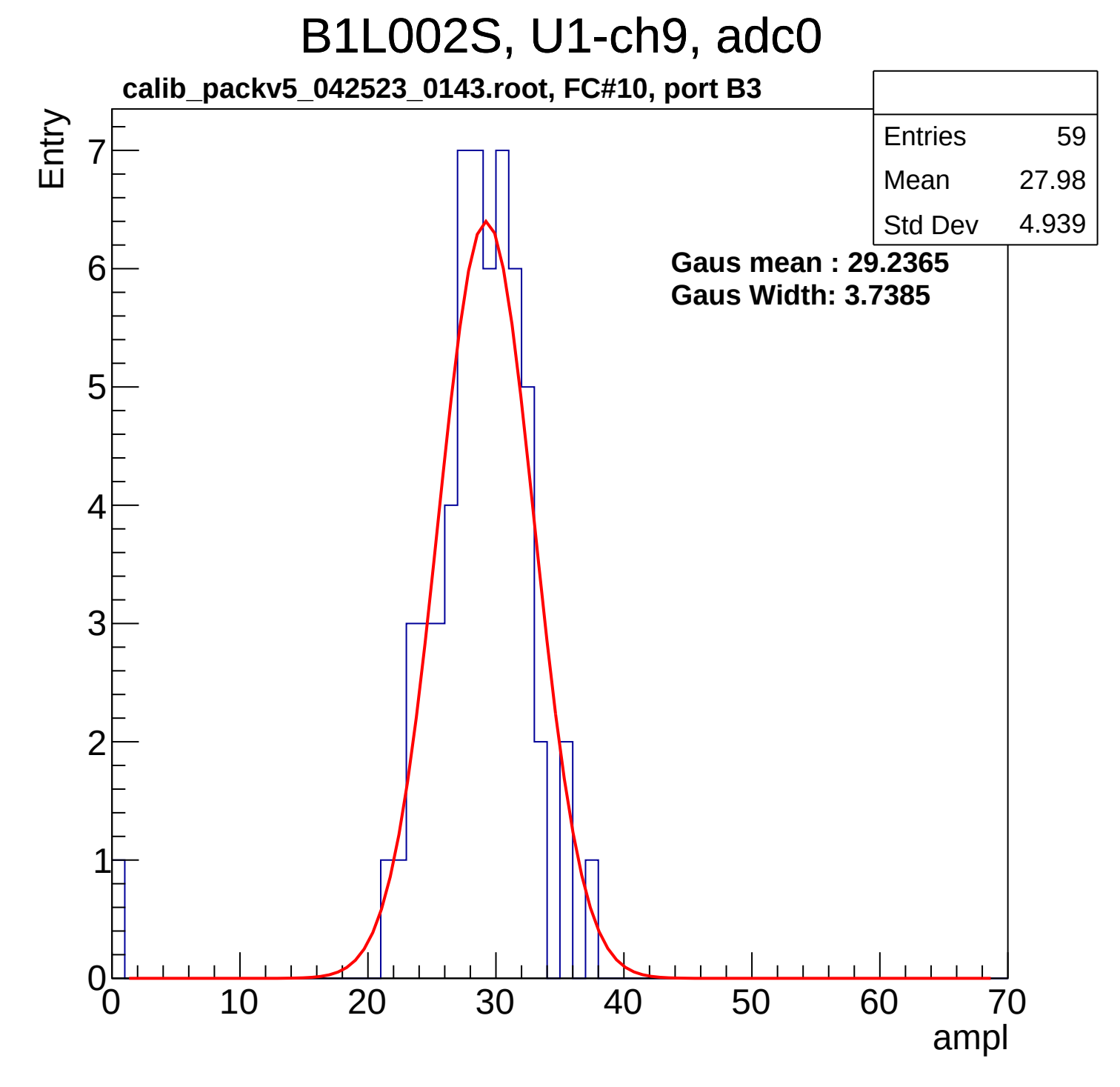
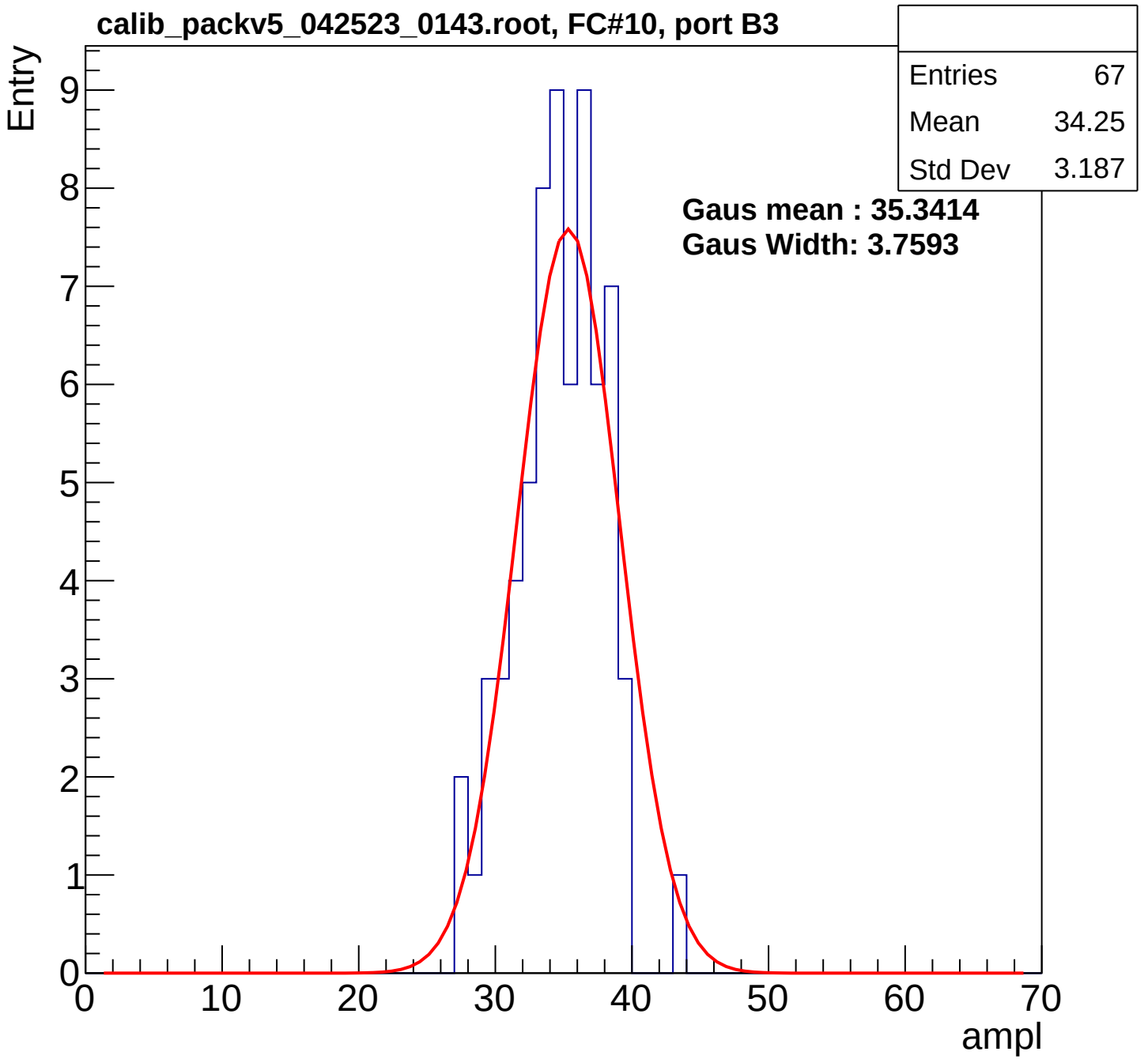
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch8, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 44.5
Std Dev 25.36
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch9, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.98
Std Dev 4.939
Gaus mean : 29.2365
Gaus Width: 3.7385
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch9, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.25
Std Dev 3.187
Gaus mean : 35.3414
Gaus Width: 3.7593
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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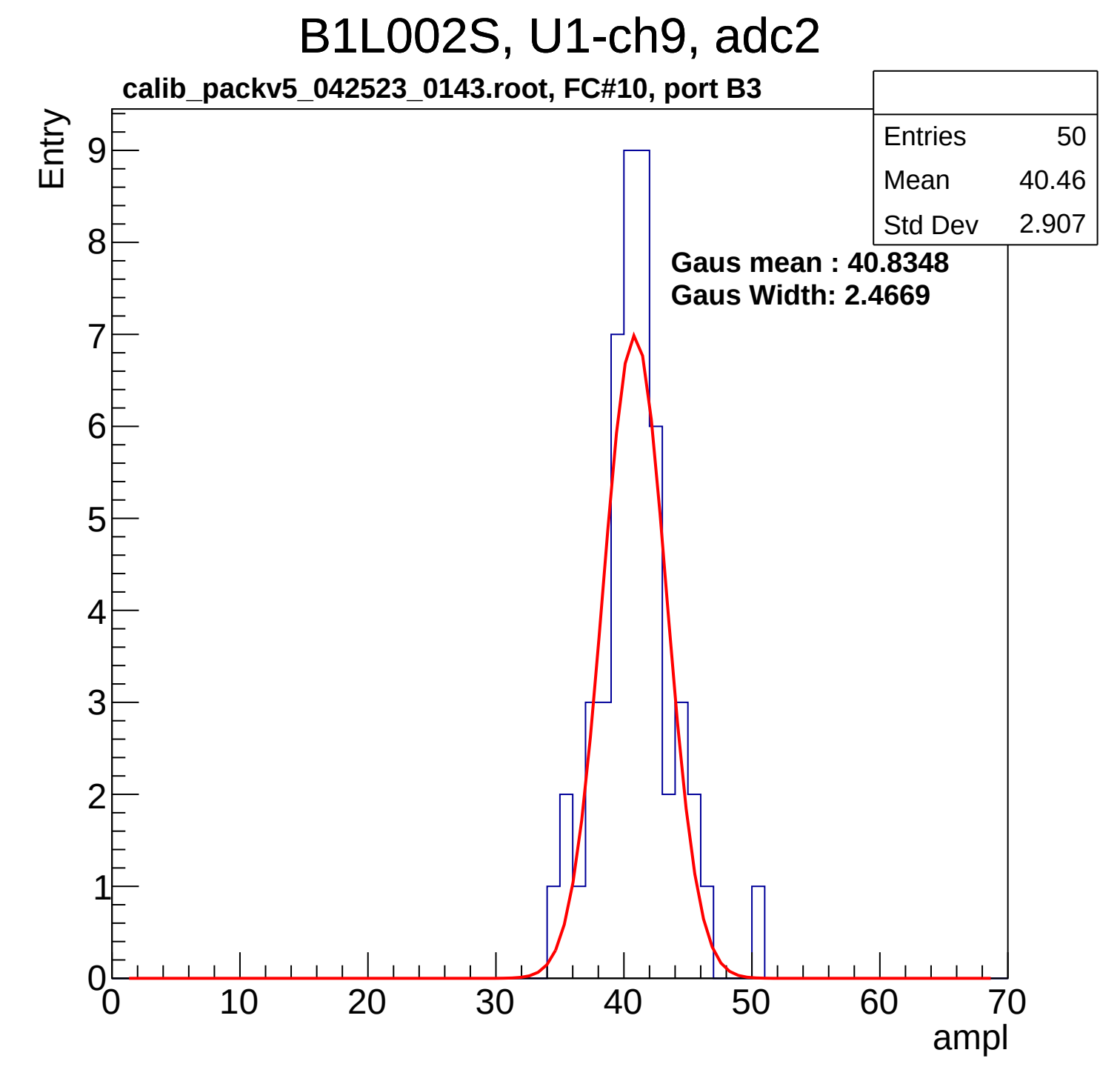
B1L002S, U1-ch9, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 40.46
Std Dev 2.907
Gaus mean : 40.8348
Gaus Width: 2.4669
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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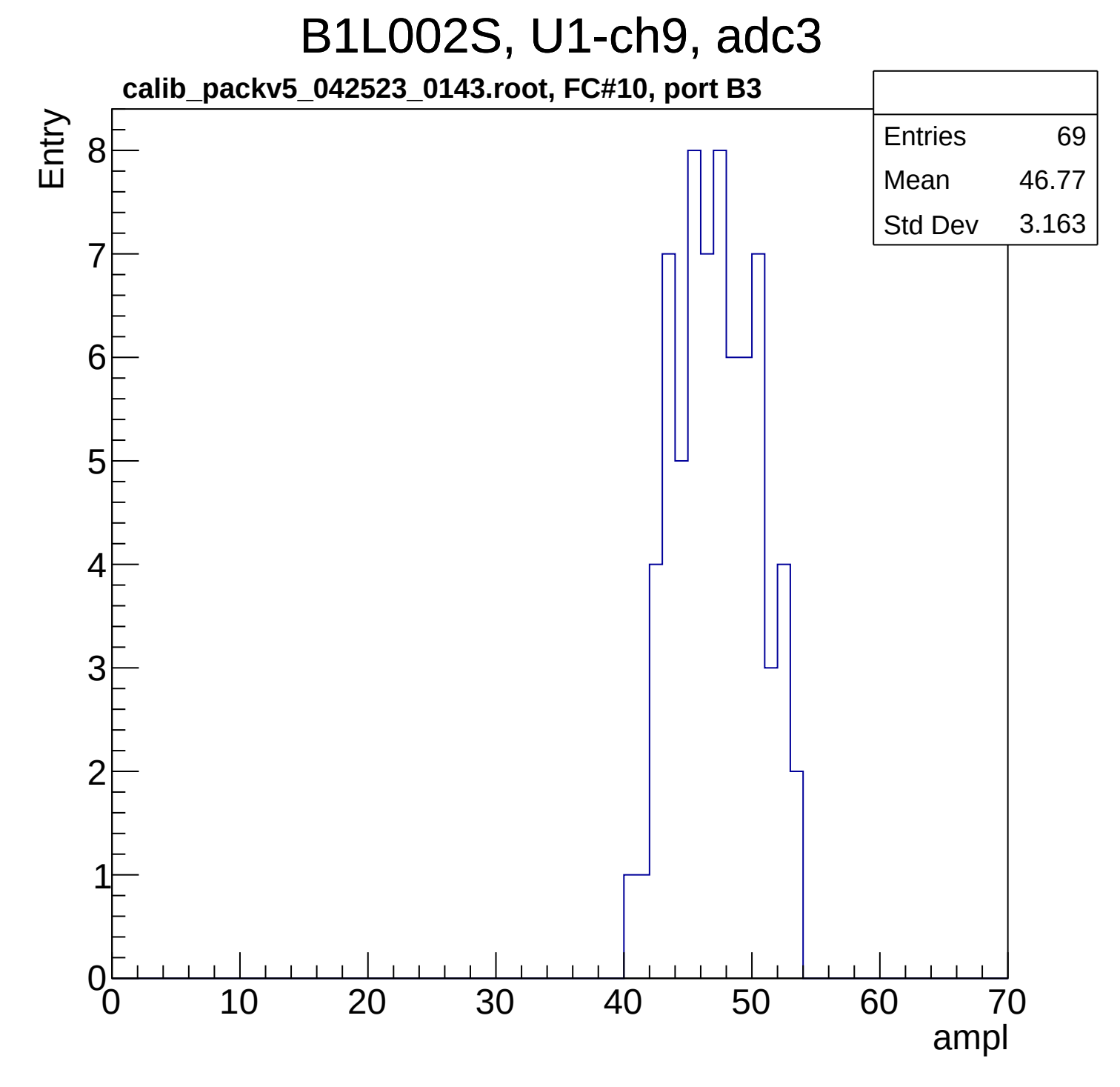
B1L002S, U1-ch9, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.77
Std Dev 3.163
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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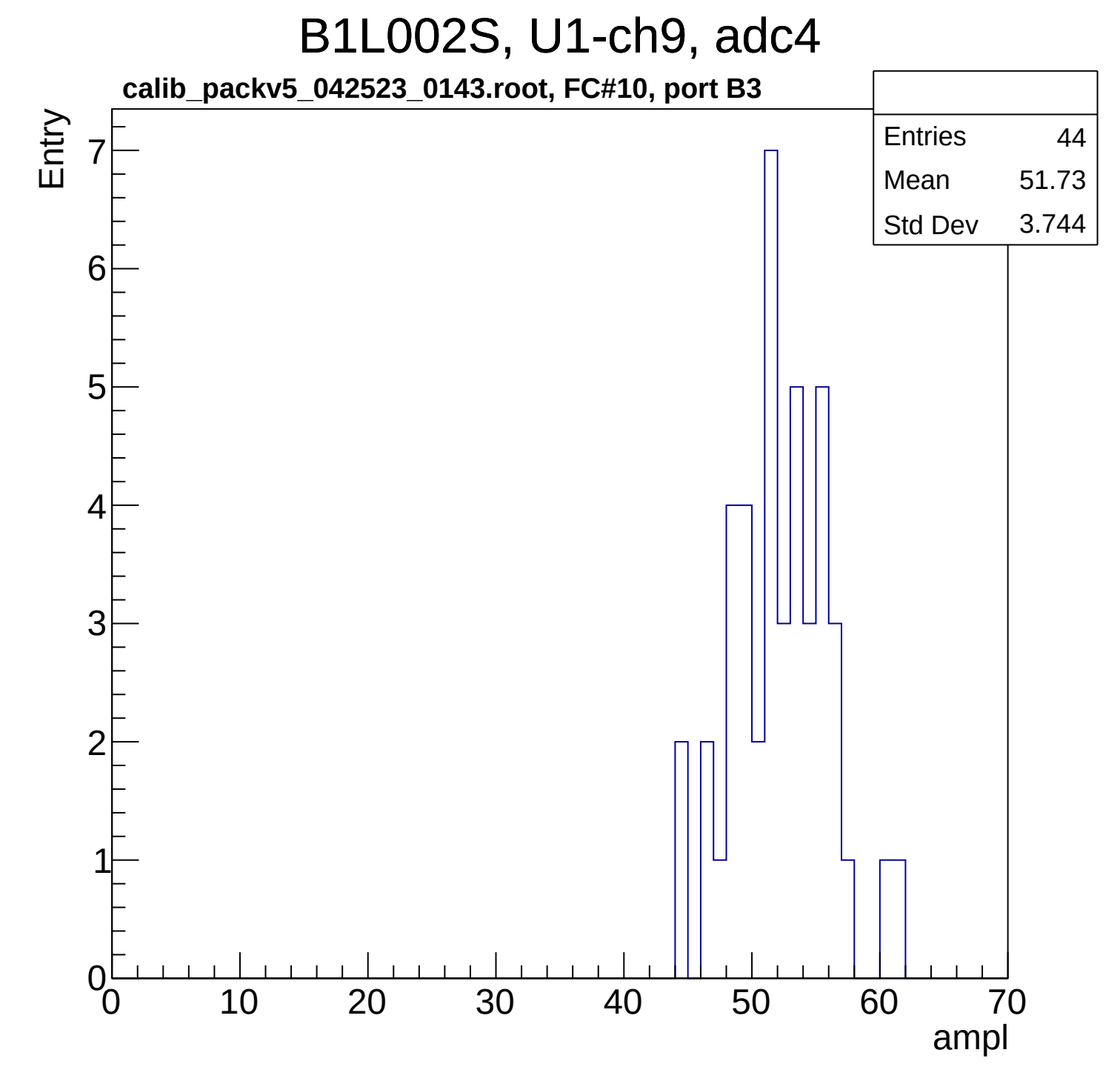
B1L002S, U1-ch9, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 51.73
Std Dev 3.744
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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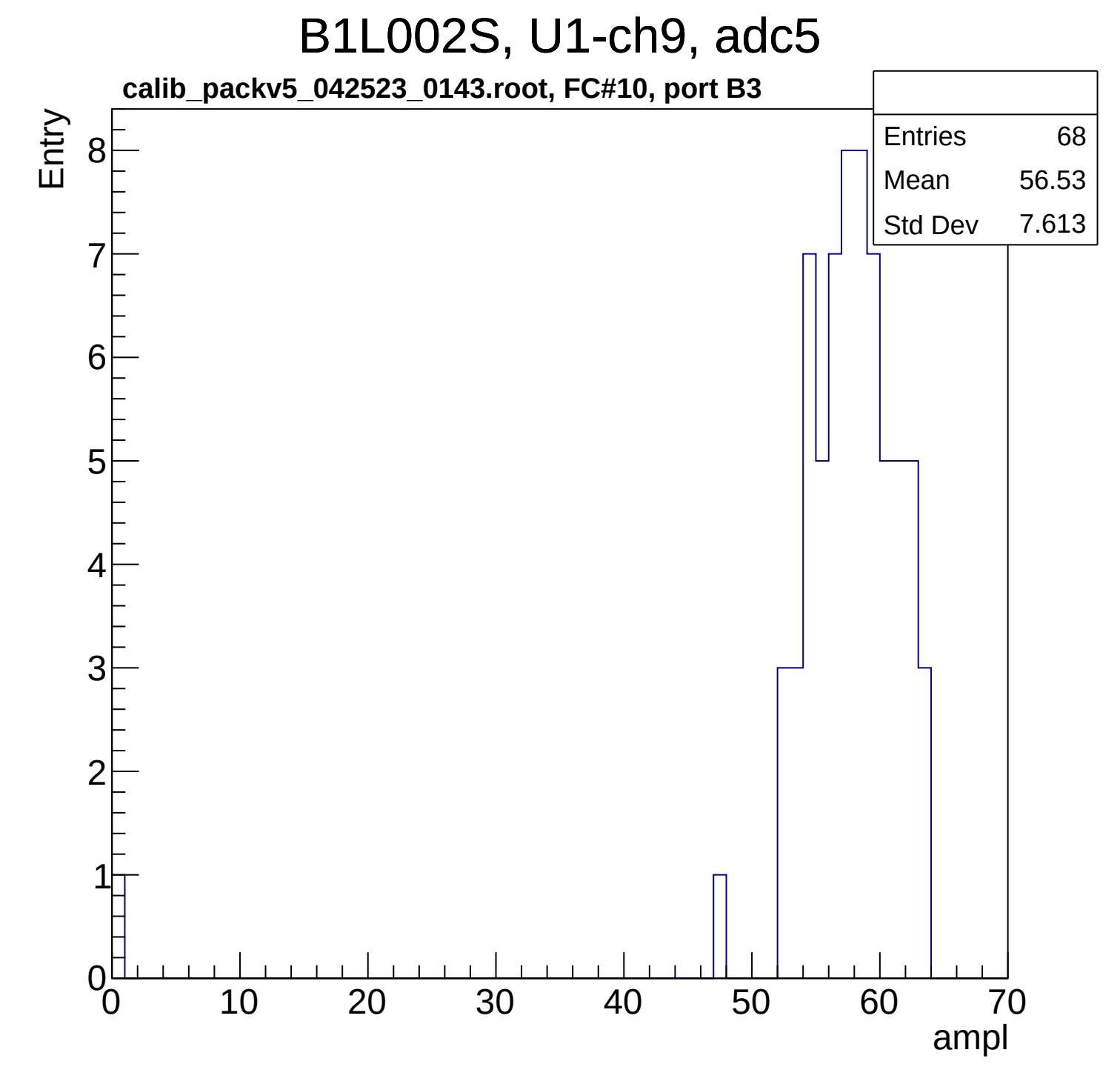
B1L002S, U1-ch9, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 56.53
Std Dev 7.613
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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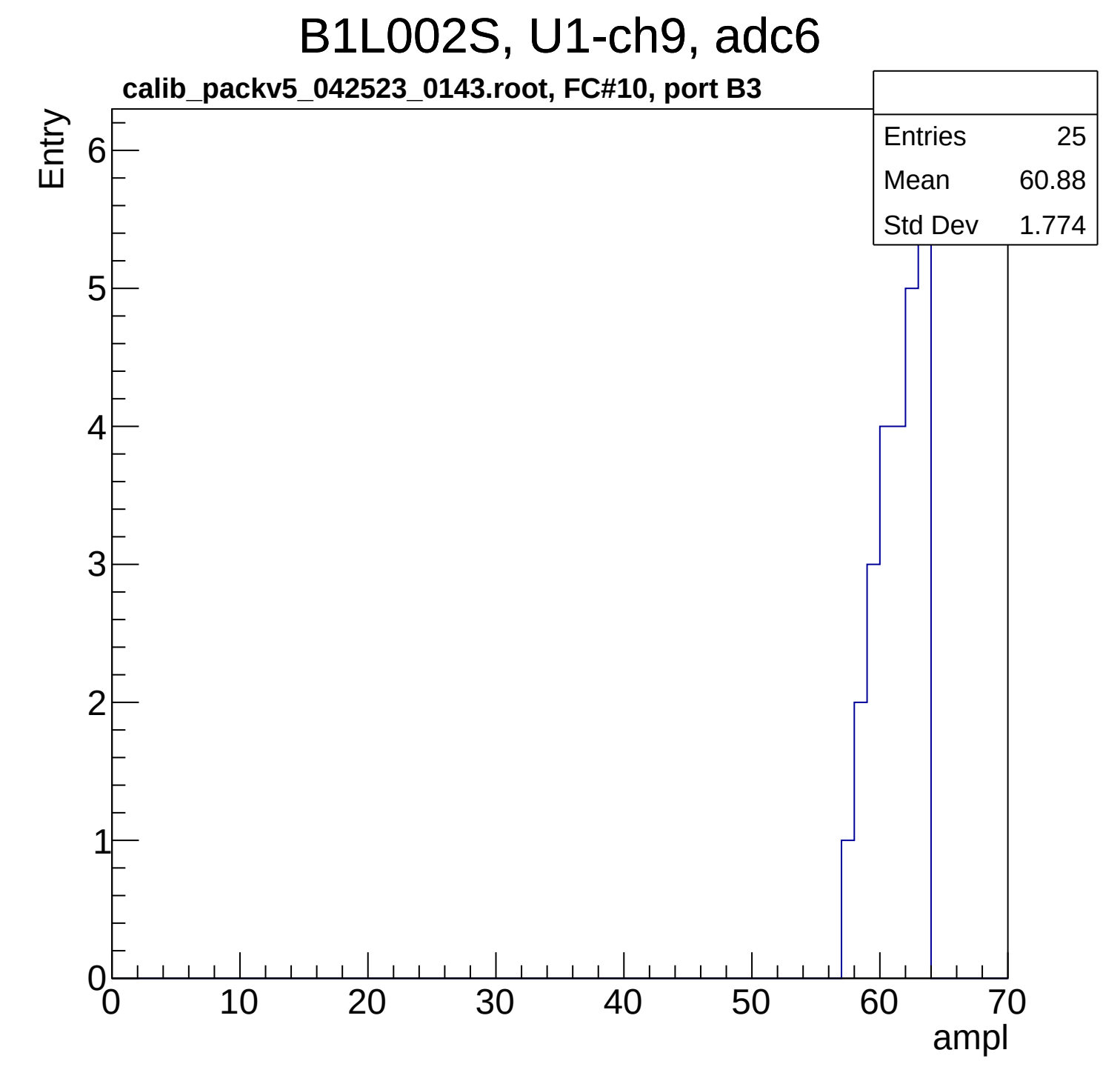
B1L002S, U1-ch9, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 60.88
Std Dev 1.774
Entry
0
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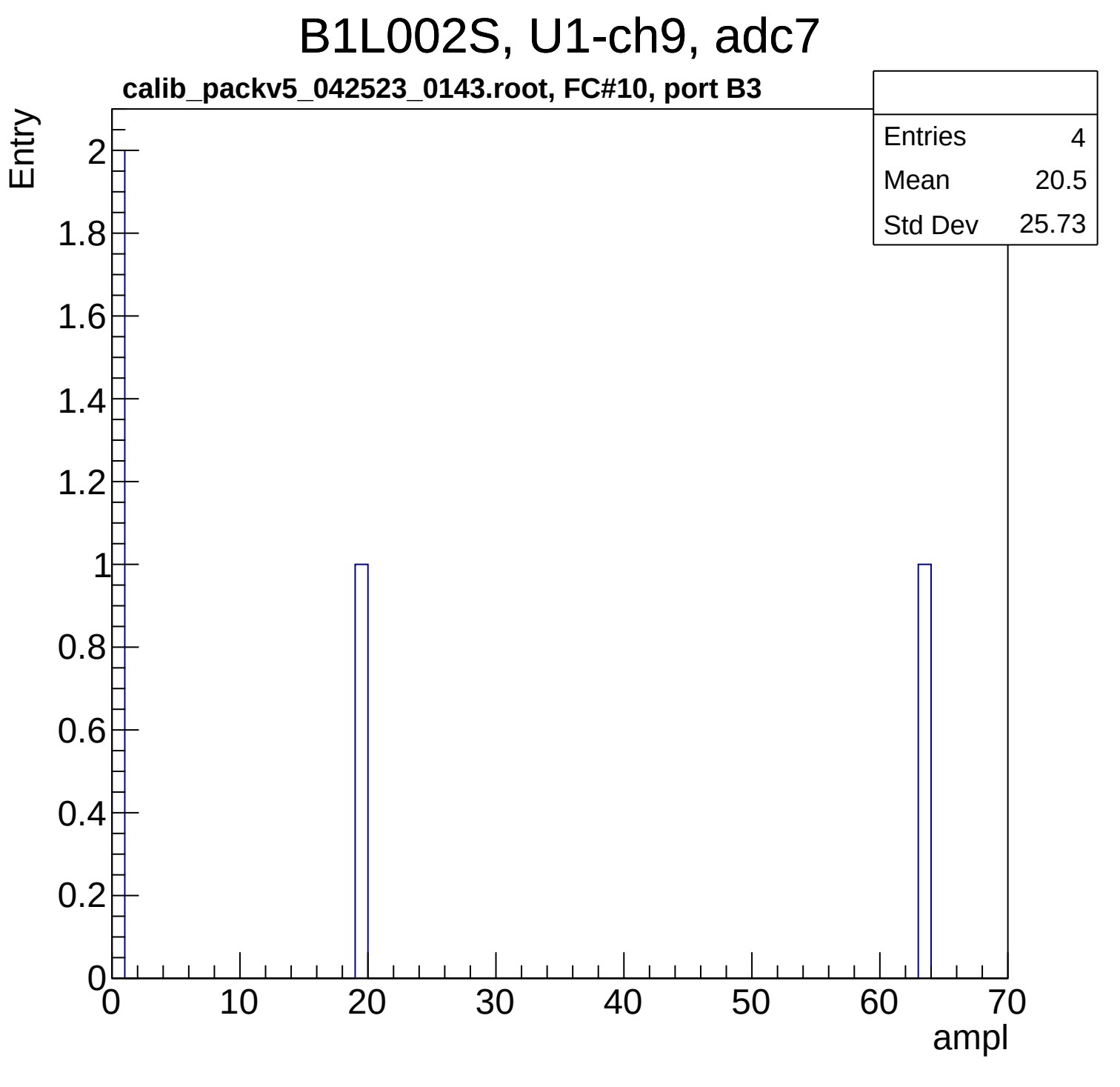
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch9, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 20.5
Std Dev 25.73
Entry
ampl
0
0.2
0.4
0.6
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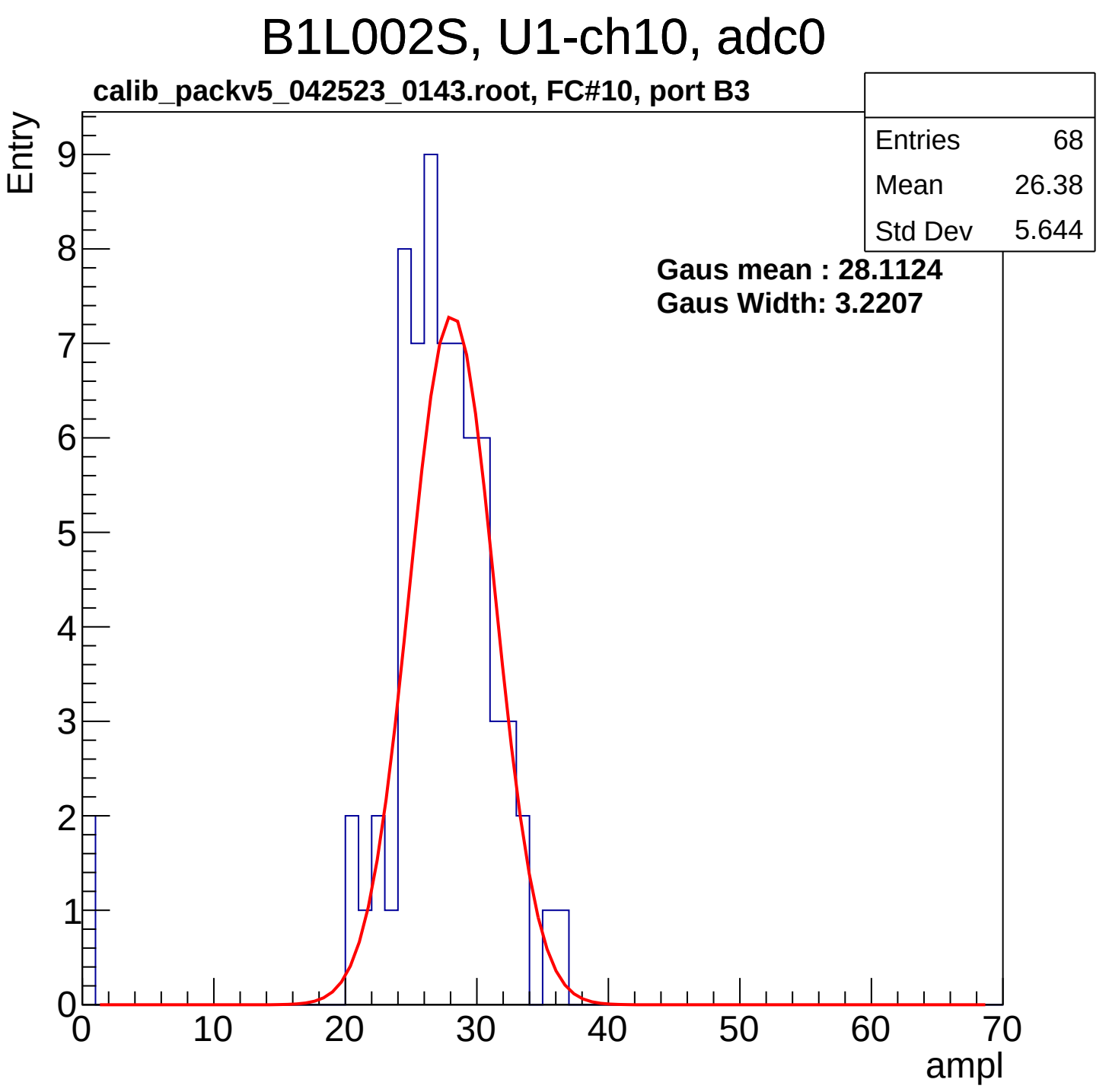
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch10, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 26.38
Std Dev 5.644
Gaus mean : 28.1124
Gaus Width: 3.2207
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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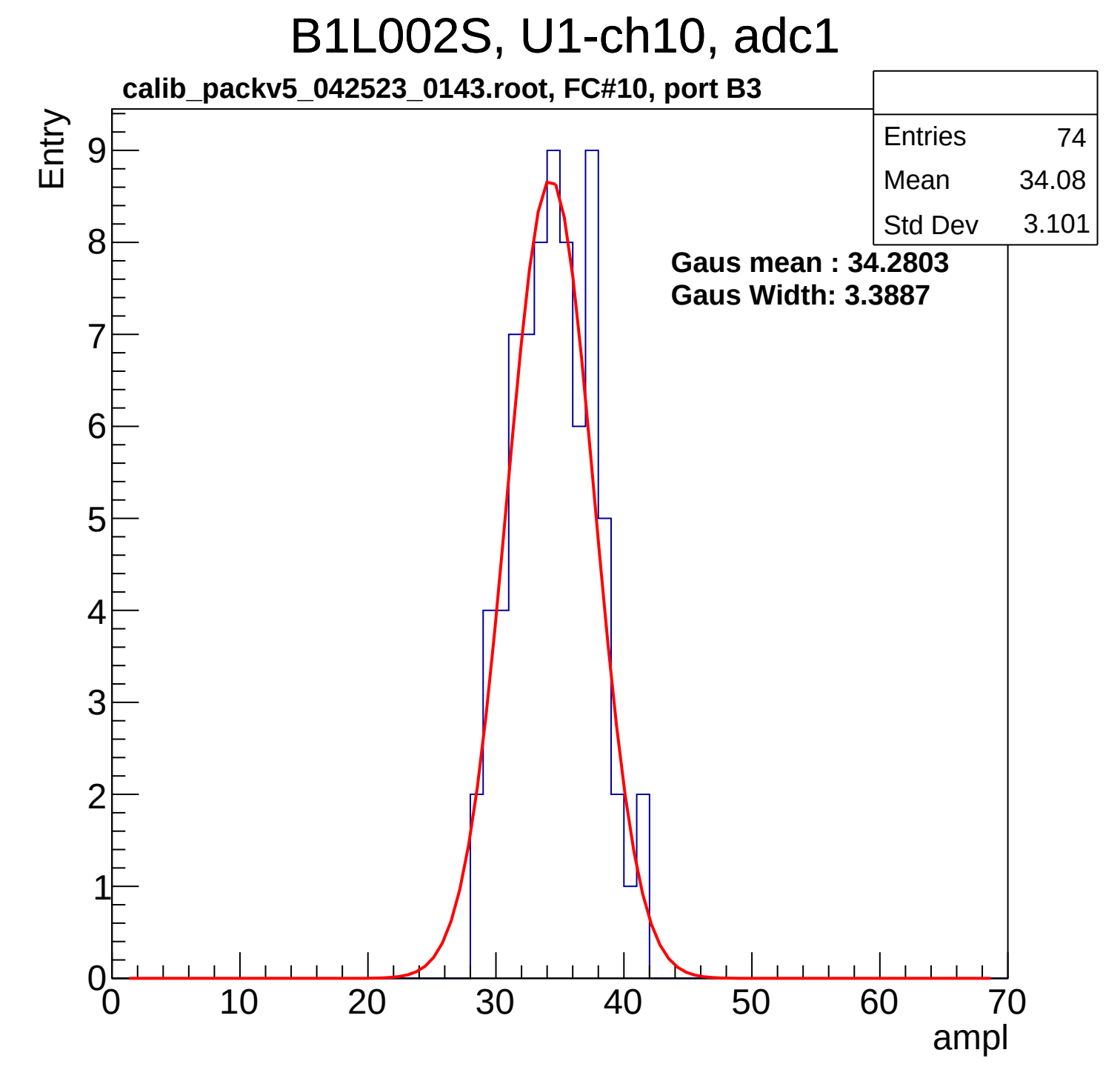
B1L002S, U1-ch10, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 34.08
Std Dev 3.101
Gaus mean : 34.2803
Gaus Width: 3.3887
Entry
ampl
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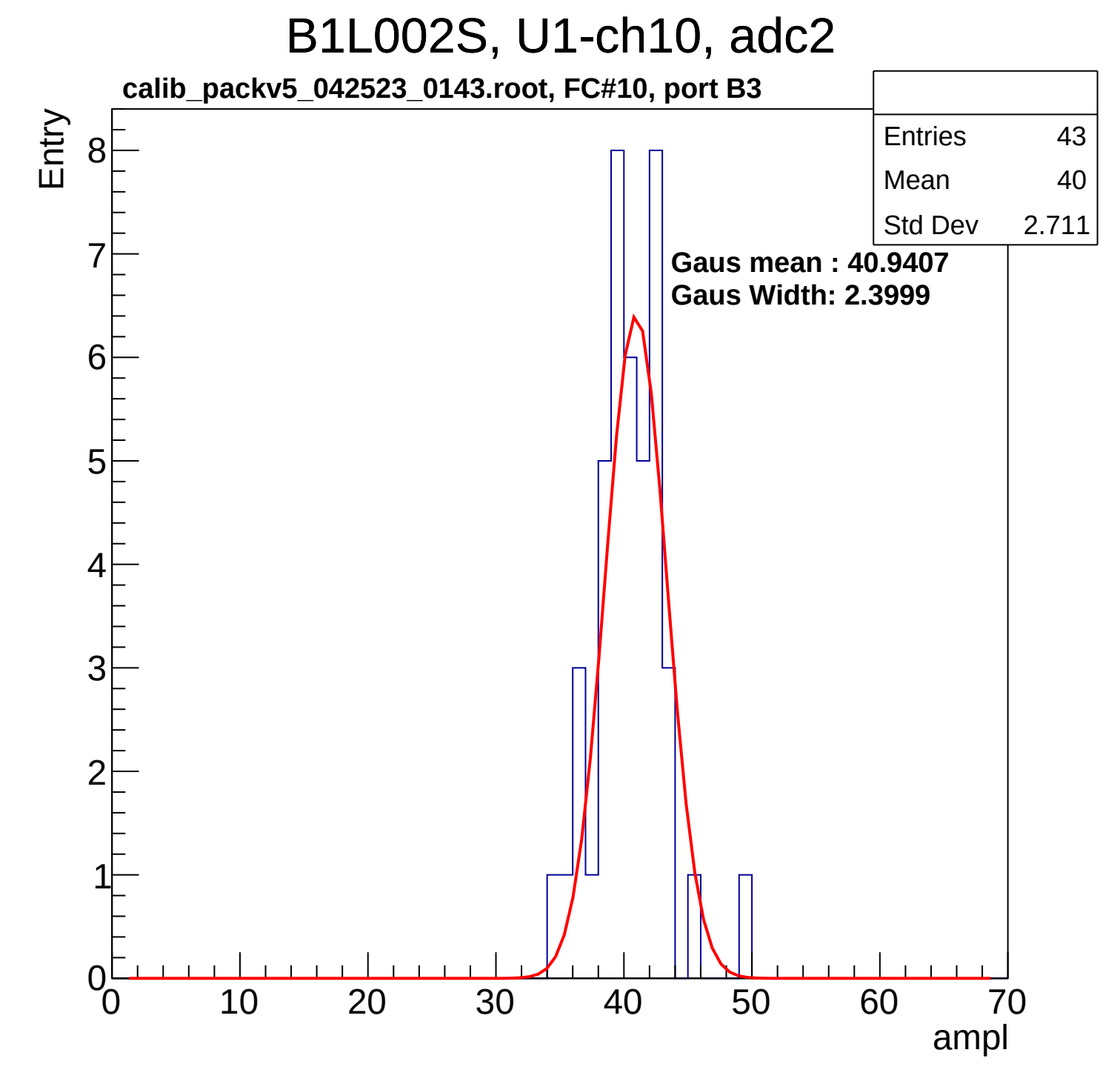
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch10, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 40
Std Dev 2.711
Gaus mean : 40.9407
Gaus Width: 2.3999
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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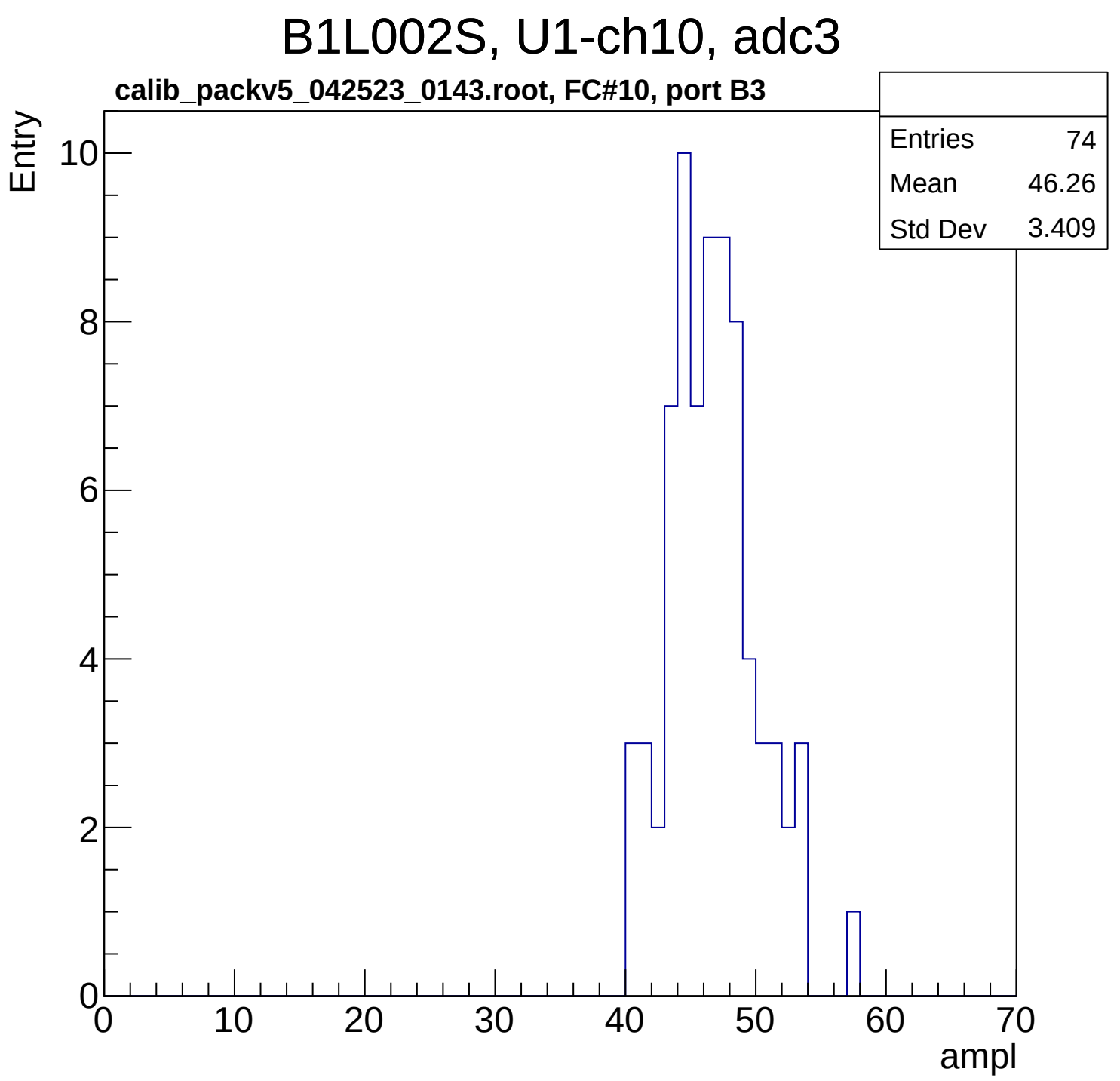
B1L002S, U1-ch10, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.26
Std Dev 3.409
Entry
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4
6
8
10
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10
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30
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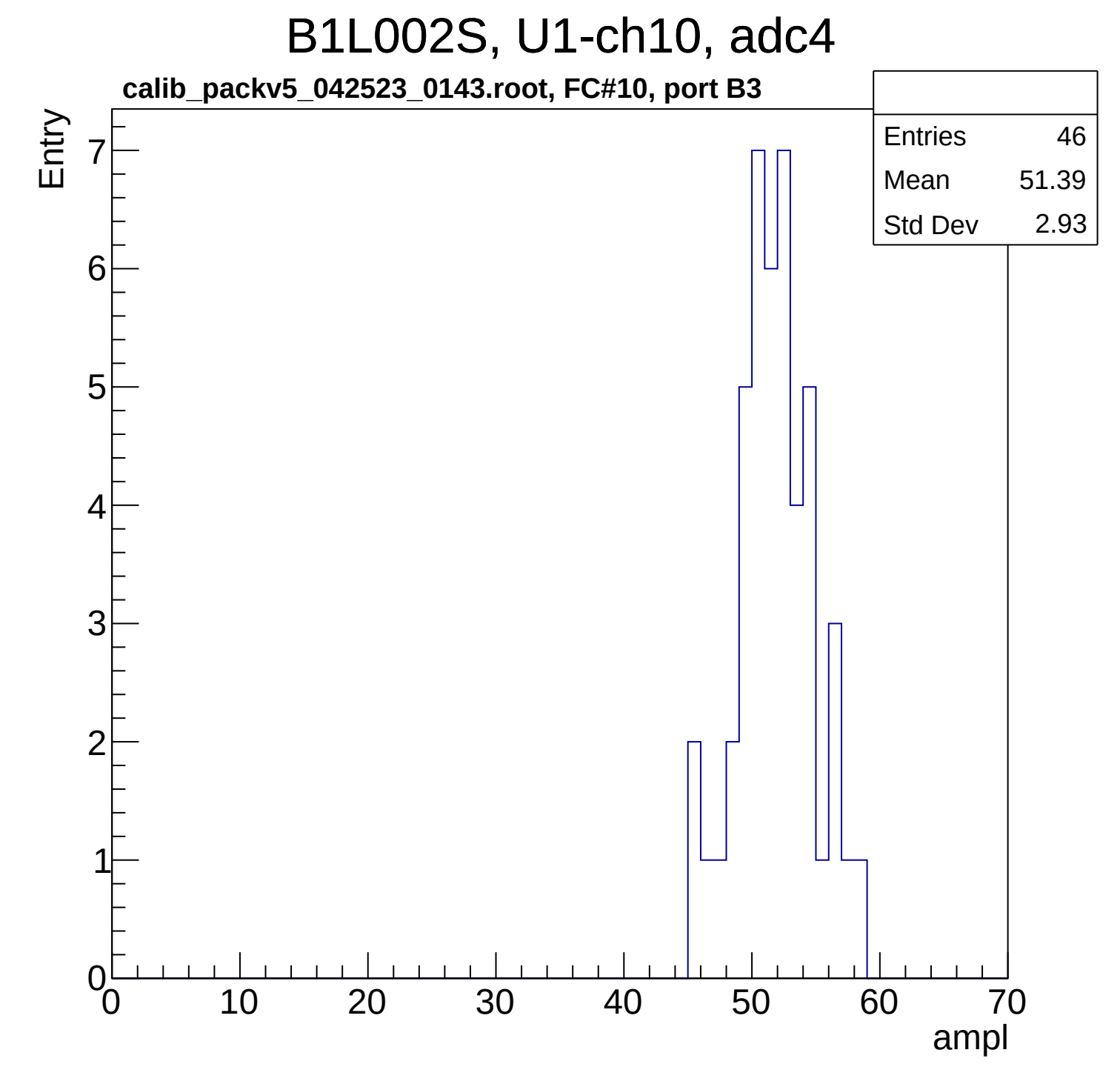
50
60
70
ampl


B1L002S, U1-ch10, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 51.39
Std Dev 2.93
Entry
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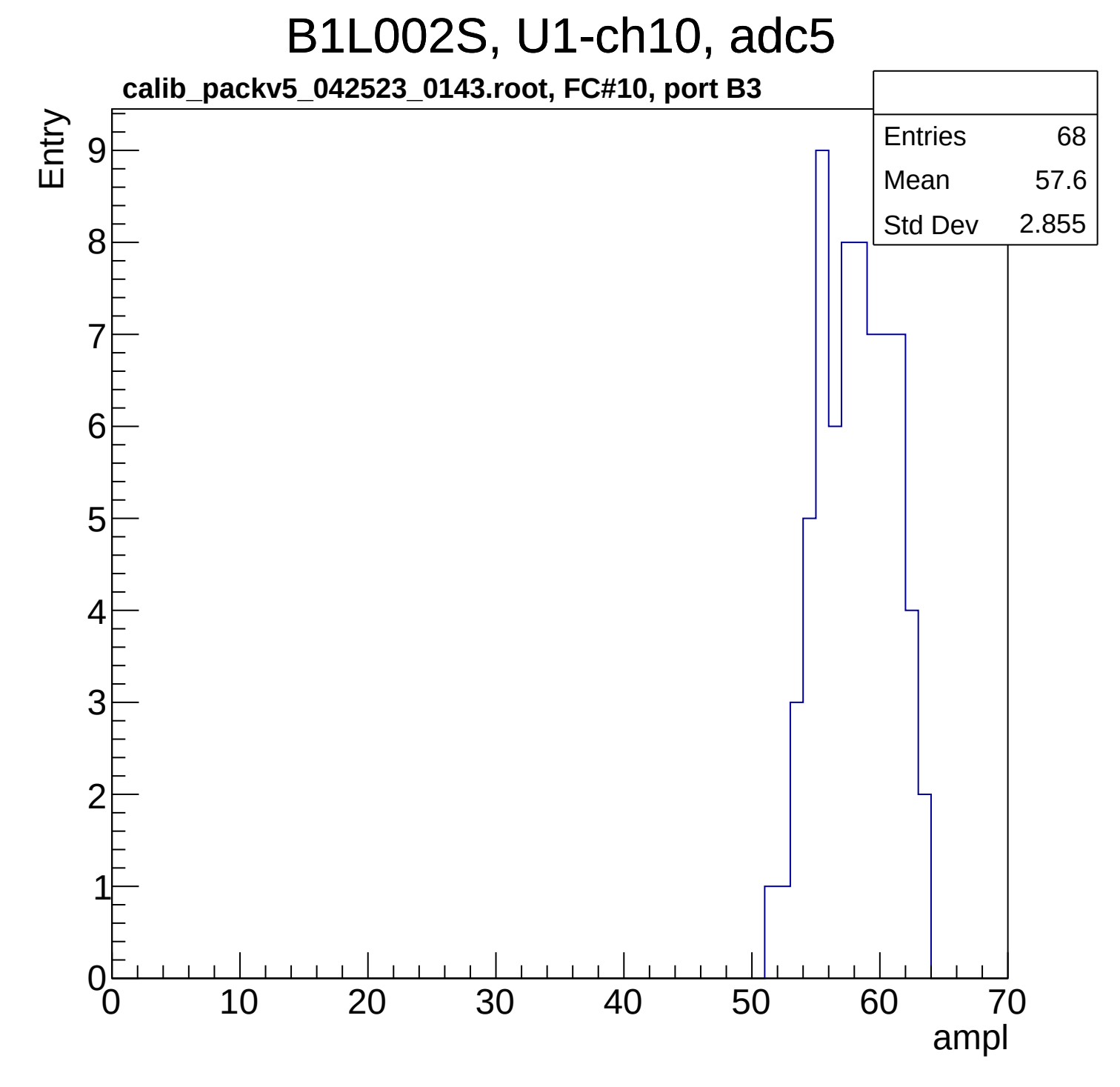
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch10, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.6
Std Dev 2.855
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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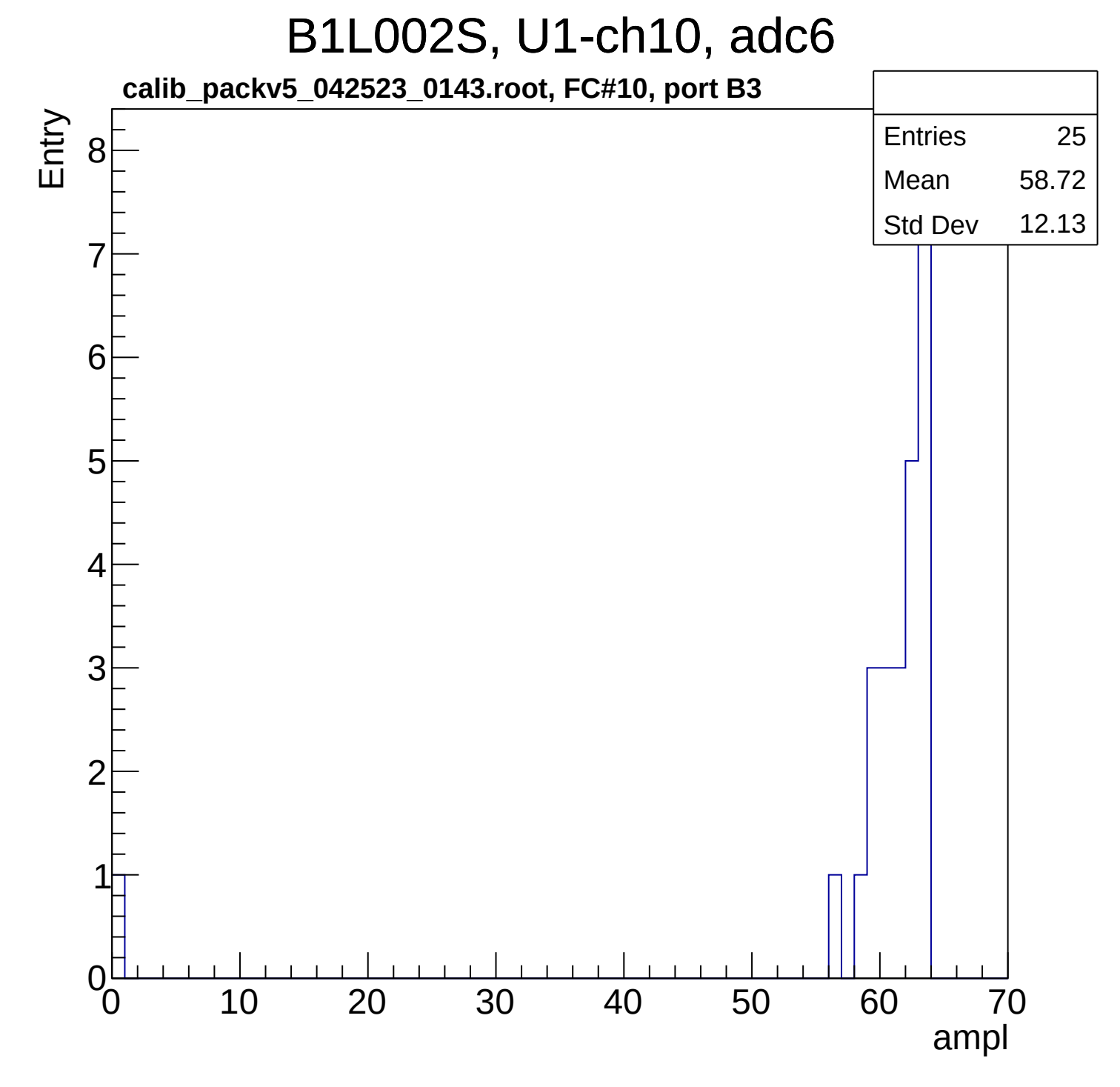
60
70


B1L002S, U1-ch10, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.72
Std Dev 12.13
Entry
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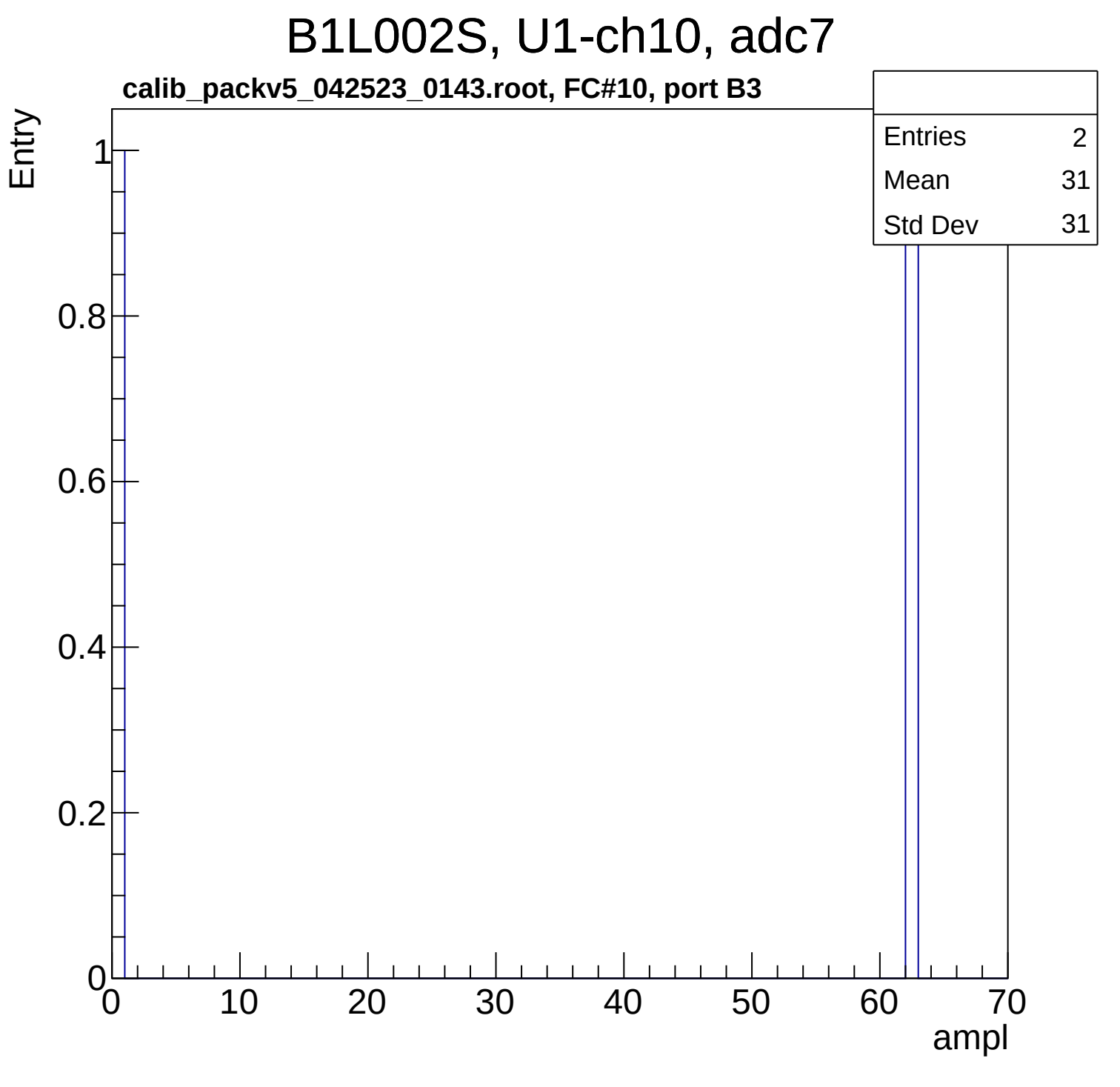
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch10, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
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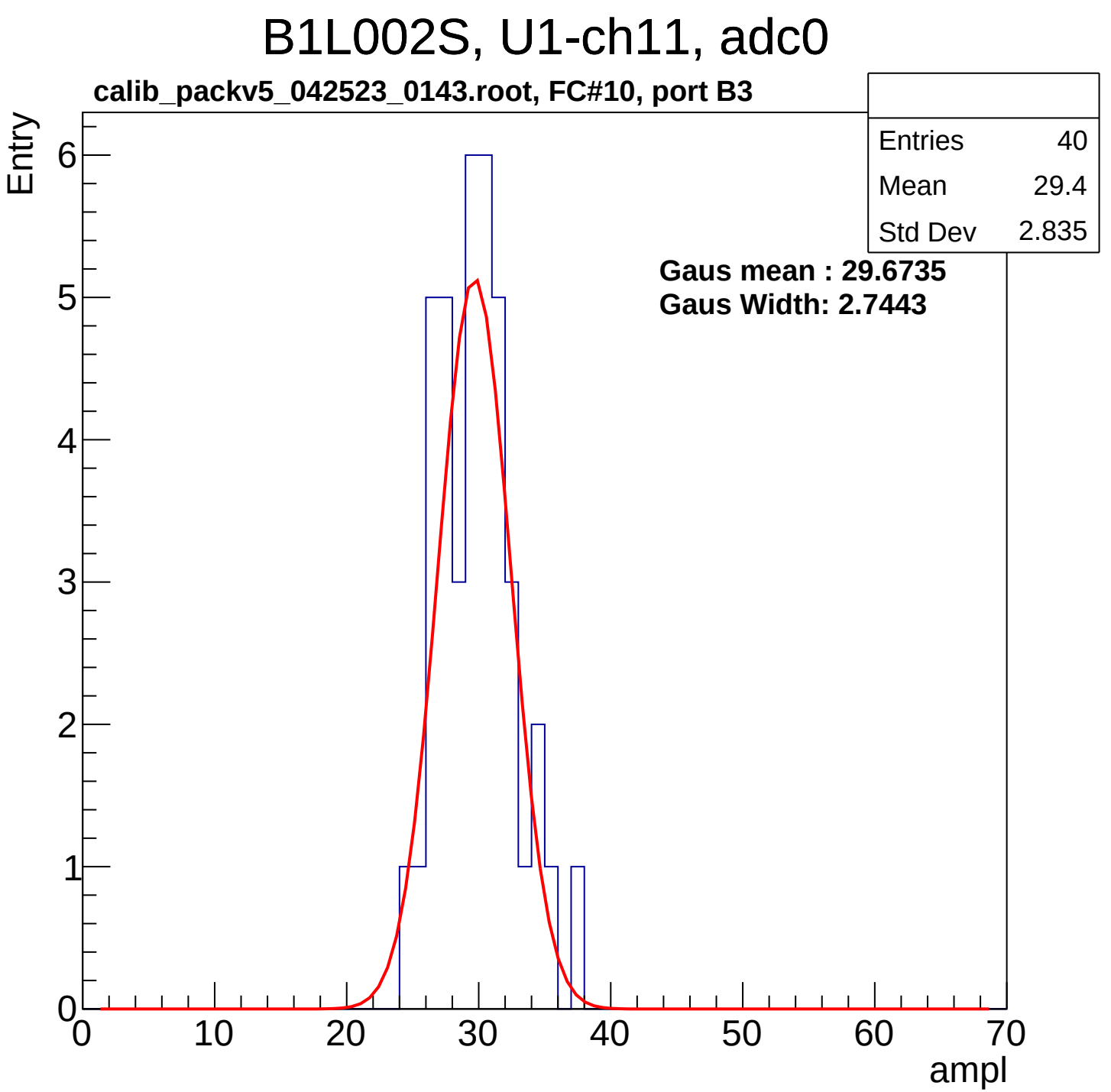
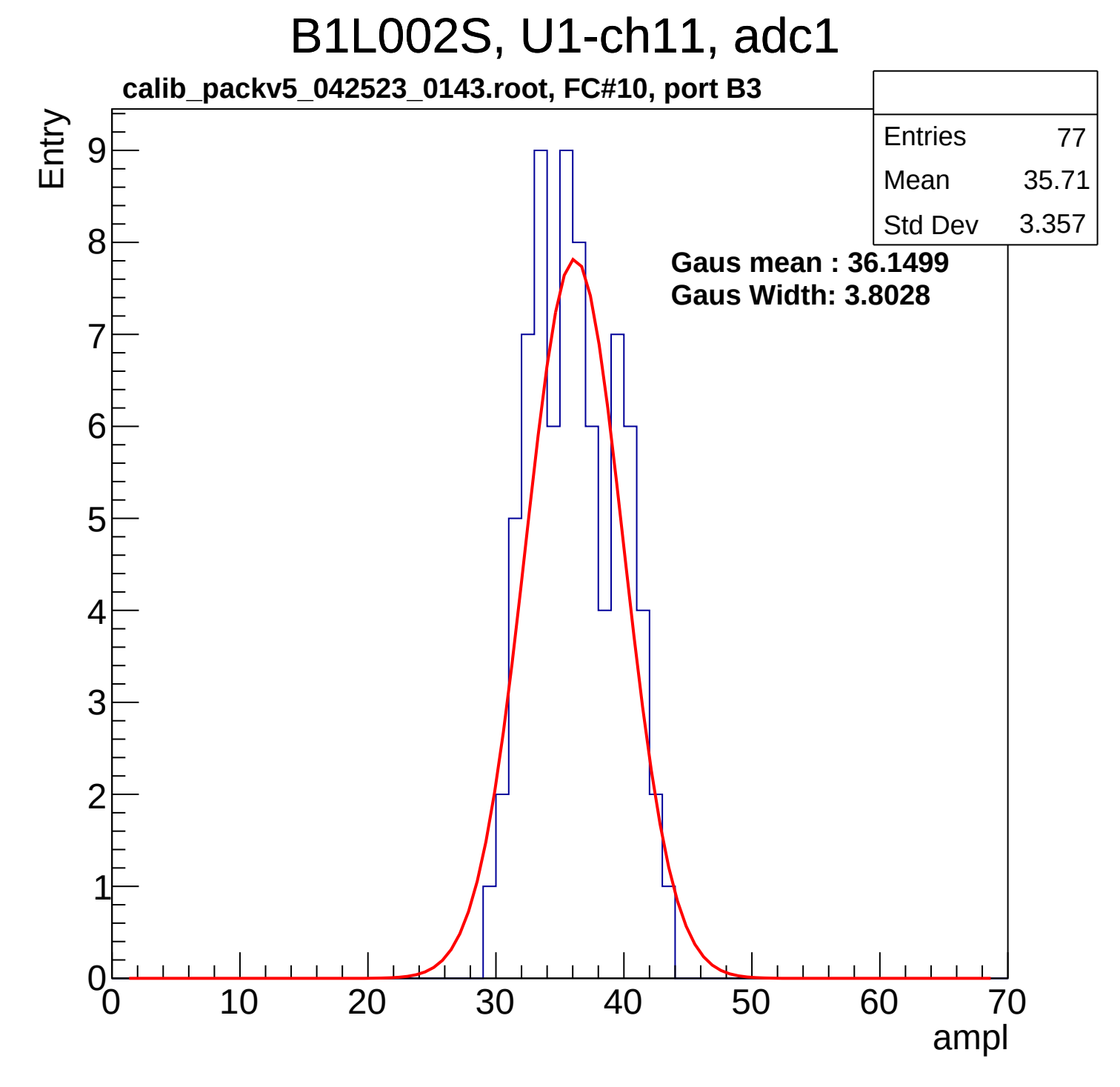
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch11, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 29.4
Std Dev 2.835
Gaus mean : 29.6735
Gaus Width: 2.7443
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U1-ch11, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.71
Std Dev 3.357
Gaus mean : 36.1499
Gaus Width: 3.8028
Entry
ampl
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2
3
4
5
6
7
8
9
0
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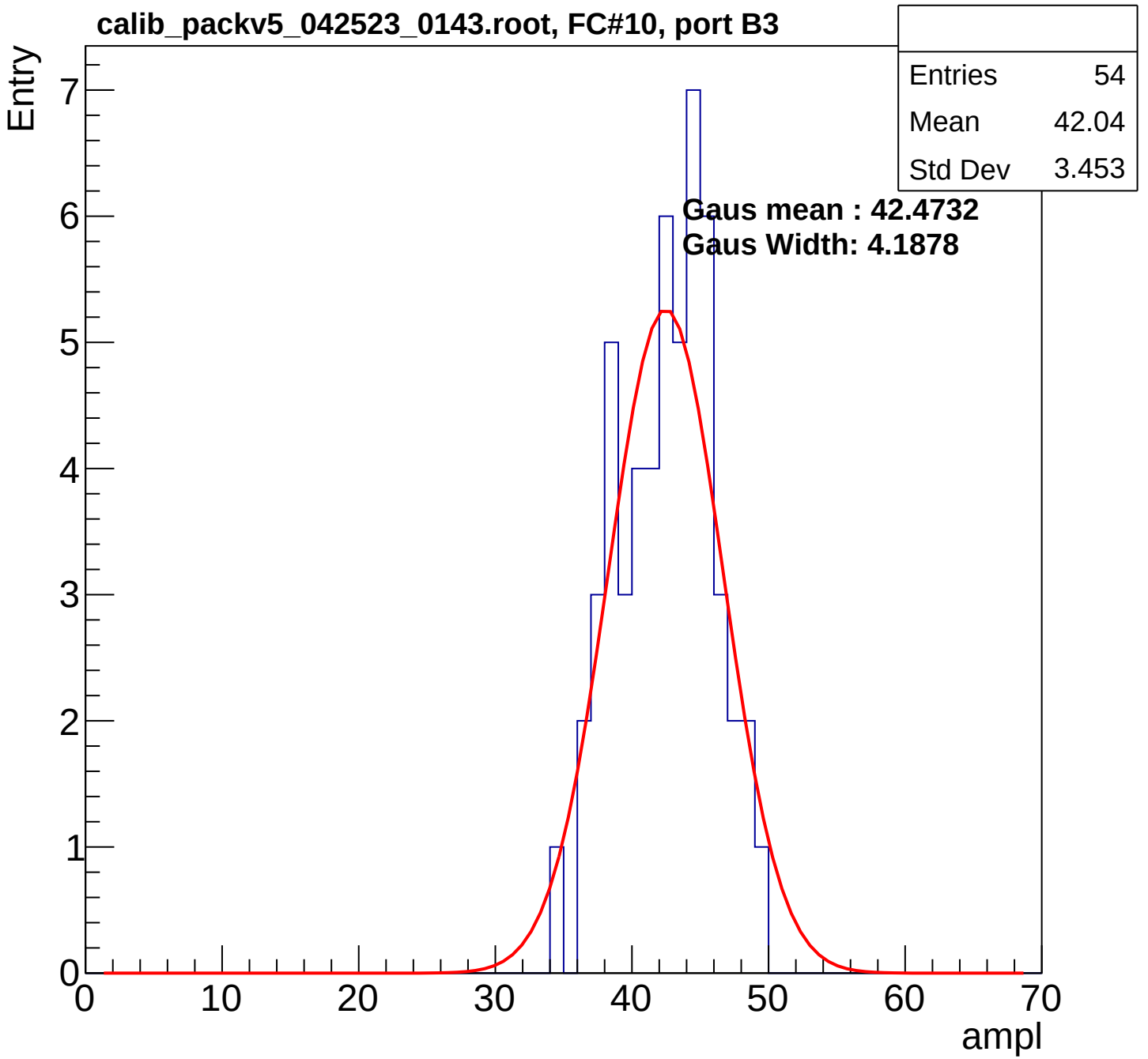
B1L002S, U1-ch11, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 42.04
Std Dev 3.453
Gaus mean : 42.4732
Gaus Width: 4.1878
Entry
ampl
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1
2
3
4
5
6
7
0
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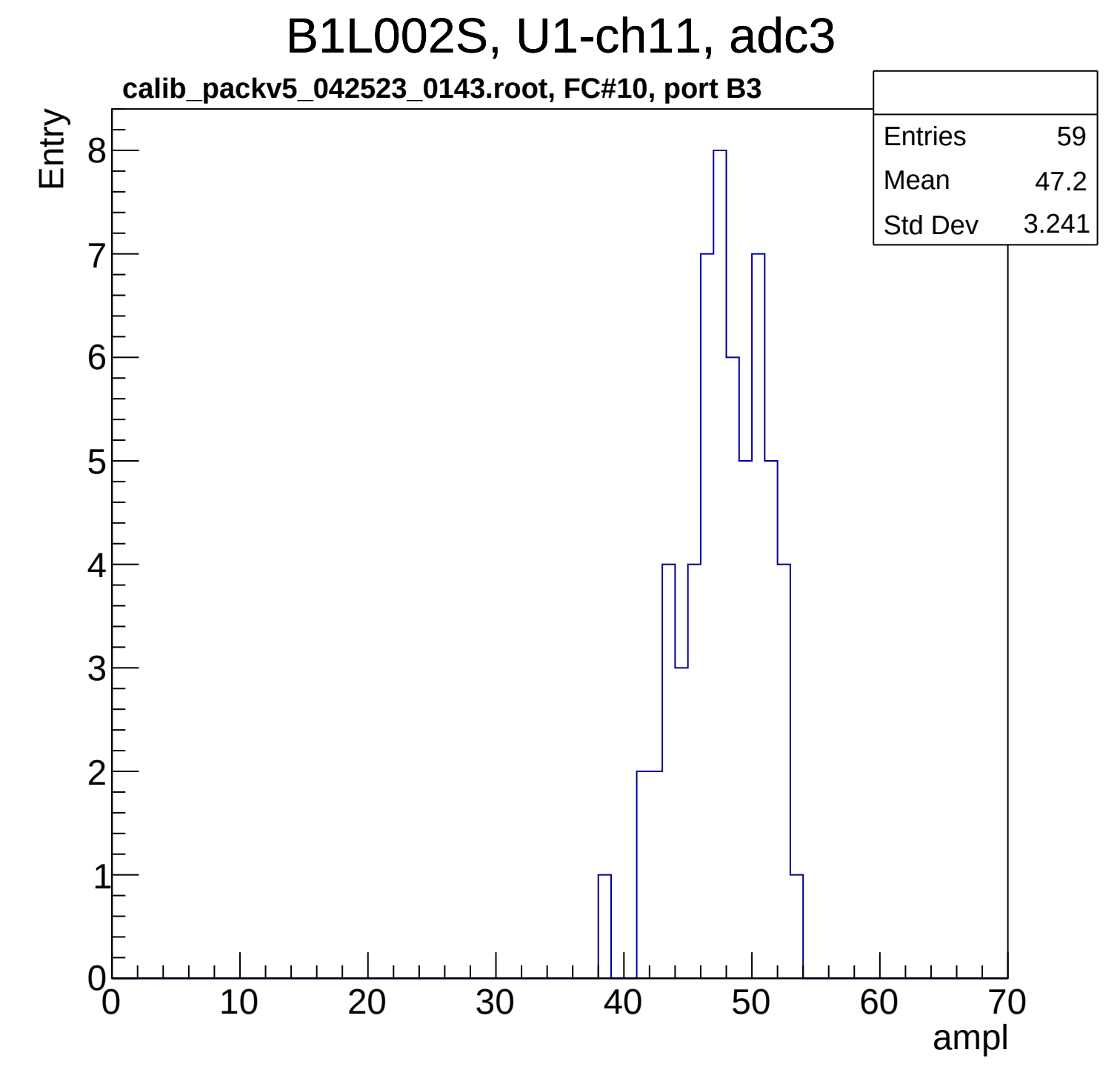
B1L002S, U1-ch11, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.2
Std Dev 3.241
Entry
ampl
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5
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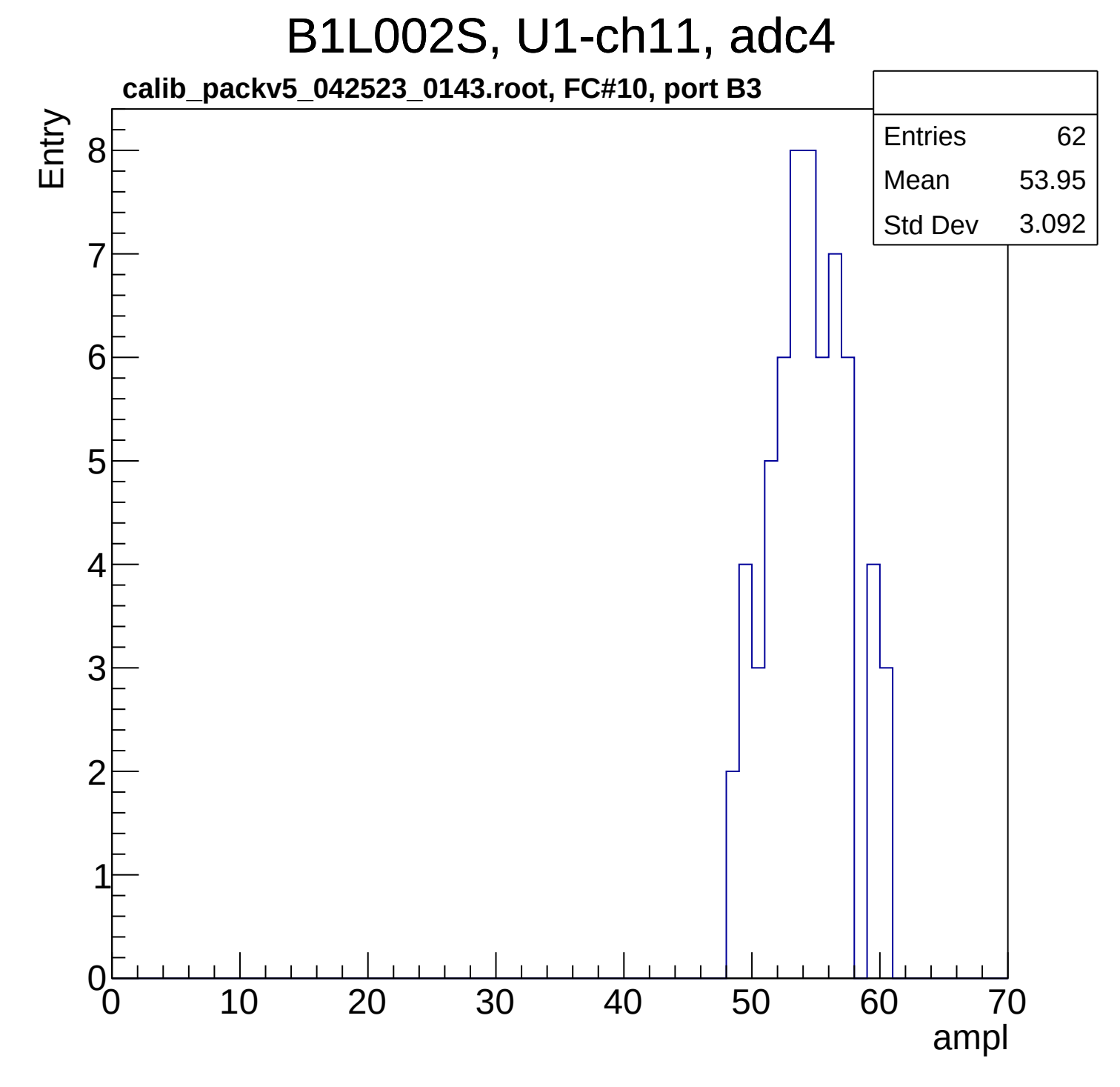
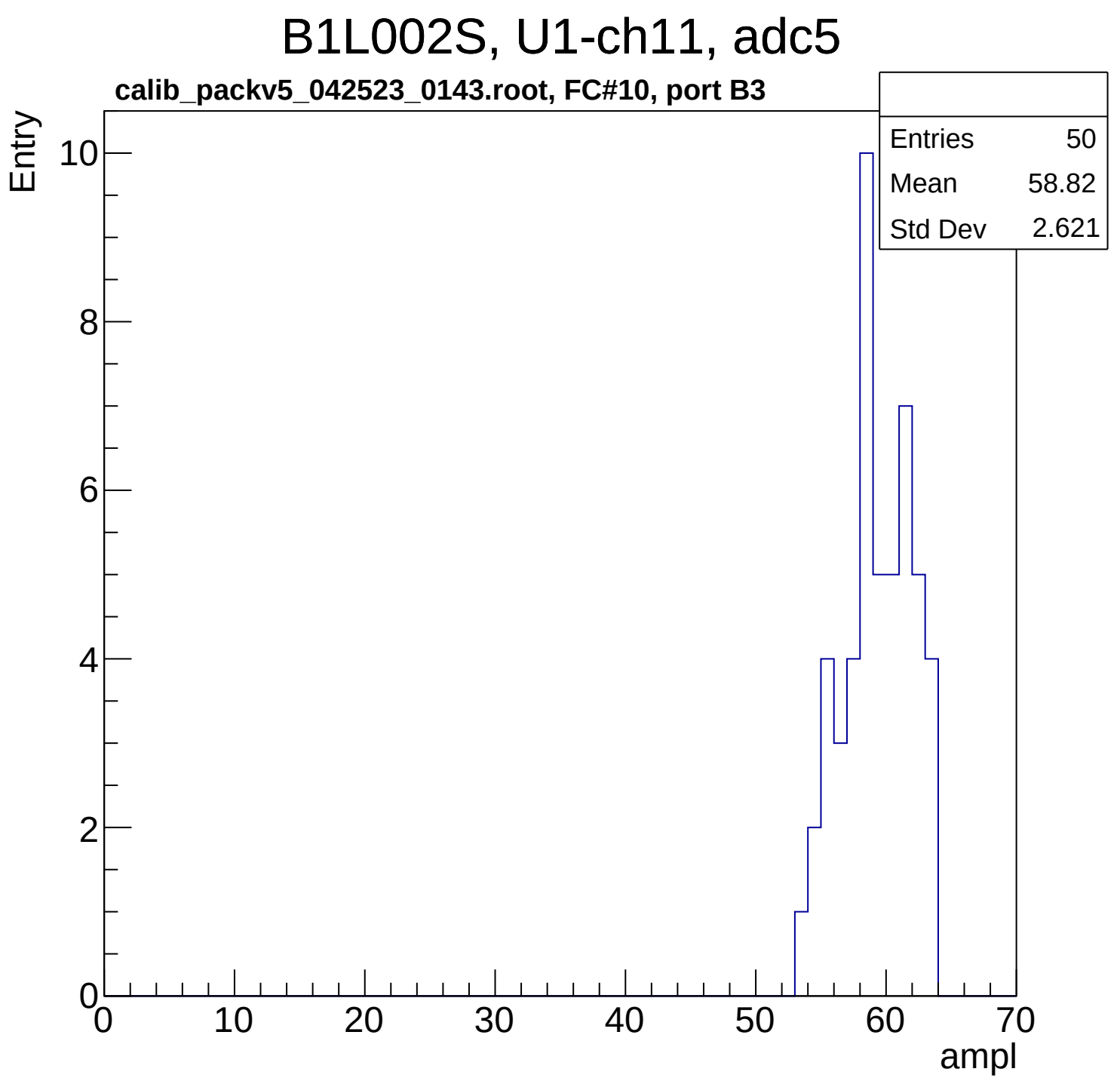

B1L002S, U1-ch11, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 53.95
Std Dev 3.092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch11, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.82
Std Dev 2.621
Entry
0
2
4
6
8
10
0
10
20
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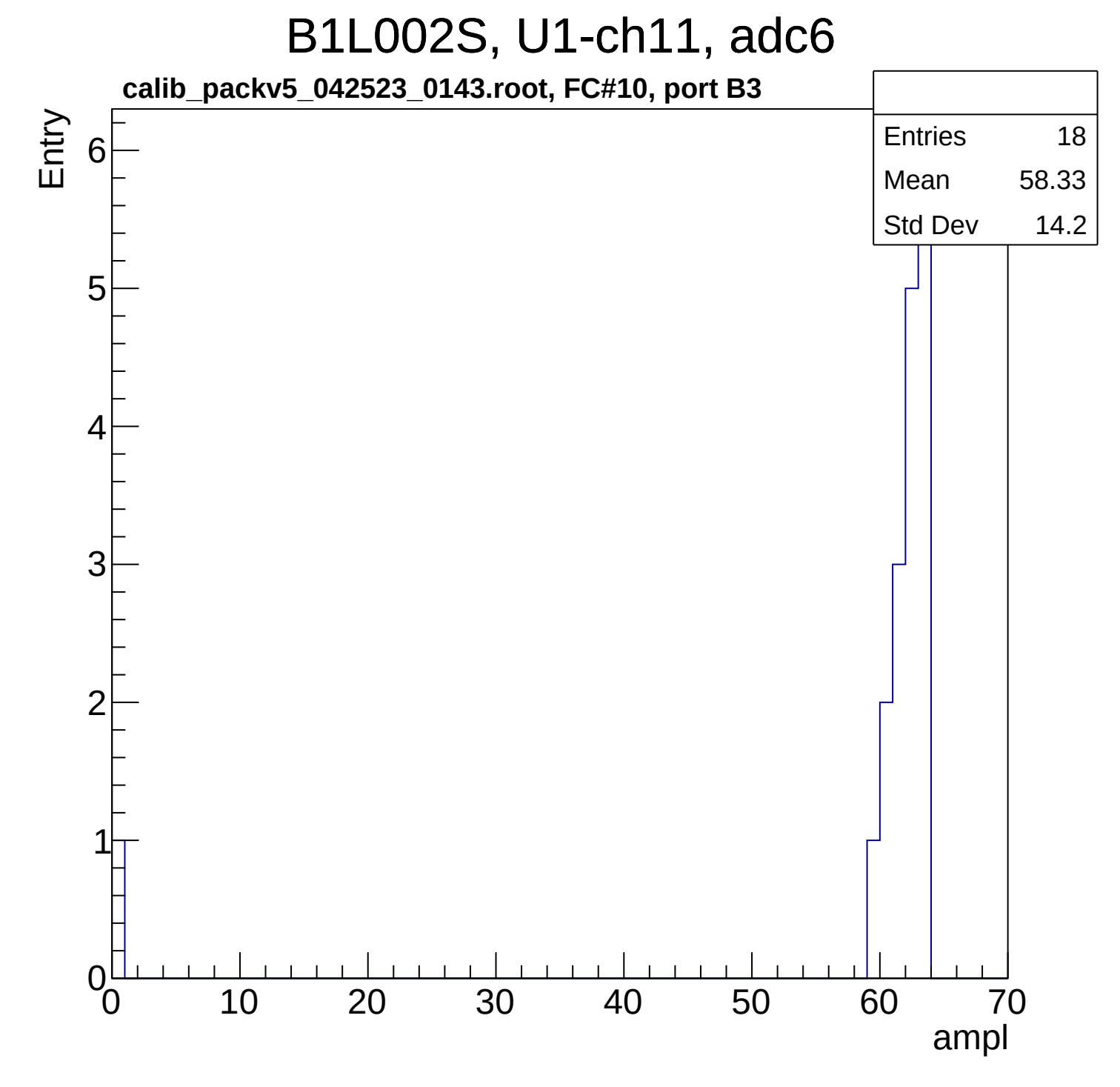
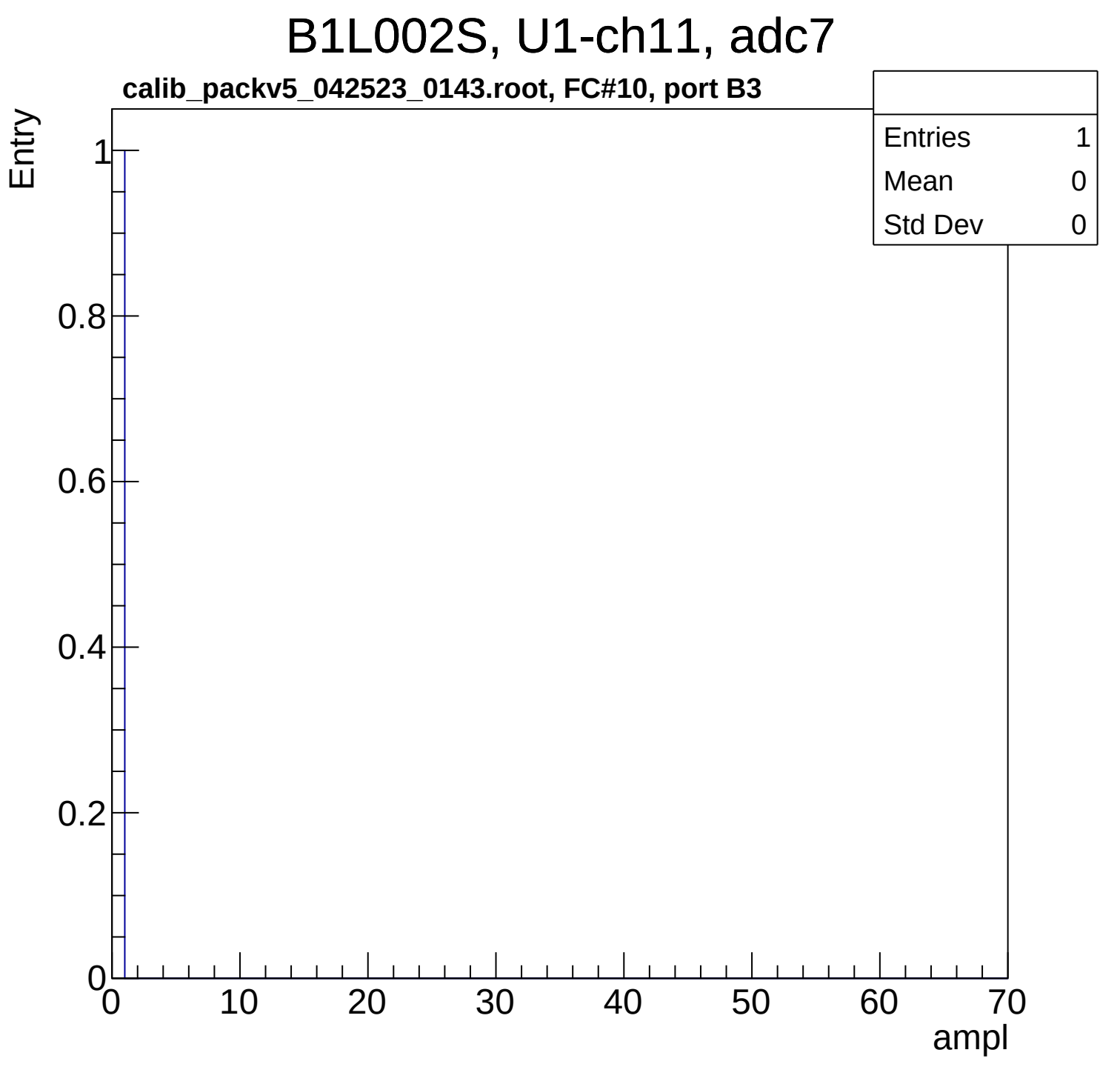
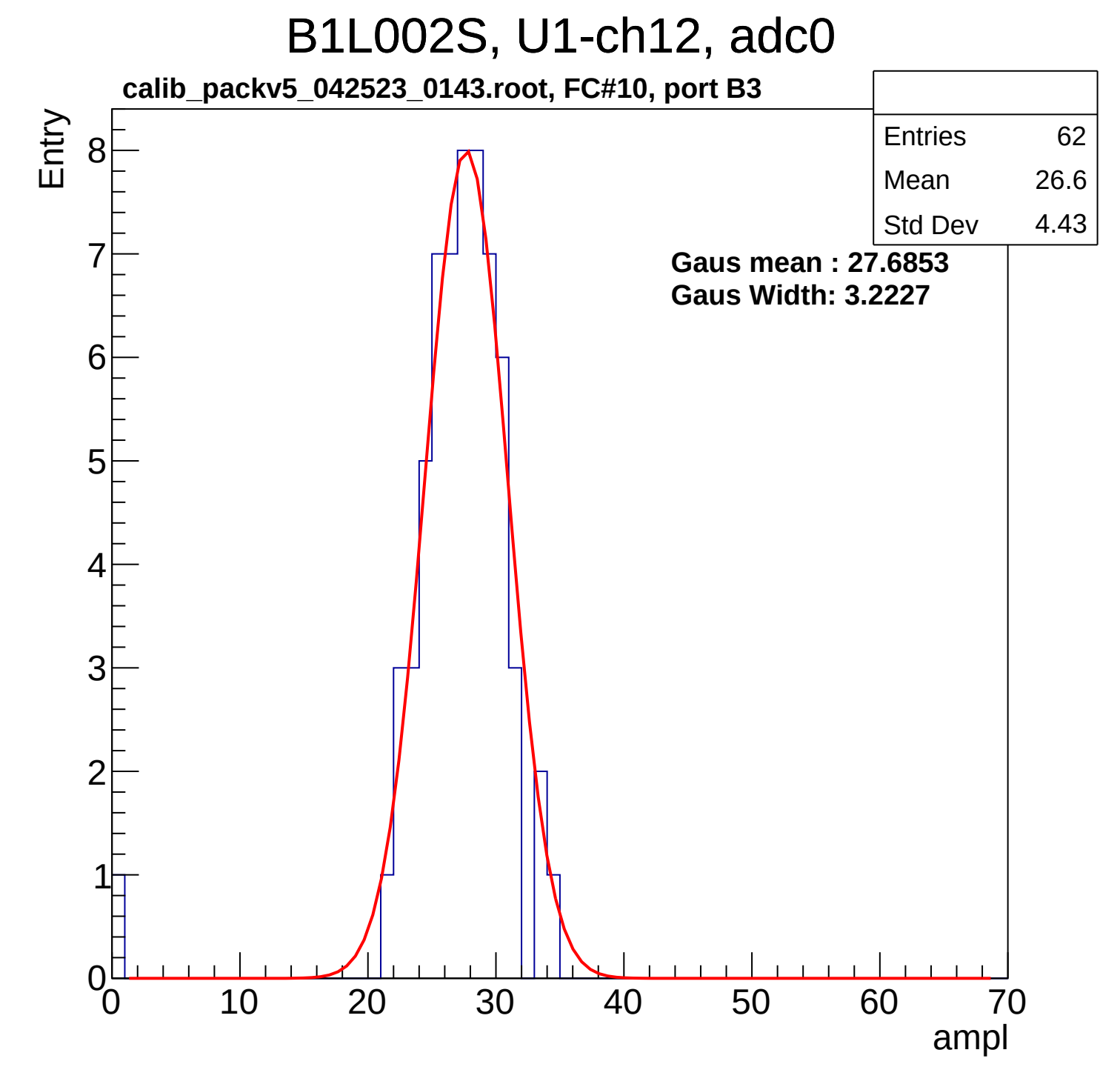
50
60
70
ampl


B1L002S, U1-ch11, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.33
Std Dev 14.2
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch11, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch12, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.6
Std Dev 4.43
Gaus mean : 27.6853
Gaus Width: 3.2227
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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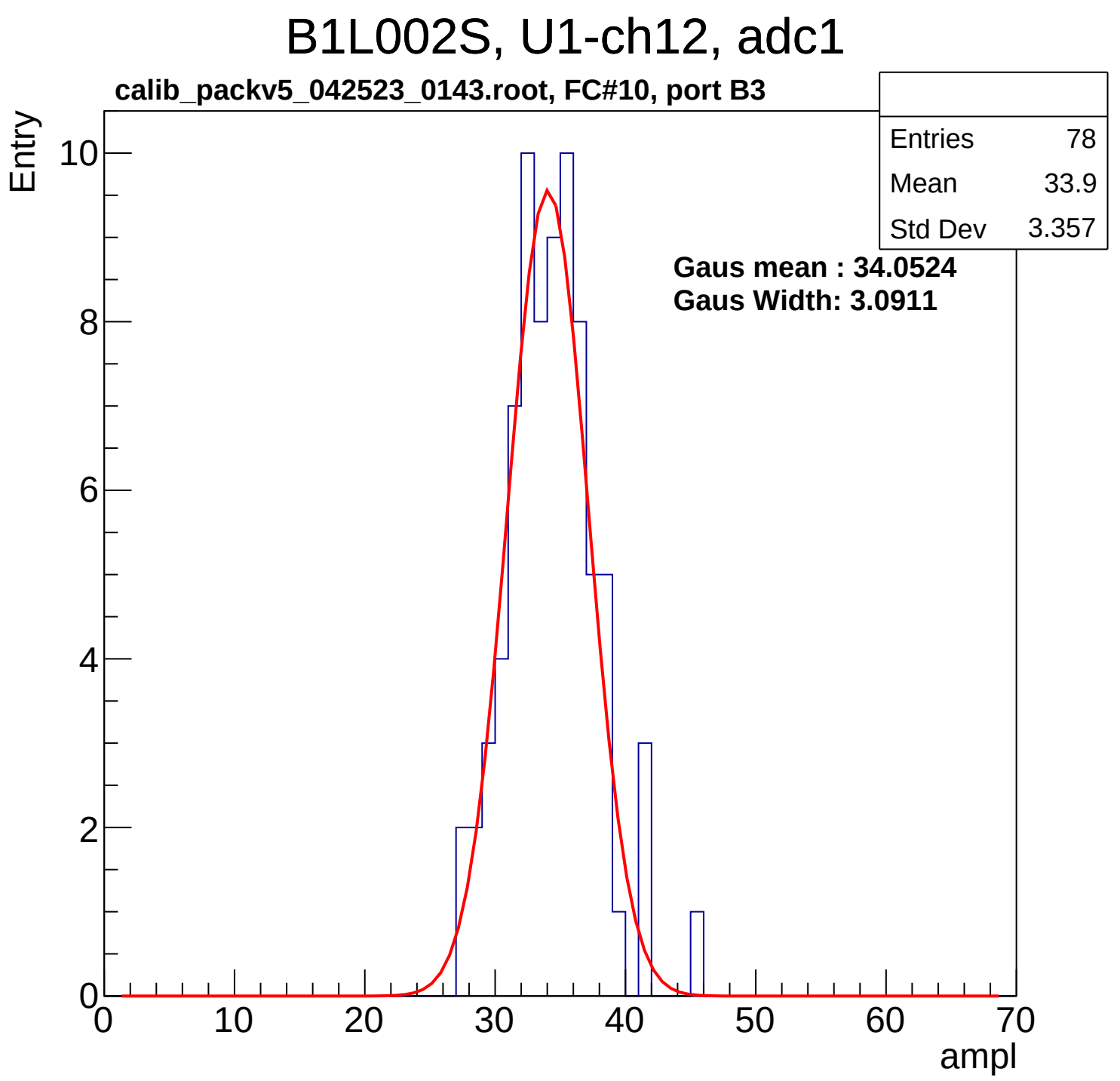
B1L002S, U1-ch12, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 33.9
Std Dev 3.357
Gaus mean : 34.0524
Gaus Width: 3.0911
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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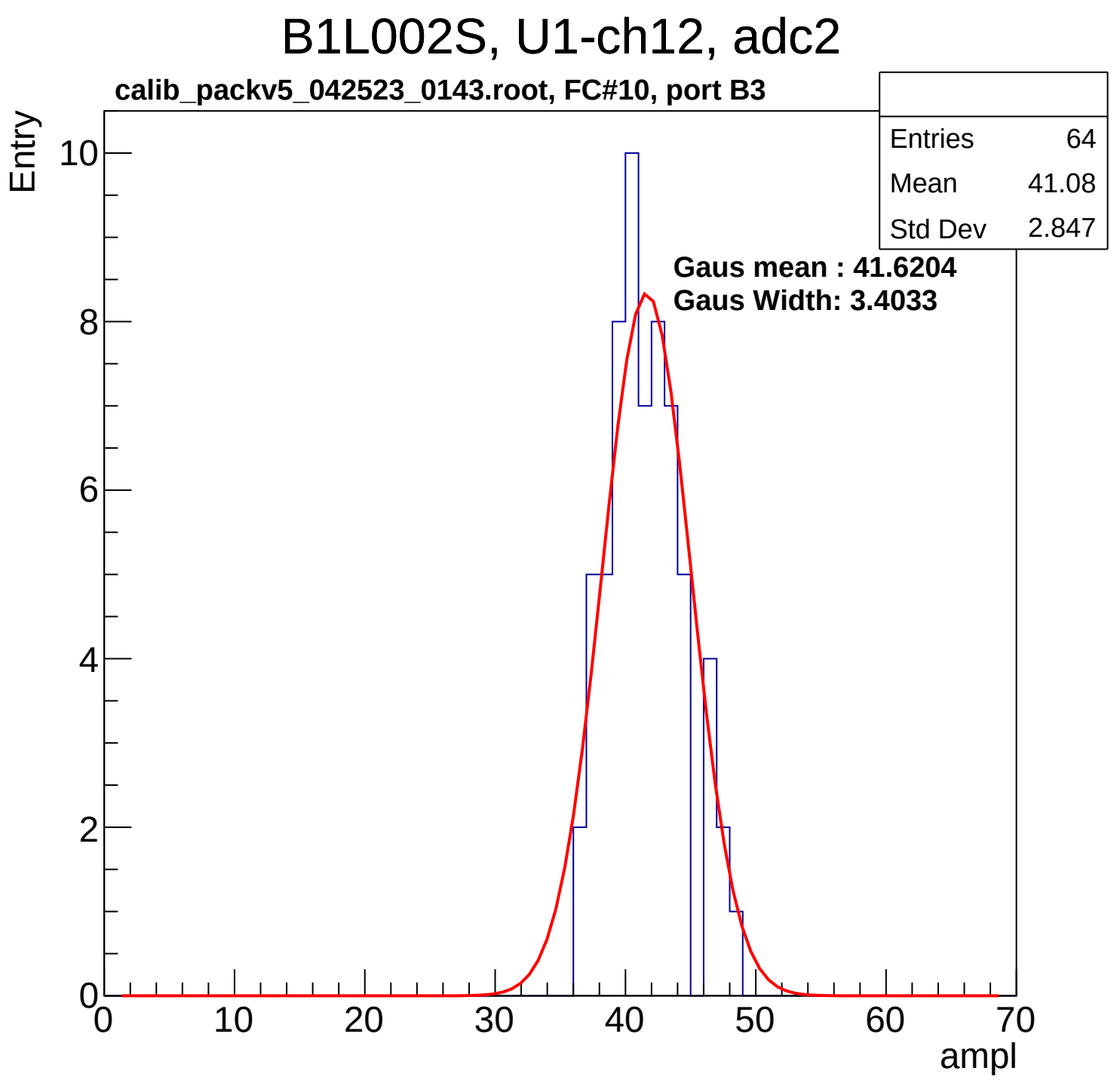
B1L002S, U1-ch12, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.08
Std Dev 2.847
Gaus mean : 41.6204
Gaus Width: 3.4033
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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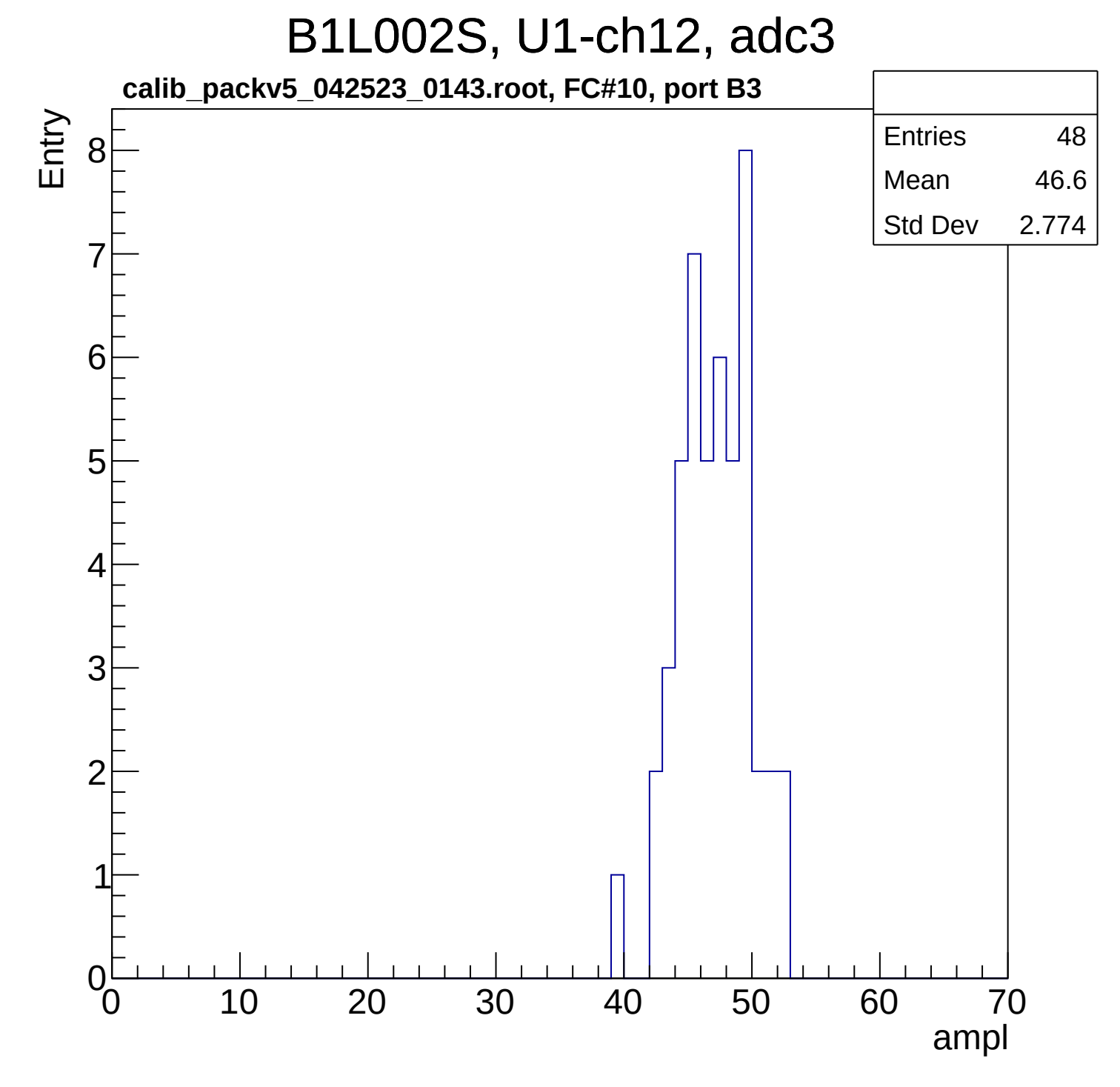
B1L002S, U1-ch12, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 46.6
Std Dev 2.774
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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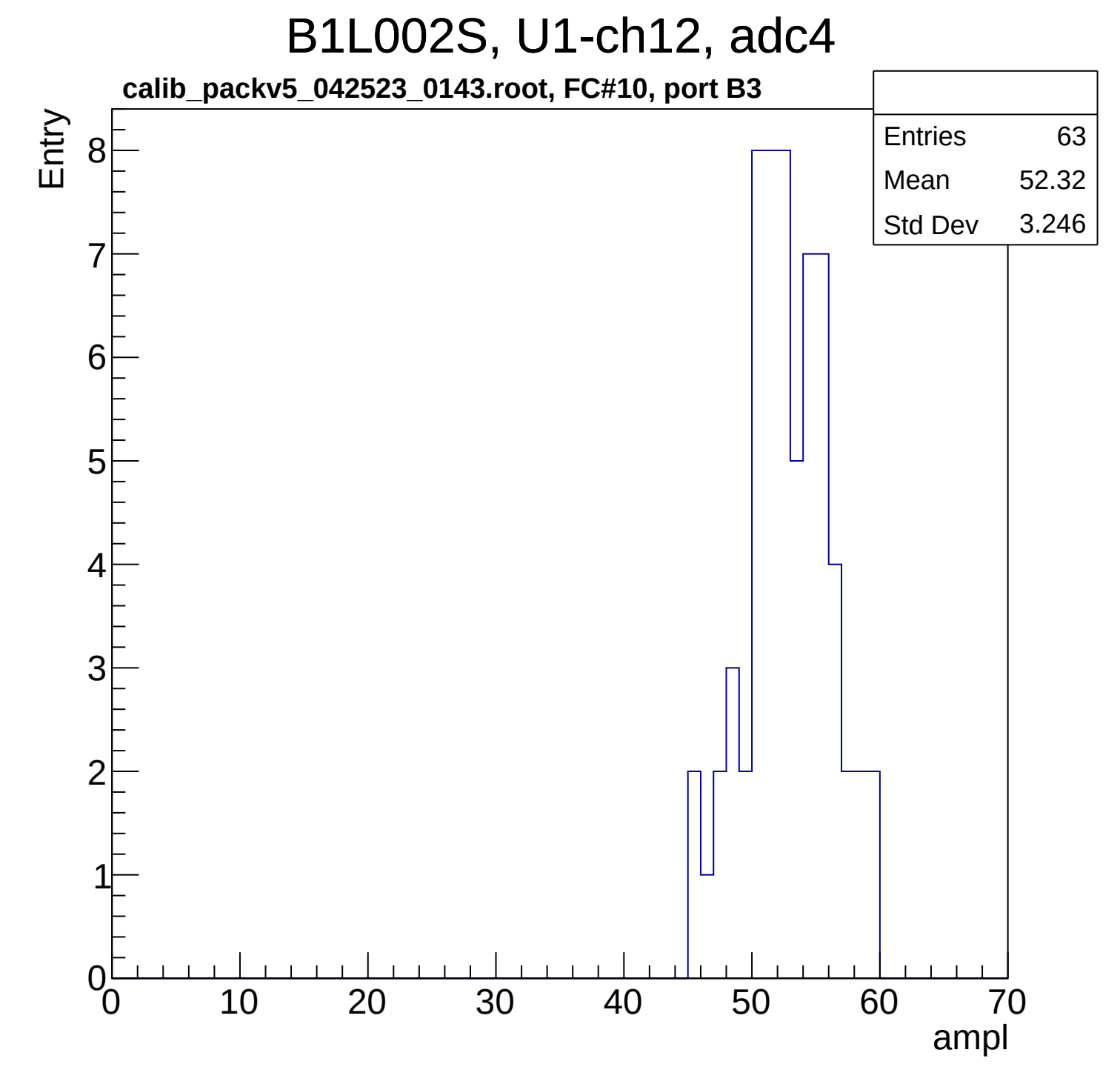
B1L002S, U1-ch12, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 52.32
Std Dev 3.246
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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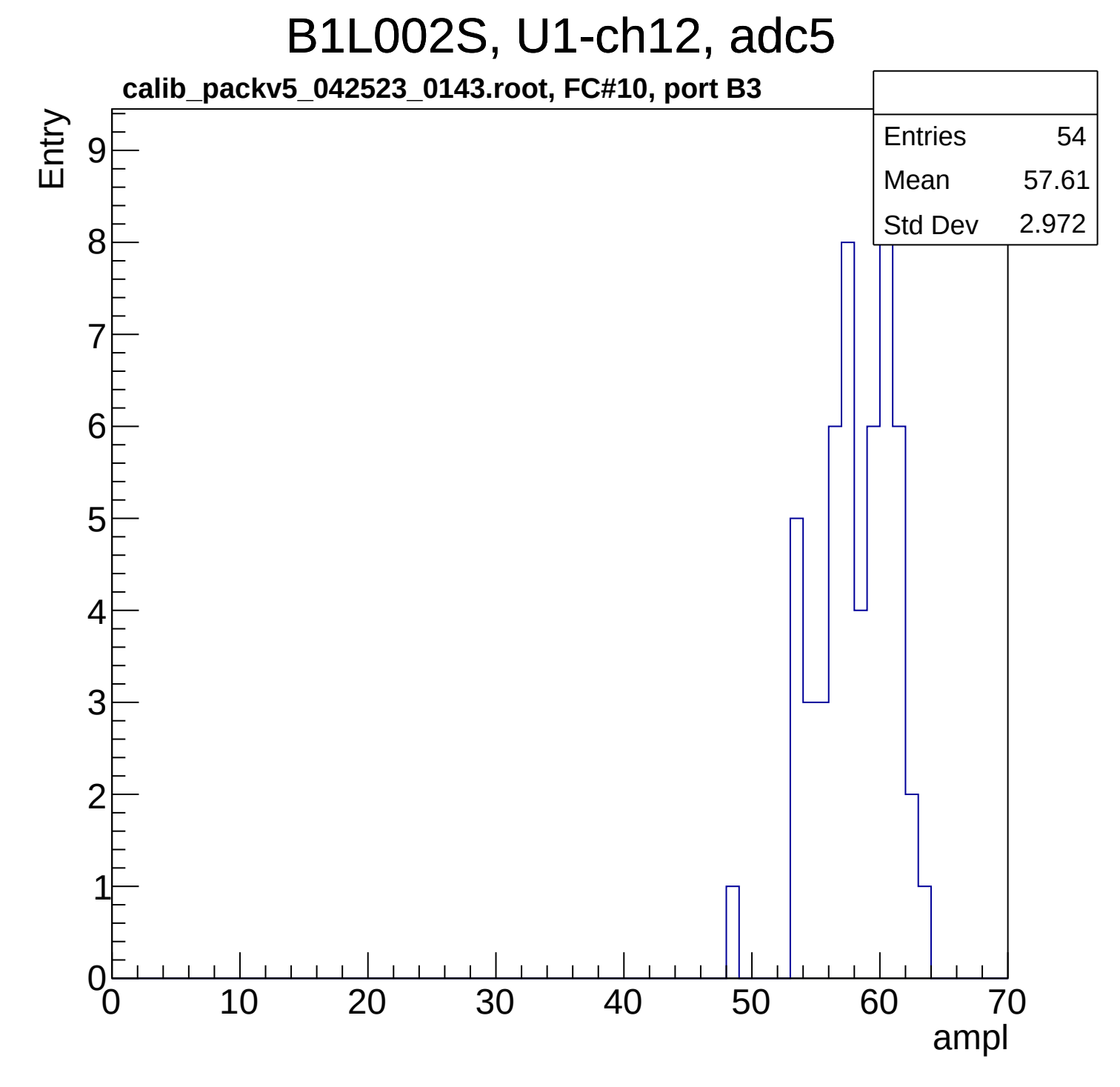
B1L002S, U1-ch12, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.61
Std Dev 2.972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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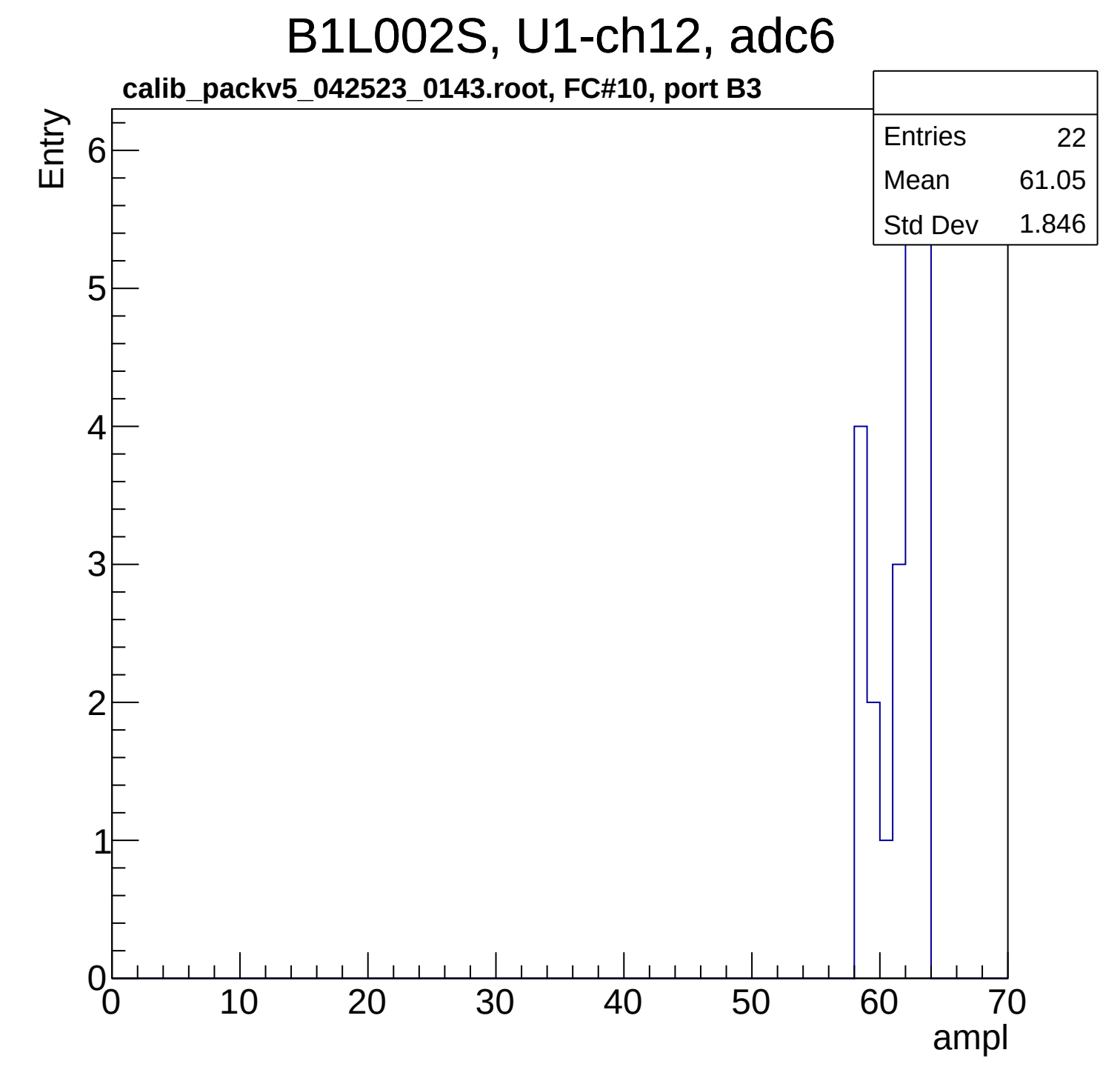
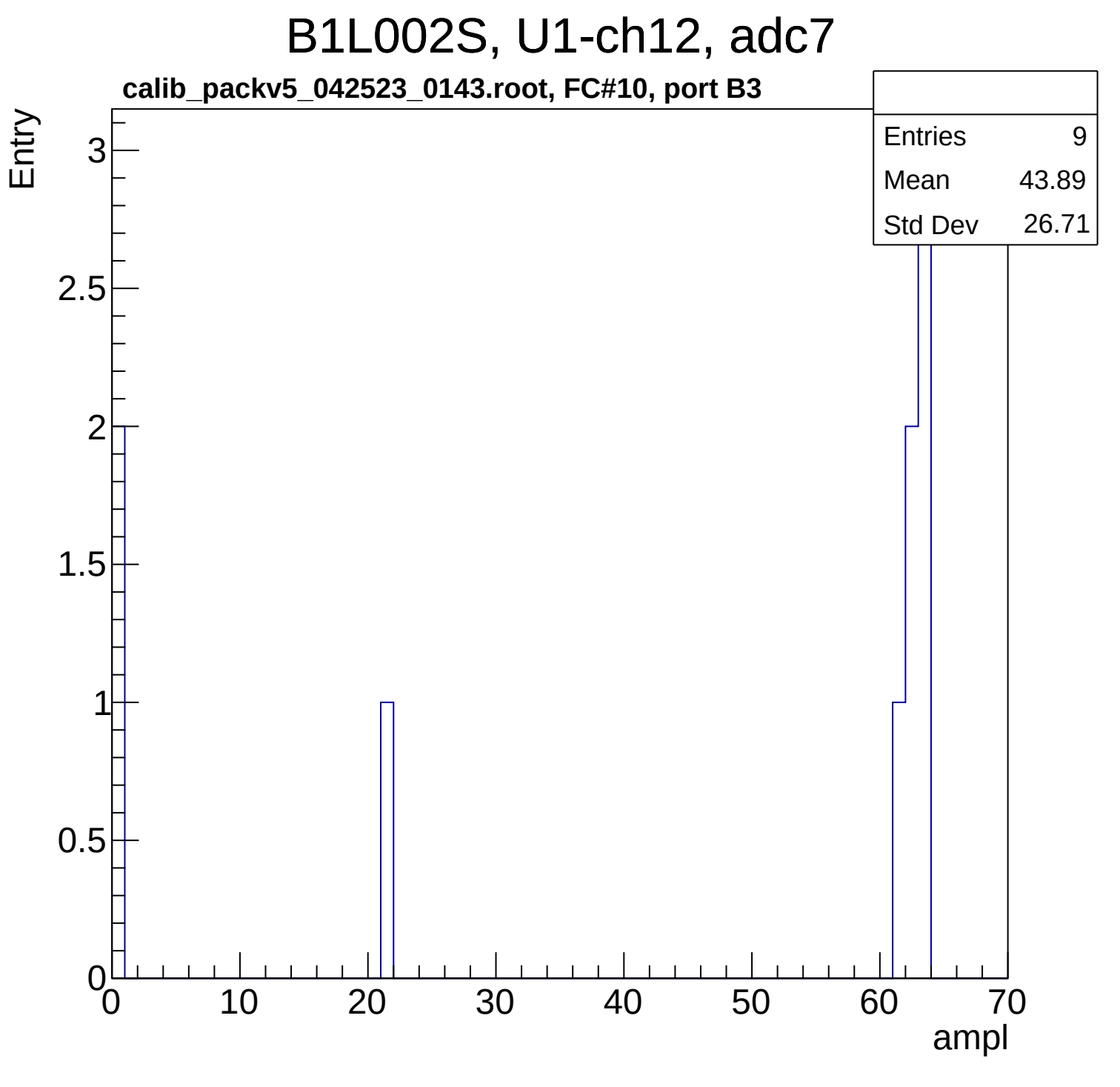
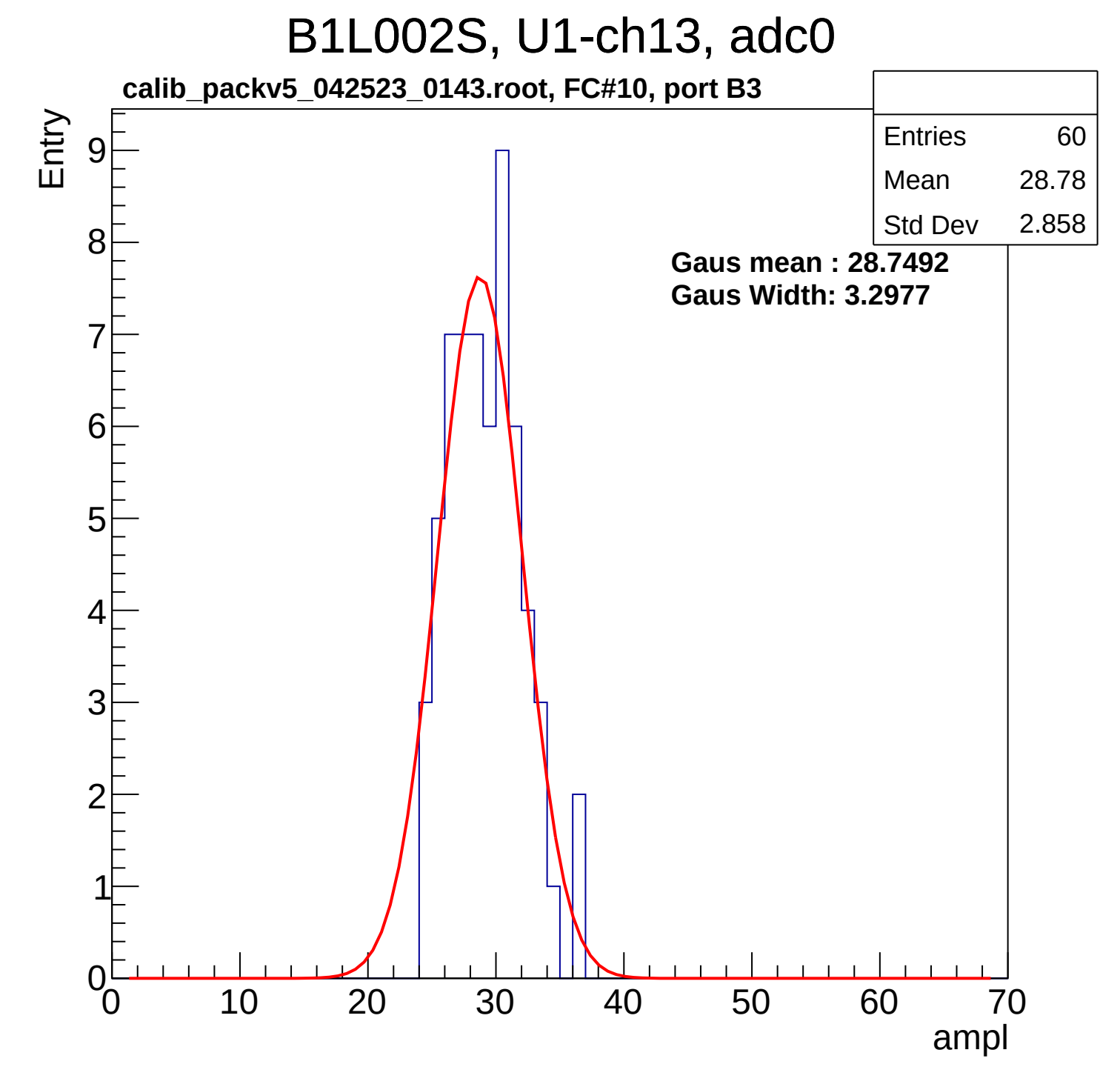
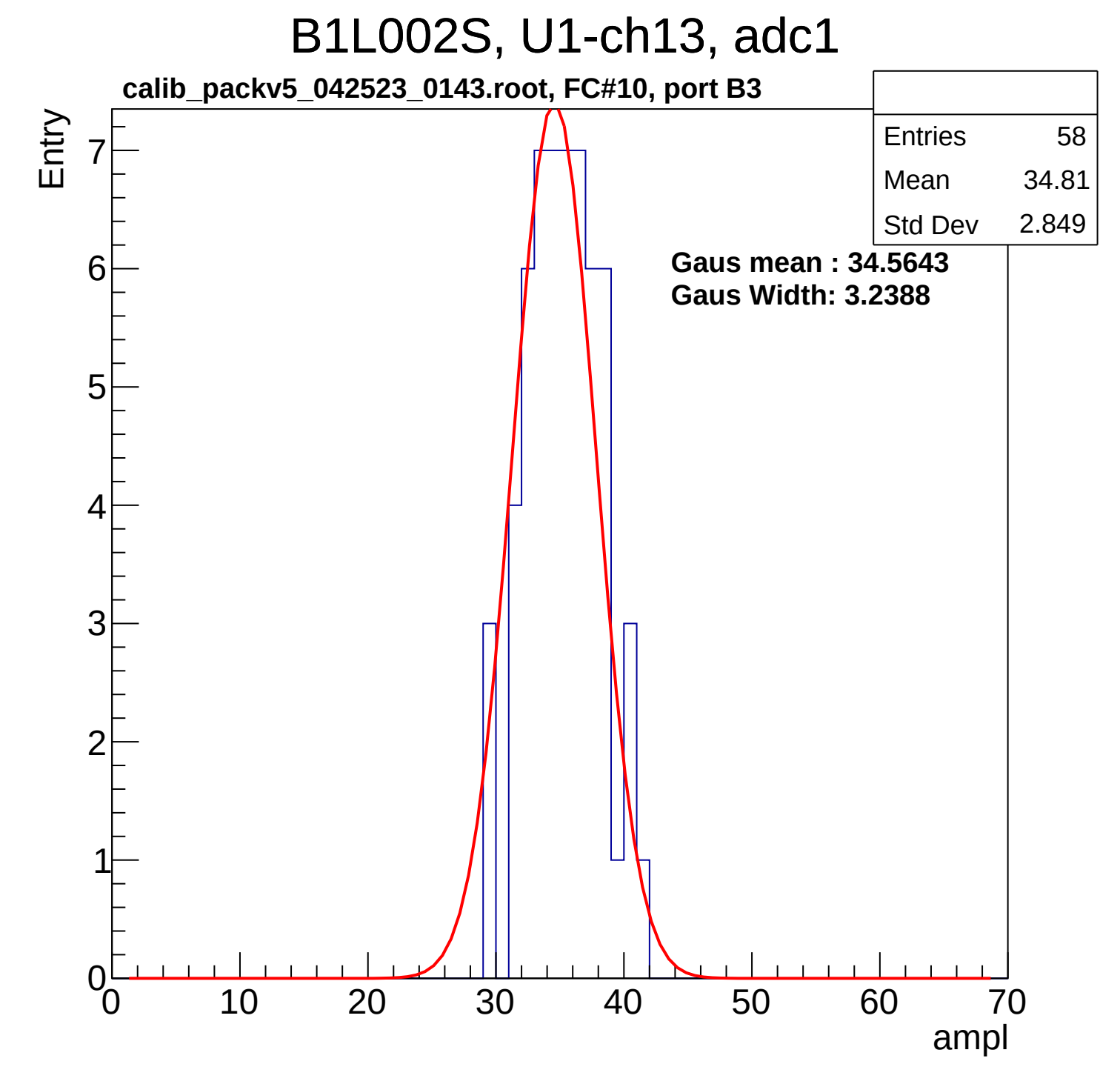
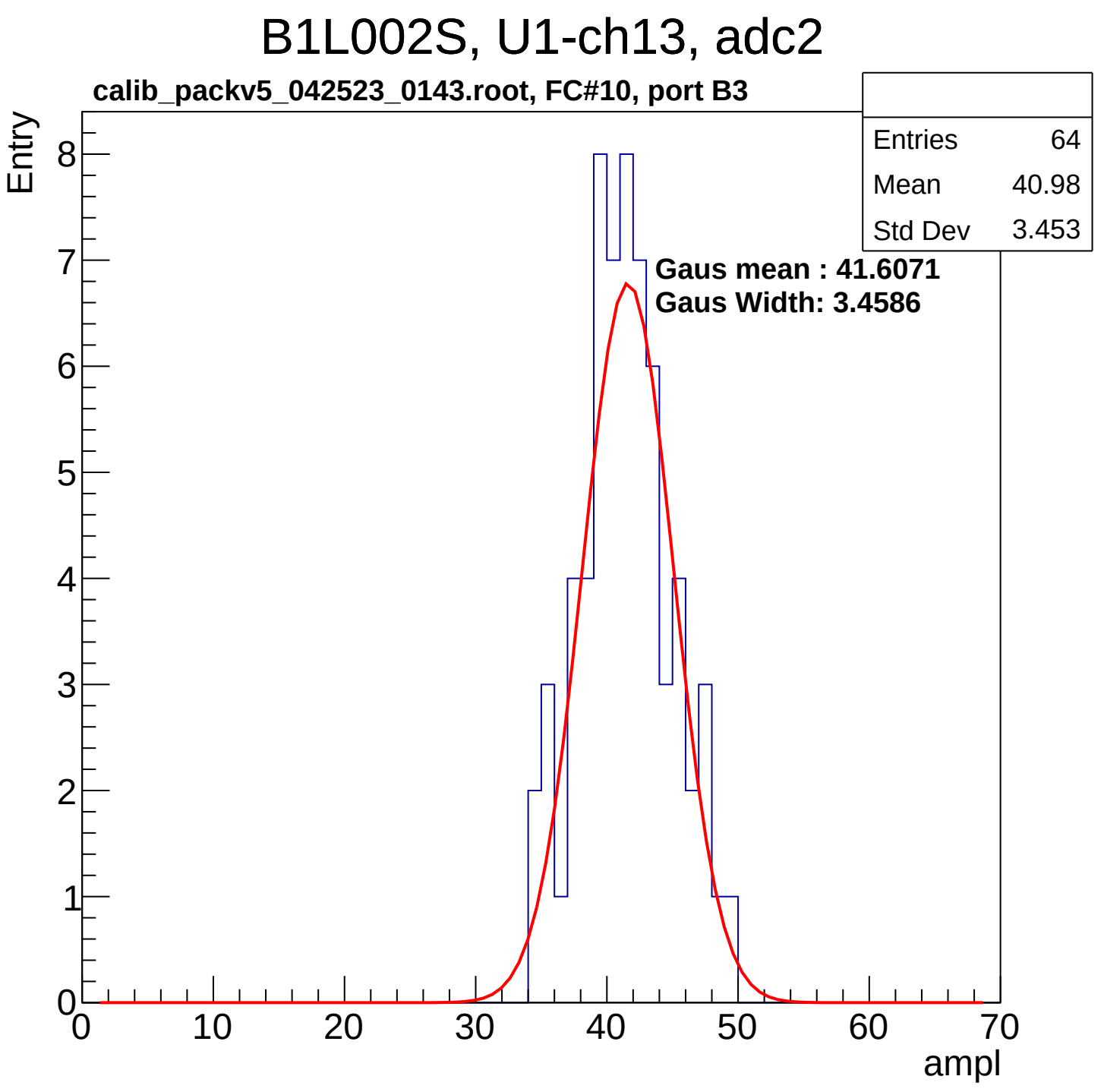

B1L002S, U1-ch12, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.05
Std Dev 1.846
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch12, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 43.89
Std Dev 26.71
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch13, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.78
Std Dev 2.858
Gaus mean : 28.7492
Gaus Width: 3.2977
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch13, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.81
Std Dev 2.849
Gaus mean : 34.5643
Gaus Width: 3.2388
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch13, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.98
Std Dev 3.453
Gaus mean : 41.6071
Gaus Width: 3.4586
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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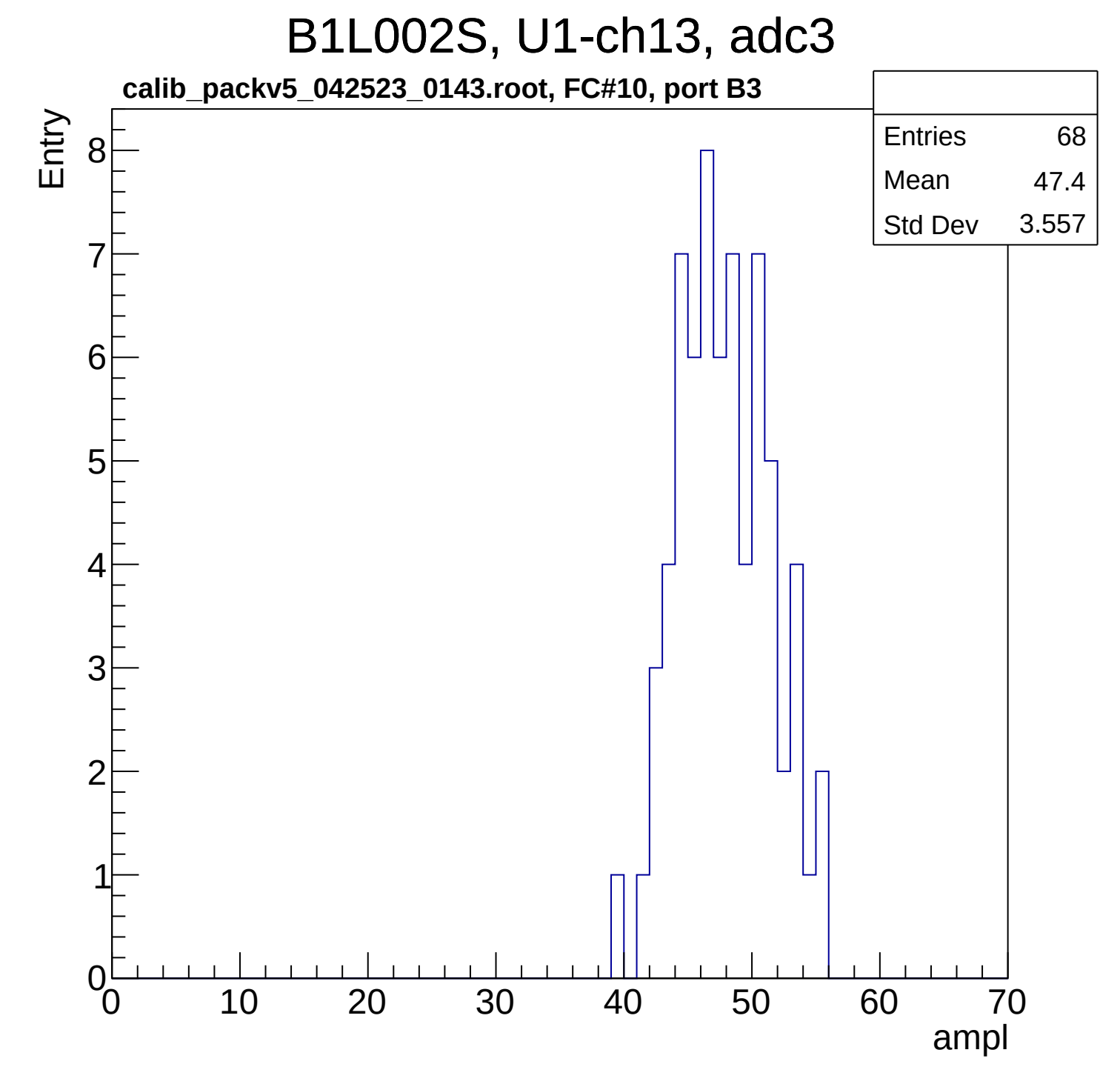
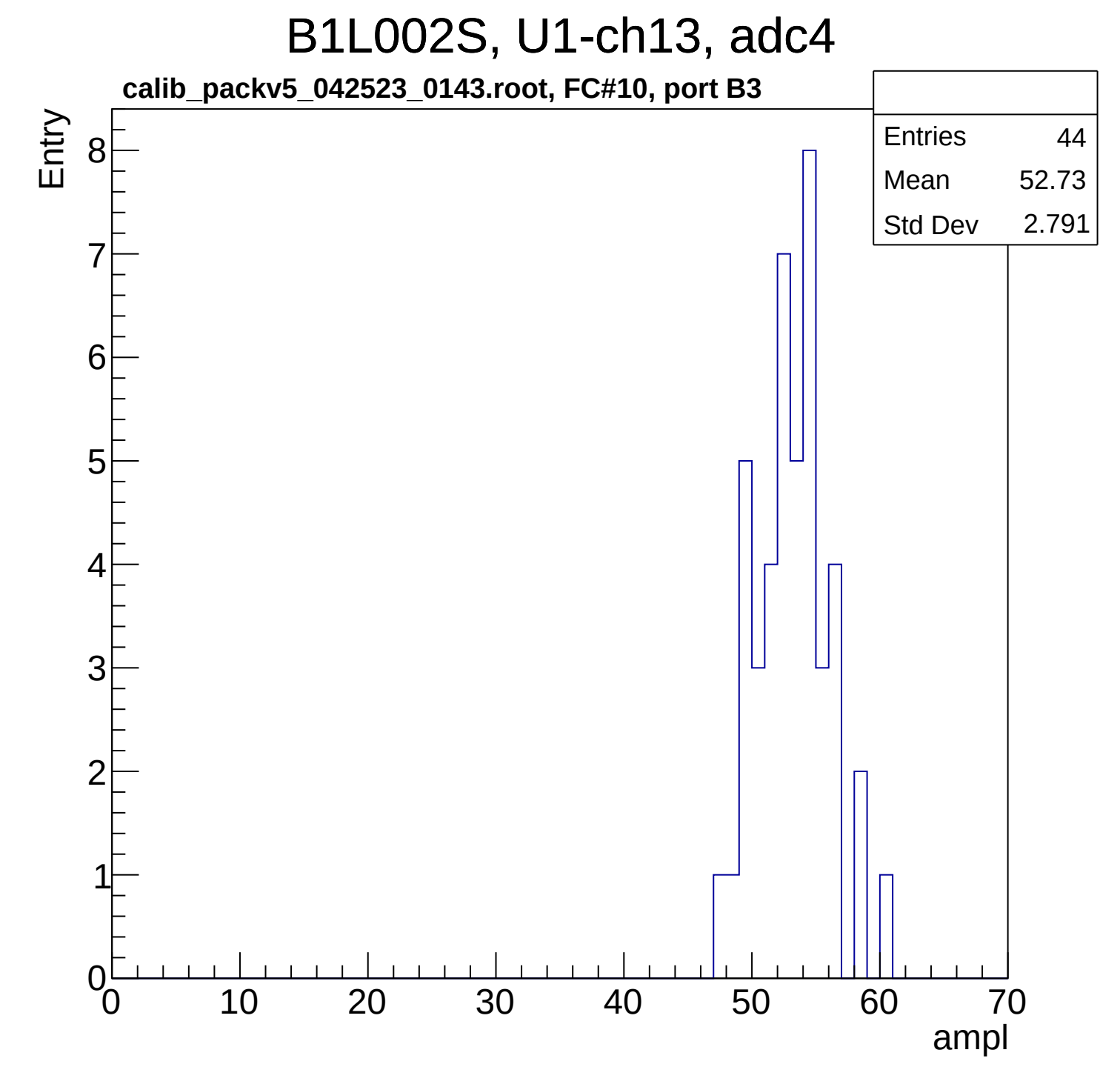
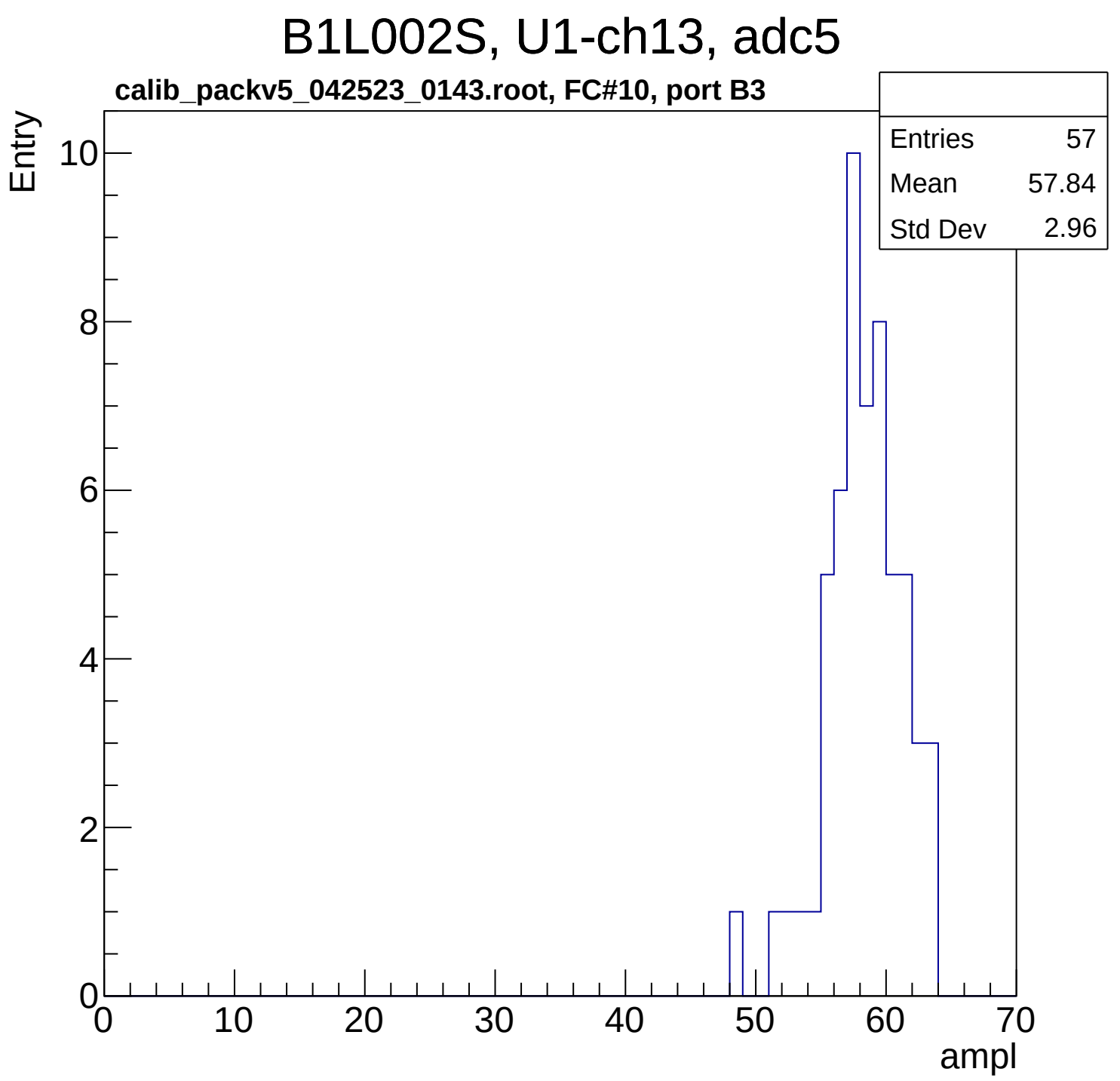
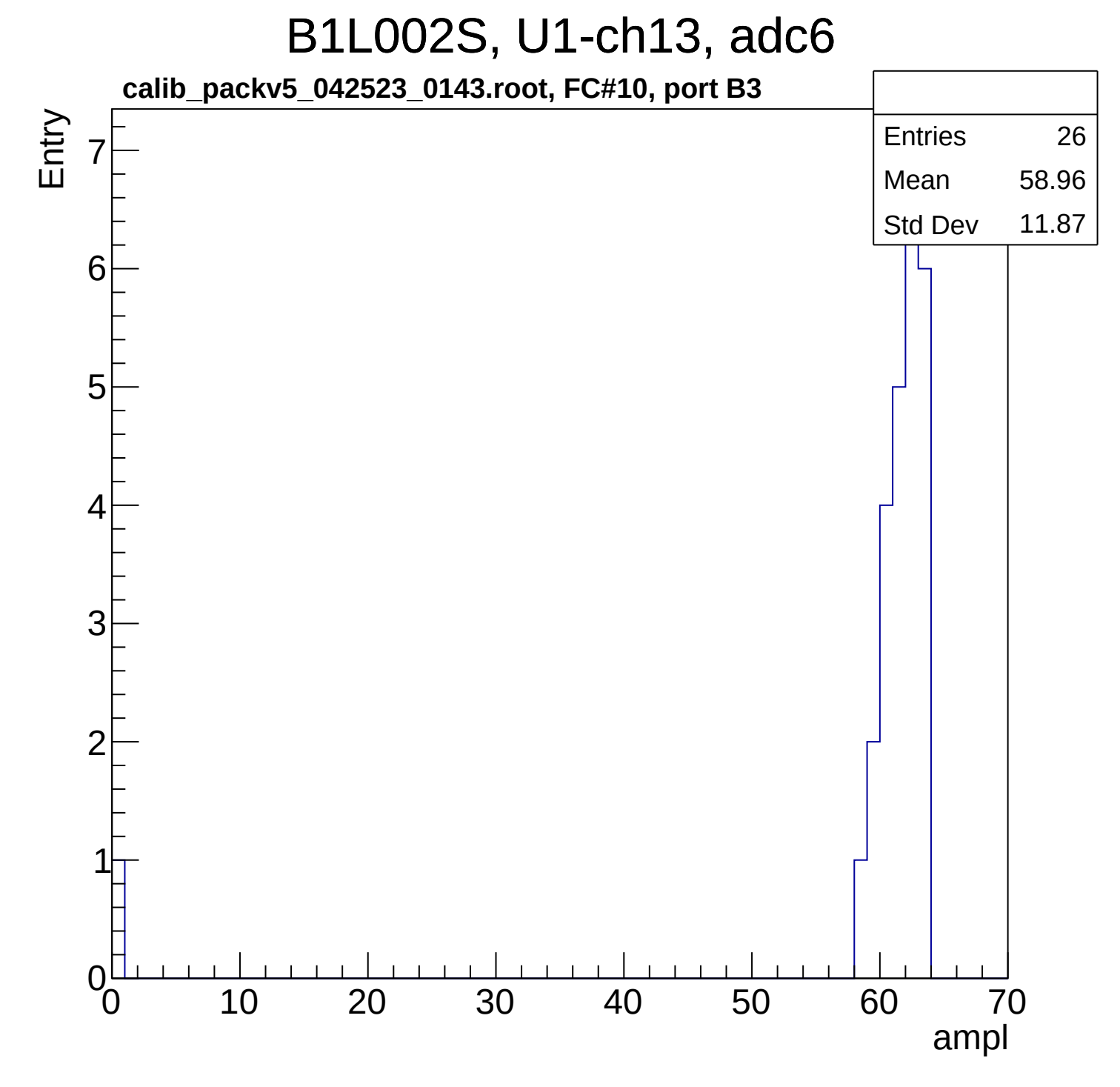
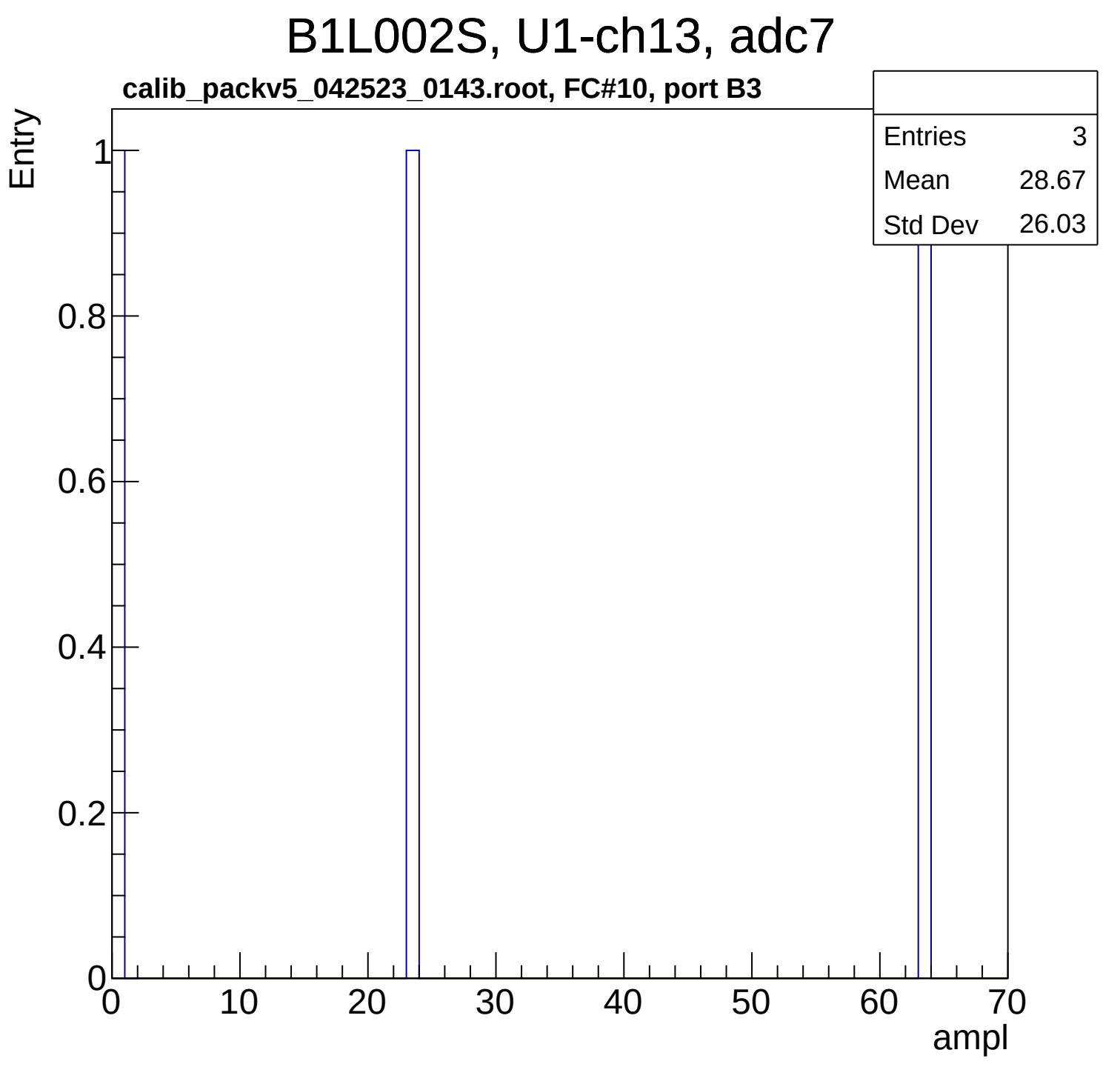

B1L002S, U1-ch13, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.4
Std Dev 3.557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch13, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 52.73
Std Dev 2.791
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch13, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.84
Std Dev 2.96
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch13, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.96
Std Dev 11.87
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch13, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28.67
Std Dev 26.03
Entry
ampl
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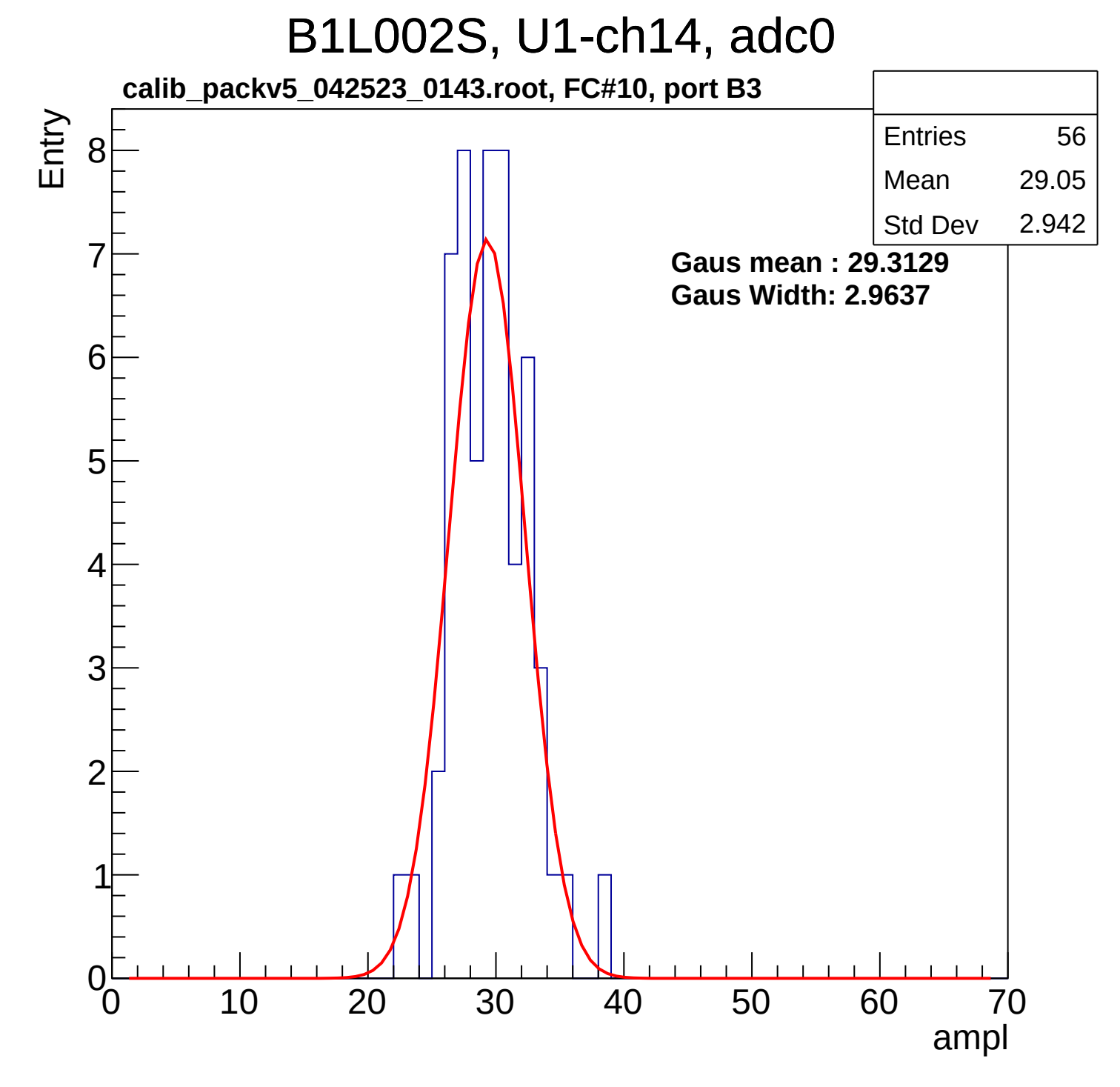
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U1-ch14, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 29.05
Std Dev 2.942
Gaus mean : 29.3129
Gaus Width: 2.9637
Entry
ampl
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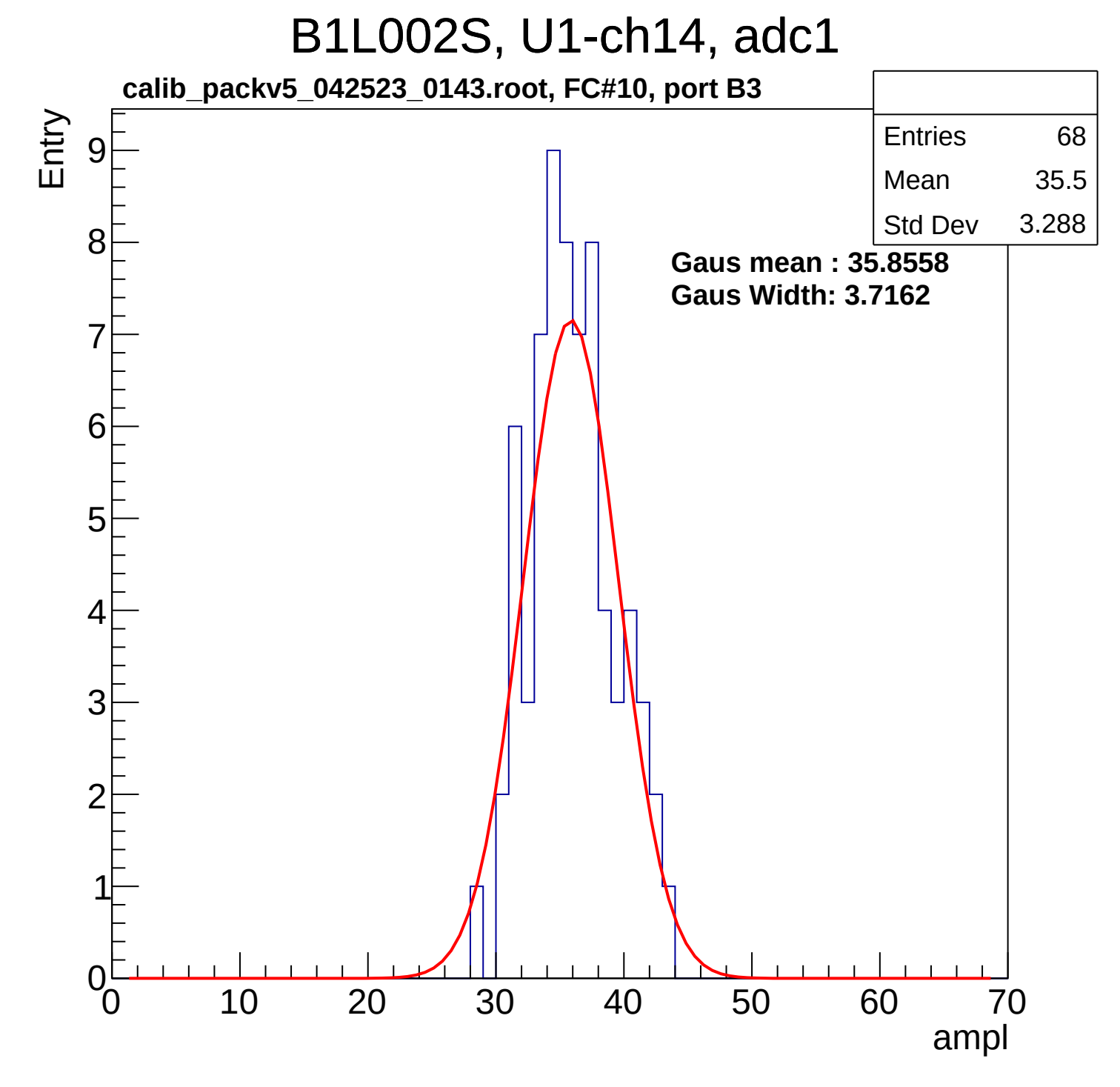
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch14, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.5
Std Dev 3.288
Gaus mean : 35.8558
Gaus Width: 3.7162
Entry
ampl
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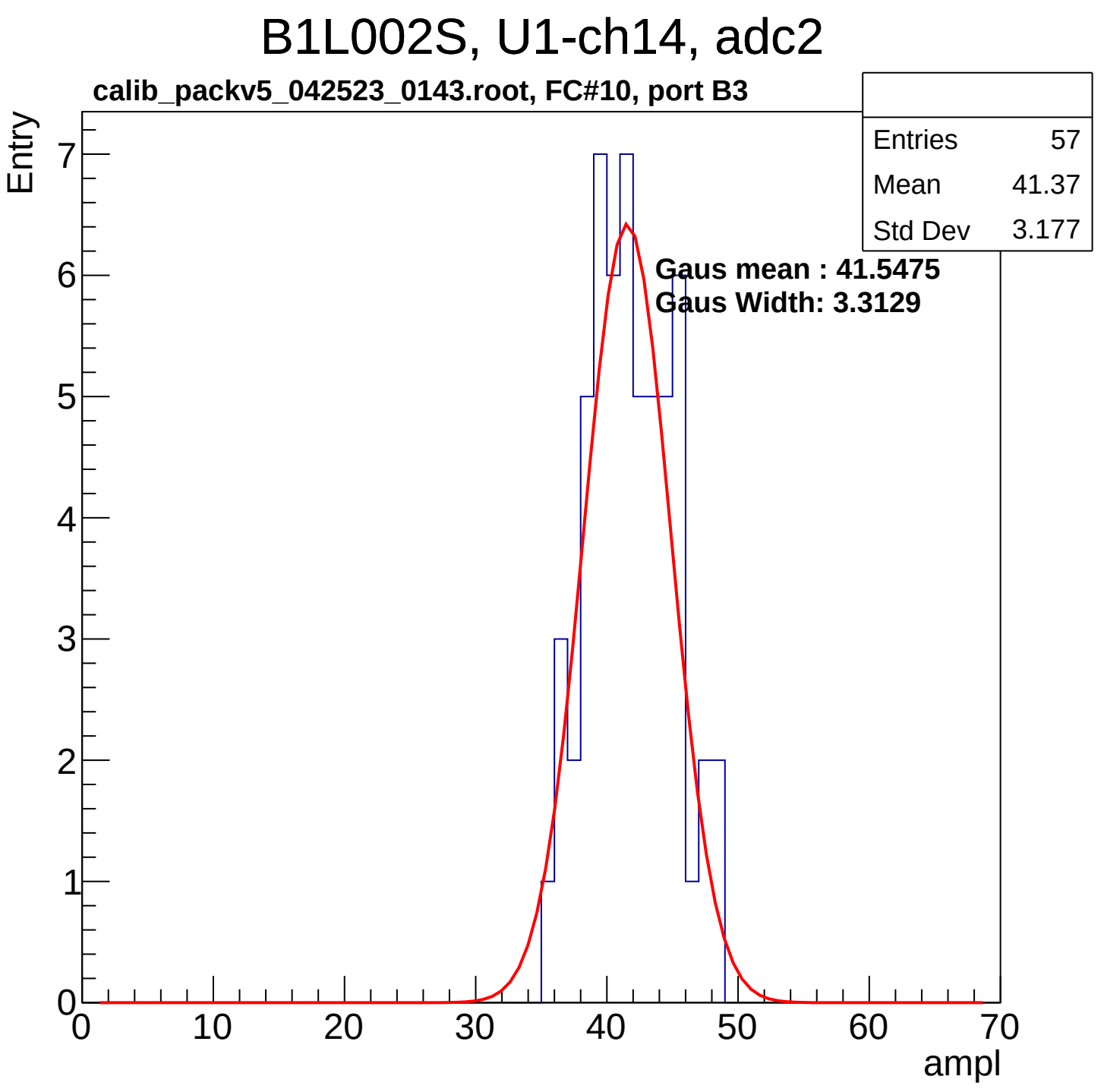
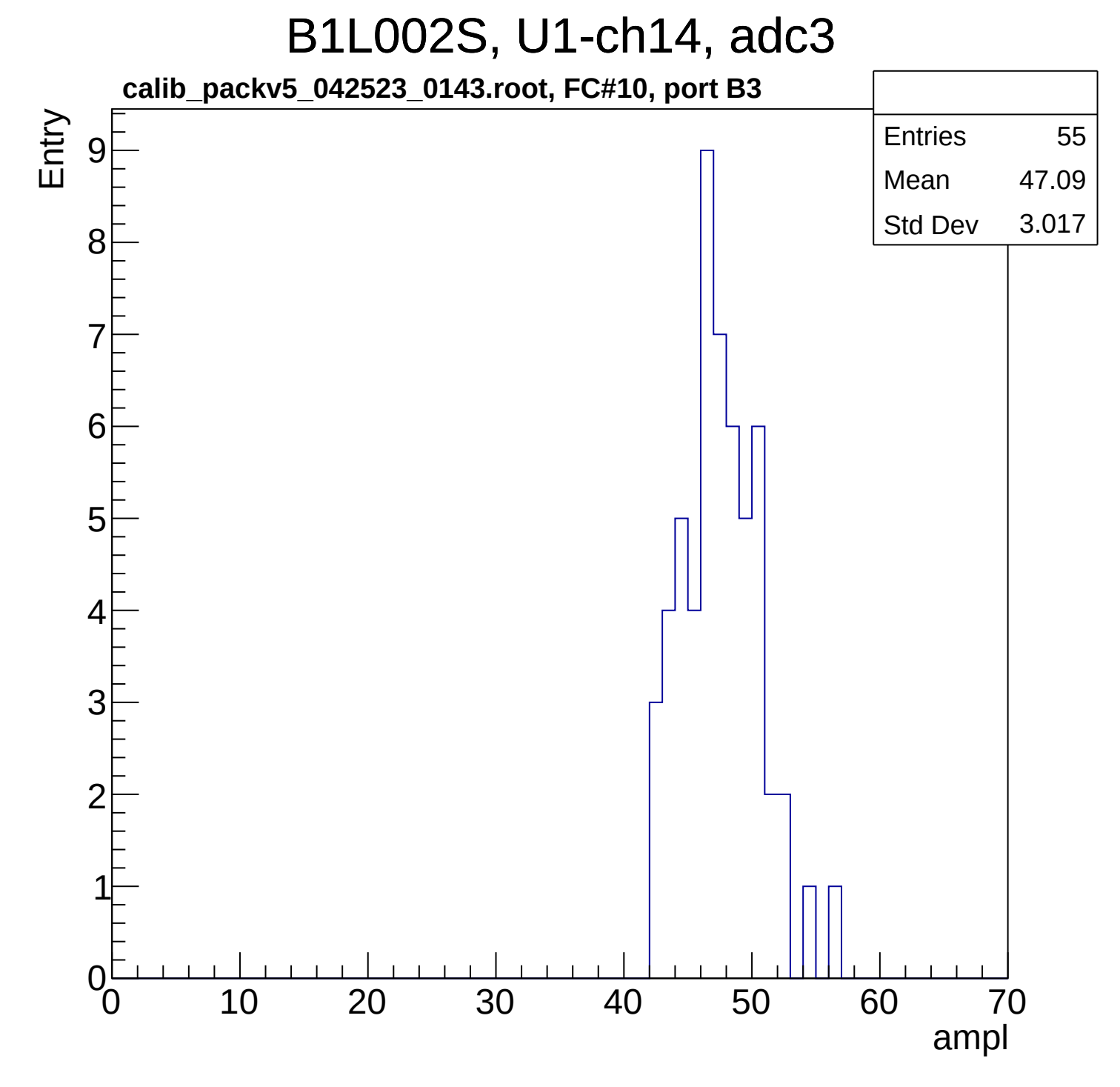
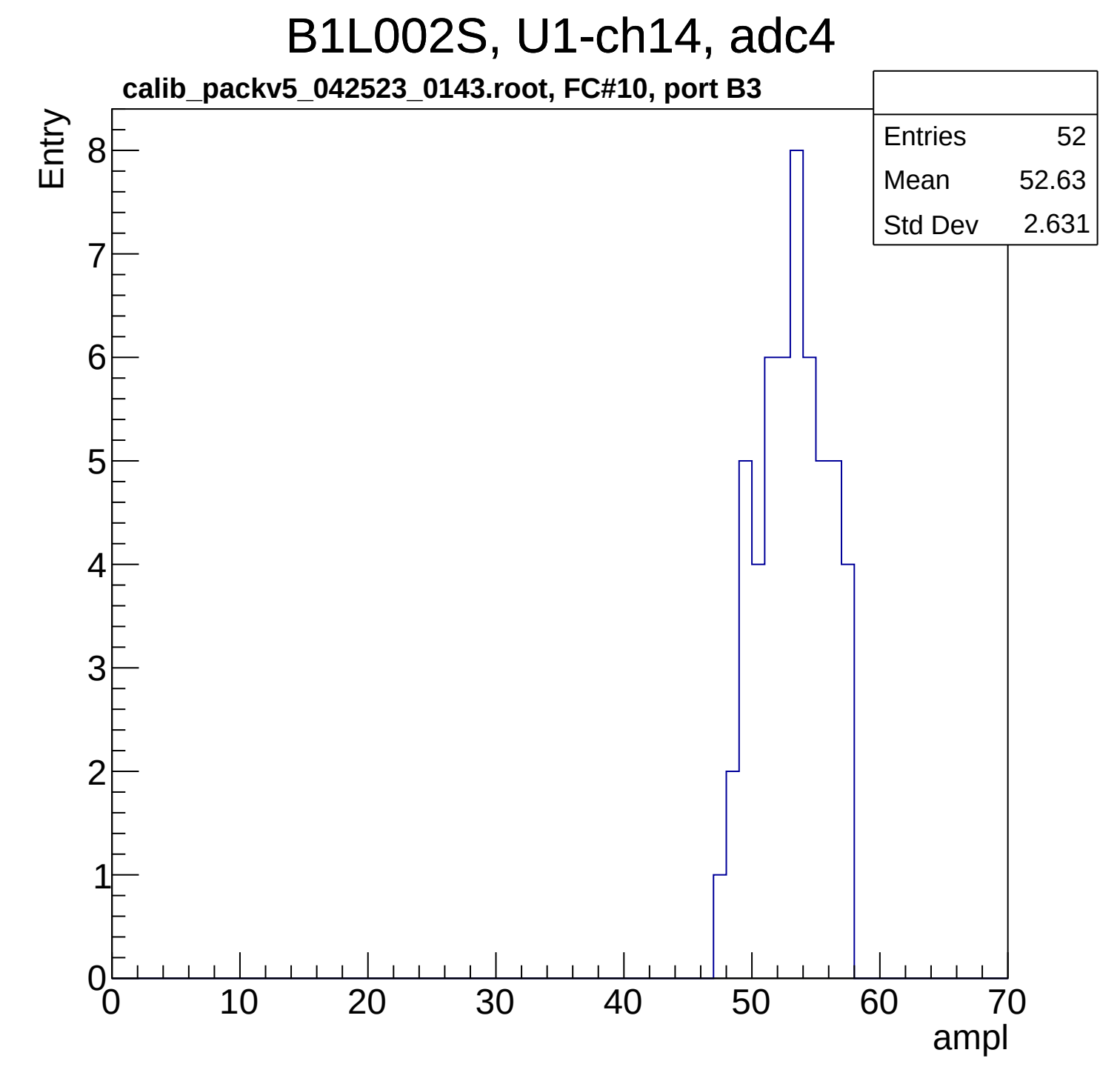
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch14, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.37
Std Dev 3.177
Gaus mean : 41.5475
Gaus Width: 3.3129
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch14, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.09
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch14, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.63
Std Dev 2.631
Entry
ampl
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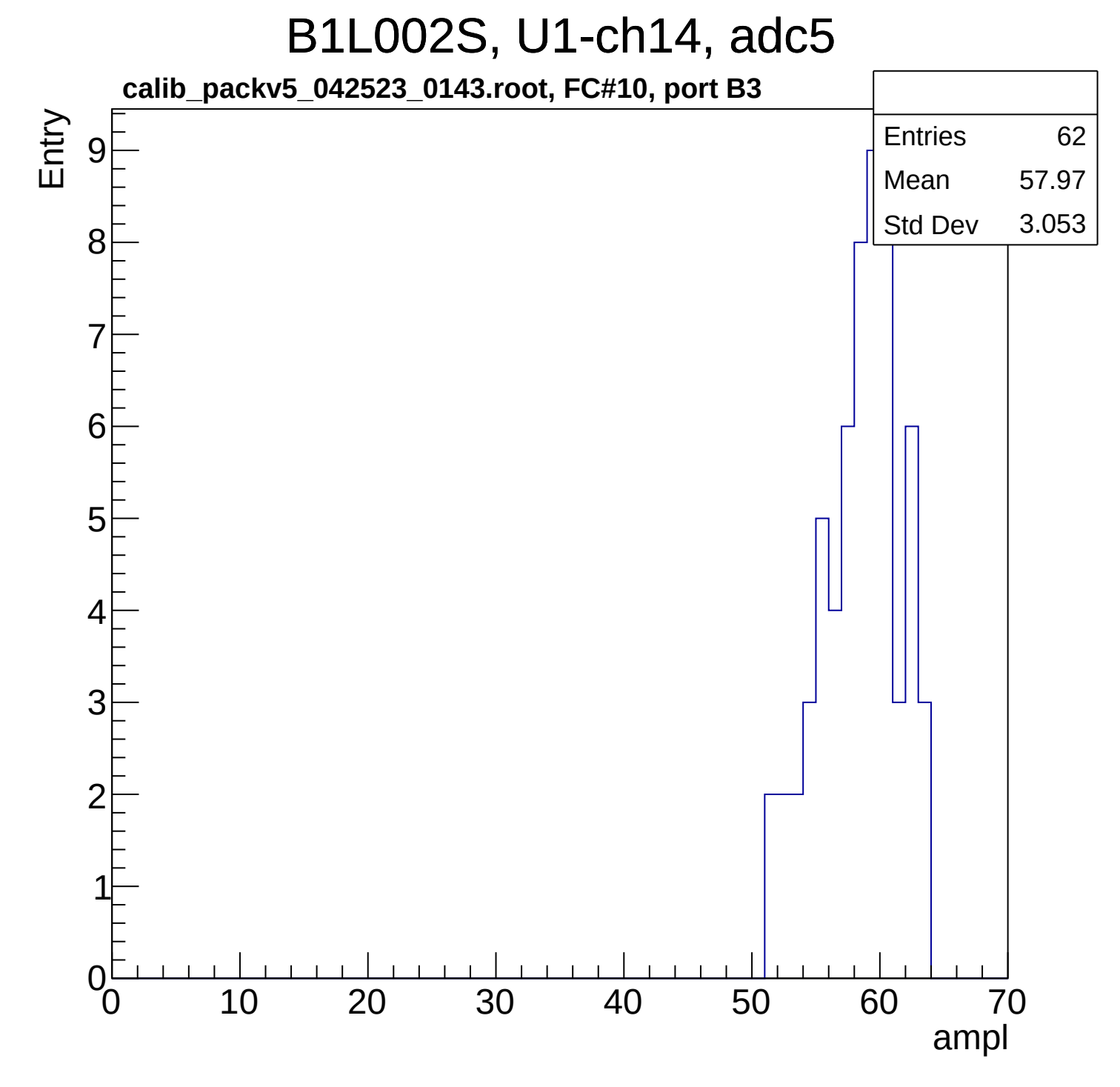
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch14, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.97
Std Dev 3.053
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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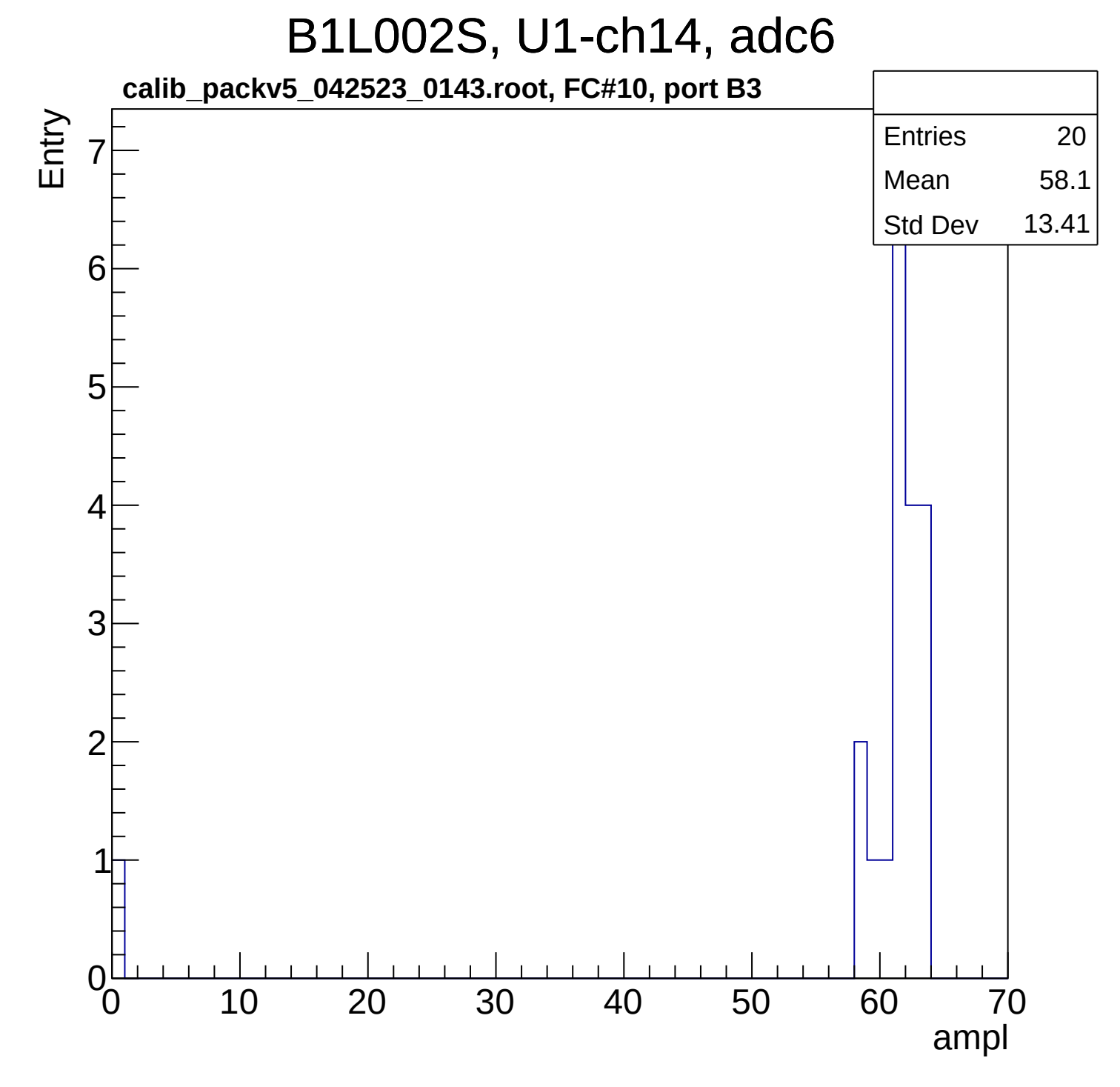
B1L002S, U1-ch14, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.1
Std Dev 13.41
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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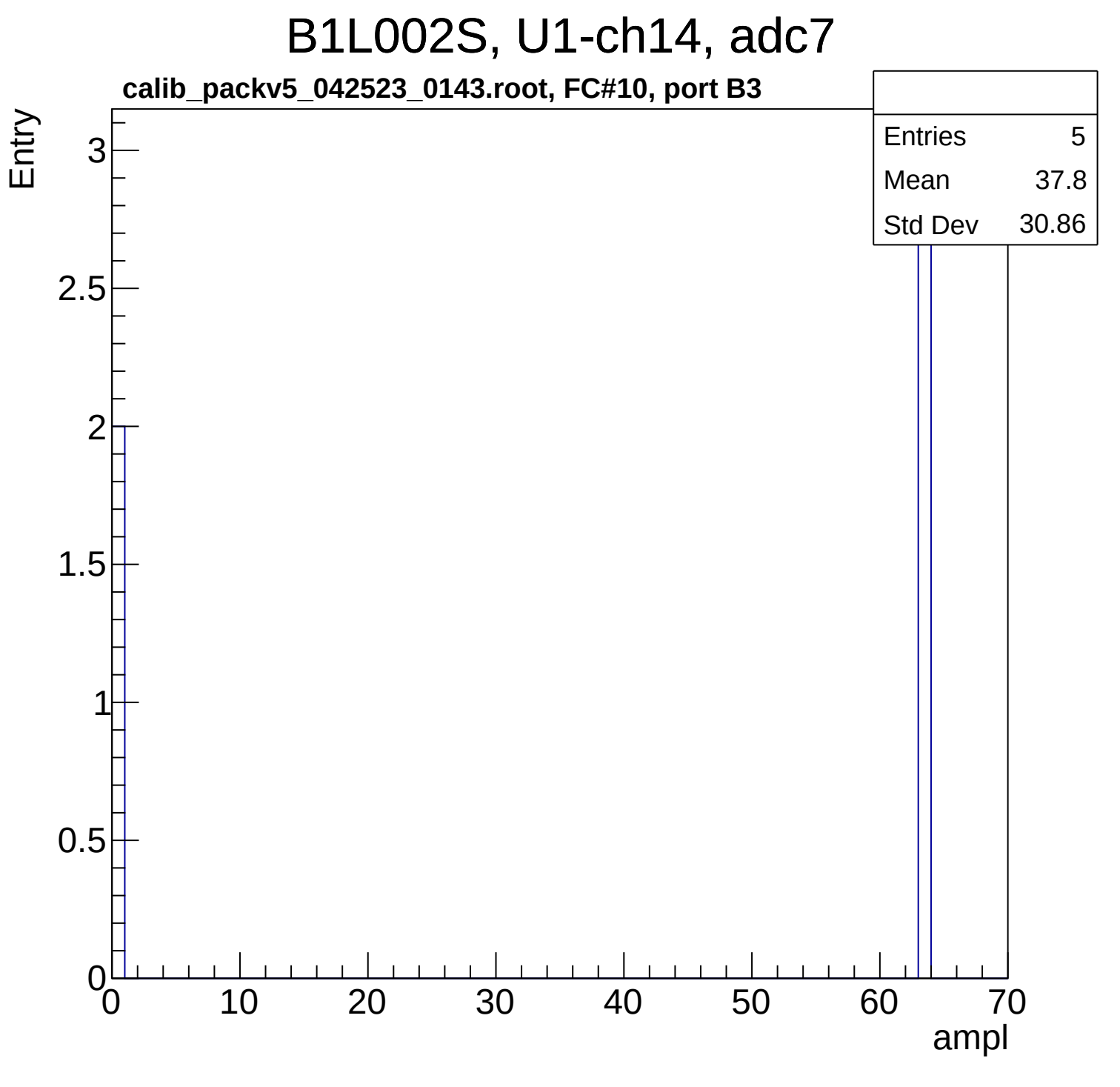
B1L002S, U1-ch14, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.8
Std Dev 30.86
Entry
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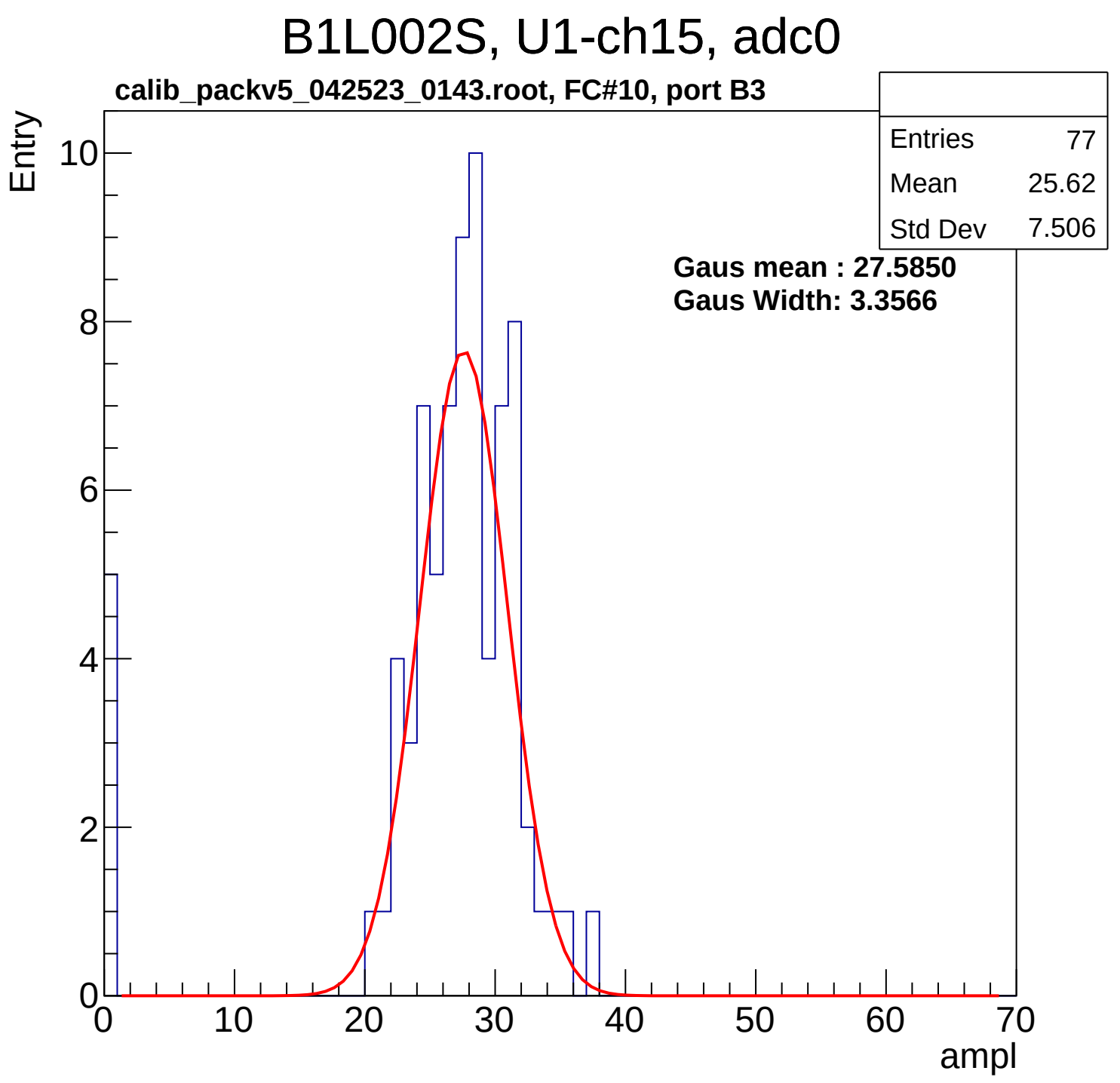
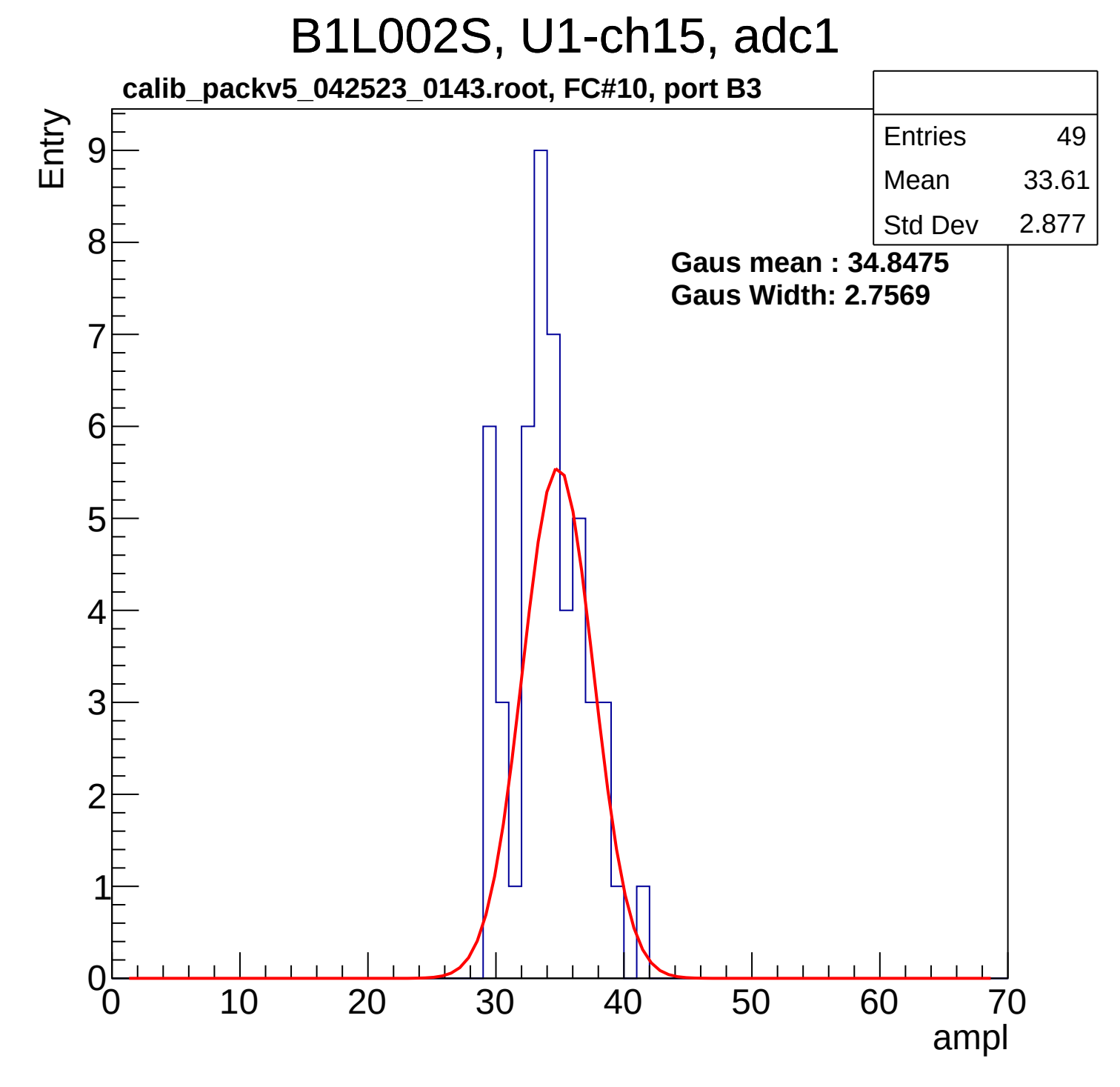
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch15, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 25.62
Std Dev 7.506
Gaus mean : 27.5850
Gaus Width: 3.3566
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch15, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 33.61
Std Dev 2.877
Gaus mean : 34.8475
Gaus Width: 2.7569
Entry
ampl
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3
4
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6
7
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0
10
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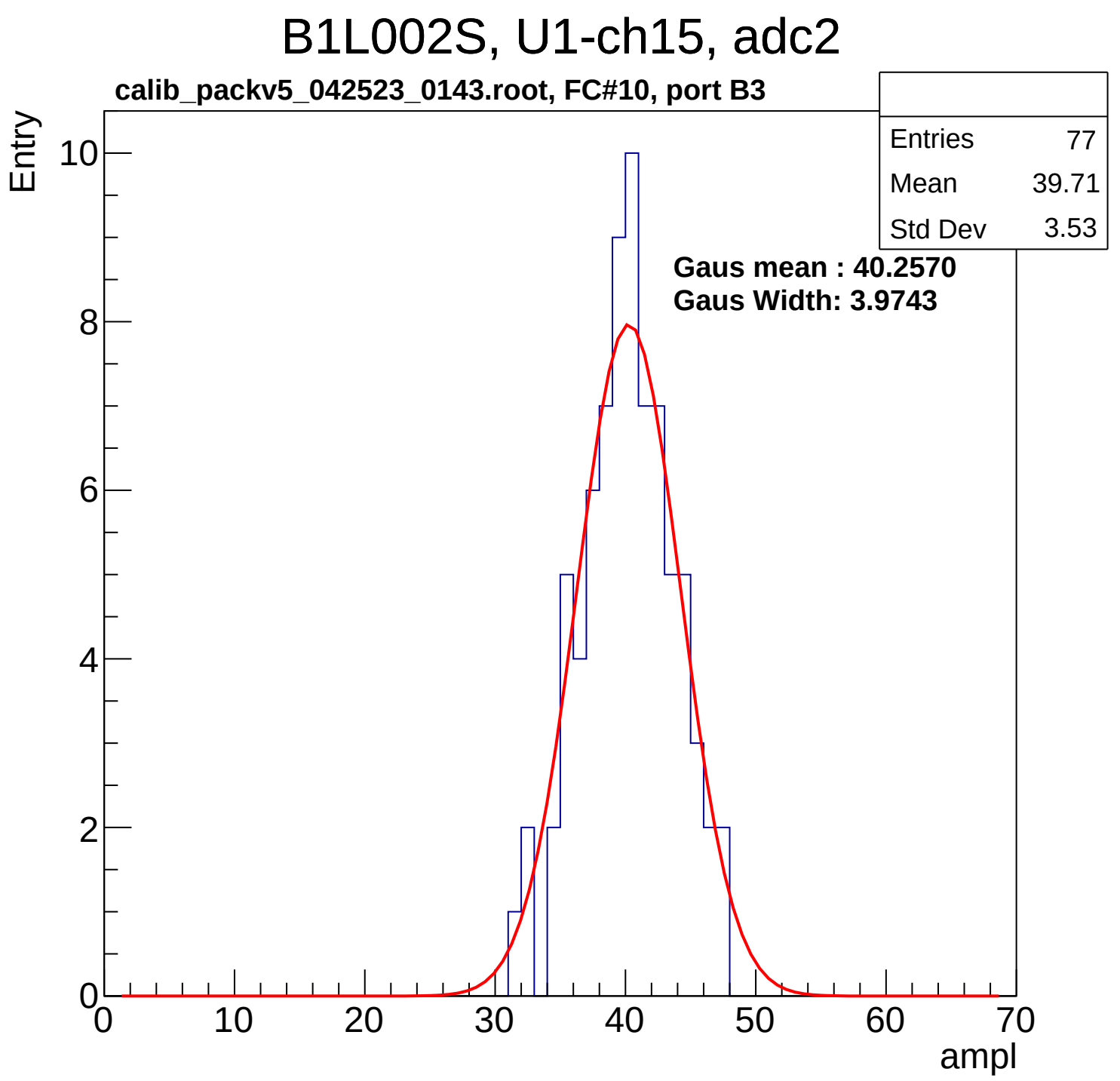
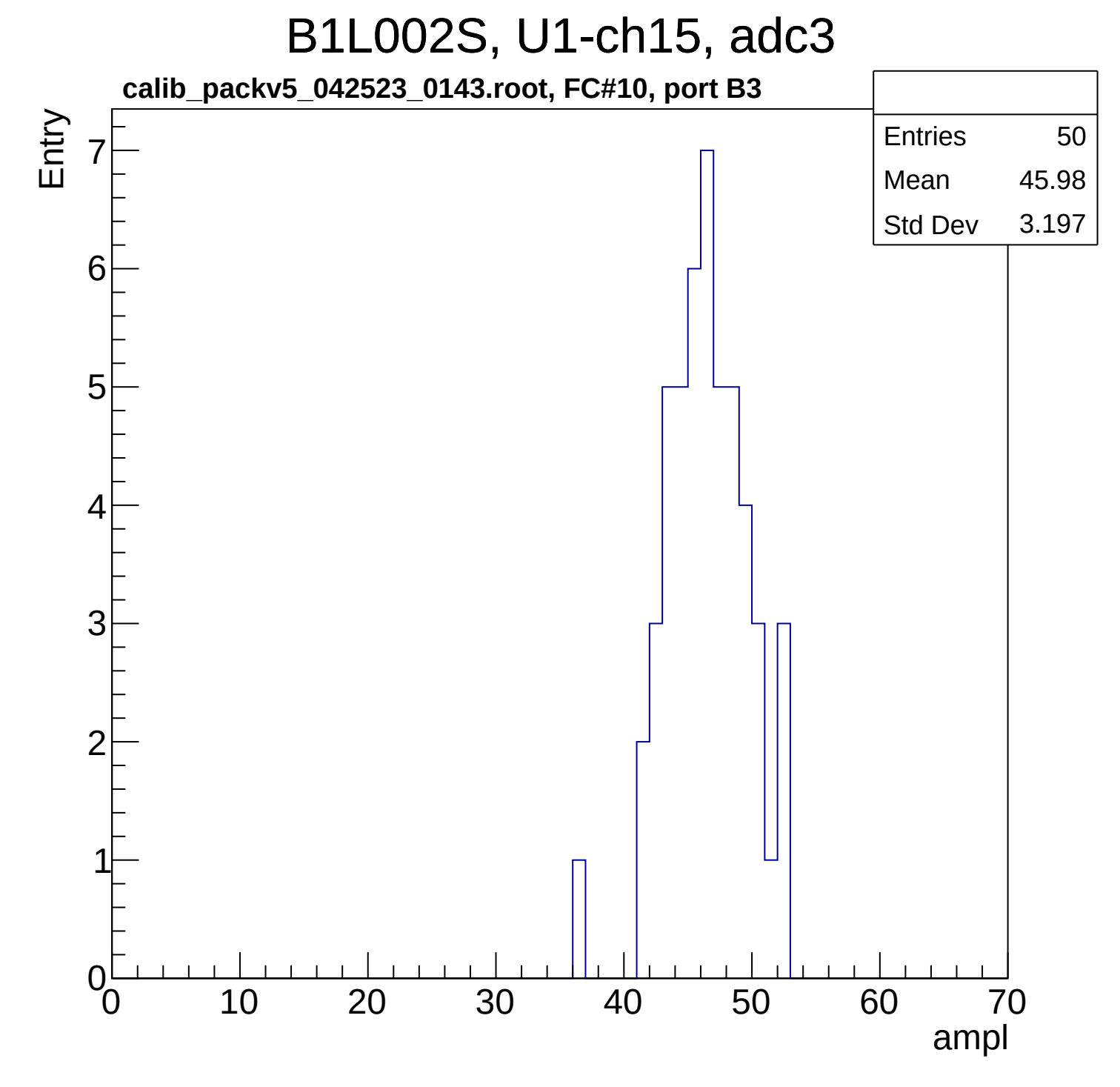

B1L002S, U1-ch15, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 39.71
Std Dev 3.53
Gaus mean : 40.2570
Gaus Width: 3.9743
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch15, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 45.98
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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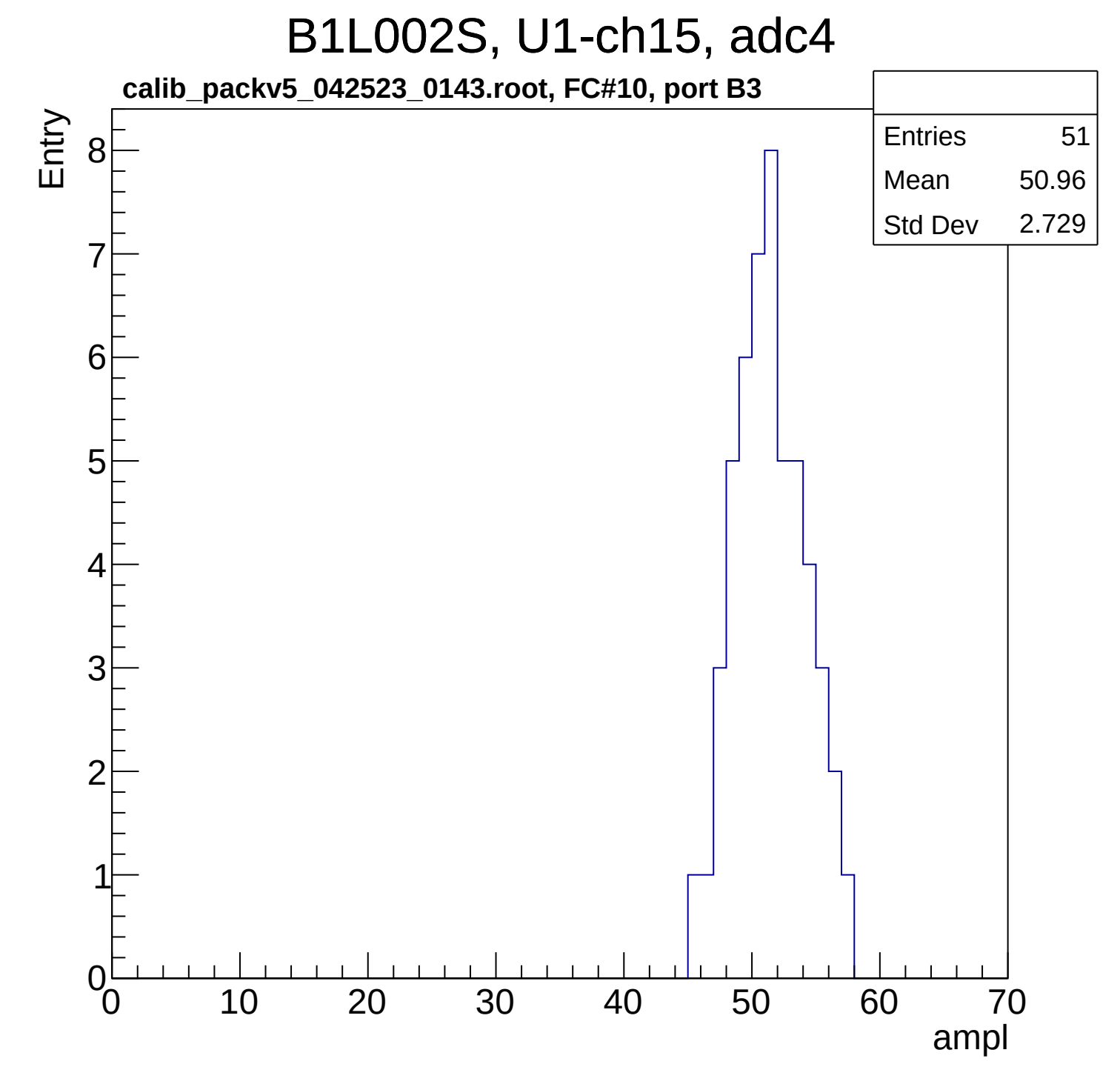
B1L002S, U1-ch15, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 50.96
Std Dev 2.729
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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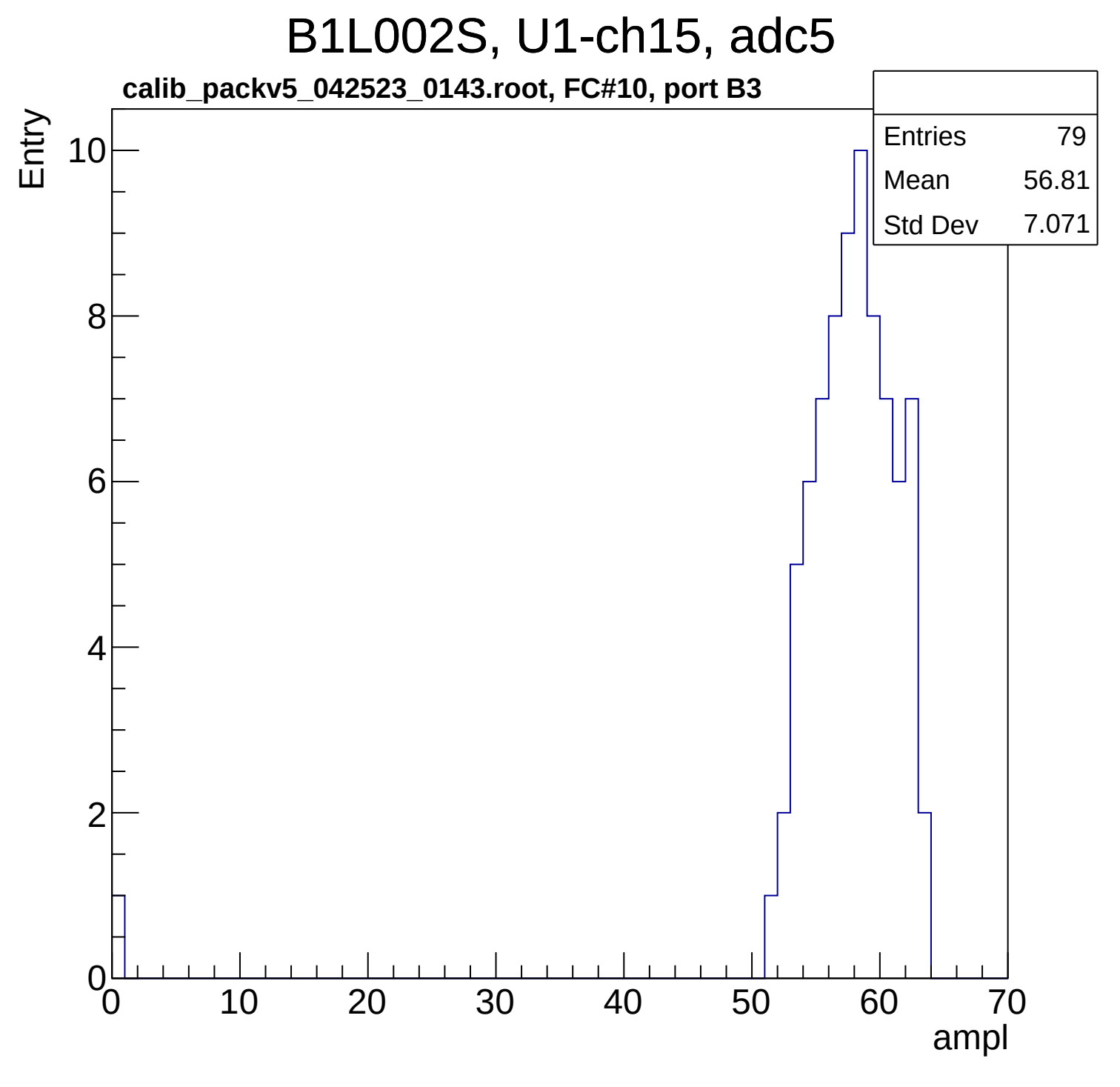
B1L002S, U1-ch15, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 56.81
Std Dev 7.071
Entry
0
2
4
6
8
10
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10
20
30
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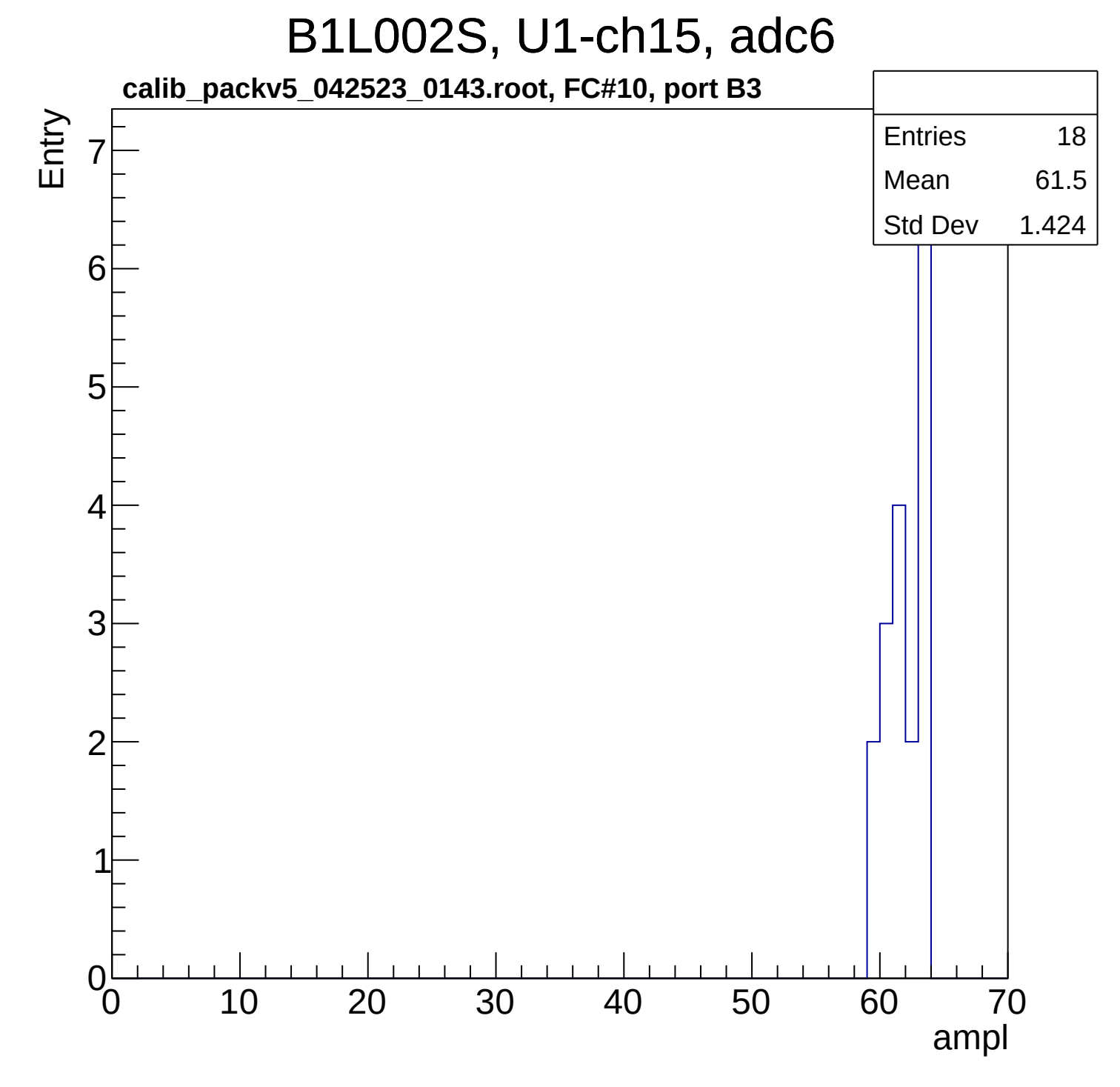
50
60
70
ampl


B1L002S, U1-ch15, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.5
Std Dev 1.424
Entry
ampl
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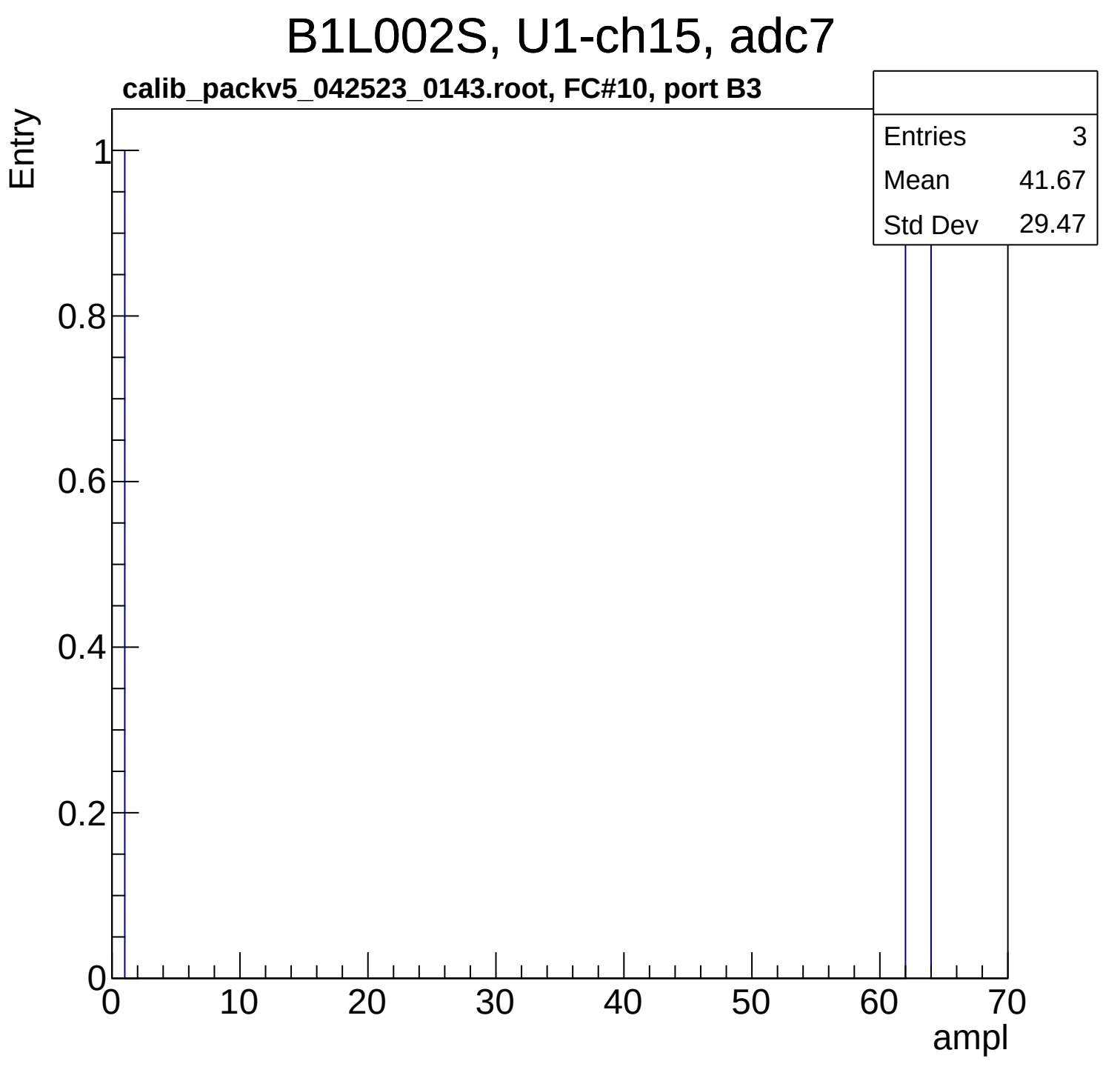
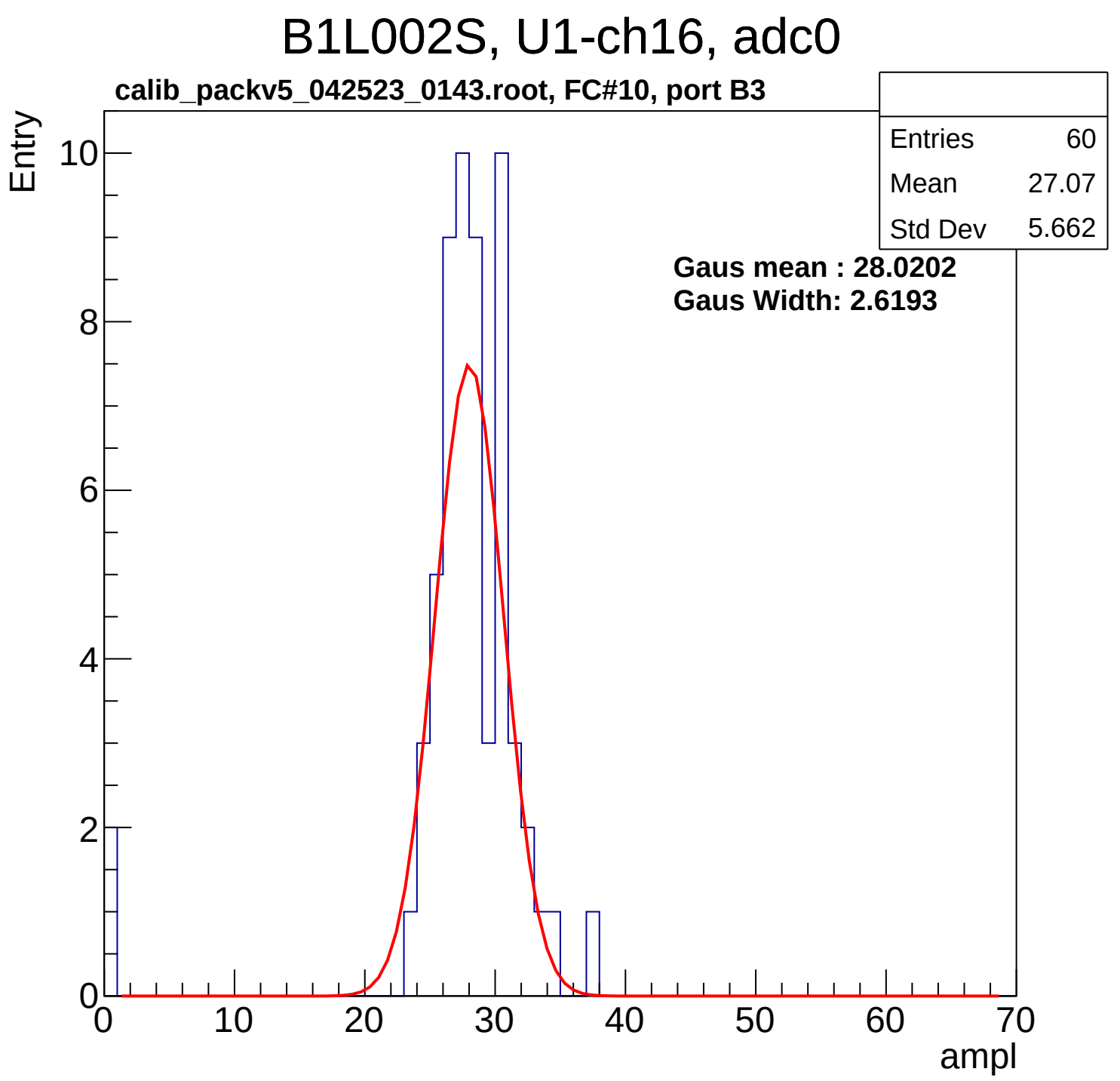
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch15, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch16, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.07
Std Dev 5.662
Gaus mean : 28.0202
Gaus Width: 2.6193
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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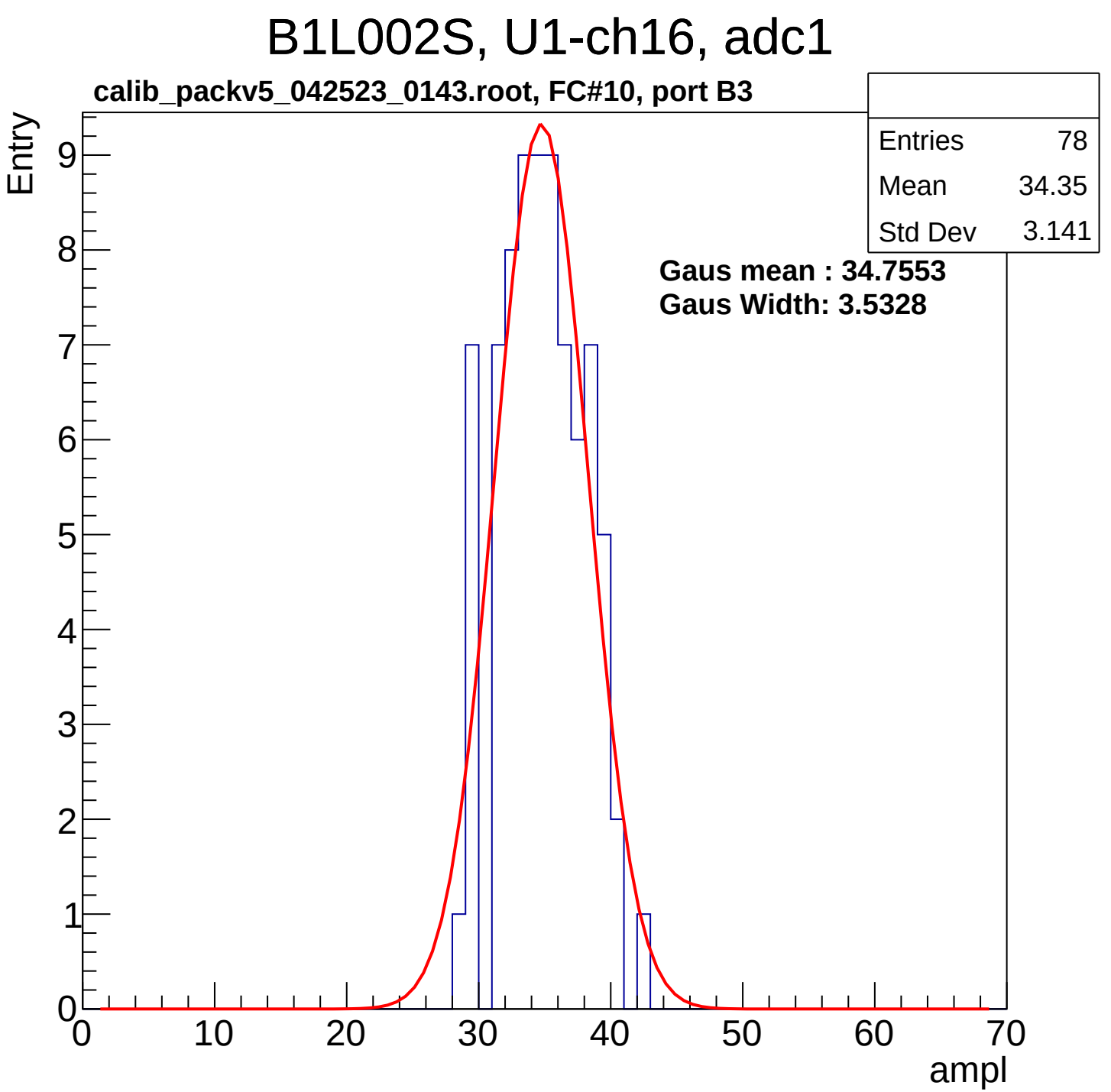
B1L002S, U1-ch16, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 34.35
Std Dev 3.141
Gaus mean : 34.7553
Gaus Width: 3.5328
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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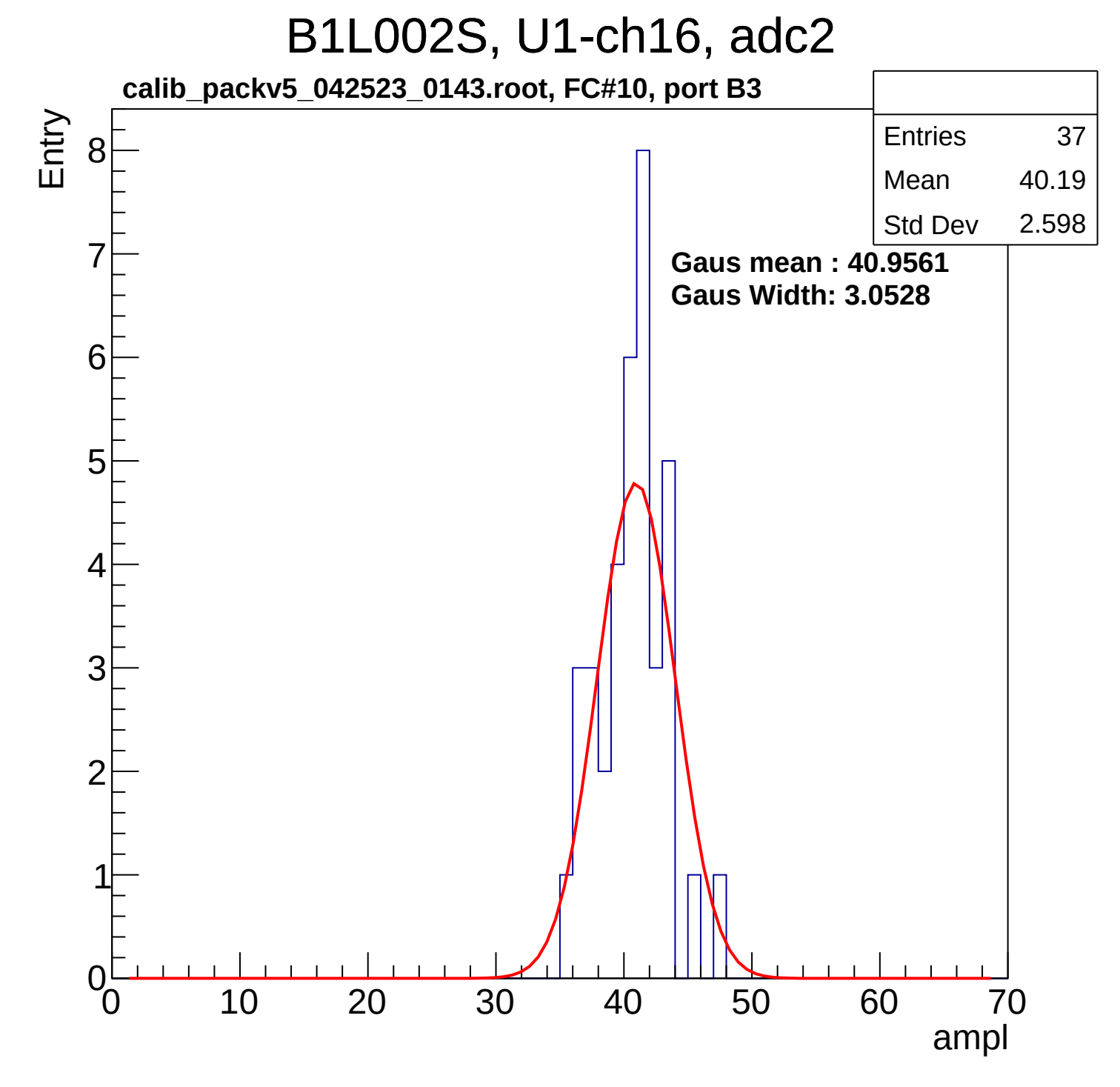
B1L002S, U1-ch16, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 40.19
Std Dev 2.598
Gaus mean : 40.9561
Gaus Width: 3.0528
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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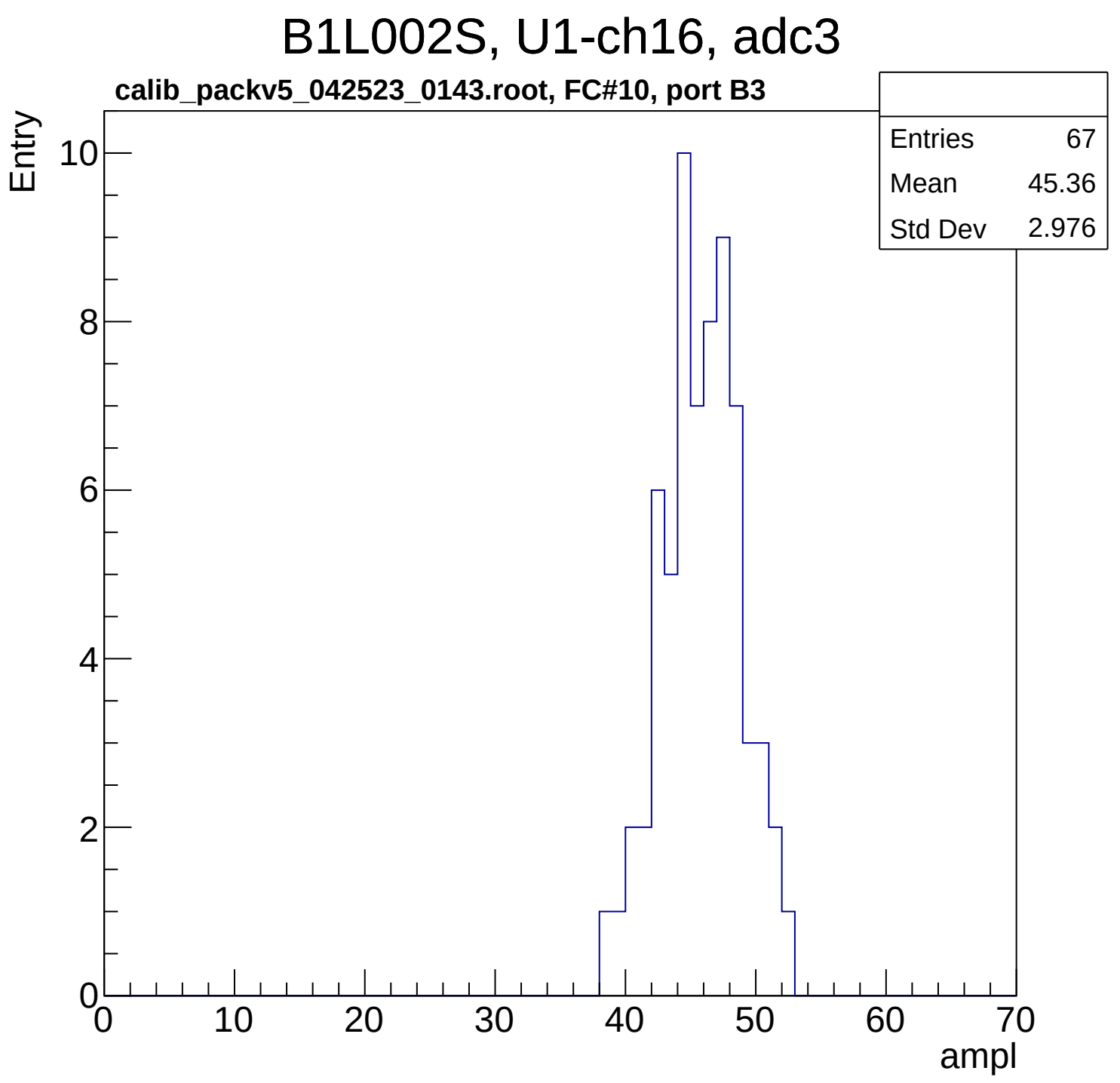
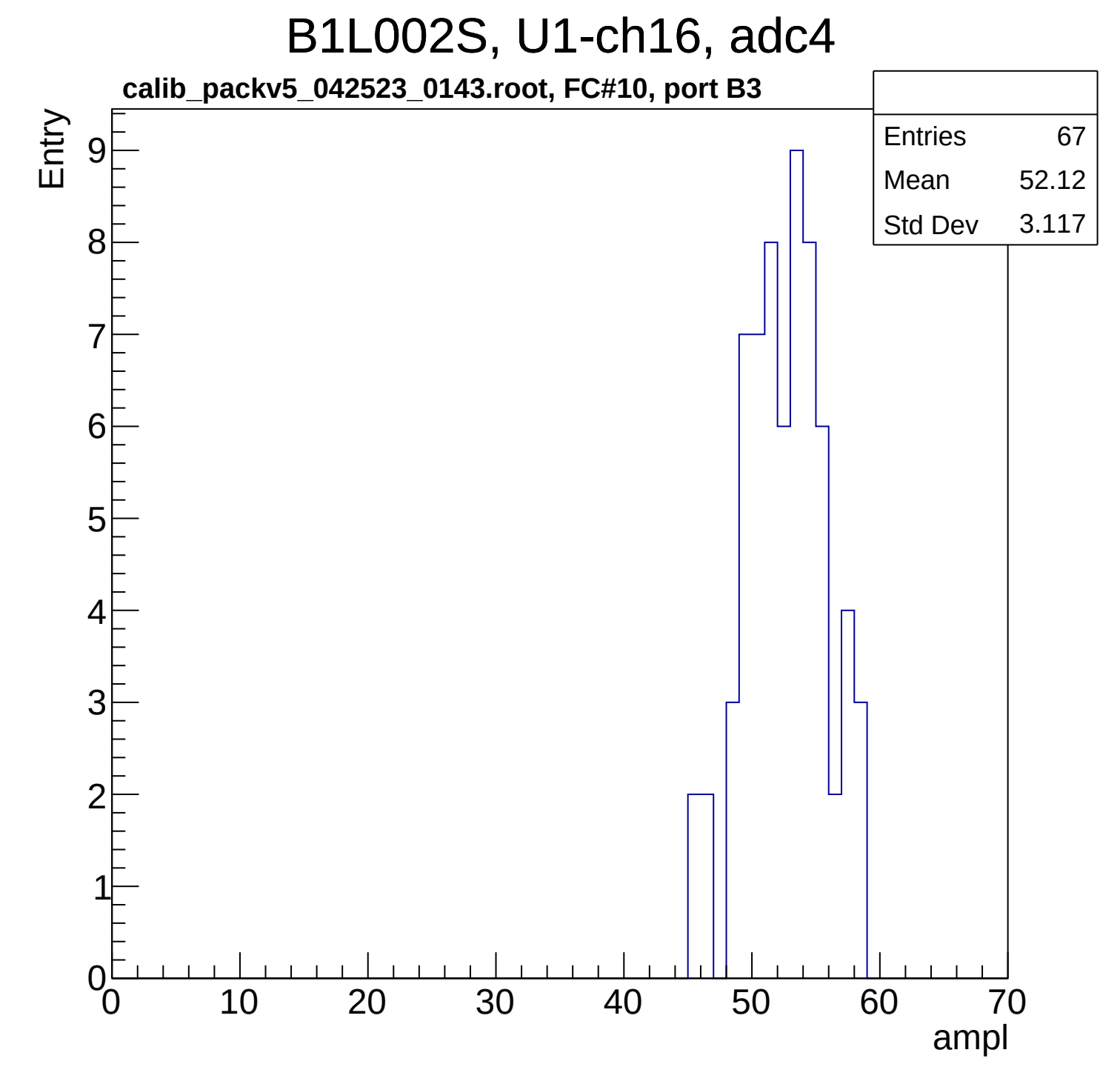
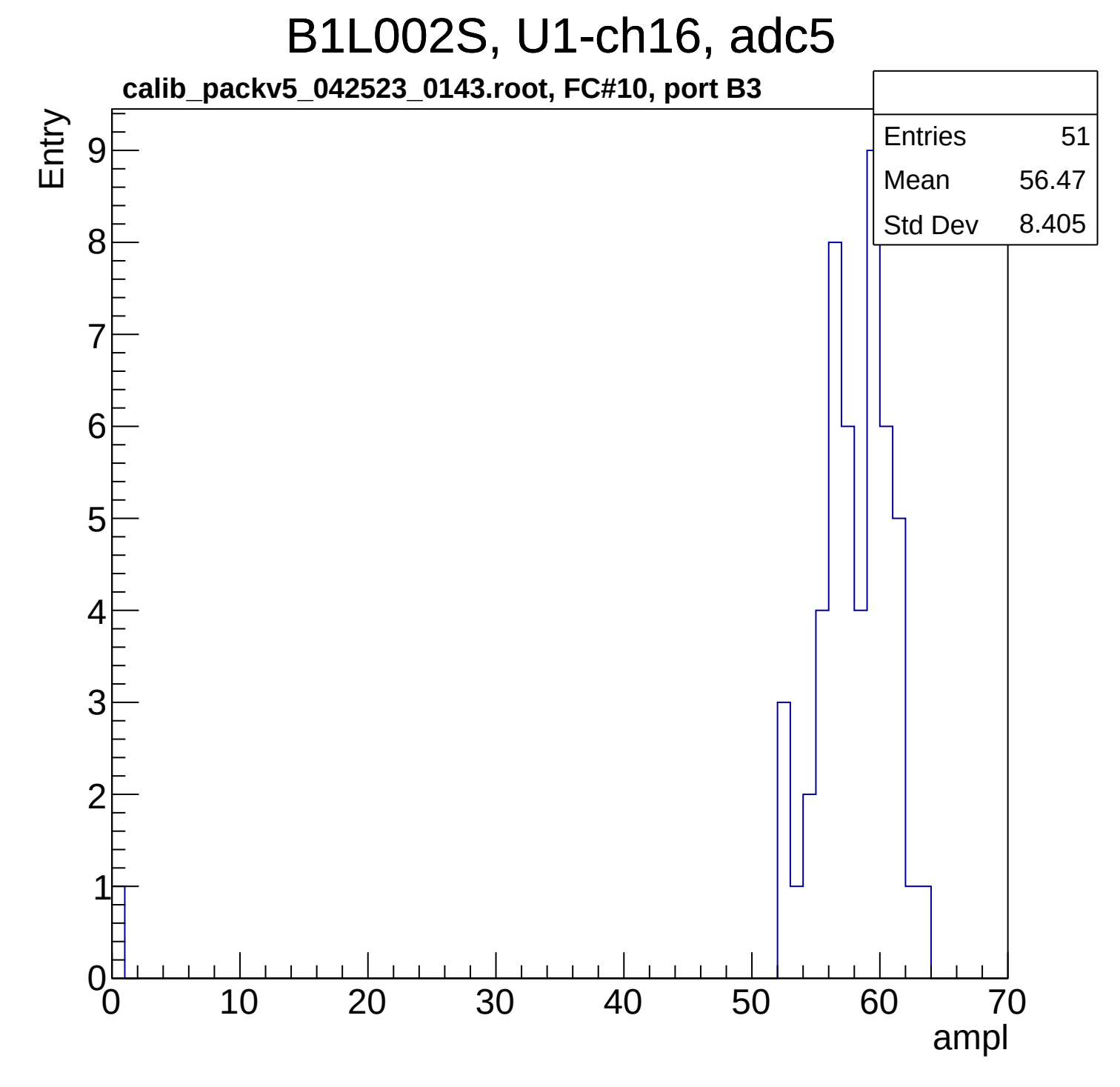

B1L002S, U1-ch16, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 45.36
Std Dev 2.976
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch16, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 52.12
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch16, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 56.47
Std Dev 8.405
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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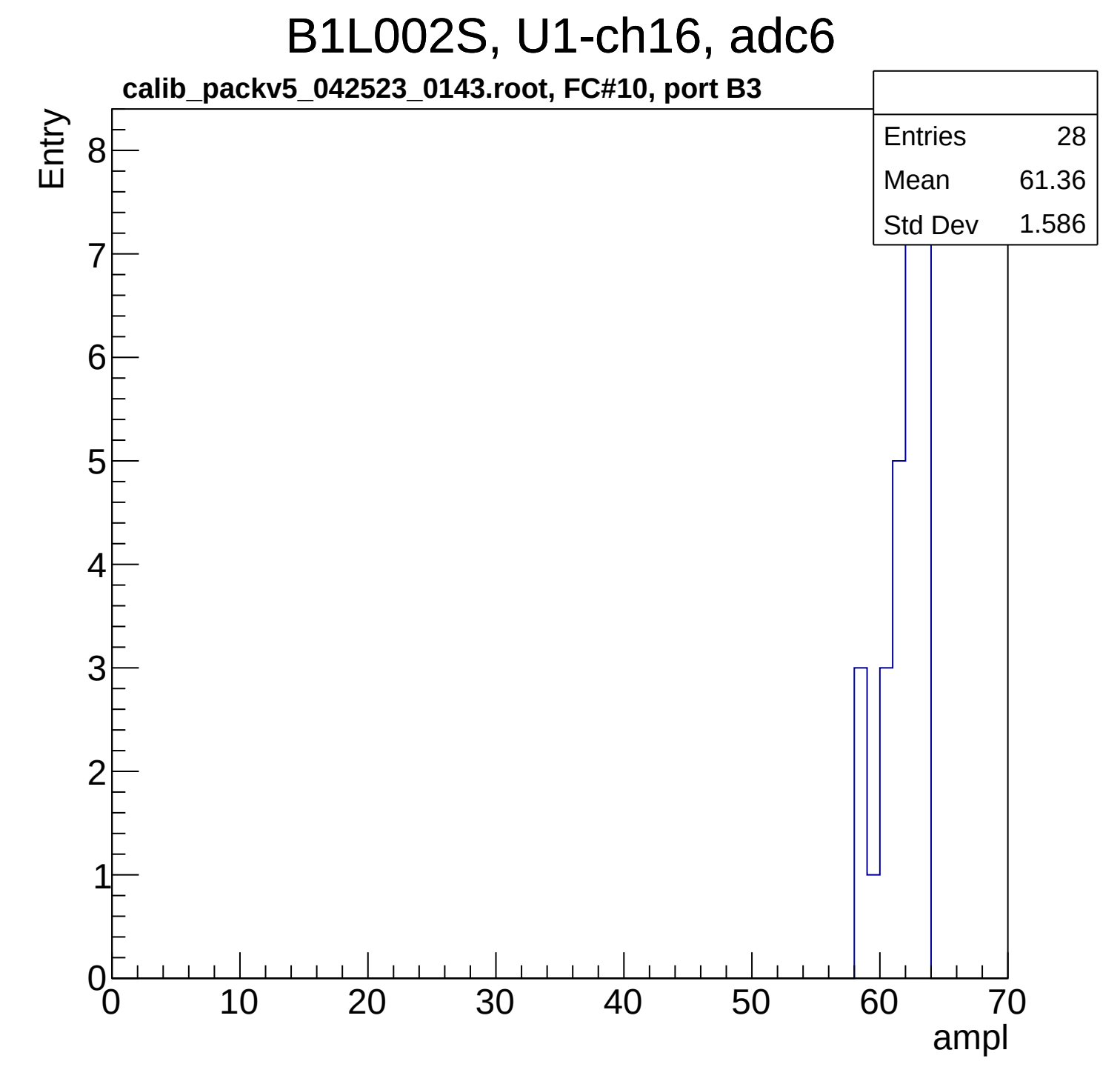
B1L002S, U1-ch16, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 61.36
Std Dev 1.586
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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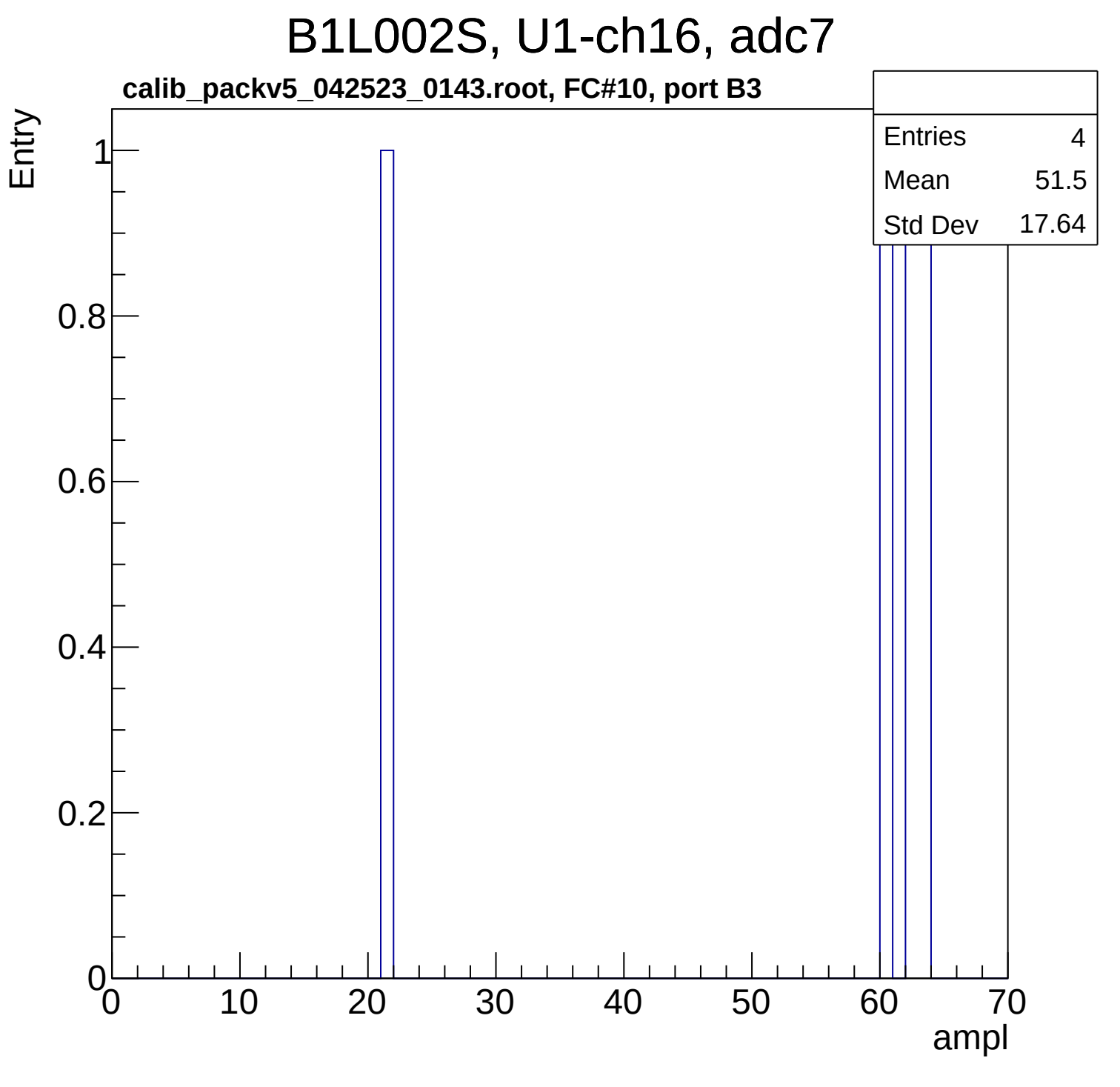
B1L002S, U1-ch16, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 51.5
Std Dev 17.64
Entry
ampl
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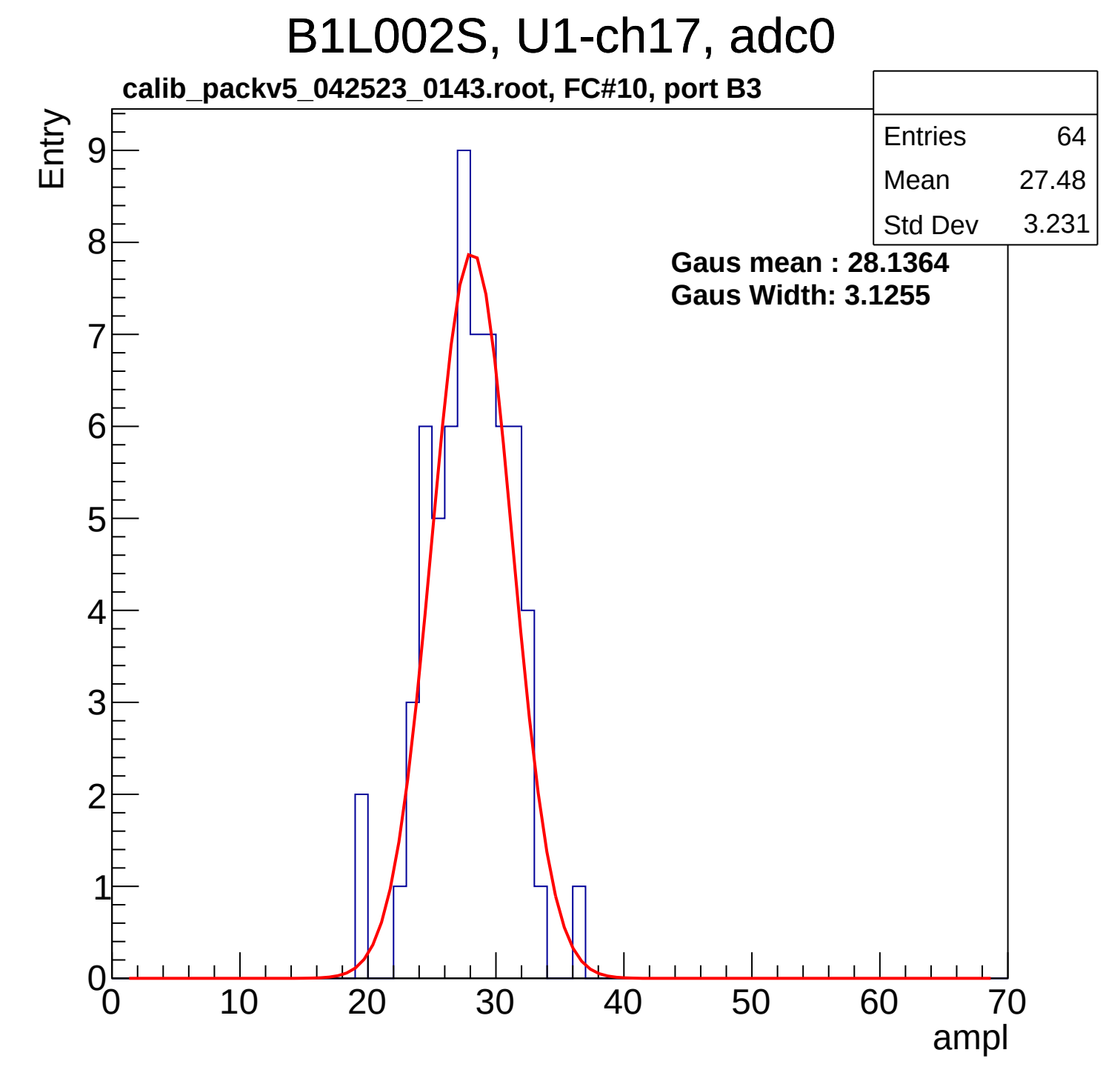
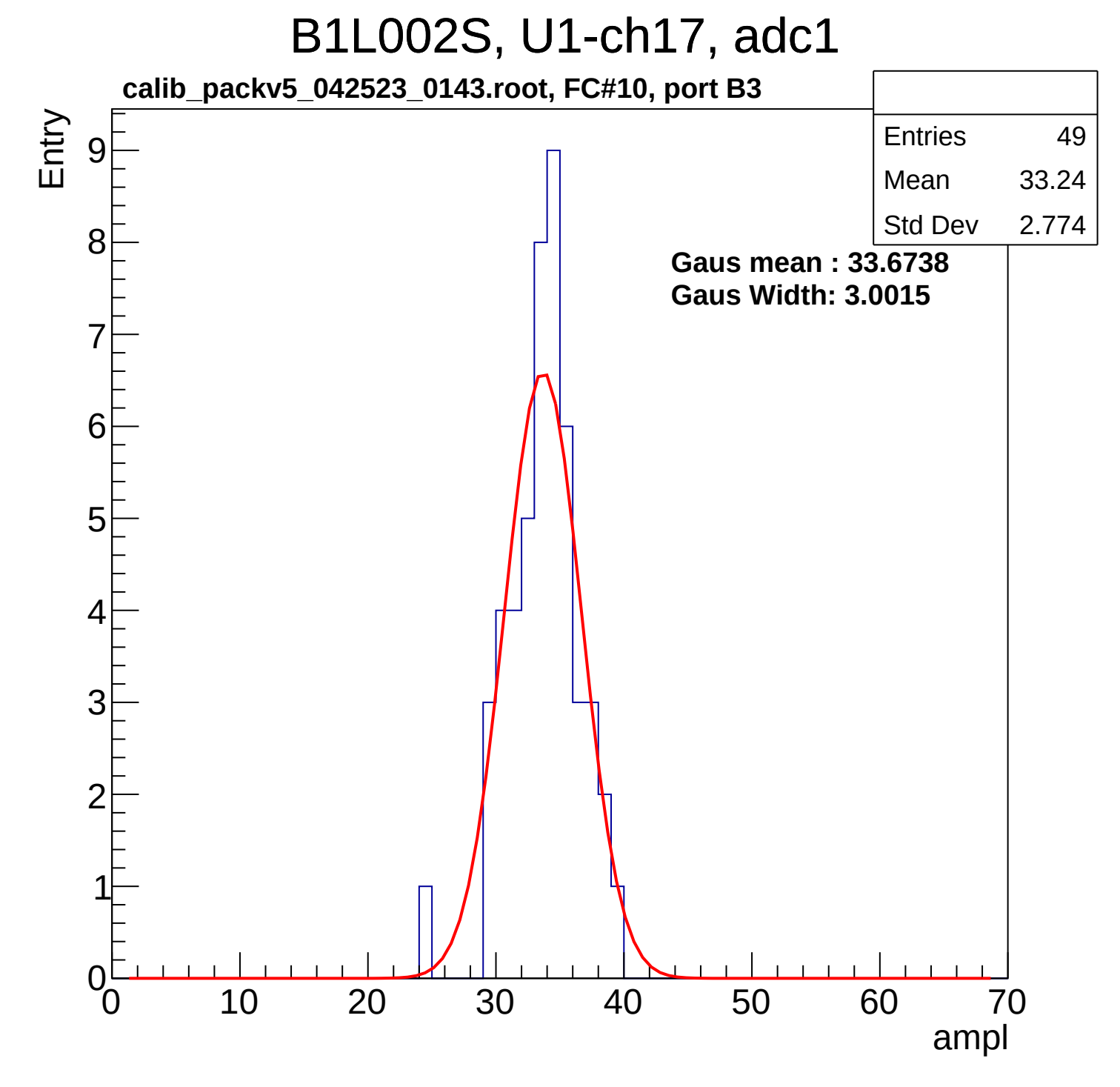
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U1-ch17, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.48
Std Dev 3.231
Gaus mean : 28.1364
Gaus Width: 3.1255
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch17, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 33.24
Std Dev 2.774
Gaus mean : 33.6738
Gaus Width: 3.0015
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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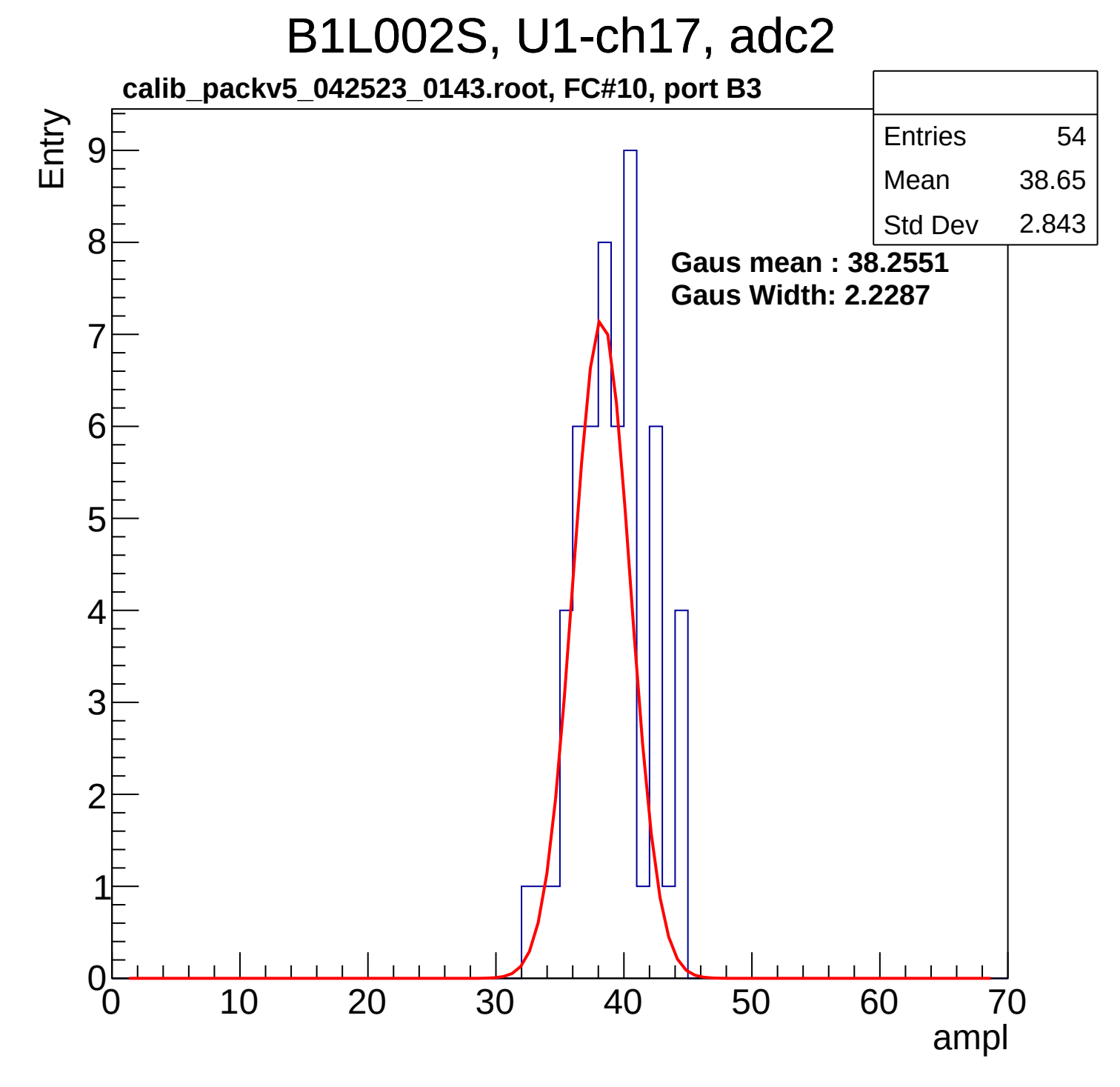
B1L002S, U1-ch17, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 38.65
Std Dev 2.843
Gaus mean : 38.2551
Gaus Width: 2.2287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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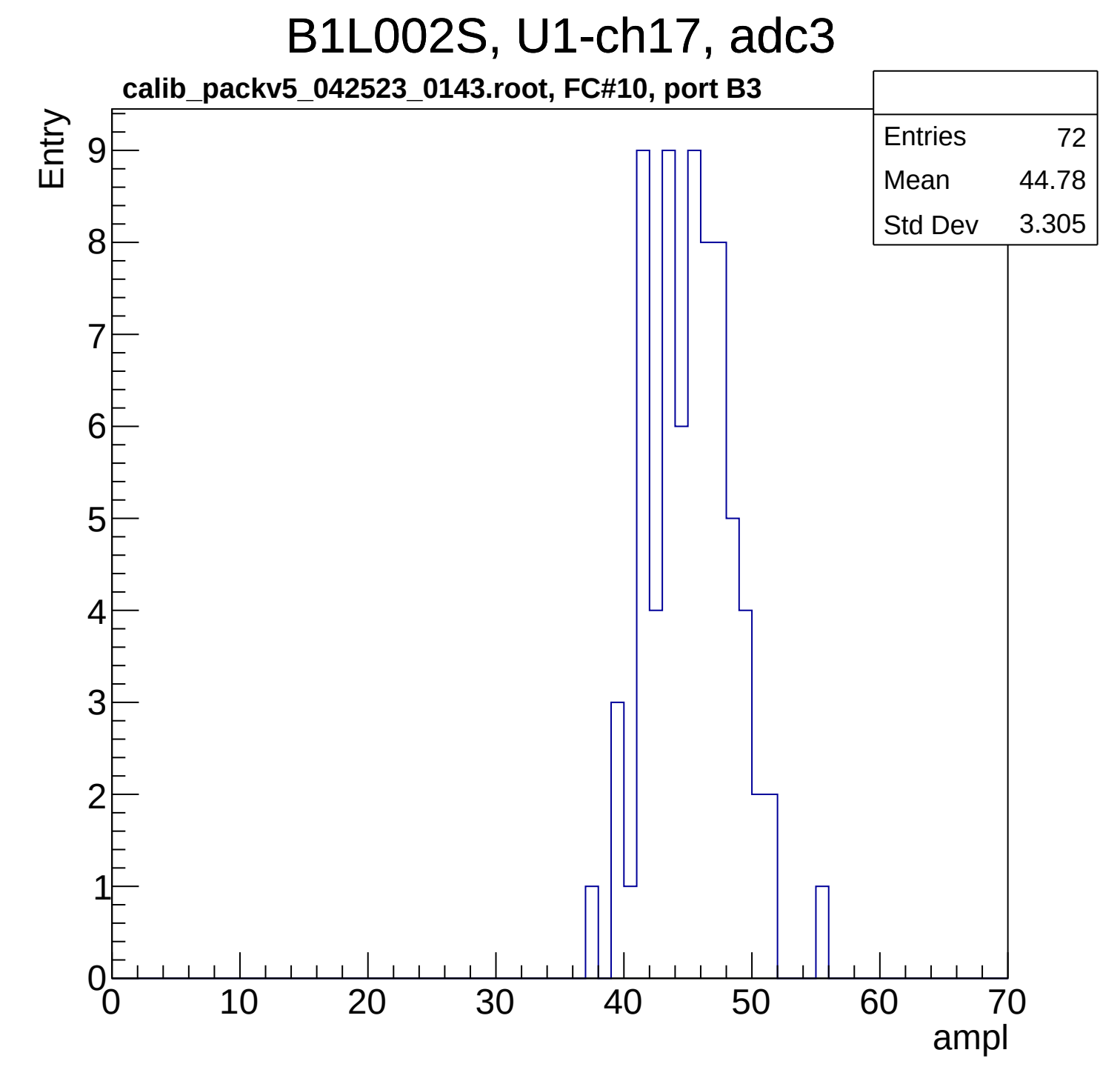
B1L002S, U1-ch17, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 44.78
Std Dev 3.305
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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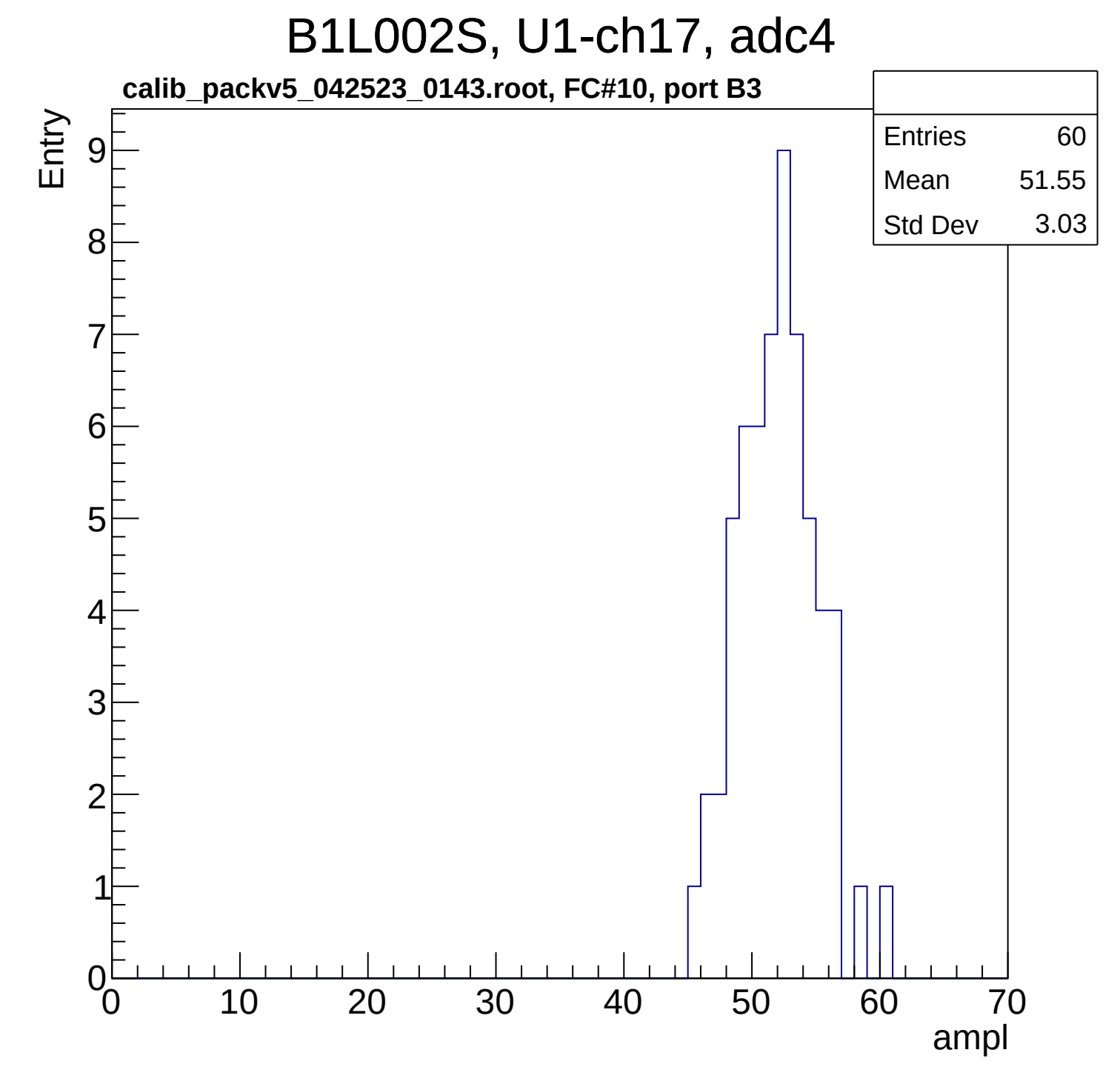
B1L002S, U1-ch17, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 51.55
Std Dev 3.03
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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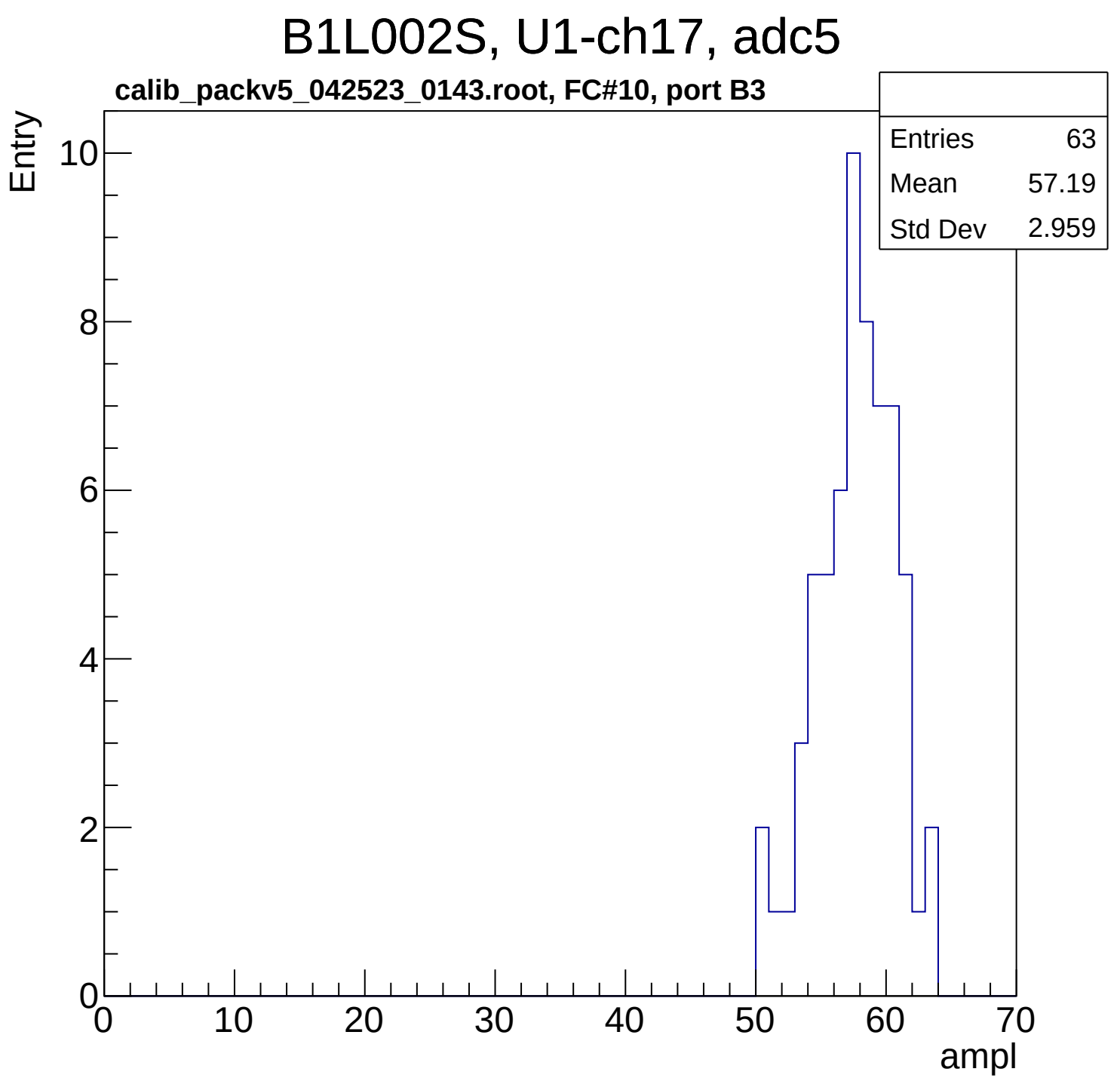
B1L002S, U1-ch17, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.19
Std Dev 2.959
Entry
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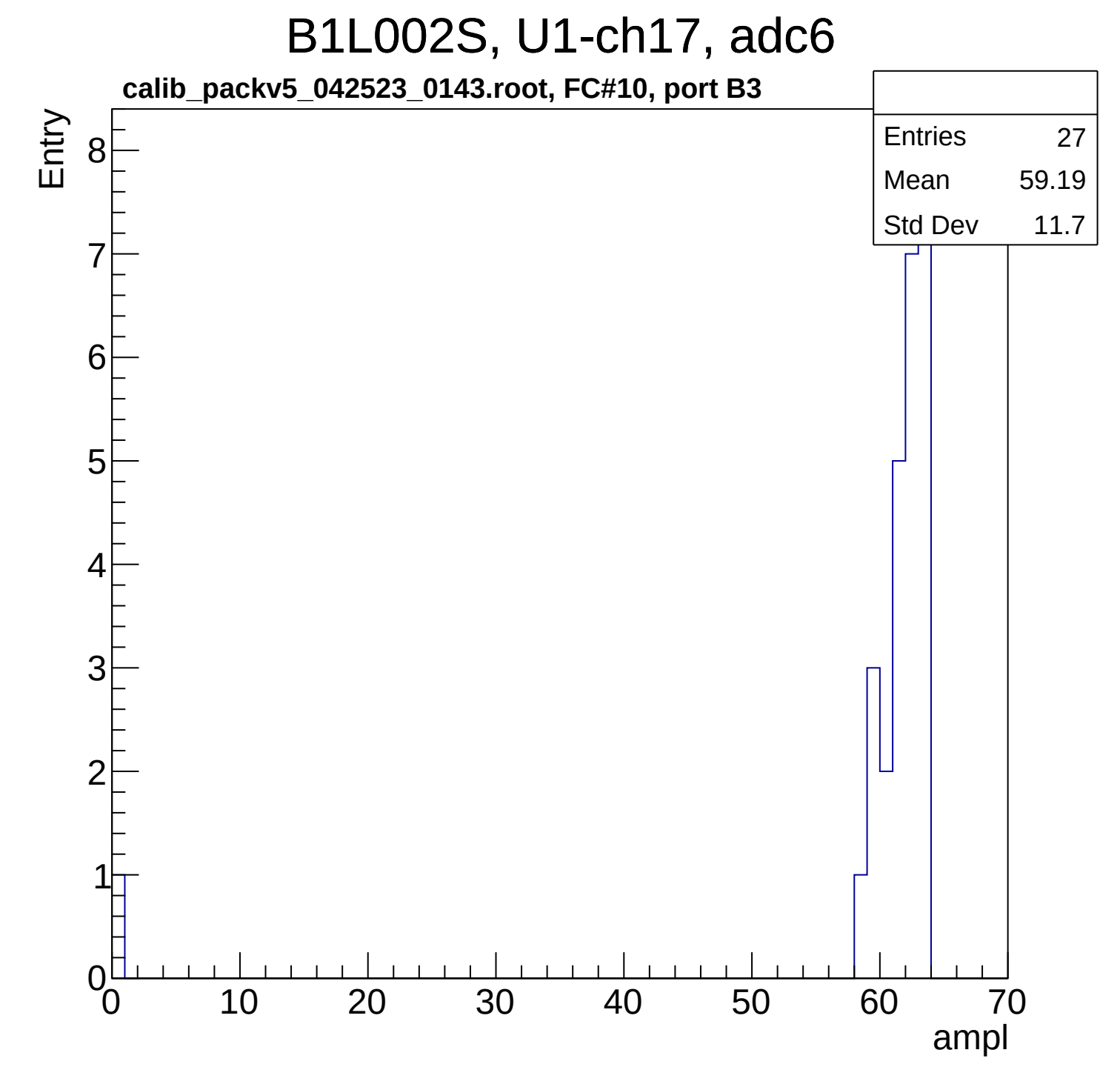
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch17, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 59.19
Std Dev 11.7
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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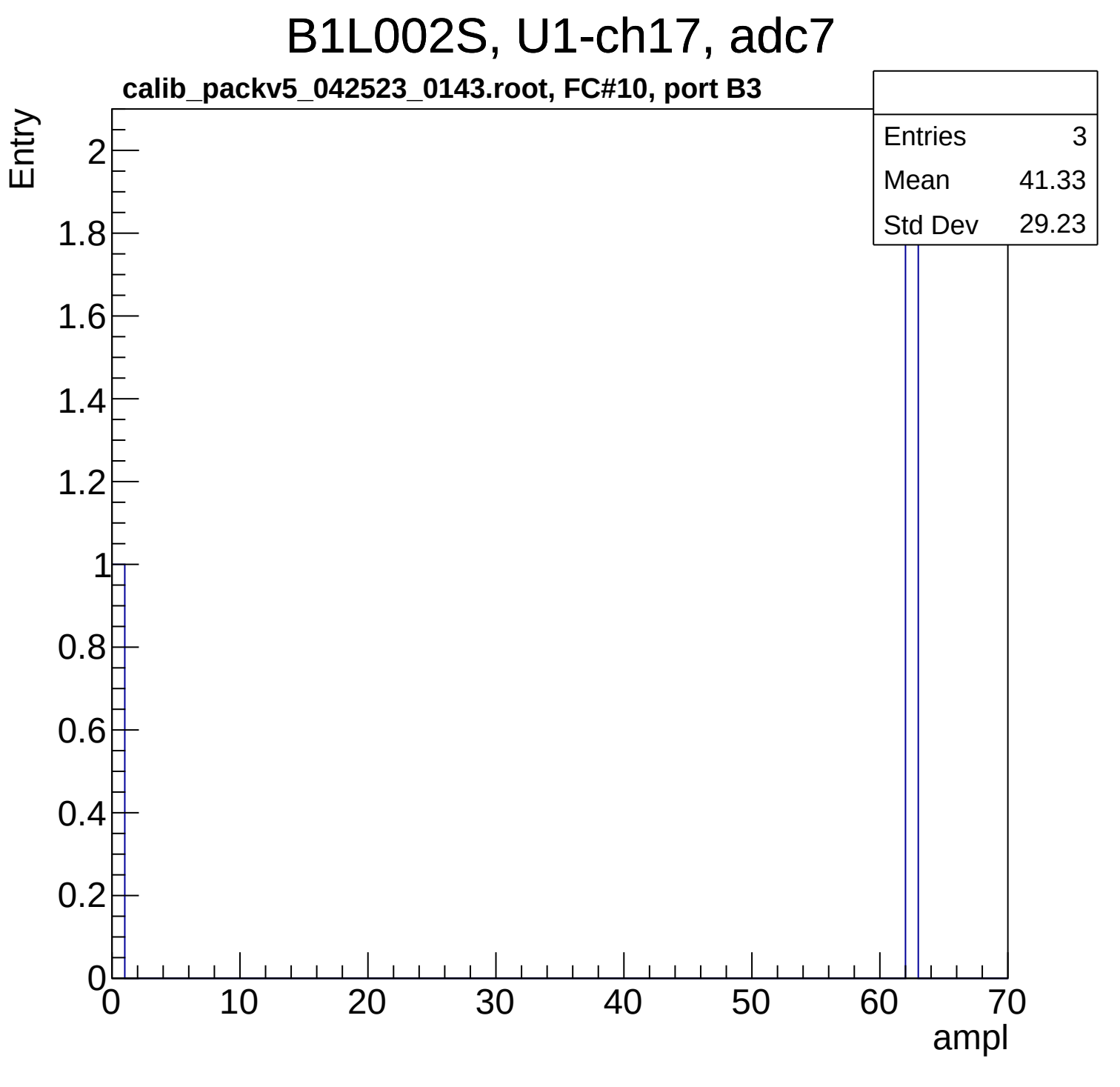
B1L002S, U1-ch17, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.23
Entry
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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10
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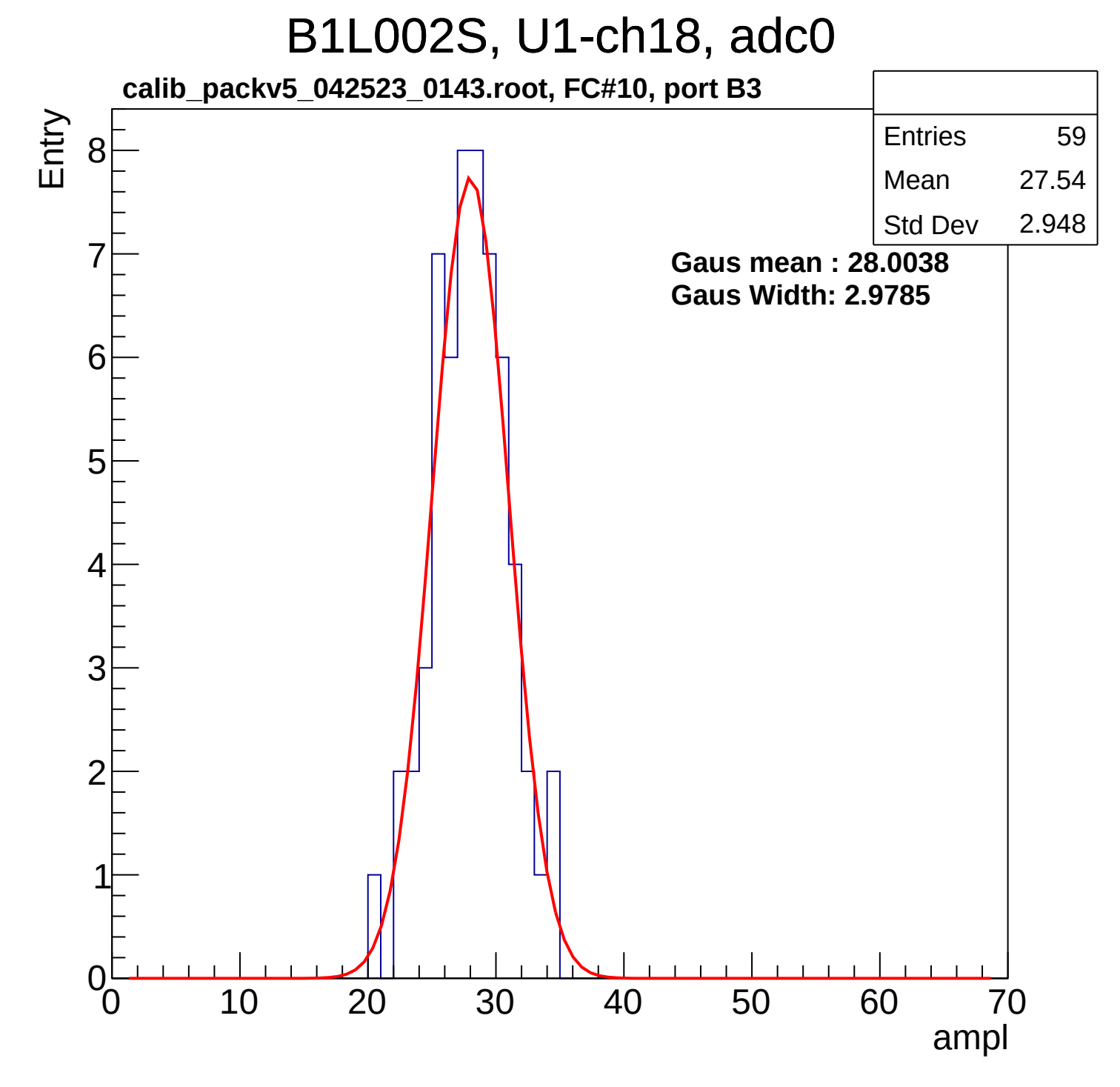
50
60
70
ampl


B1L002S, U1-ch18, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.54
Std Dev 2.948
Gaus mean : 28.0038
Gaus Width: 2.9785
Entry
ampl
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1
2
3
4
5
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7
8
0
10
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60
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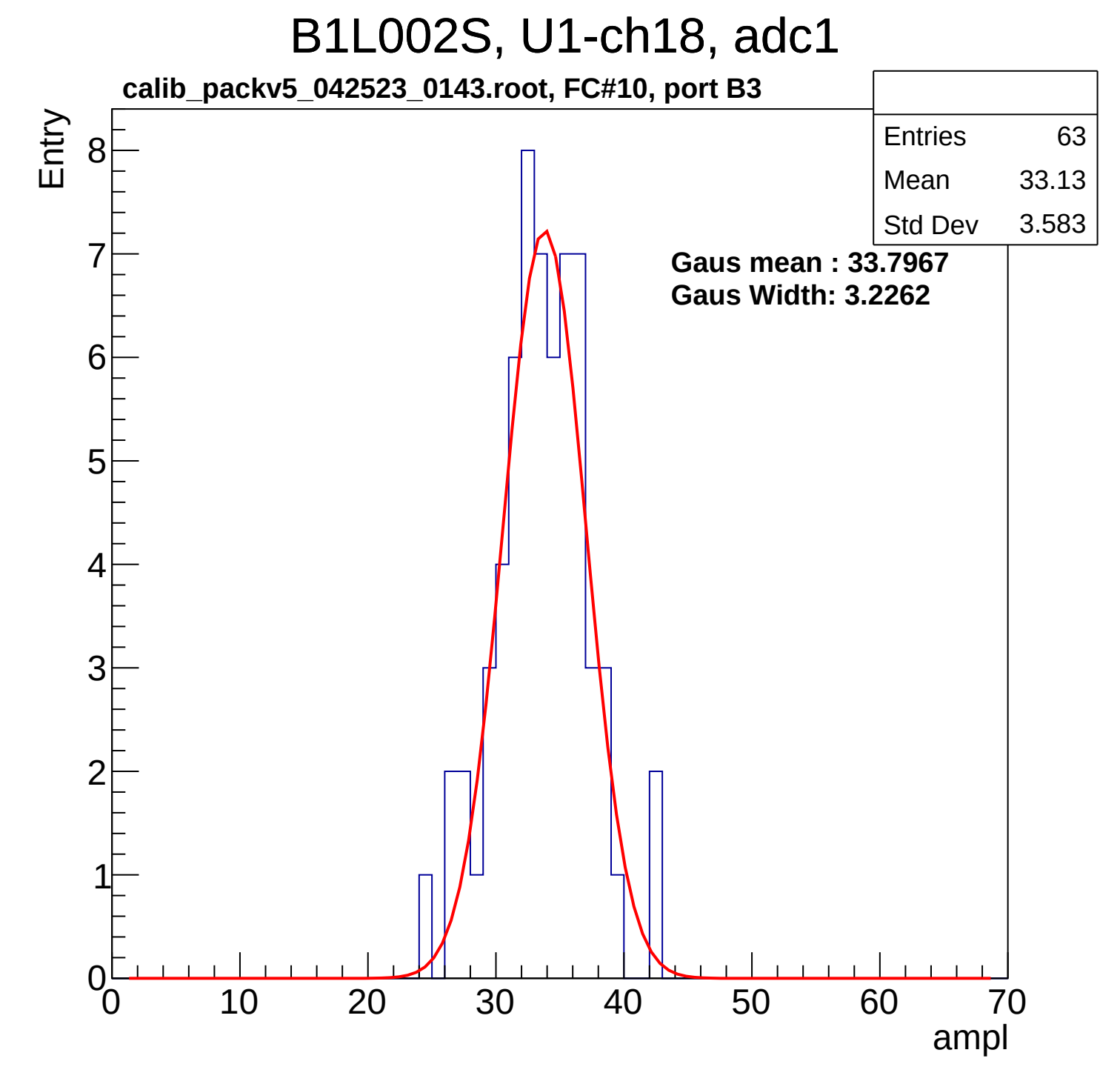
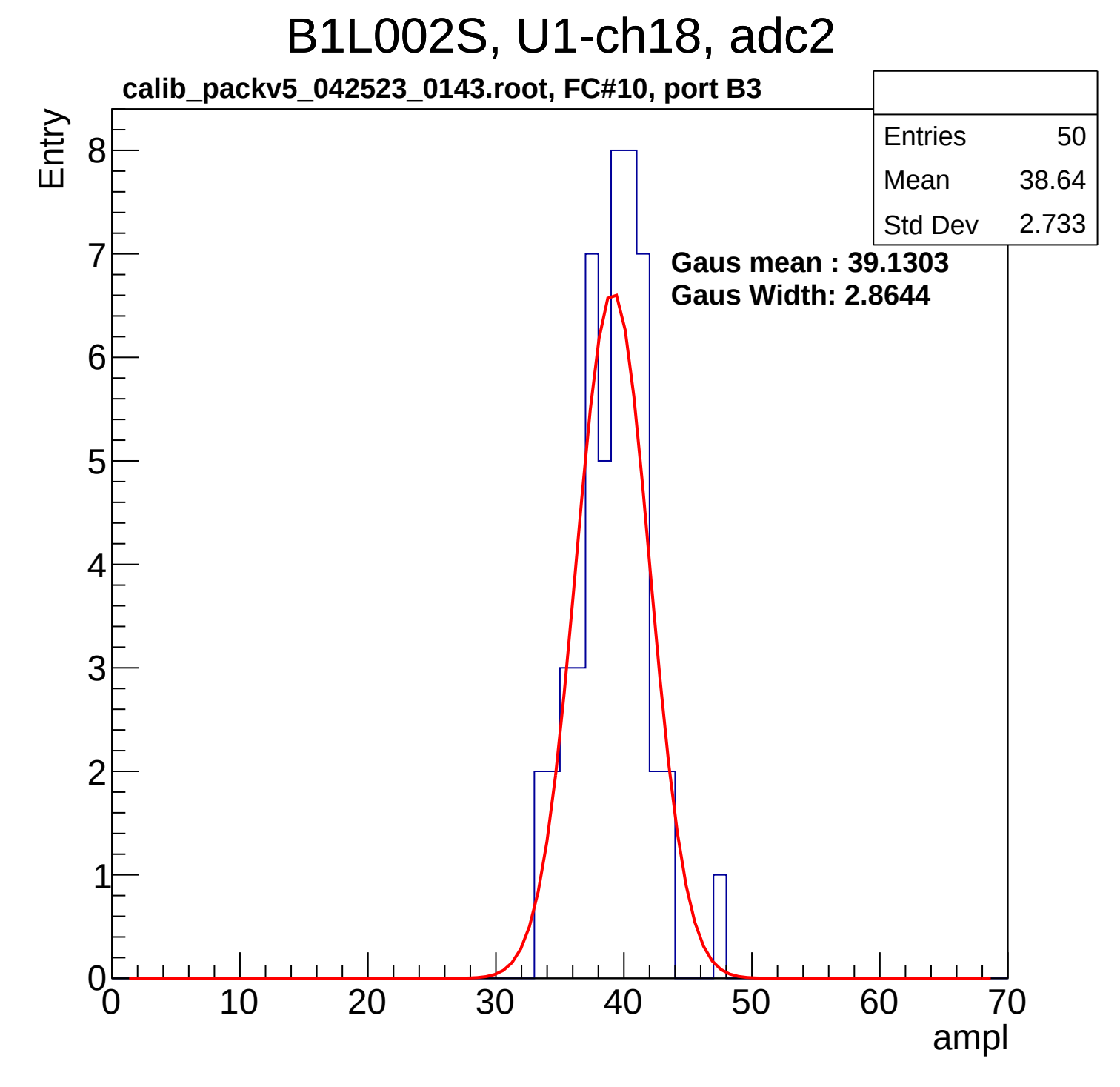

B1L002S, U1-ch18, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.13
Std Dev 3.583
Gaus mean : 33.7967
Gaus Width: 3.2262
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch18, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 38.64
Std Dev 2.733
Gaus mean : 39.1303
Gaus Width: 2.8644
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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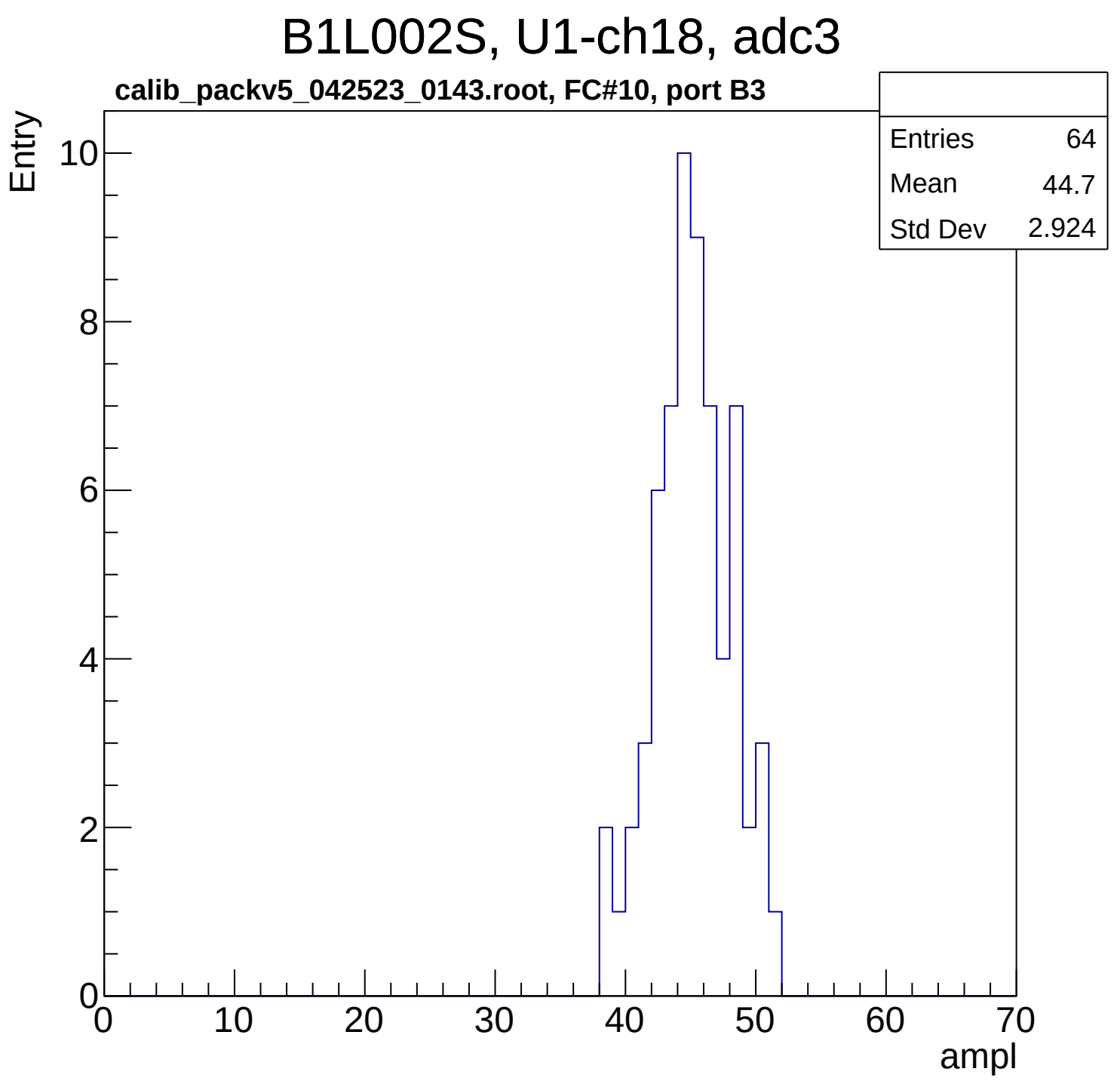
B1L002S, U1-ch18, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 44.7
Std Dev 2.924
Entry
0
2
4
6
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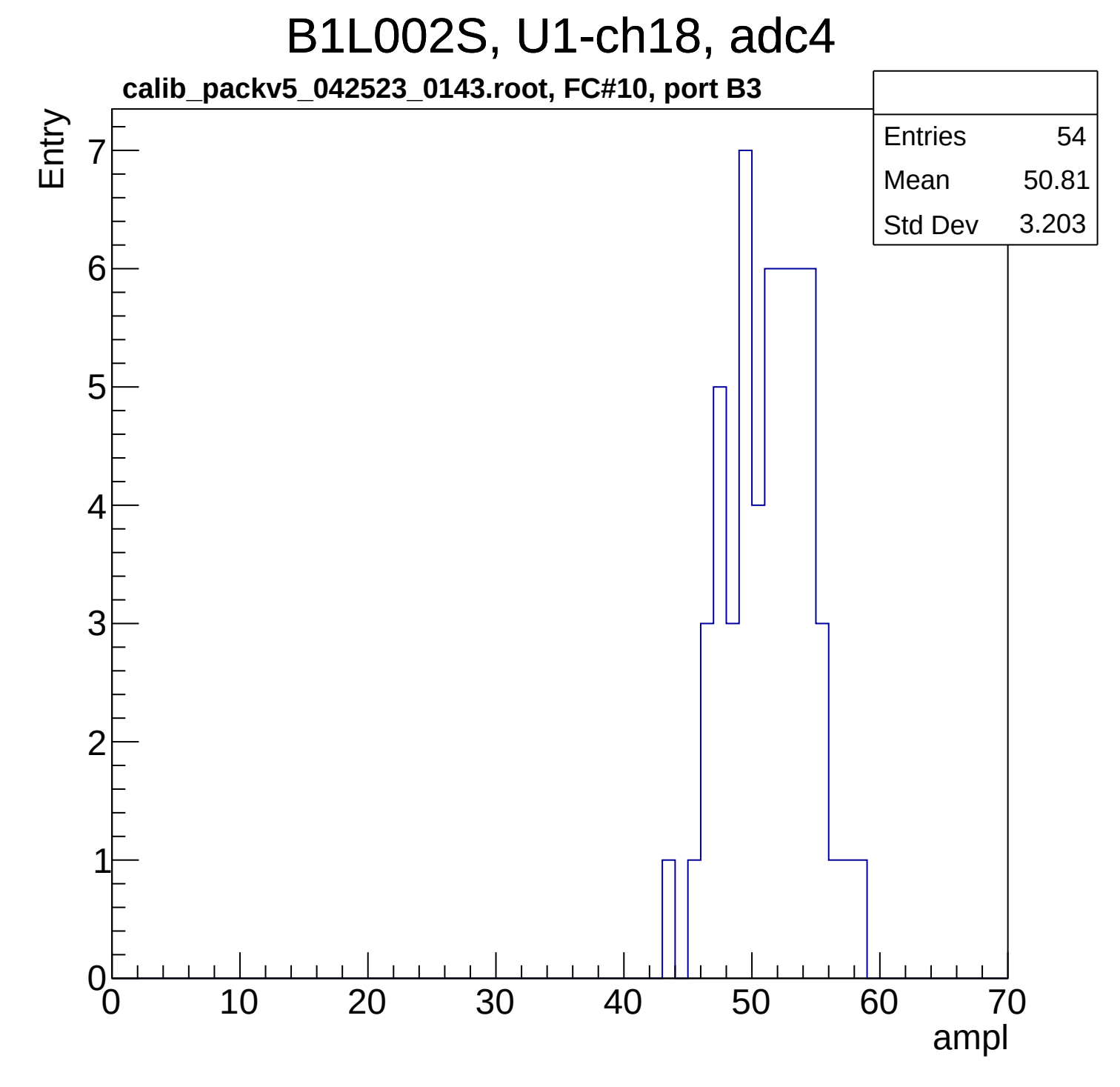
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch18, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 50.81
Std Dev 3.203
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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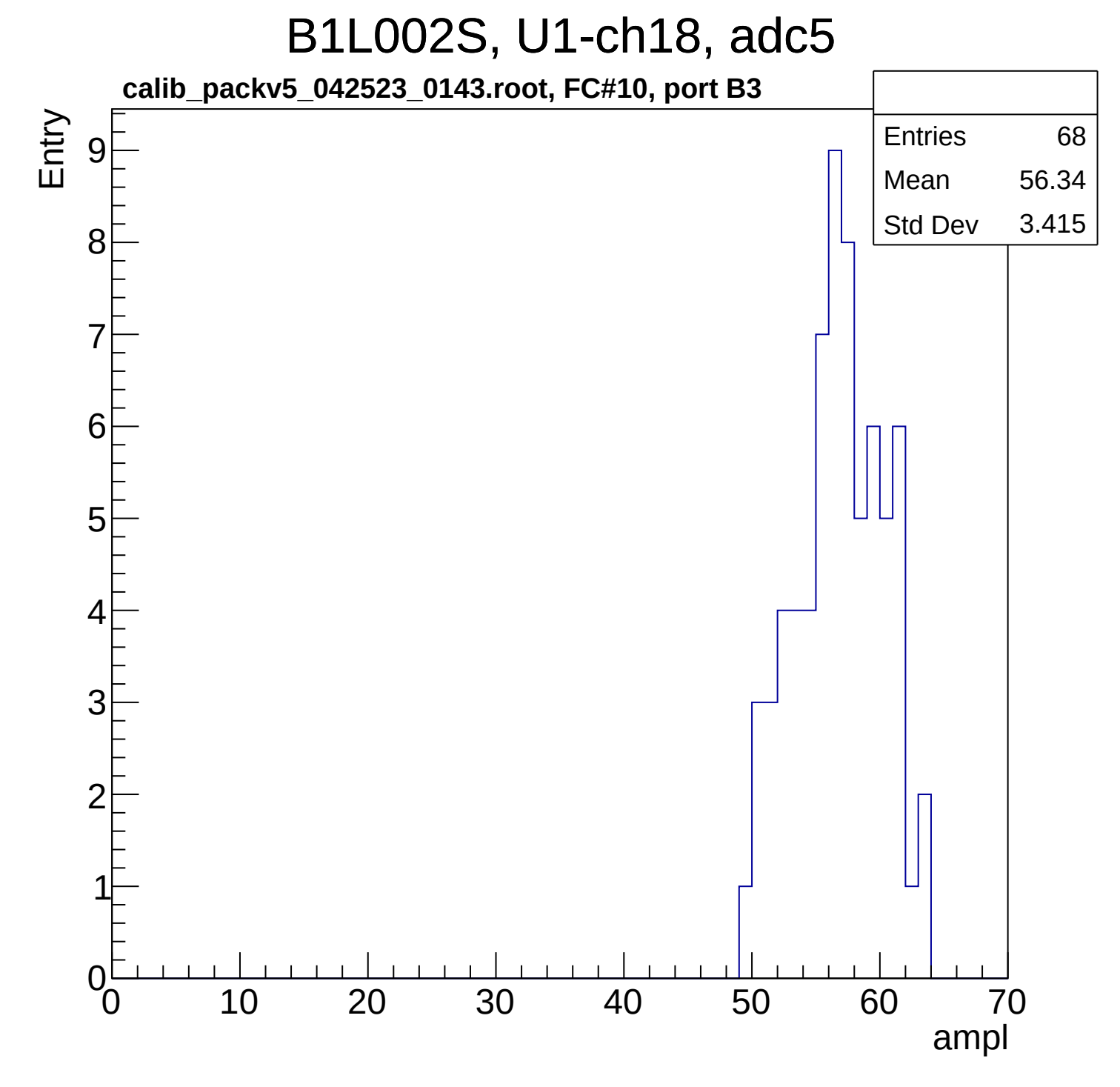
B1L002S, U1-ch18, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 56.34
Std Dev 3.415
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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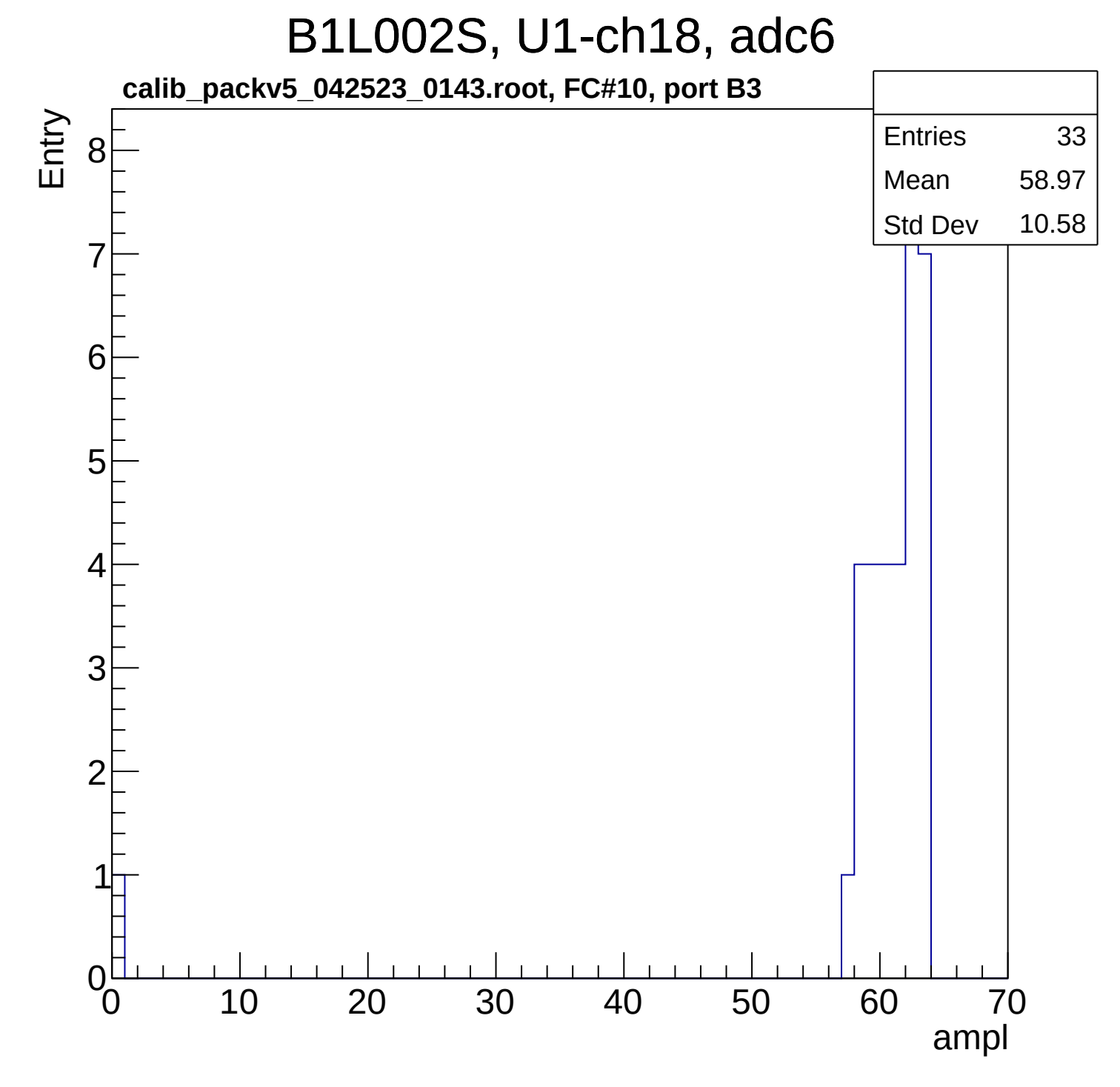
B1L002S, U1-ch18, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 58.97
Std Dev 10.58
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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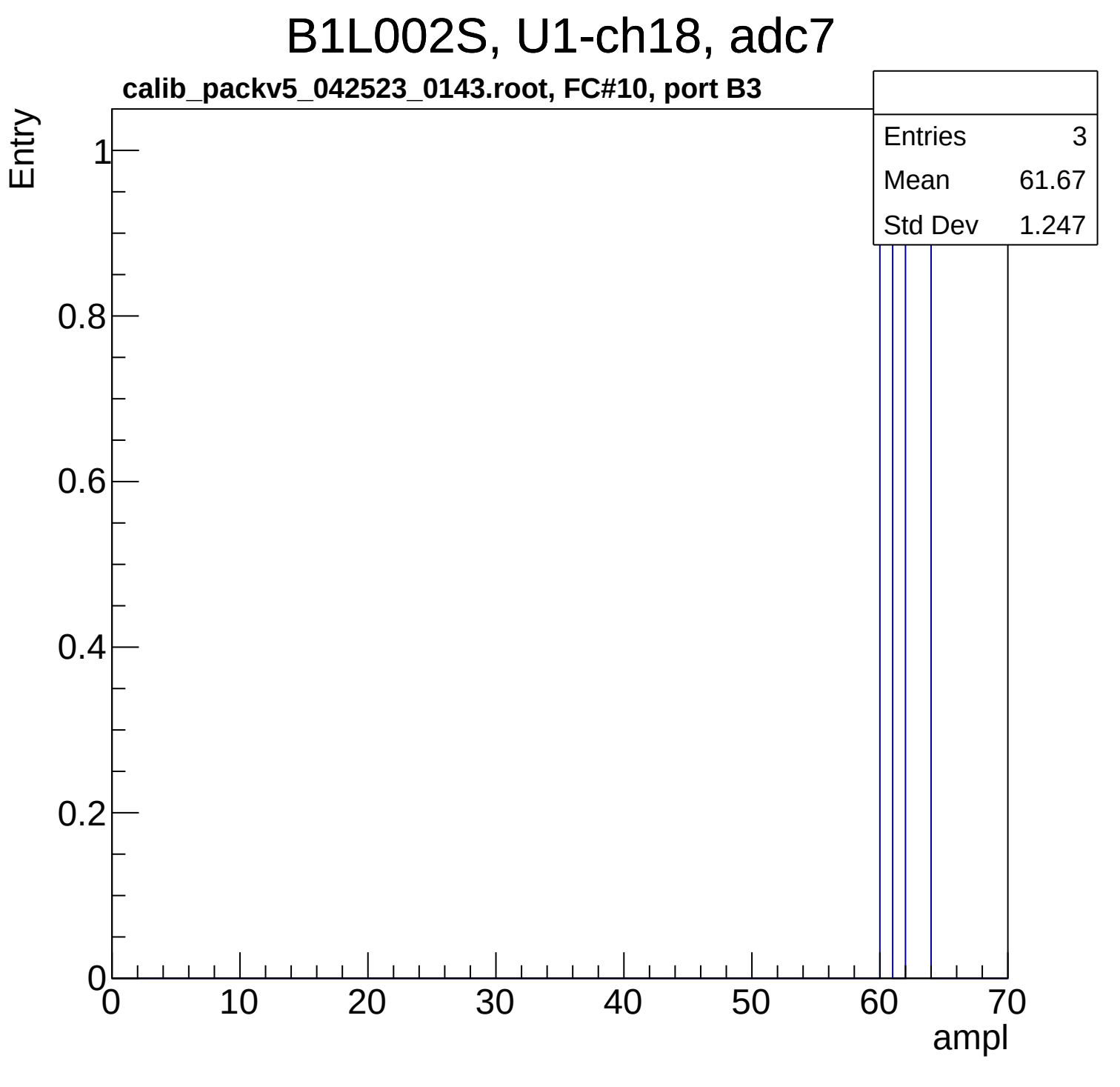
B1L002S, U1-ch18, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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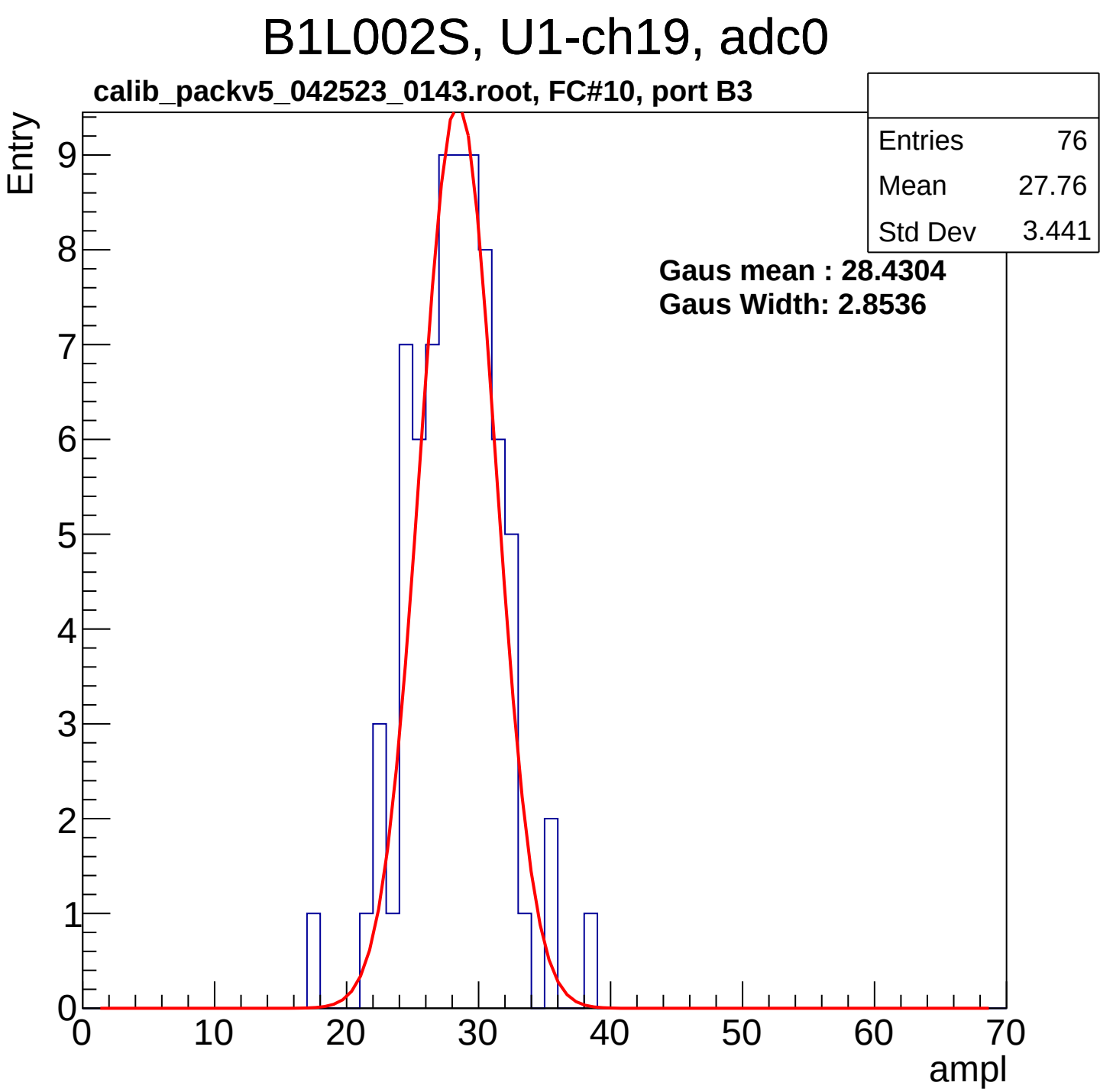
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch19, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 27.76
Std Dev 3.441
Gaus mean : 28.4304
Gaus Width: 2.8536
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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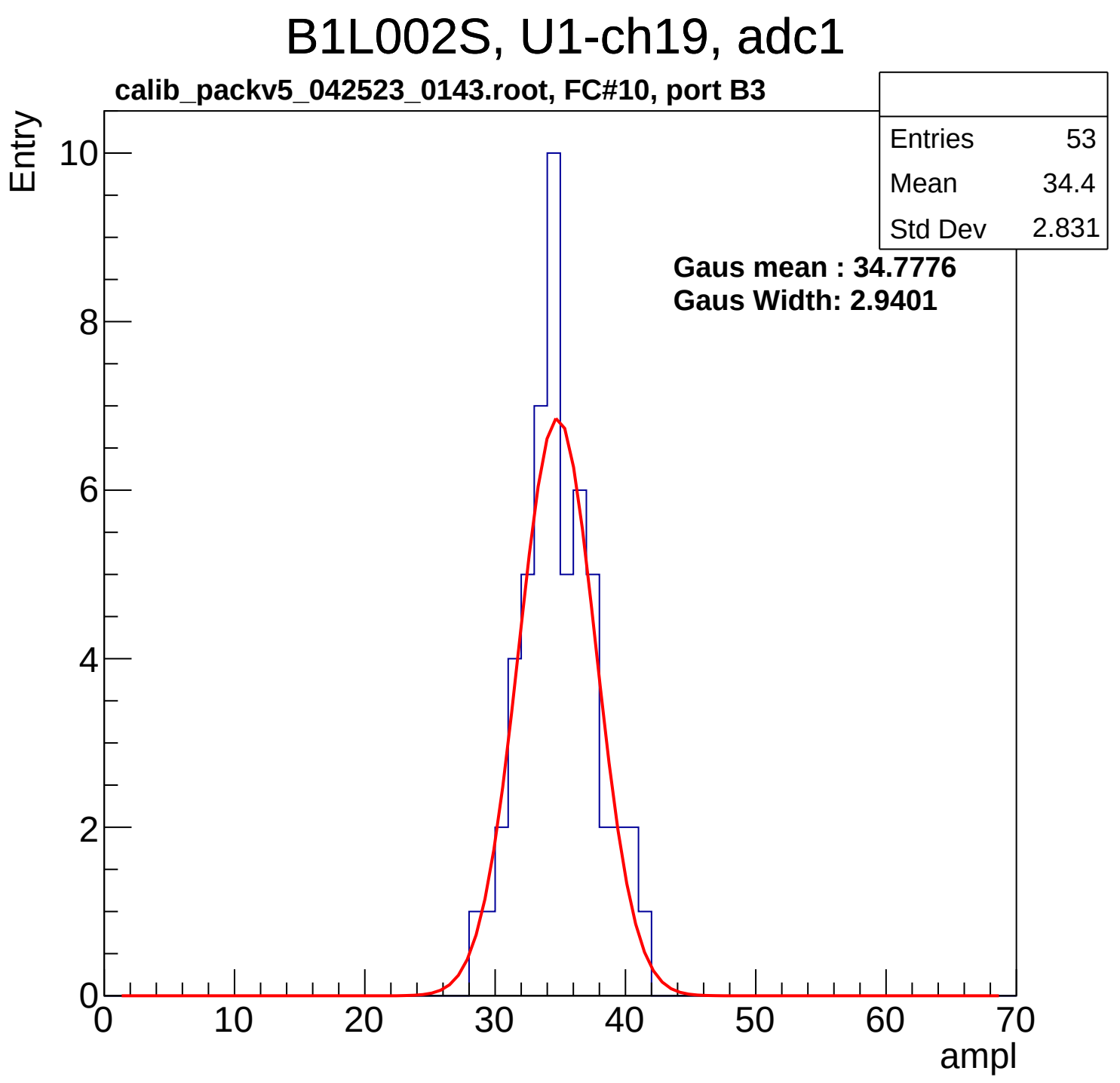
B1L002S, U1-ch19, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 34.4
Std Dev 2.831
Gaus mean : 34.7776
Gaus Width: 2.9401
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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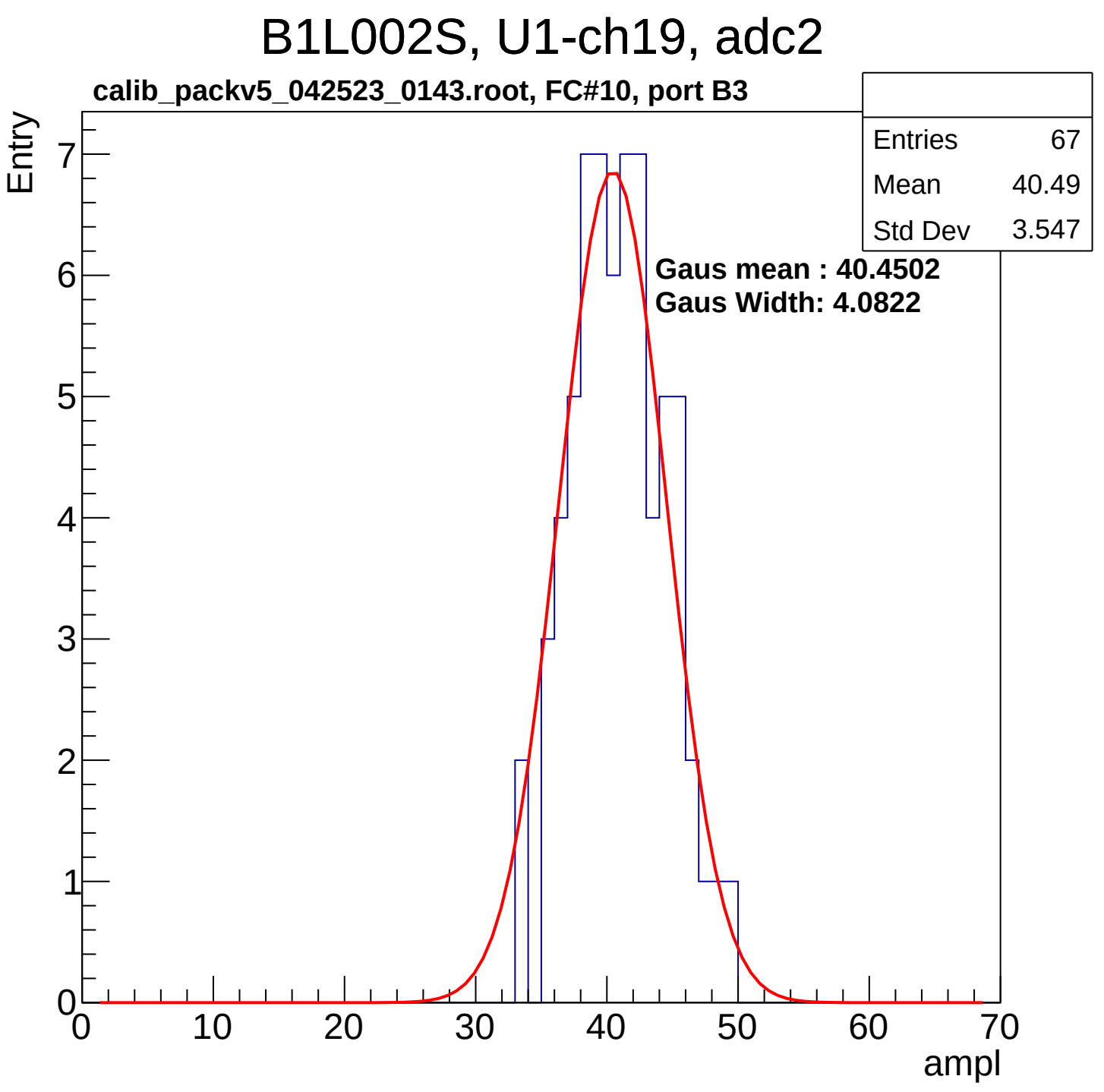
B1L002S, U1-ch19, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 40.49
Std Dev 3.547
Gaus mean : 40.4502
Gaus Width: 4.0822
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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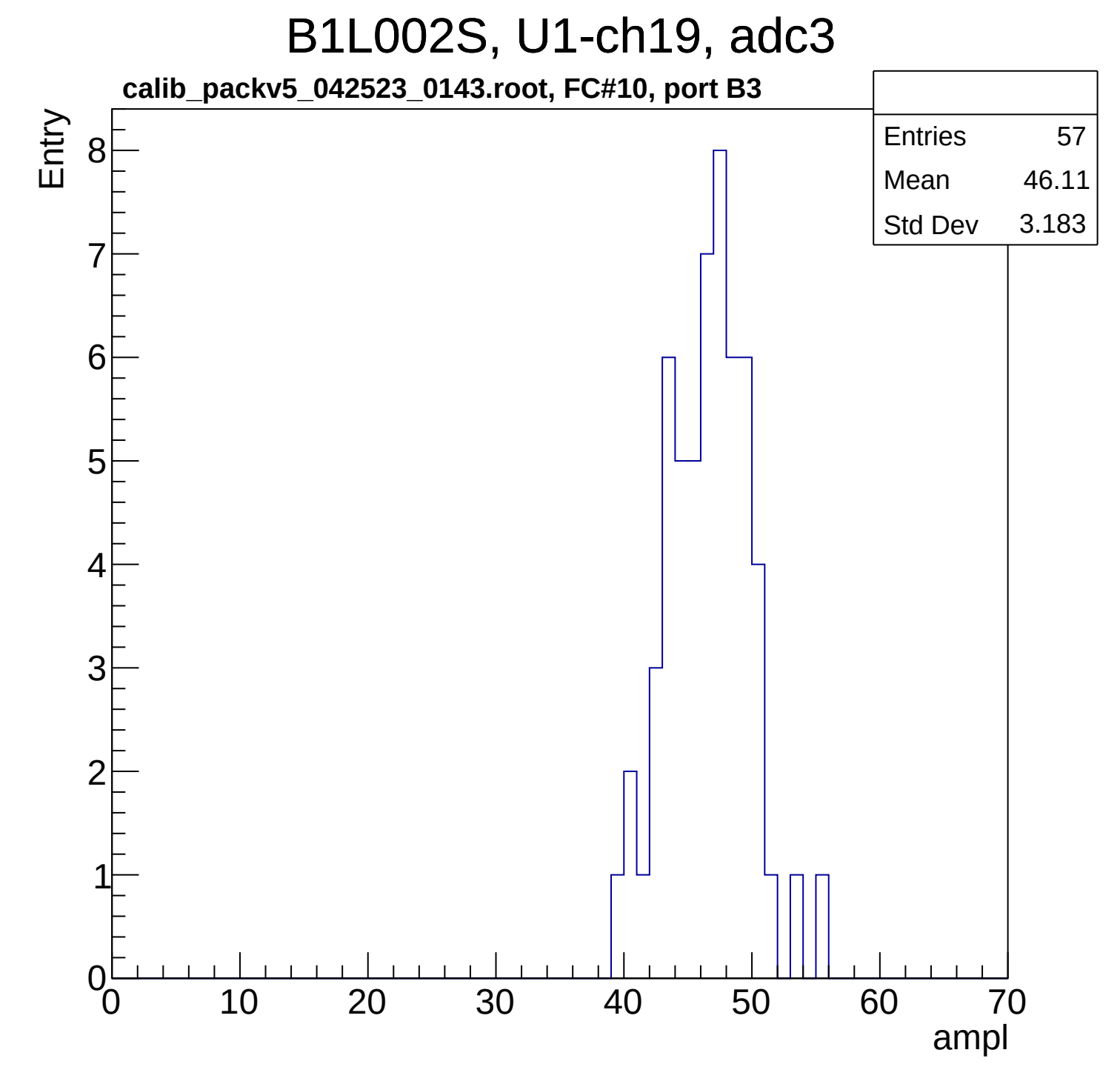
B1L002S, U1-ch19, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 46.11
Std Dev 3.183
Entry
ampl
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1
2
3
4
5
6
7
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0
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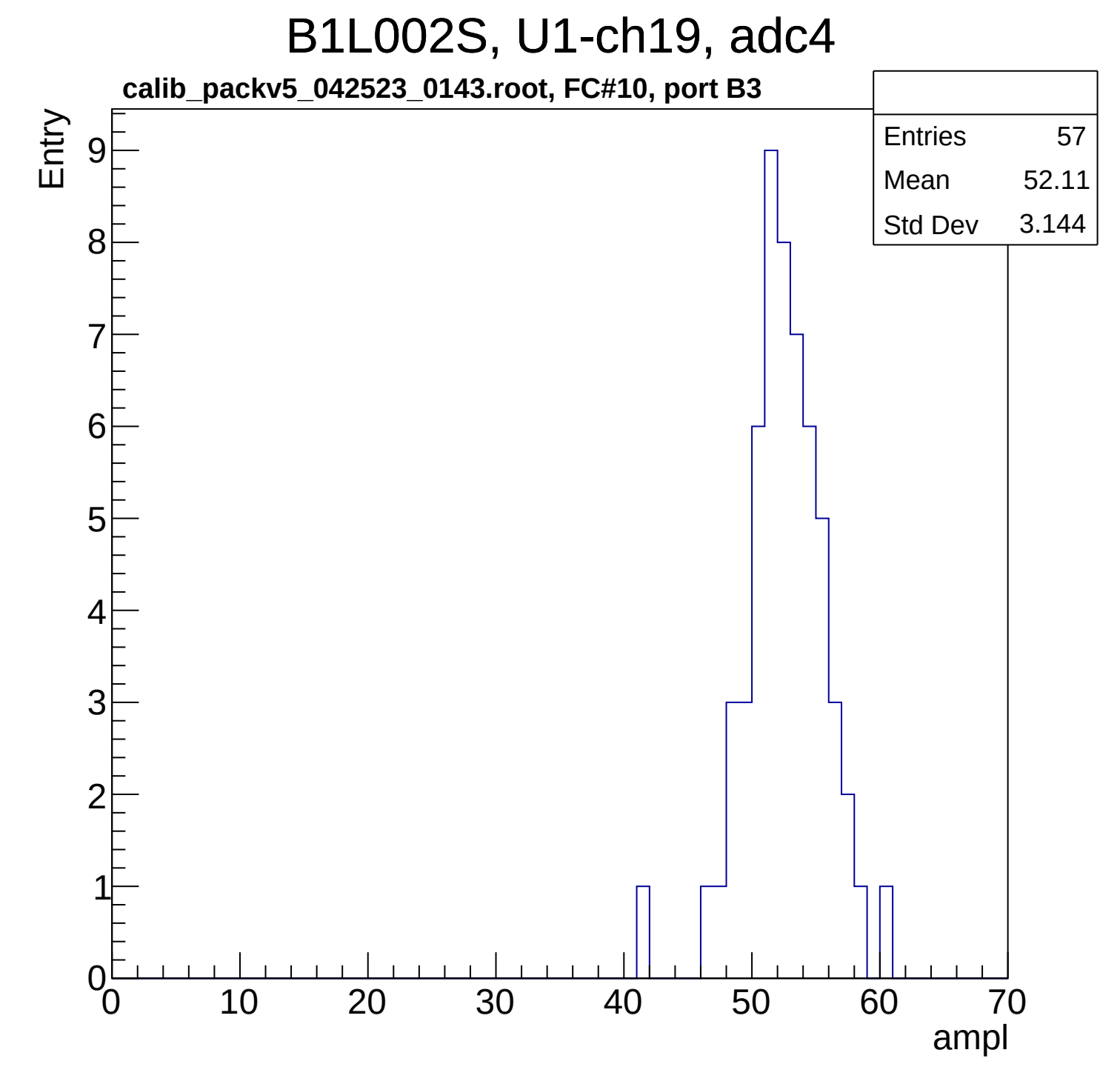
B1L002S, U1-ch19, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.11
Std Dev 3.144
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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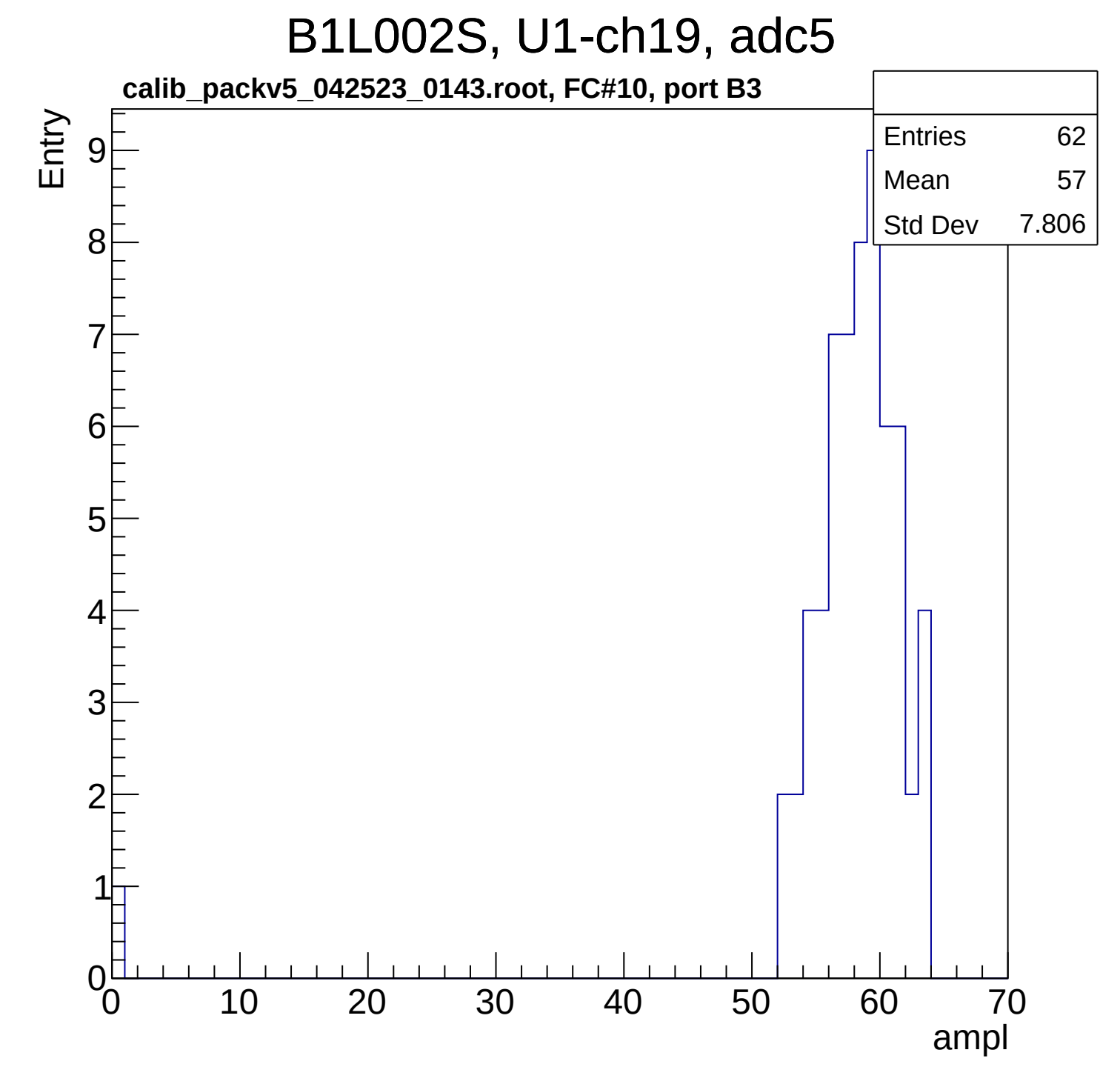
B1L002S, U1-ch19, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57
Std Dev 7.806
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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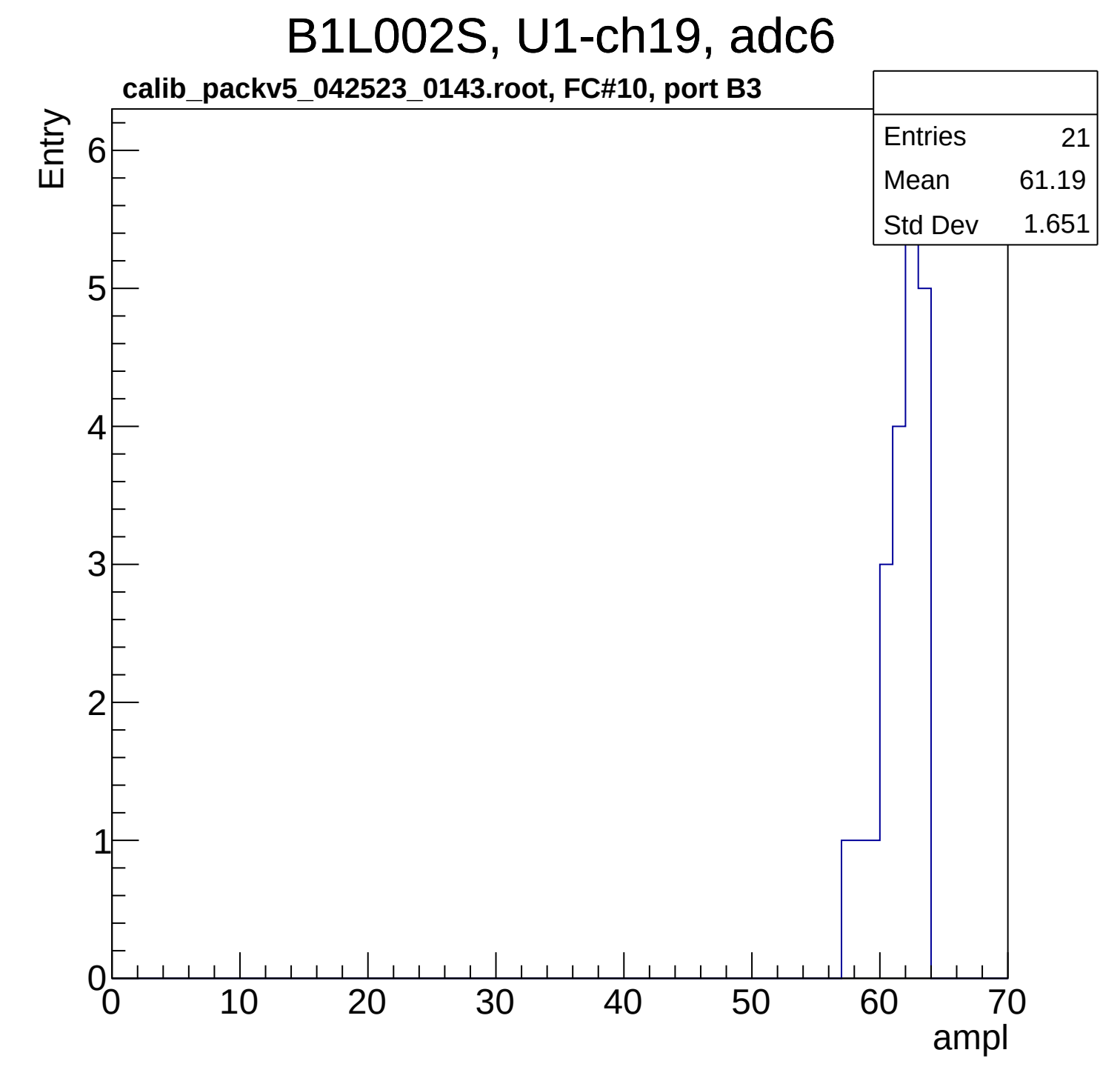
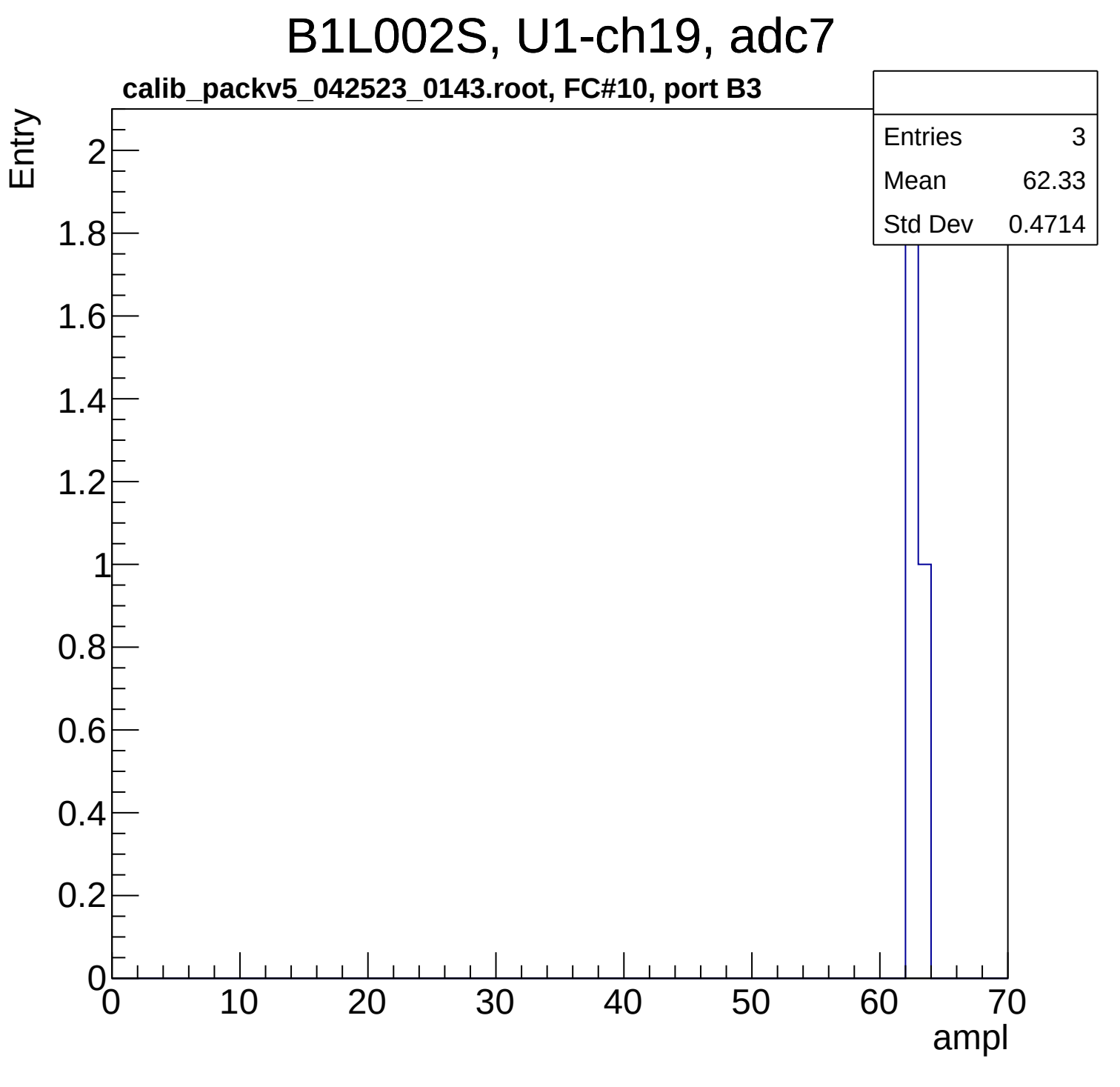

B1L002S, U1-ch19, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.19
Std Dev 1.651
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch19, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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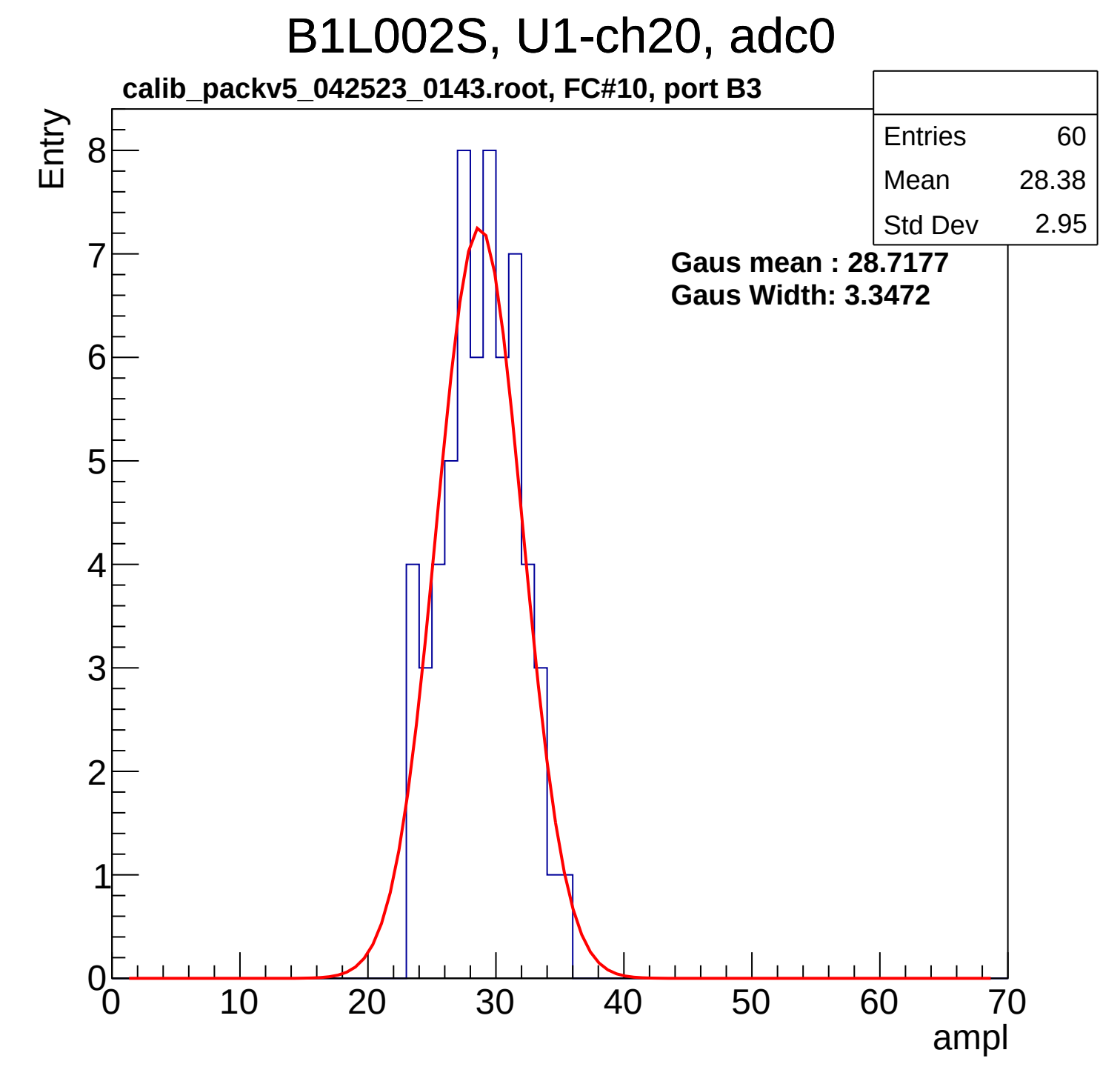
B1L002S, U1-ch20, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.38
Std Dev 2.95
Gaus mean : 28.7177
Gaus Width: 3.3472
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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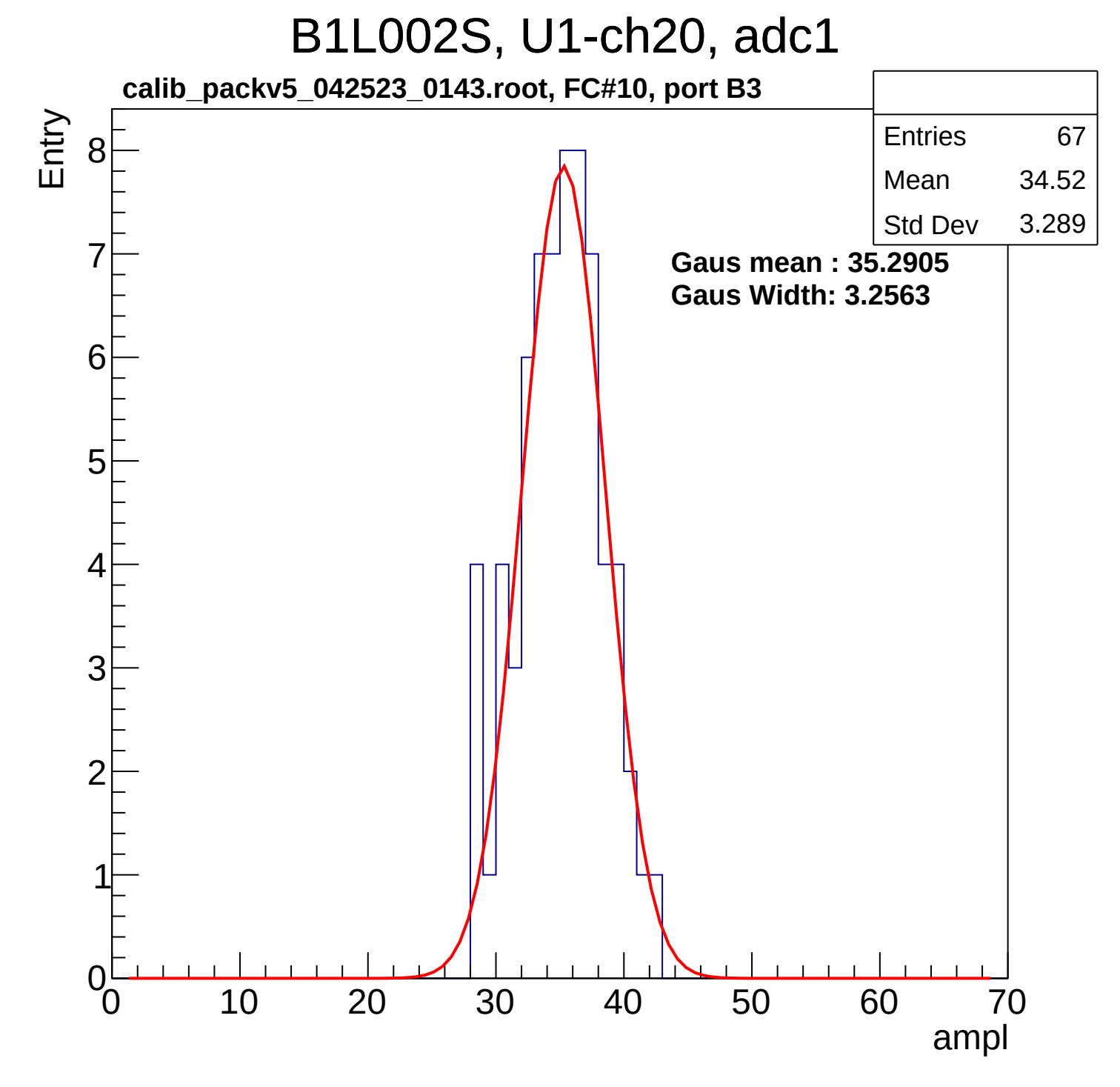
B1L002S, U1-ch20, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.52
Std Dev 3.289
Gaus mean : 35.2905
Gaus Width: 3.2563
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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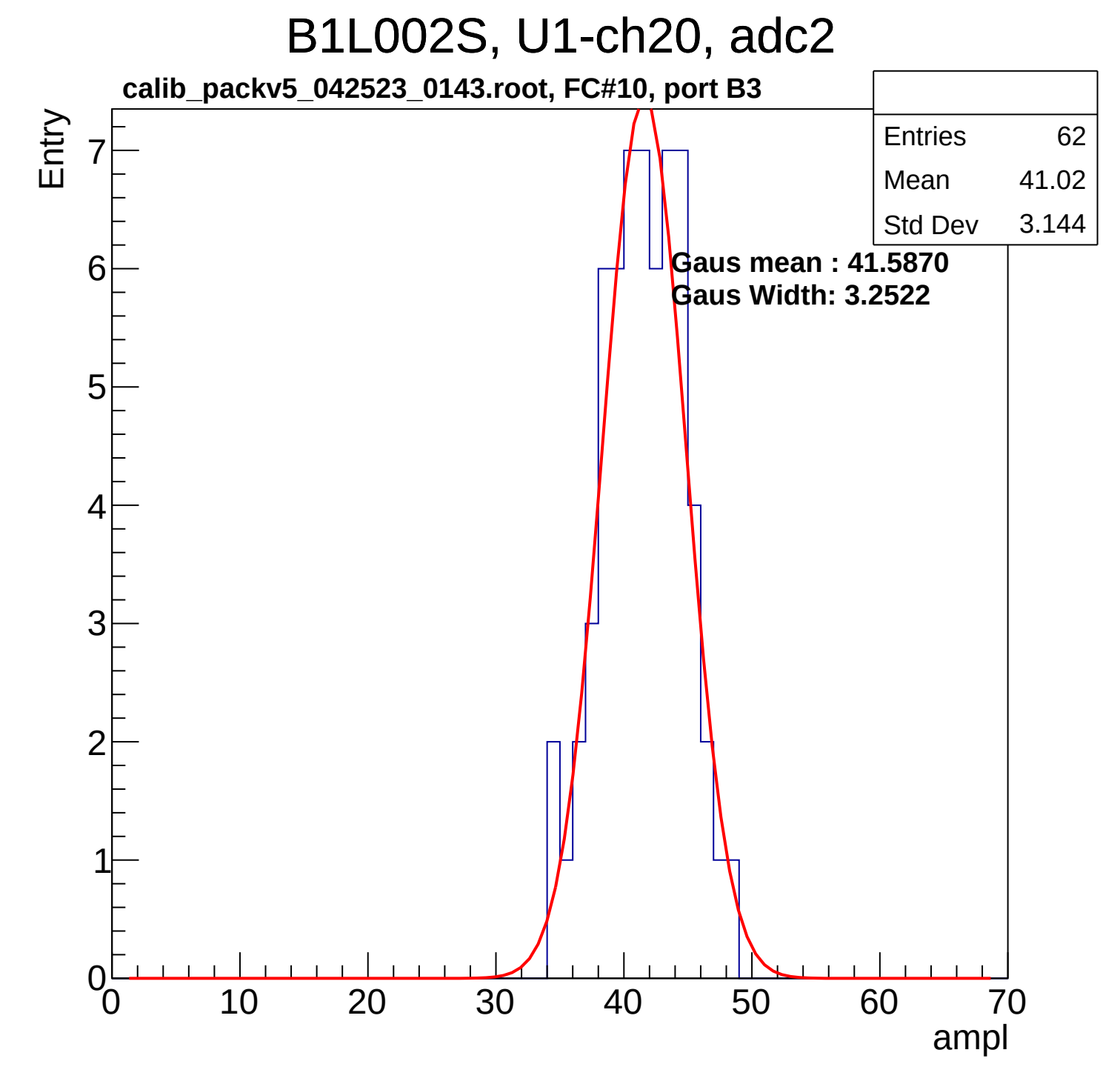
B1L002S, U1-ch20, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.02
Std Dev 3.144
Gaus mean : 41.5870
Gaus Width: 3.2522
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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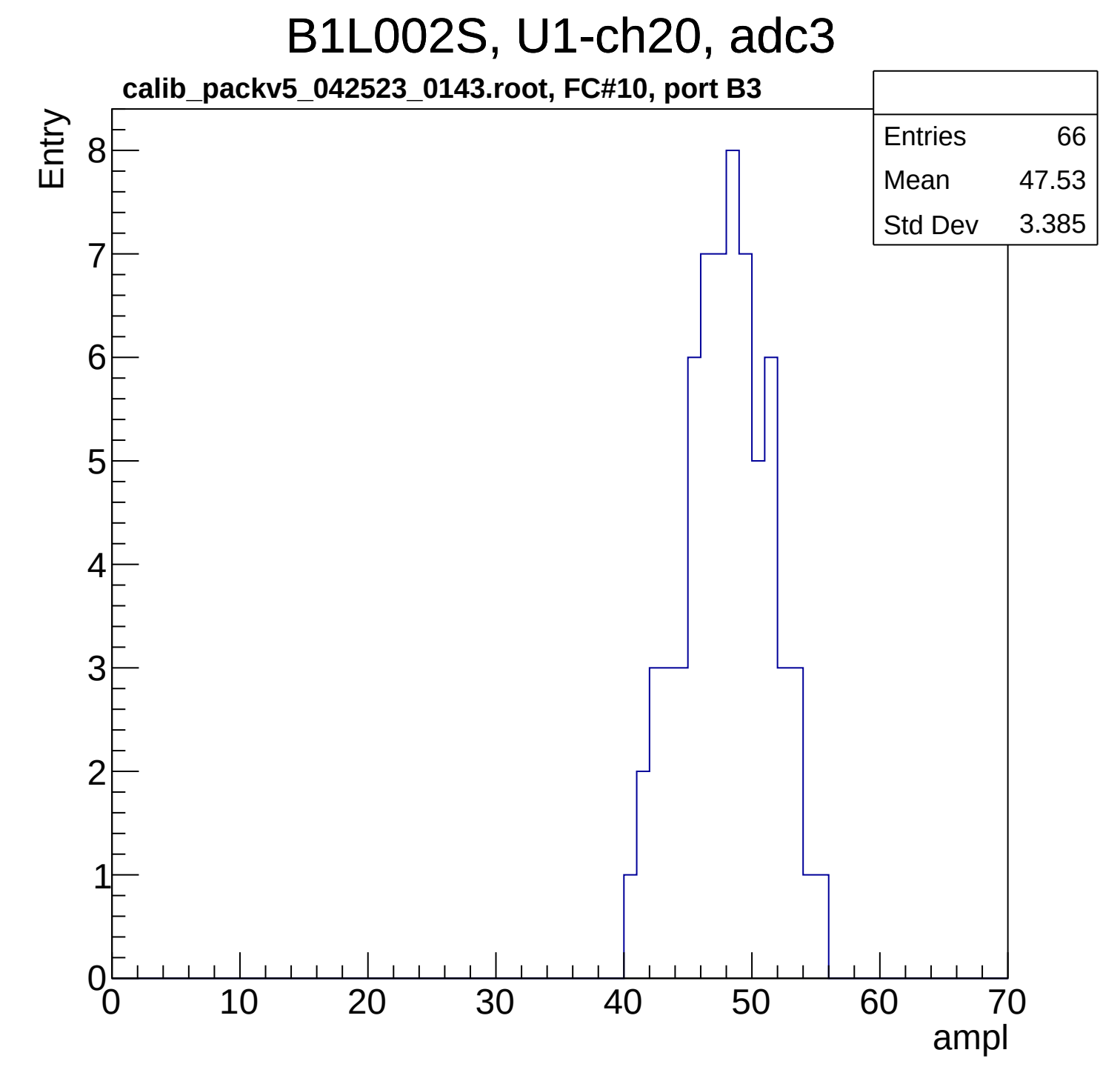
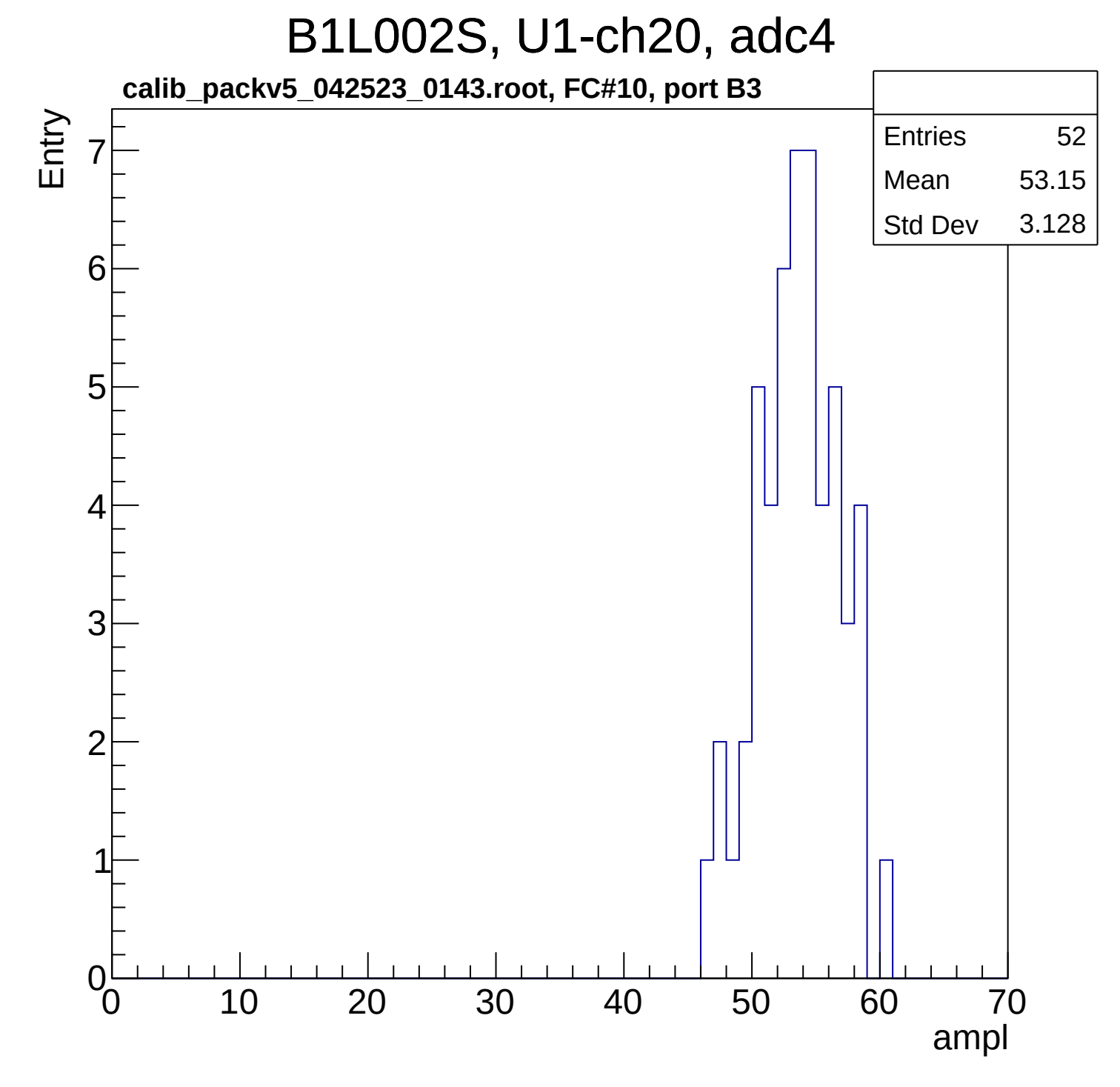
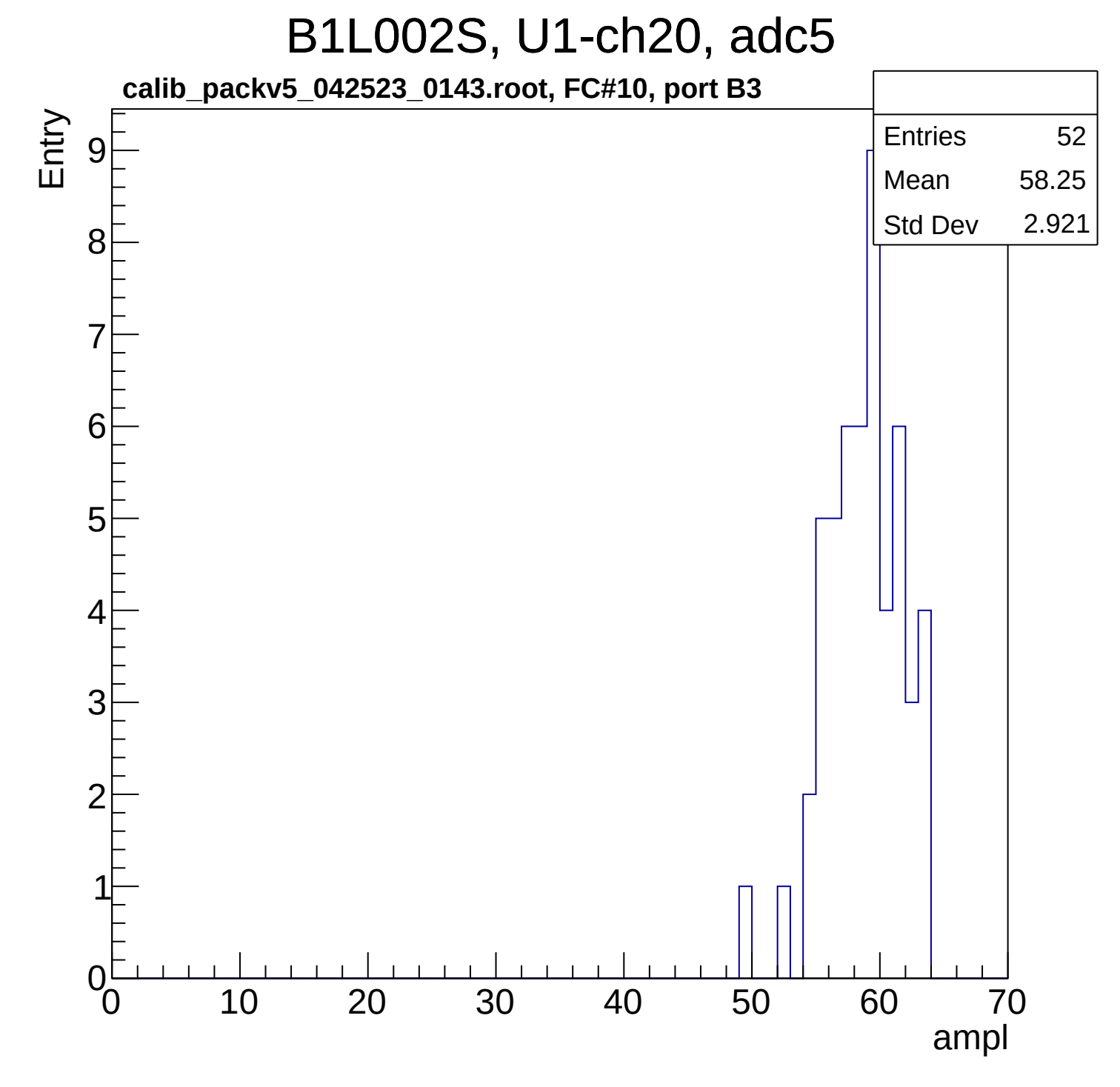

B1L002S, U1-ch20, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.53
Std Dev 3.385
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch20, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.15
Std Dev 3.128
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch20, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.25
Std Dev 2.921
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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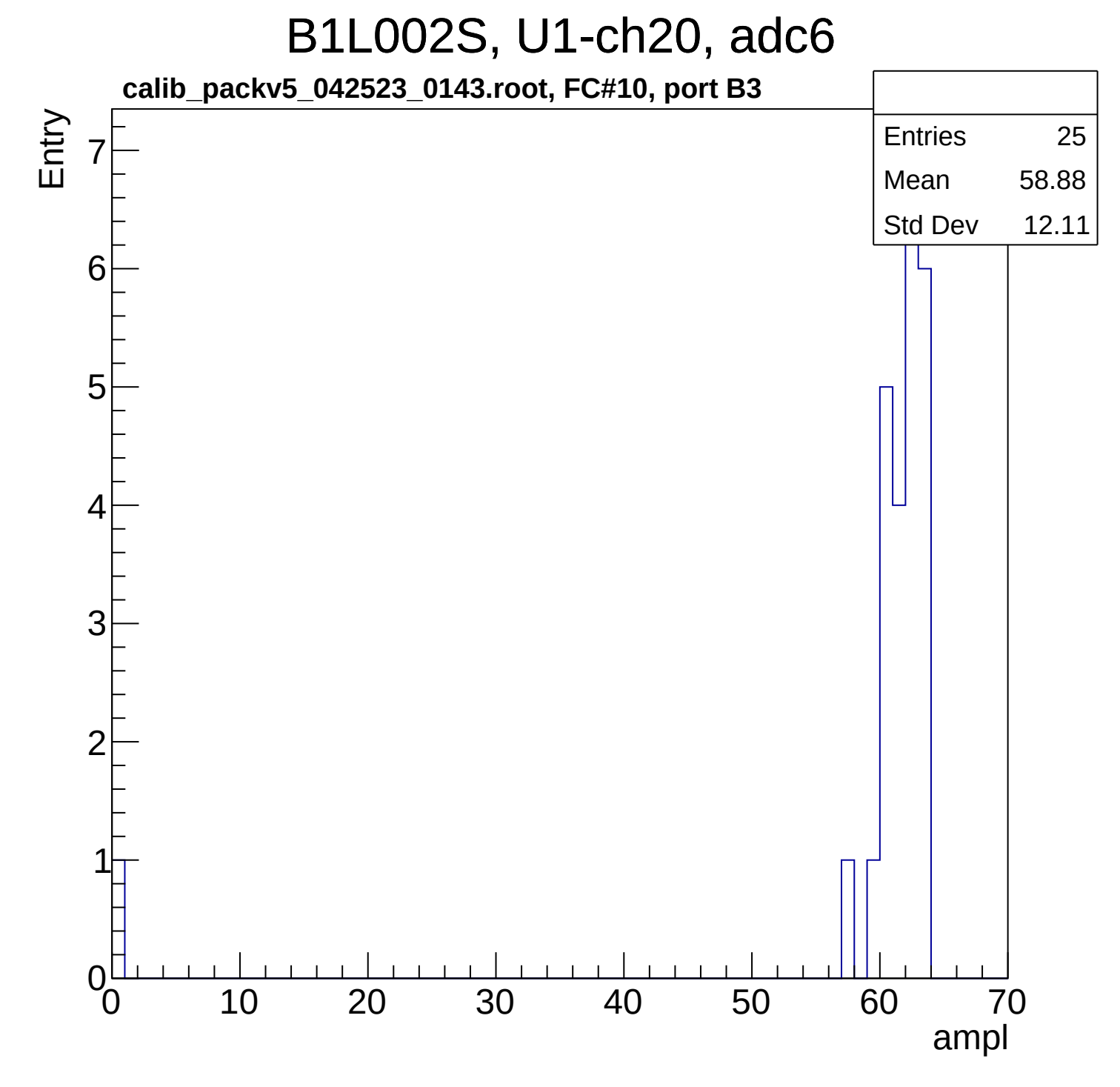
40
50
60
70


B1L002S, U1-ch20, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.88
Std Dev 12.11
Entry
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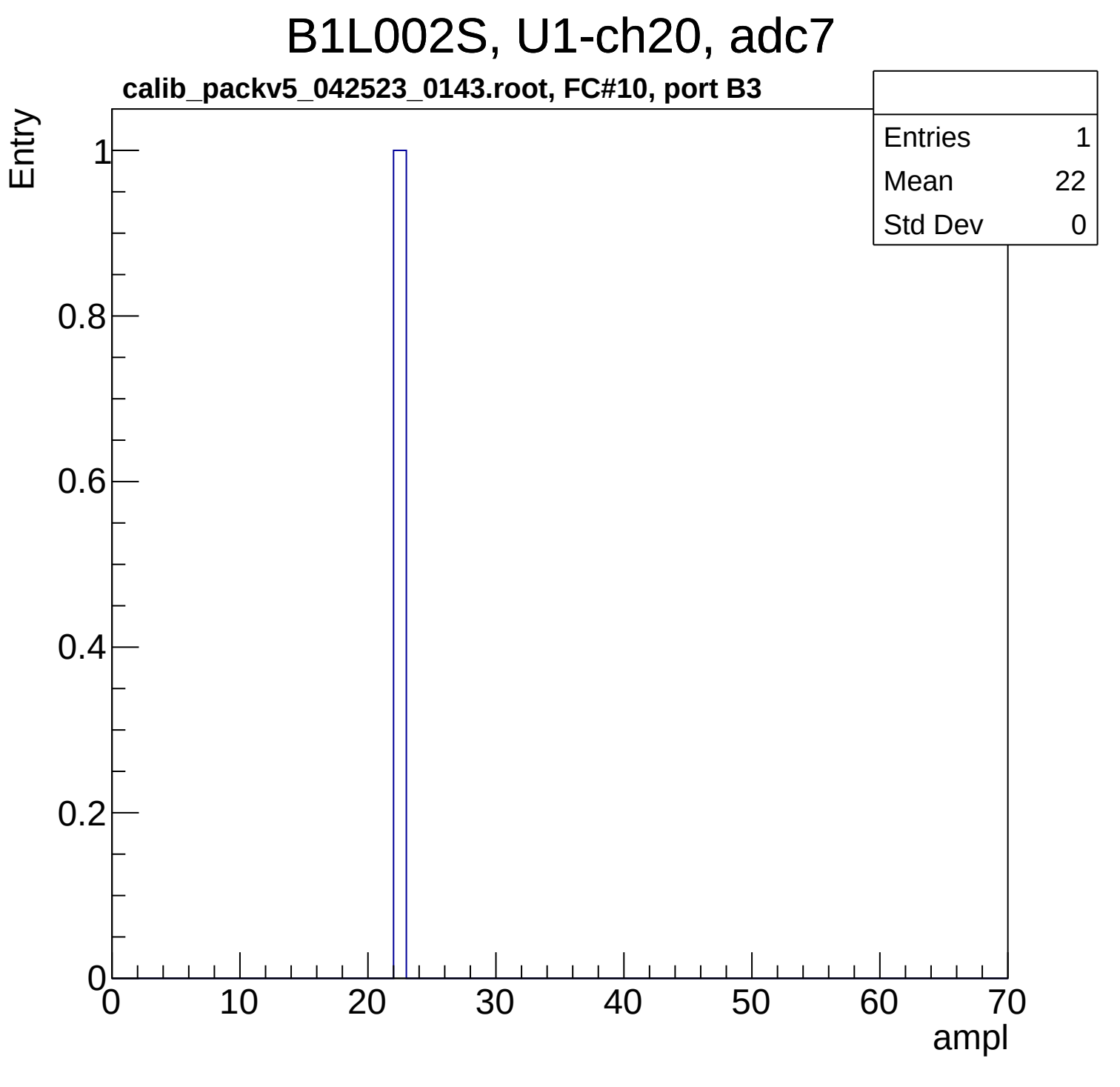
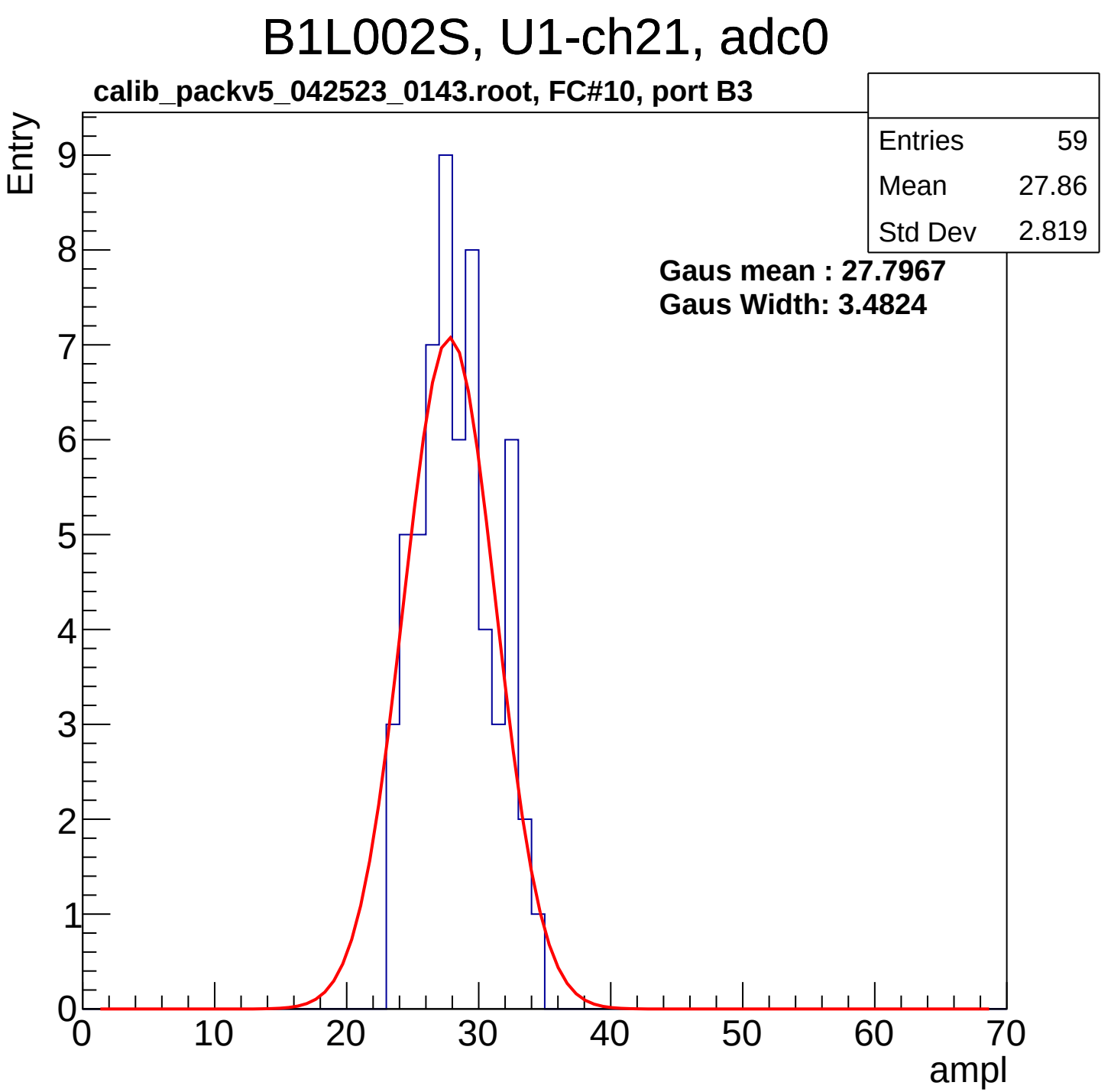
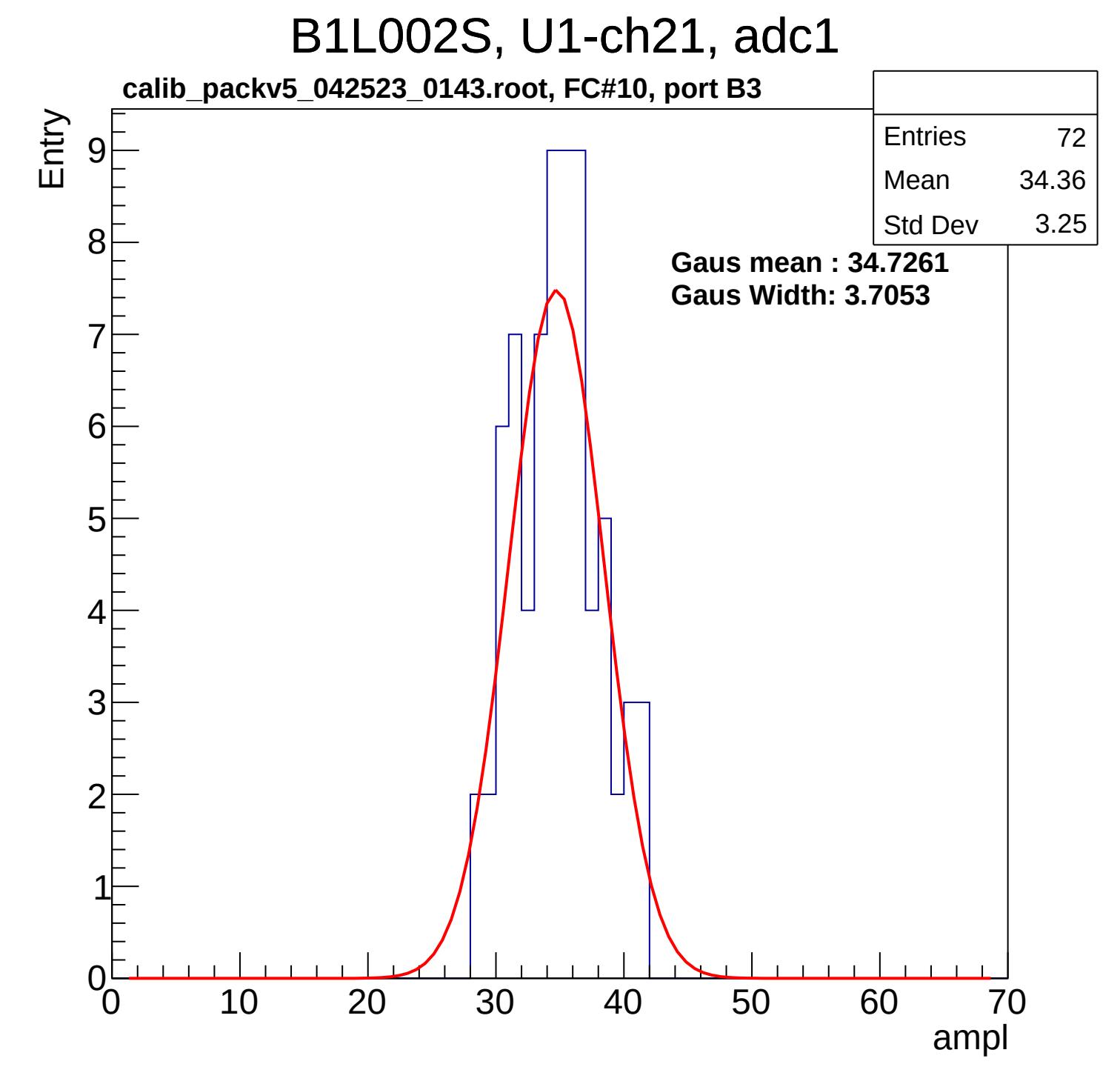
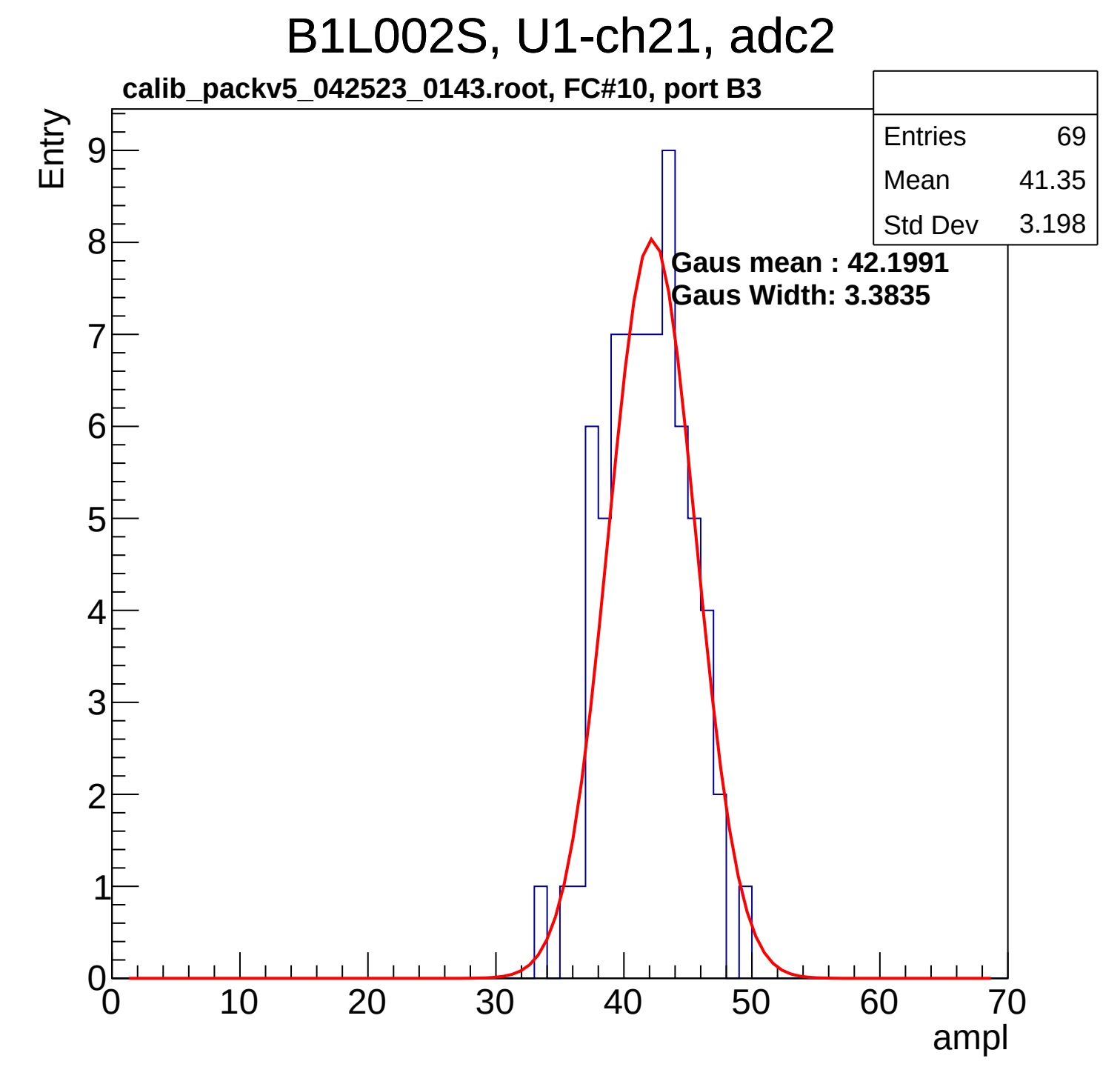
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch20, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch21, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.86
Std Dev 2.819
Gaus mean : 27.7967
Gaus Width: 3.4824
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch21, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.36
Std Dev 3.25
Gaus mean : 34.7261
Gaus Width: 3.7053
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch21, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.35
Std Dev 3.198
Gaus mean : 42.1991
Gaus Width: 3.3835
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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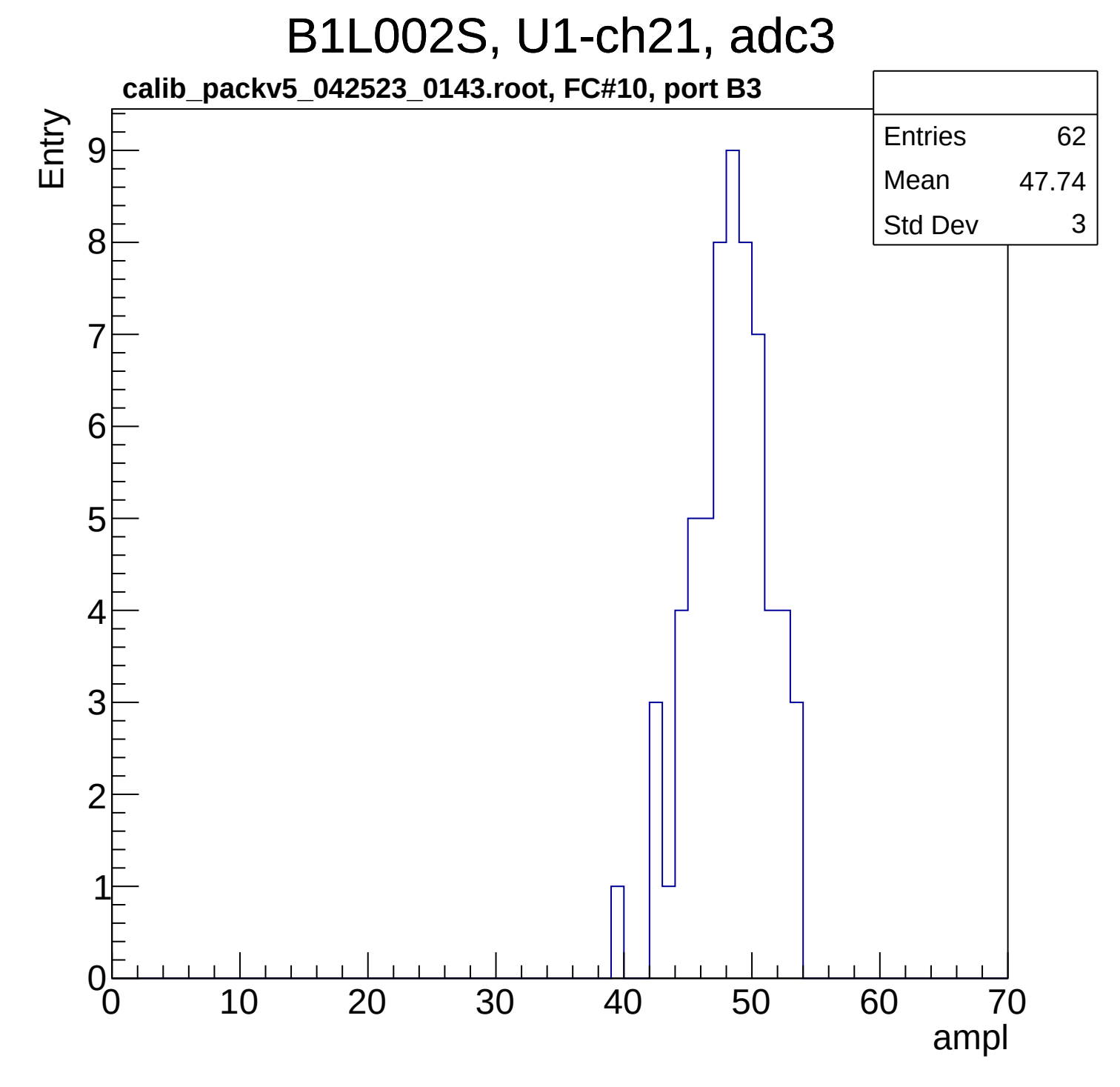
B1L002S, U1-ch21, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.74
Std Dev 3
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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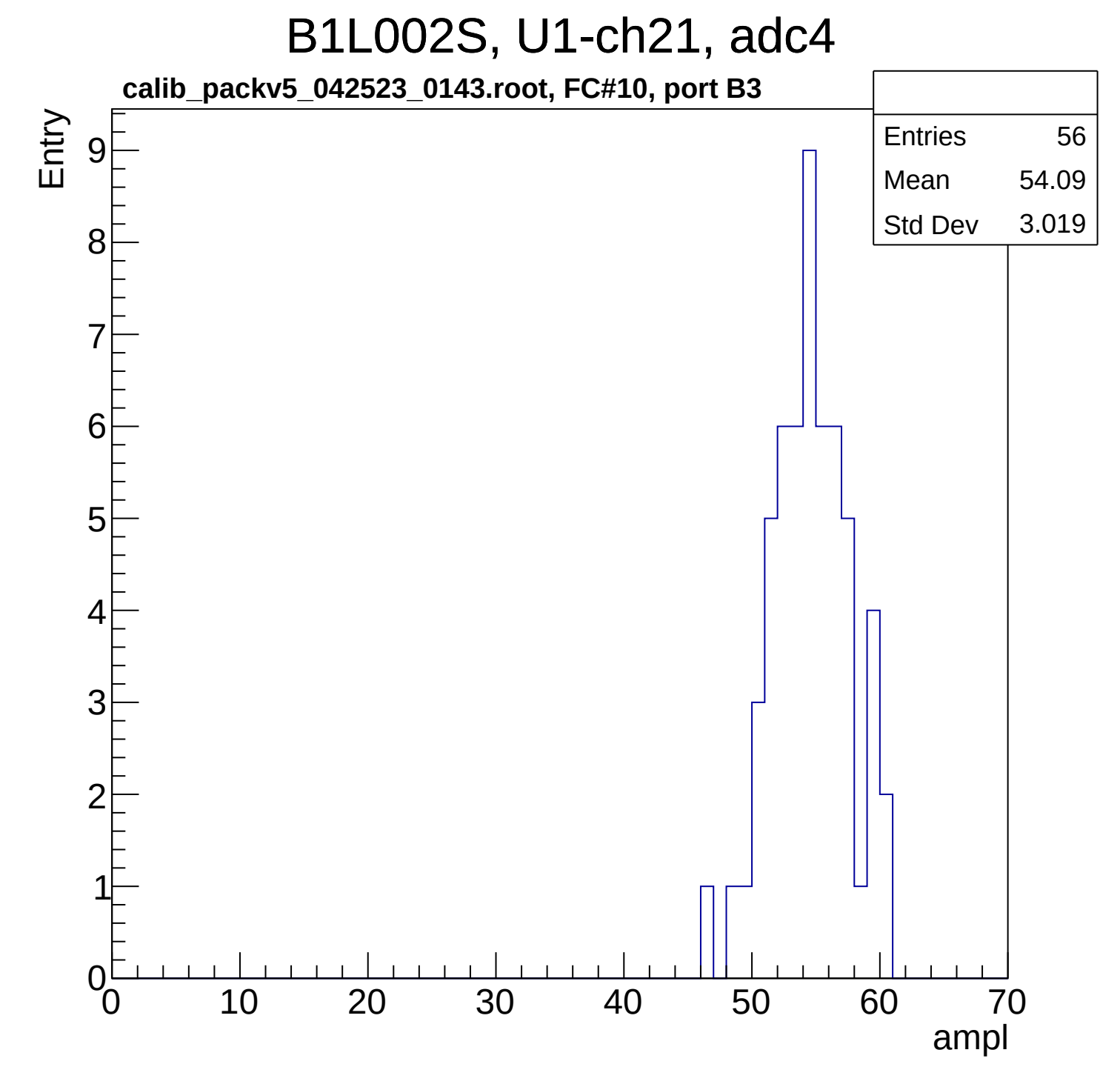
40
50
60
70


B1L002S, U1-ch21, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 54.09
Std Dev 3.019
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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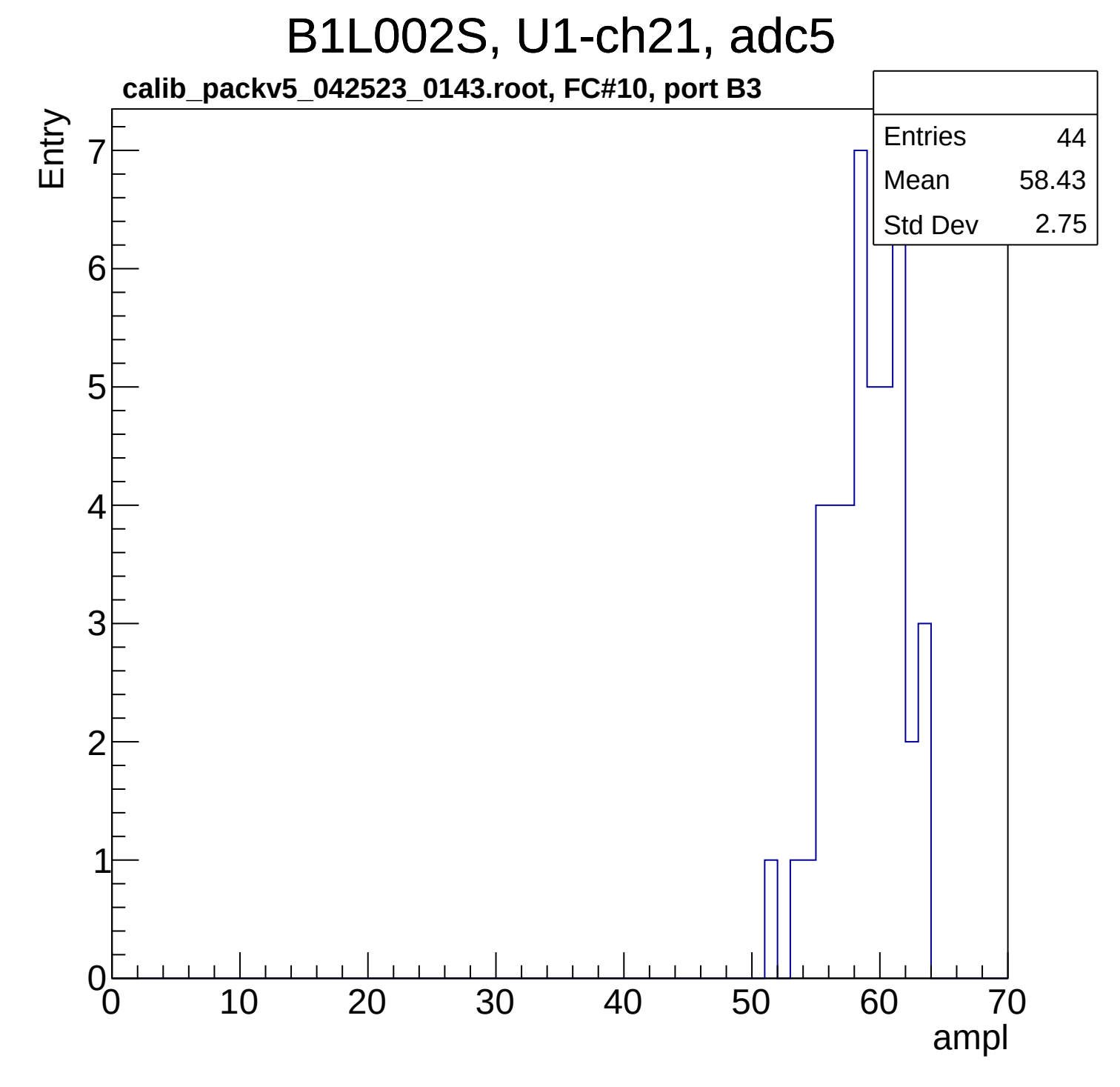
B1L002S, U1-ch21, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.43
Std Dev 2.75
Entry
ampl
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1
2
3
4
5
6
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0
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40
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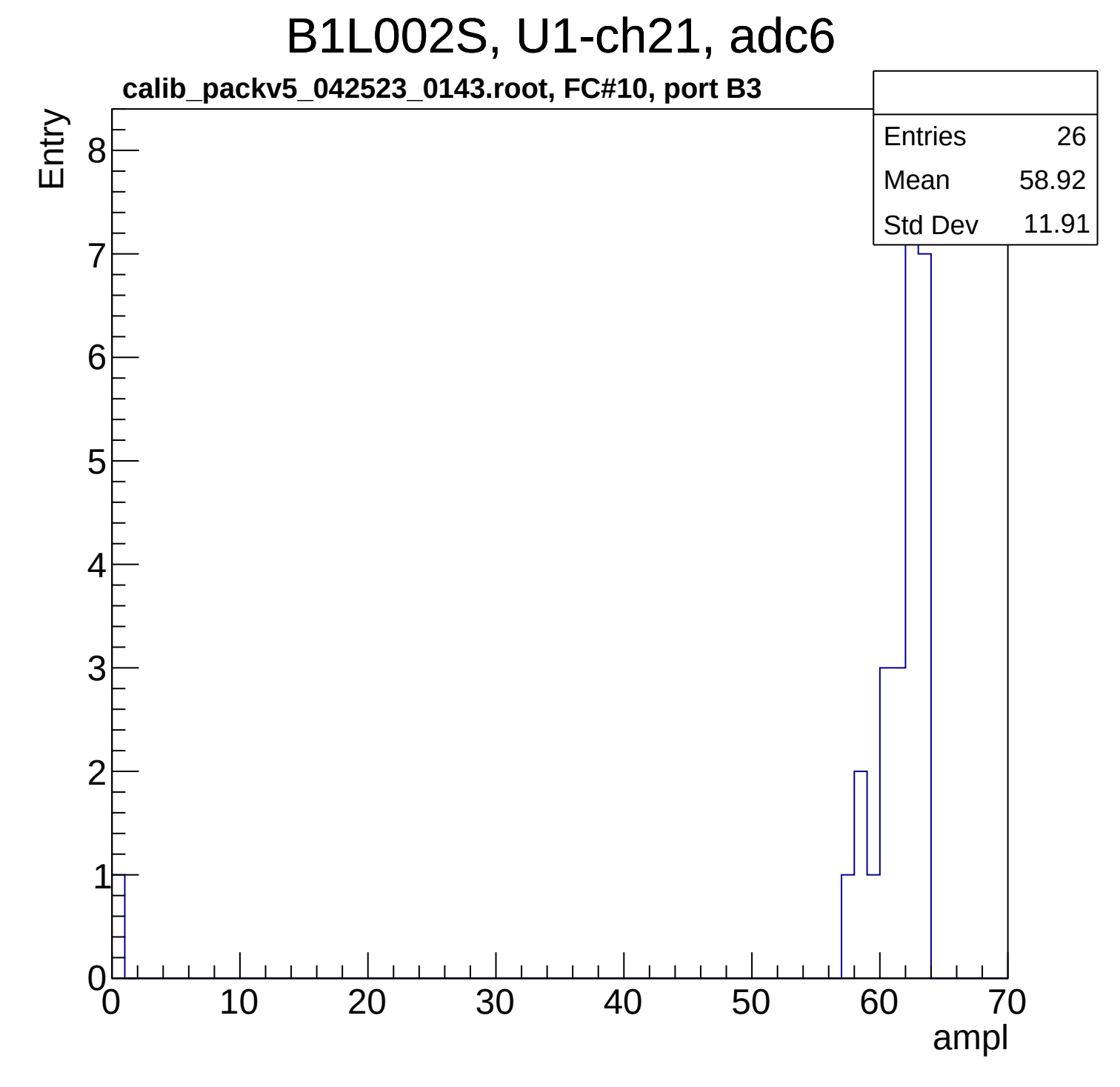
60
70


B1L002S, U1-ch21, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.92
Std Dev 11.91
Entry
ampl
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1
2
3
4
5
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7
8
0
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20
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40
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60
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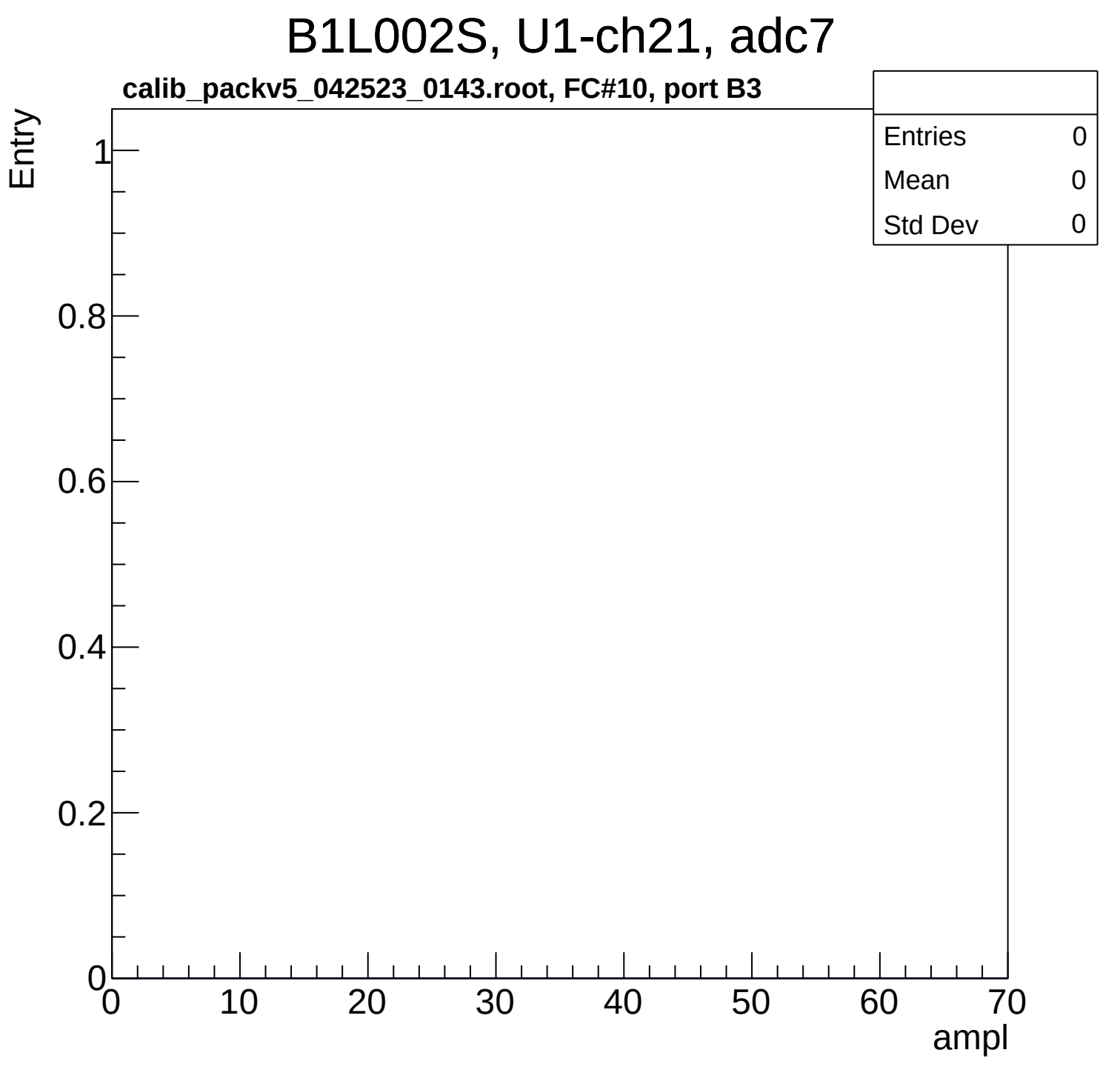
B1L002S, U1-ch21, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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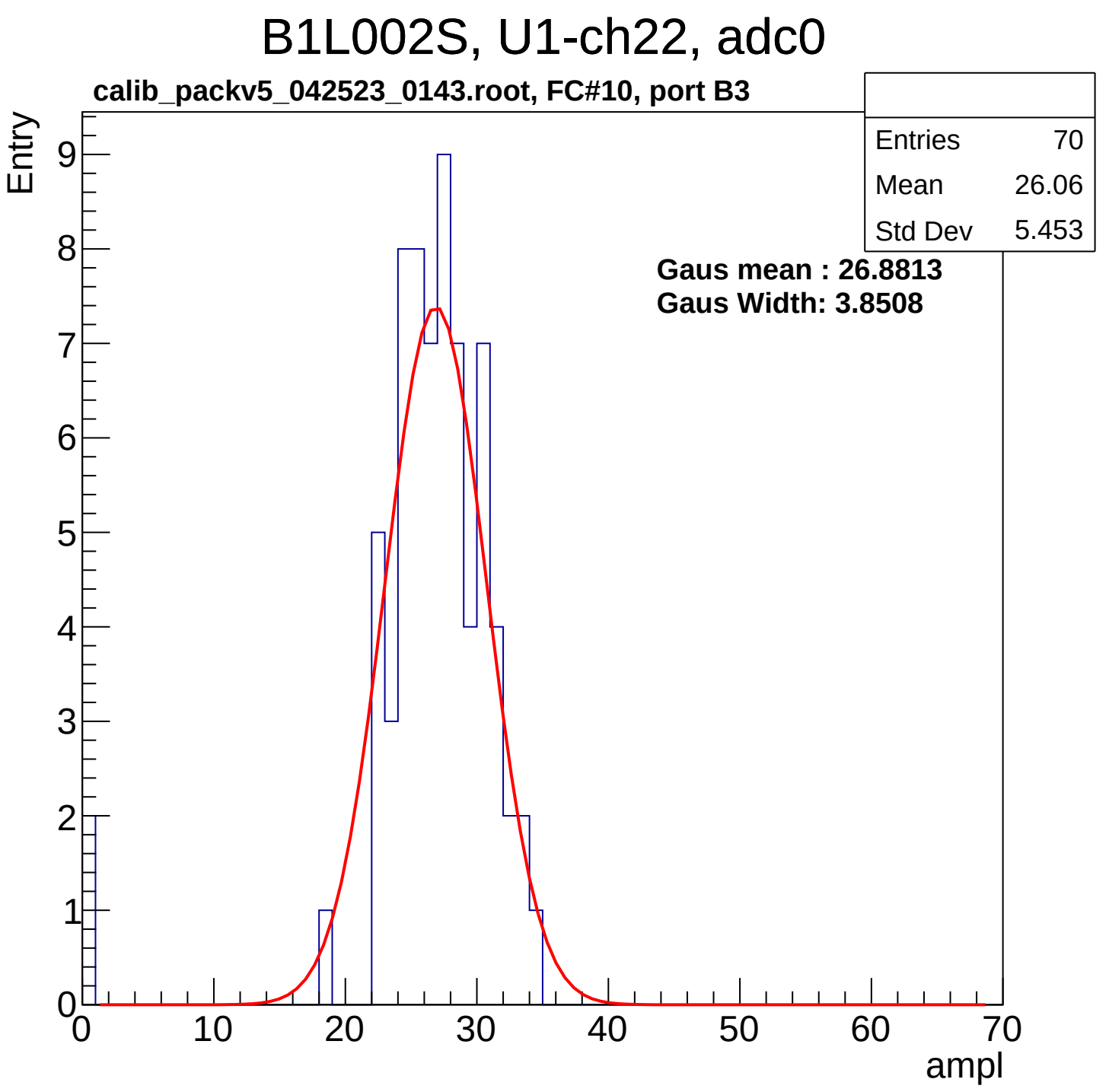
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch22, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.06
Std Dev 5.453
Gaus mean : 26.8813
Gaus Width: 3.8508
Entry
ampl
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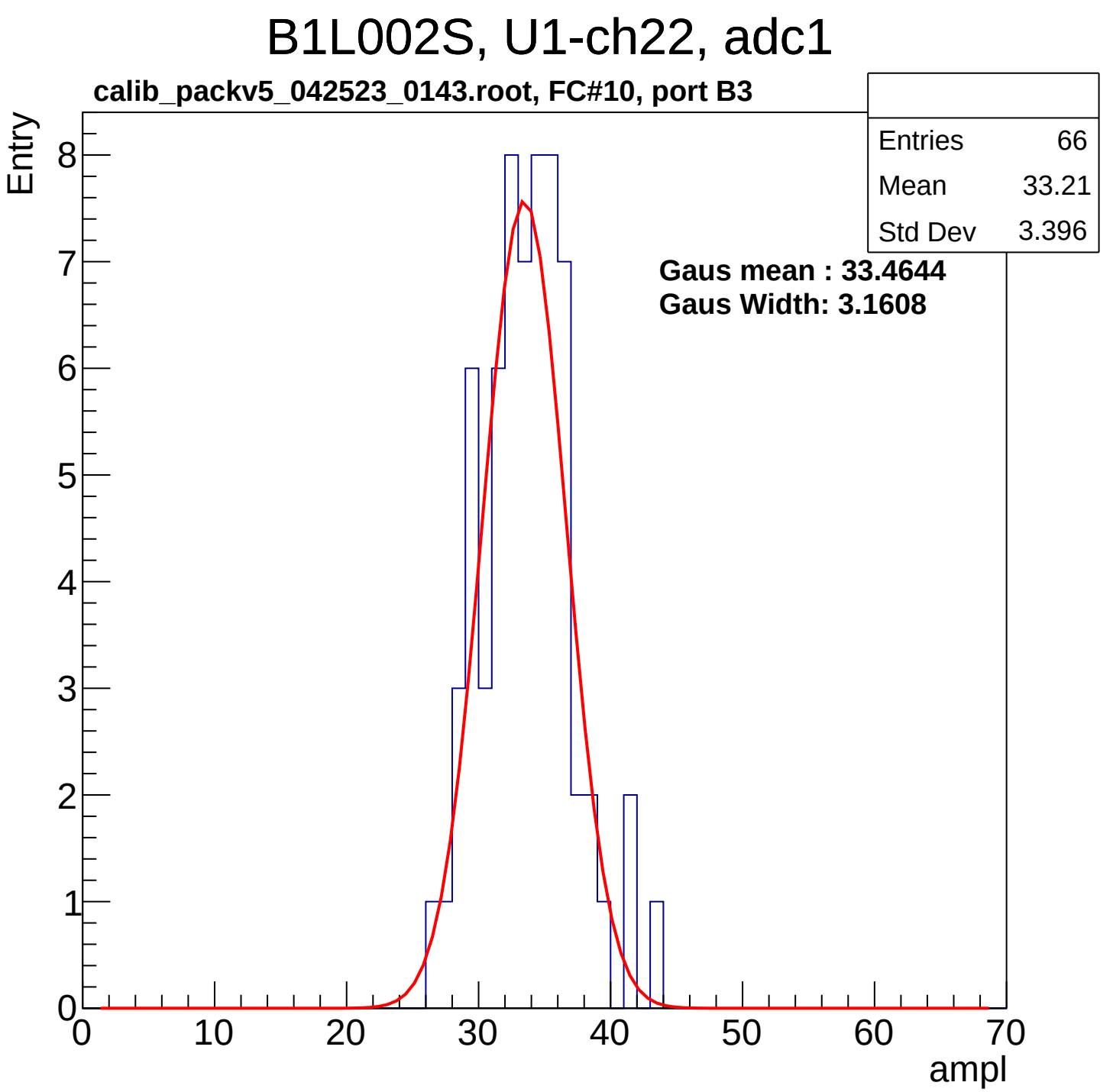
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch22, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.21
Std Dev 3.396
Gaus mean : 33.4644
Gaus Width: 3.1608
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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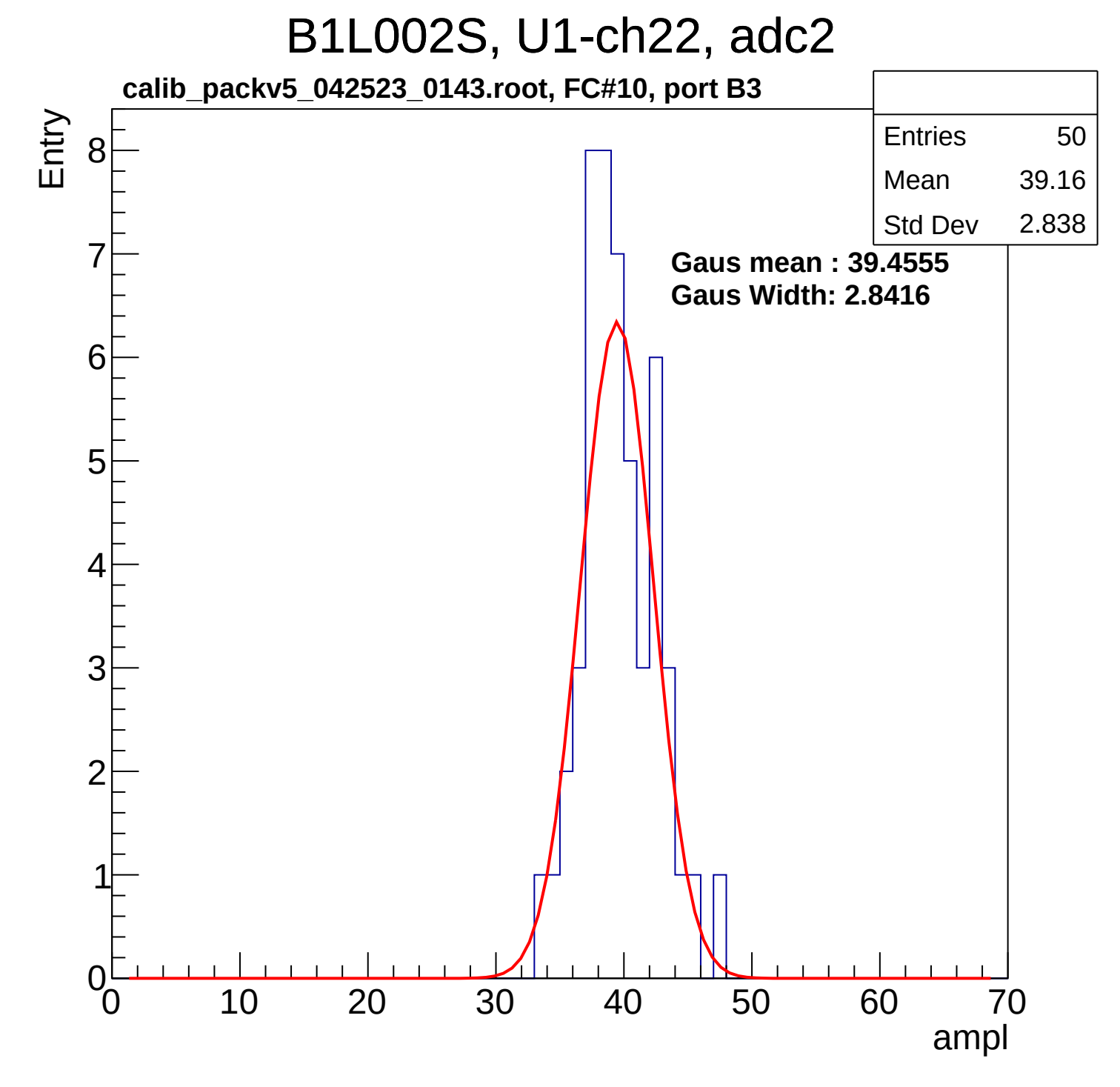
60
70


B1L002S, U1-ch22, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 39.16
Std Dev 2.838
Gaus mean : 39.4555
Gaus Width: 2.8416
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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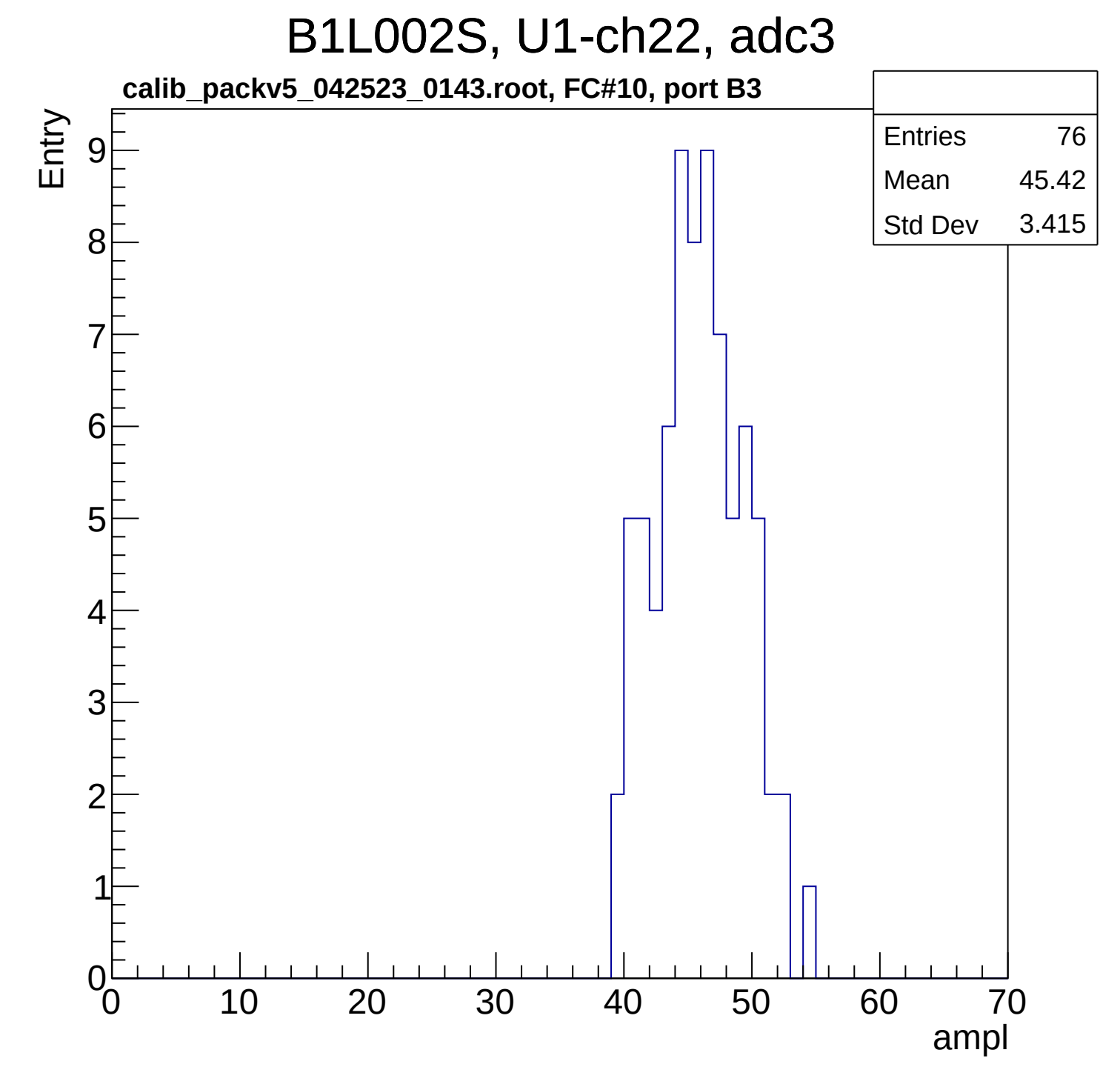
B1L002S, U1-ch22, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 45.42
Std Dev 3.415
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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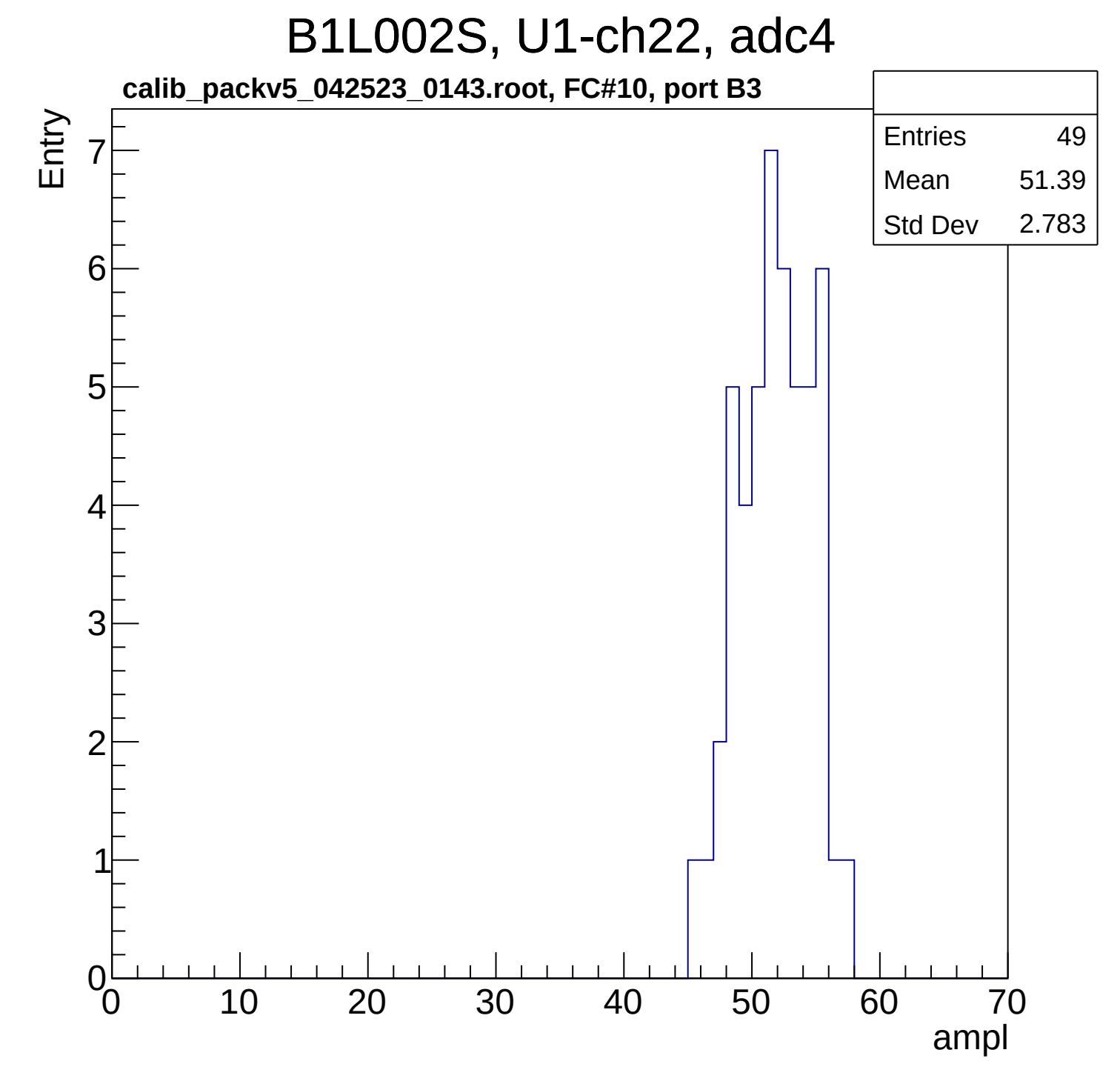
B1L002S, U1-ch22, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 51.39
Std Dev 2.783
Entry
ampl
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2
3
4
5
6
7
0
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40
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60
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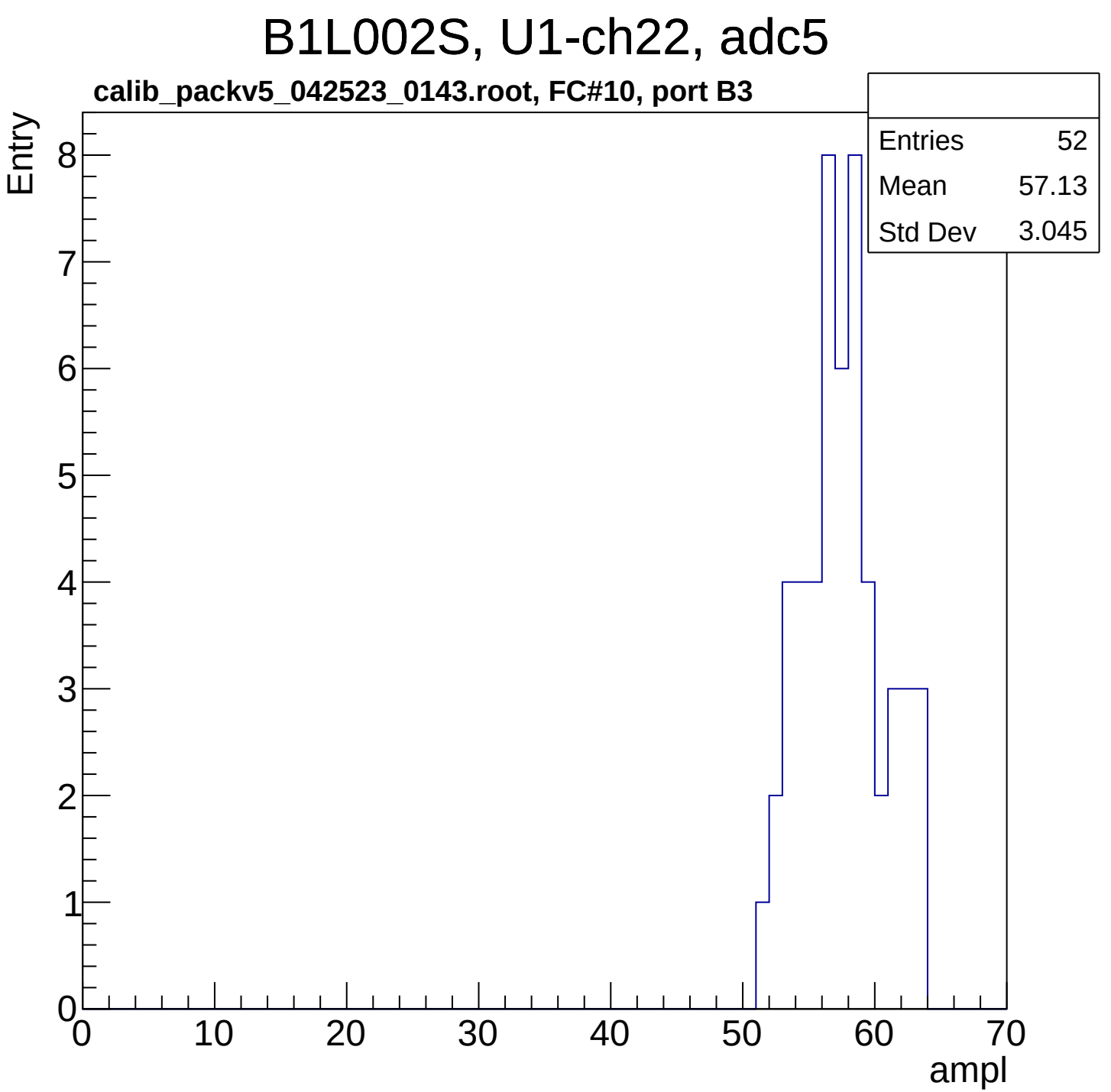
B1L002S, U1-ch22, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.13
Std Dev 3.045
Entry
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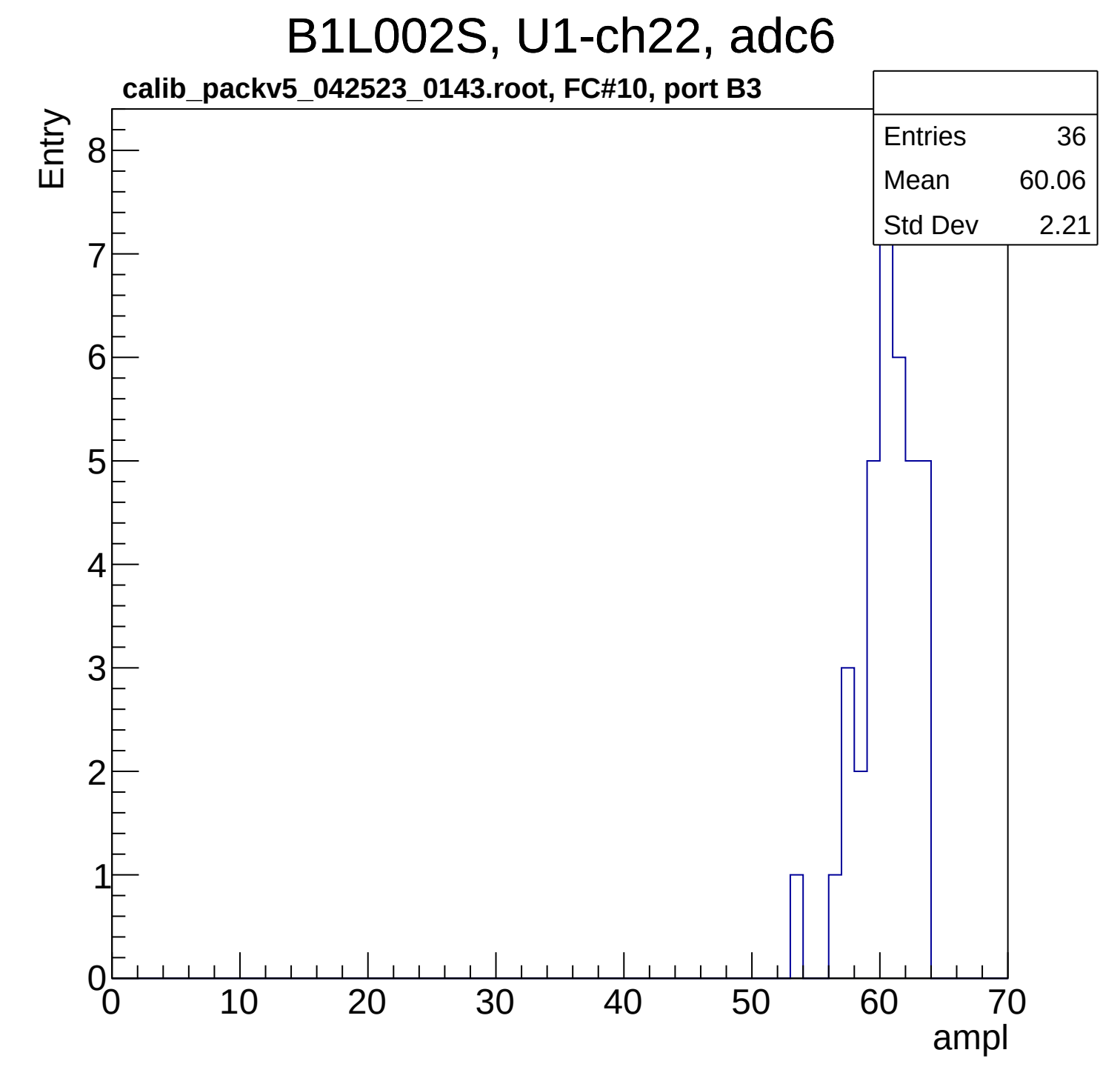
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch22, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 60.06
Std Dev 2.21
Entry
ampl
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1
2
3
4
5
6
7
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30
40
50
60
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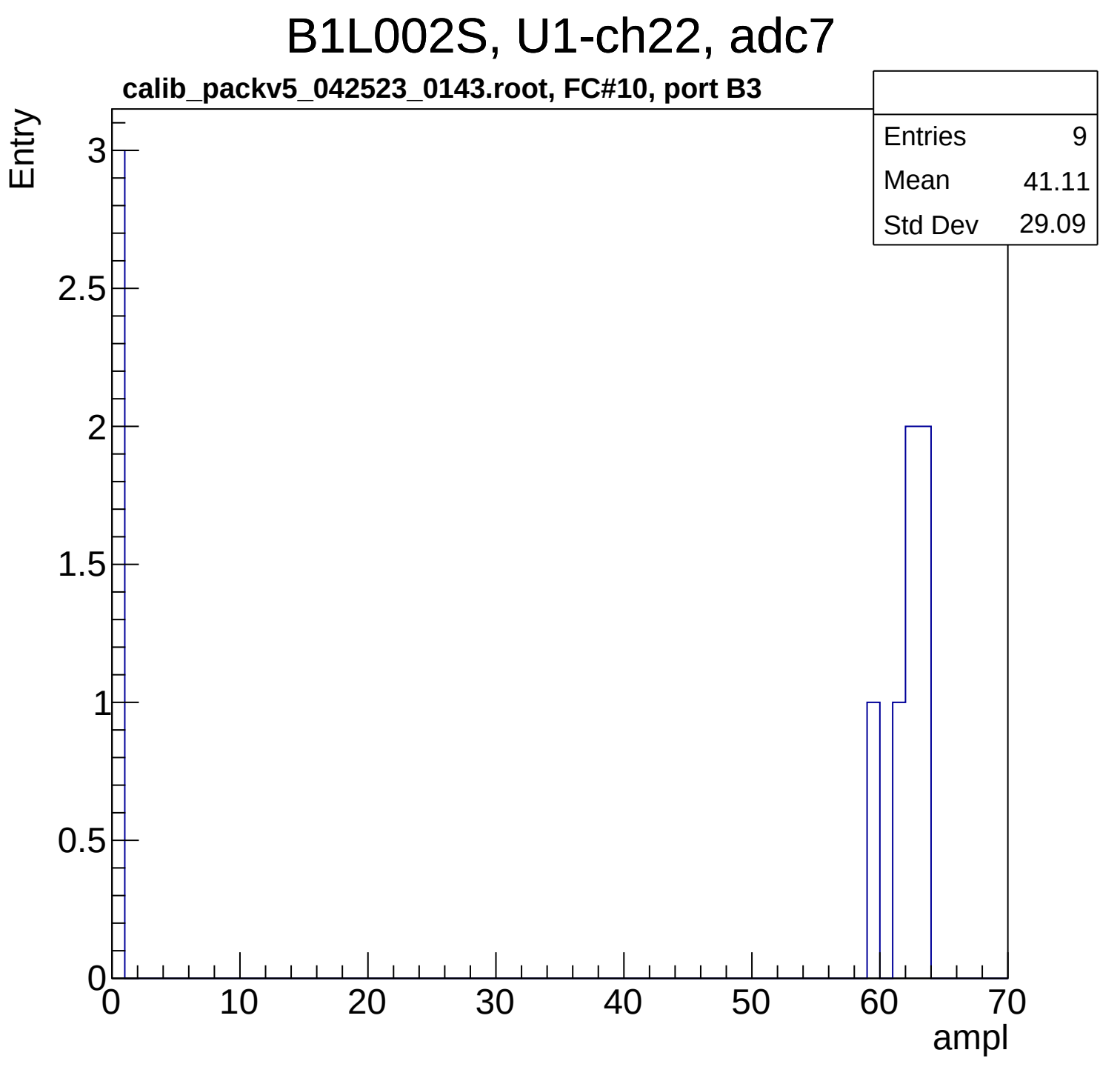
B1L002S, U1-ch22, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 41.11
Std Dev 29.09
Entry
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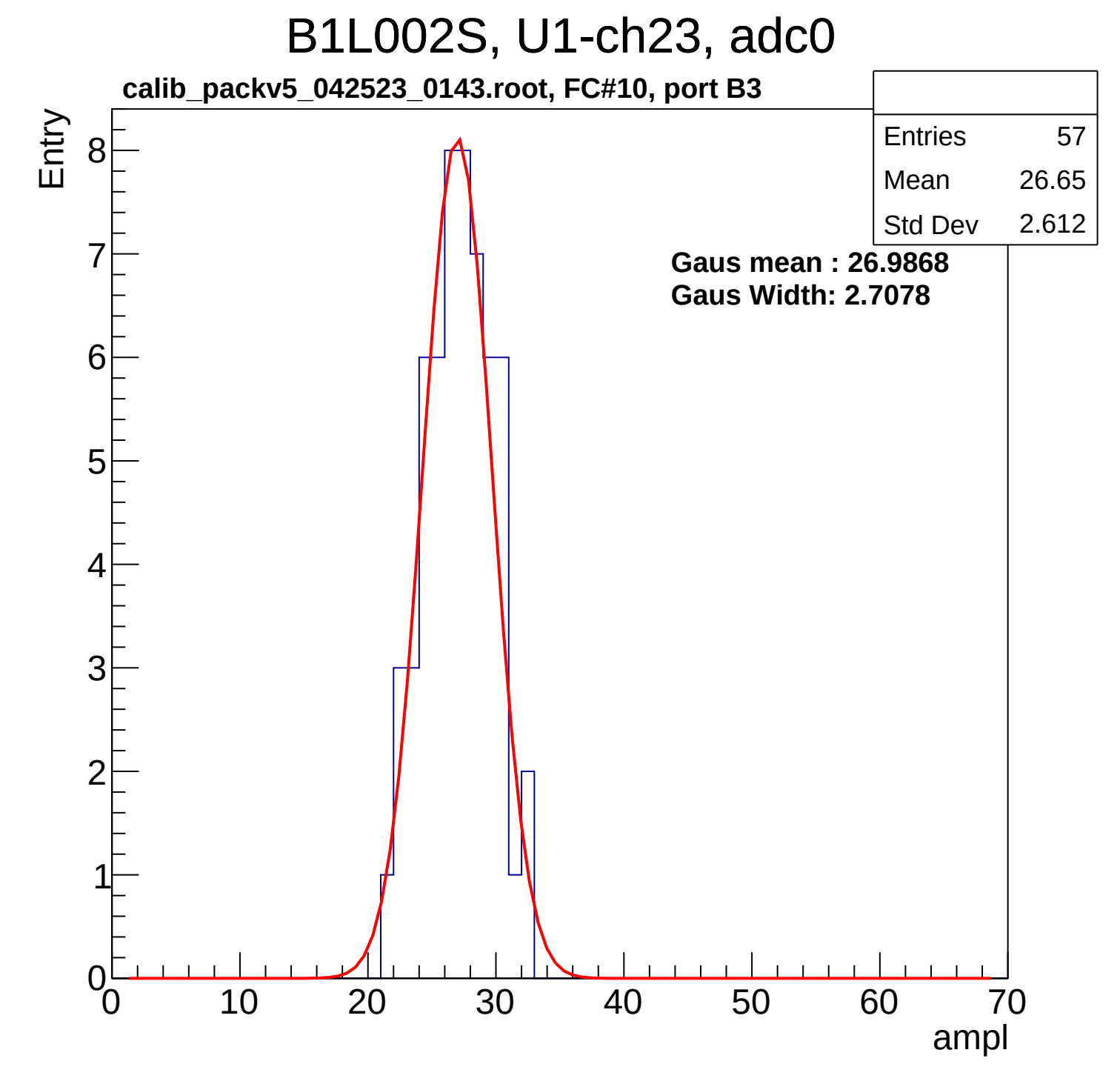
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch23, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 26.65
Std Dev 2.612
Gaus mean : 26.9868
Gaus Width: 2.7078
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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40
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60
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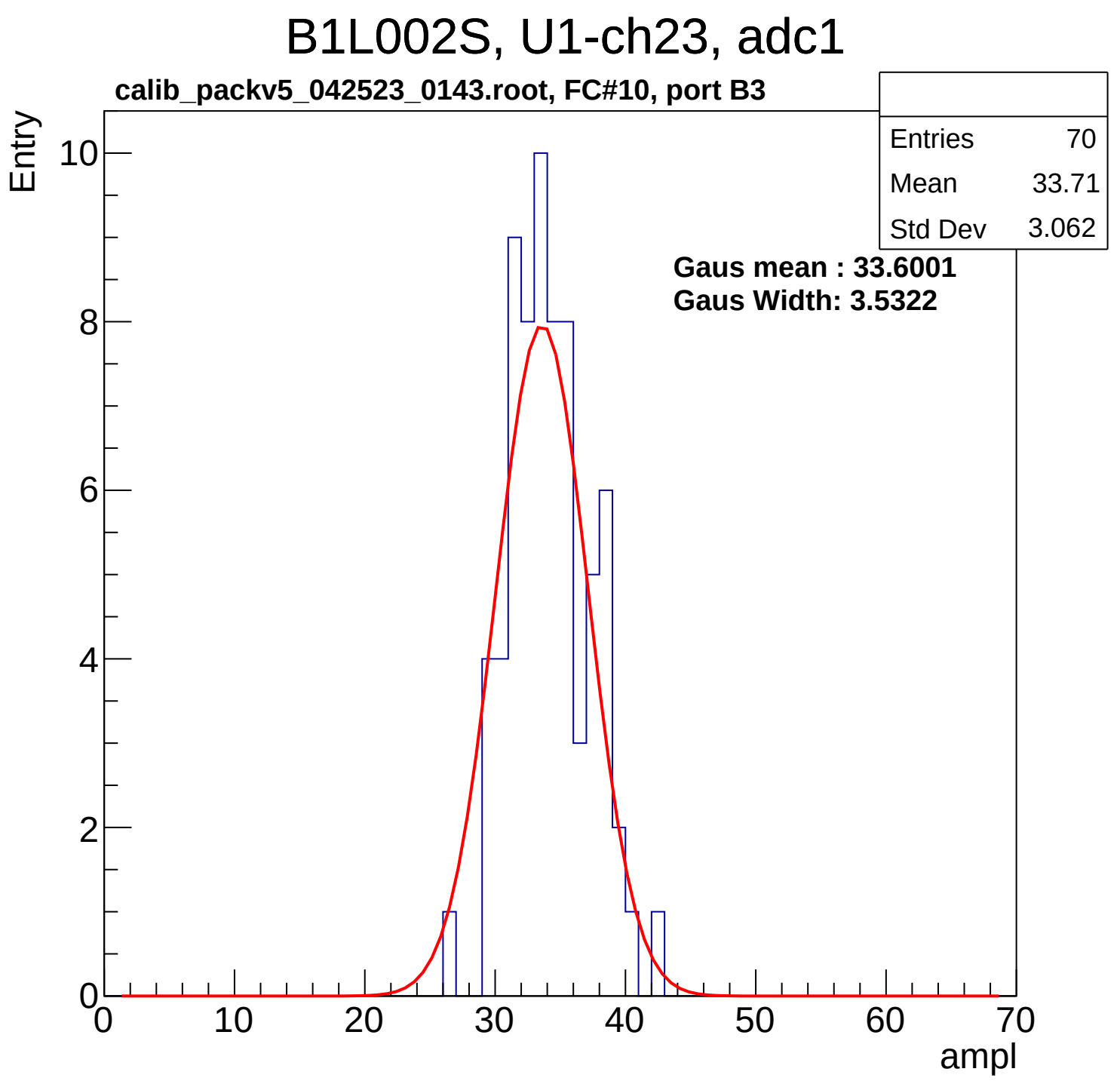
B1L002S, U1-ch23, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 33.71
Std Dev 3.062
Gaus mean : 33.6001
Gaus Width: 3.5322
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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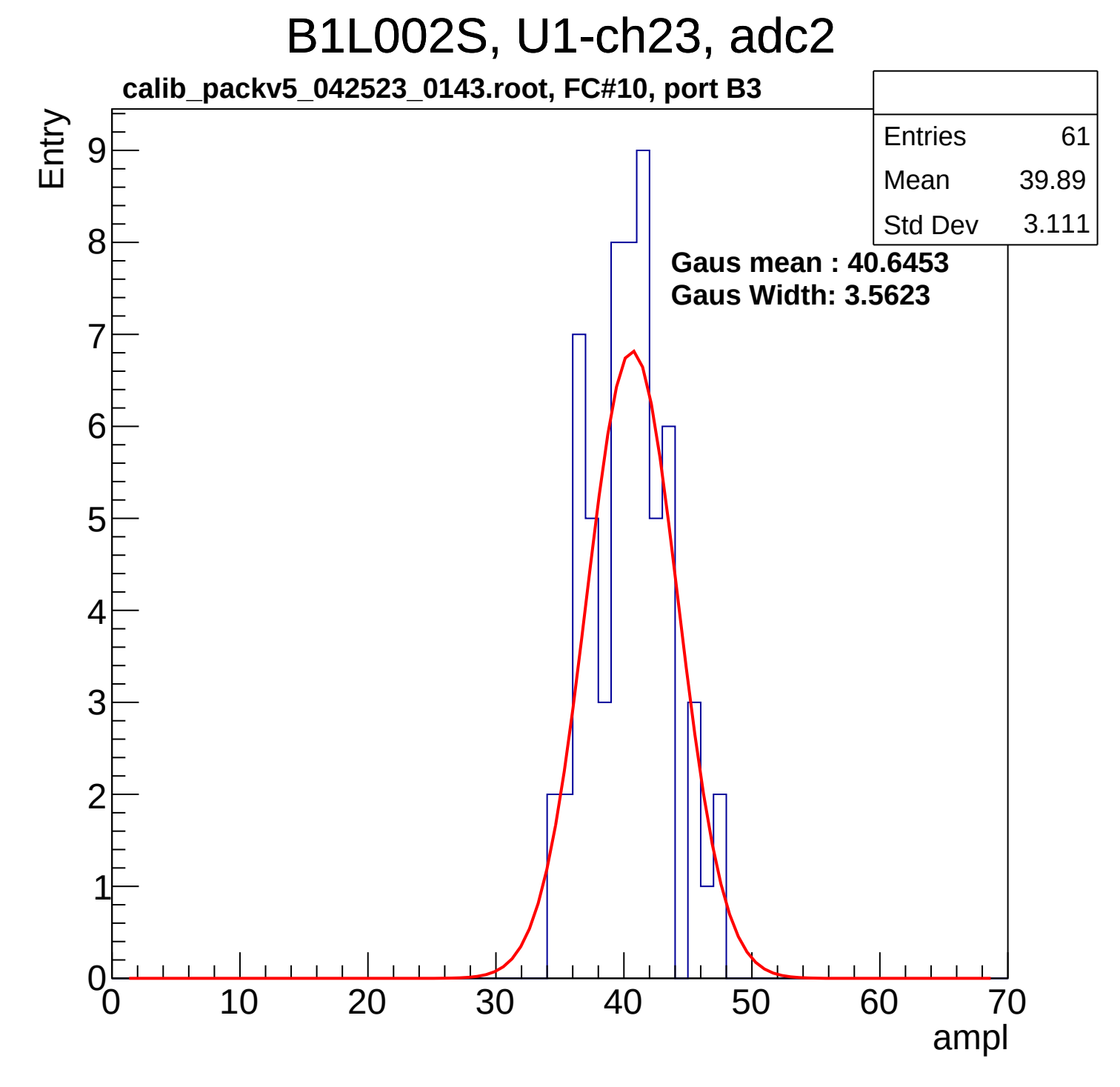
60
70


B1L002S, U1-ch23, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 39.89
Std Dev 3.111
Gaus mean : 40.6453
Gaus Width: 3.5623
Entry
ampl
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3
4
5
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0
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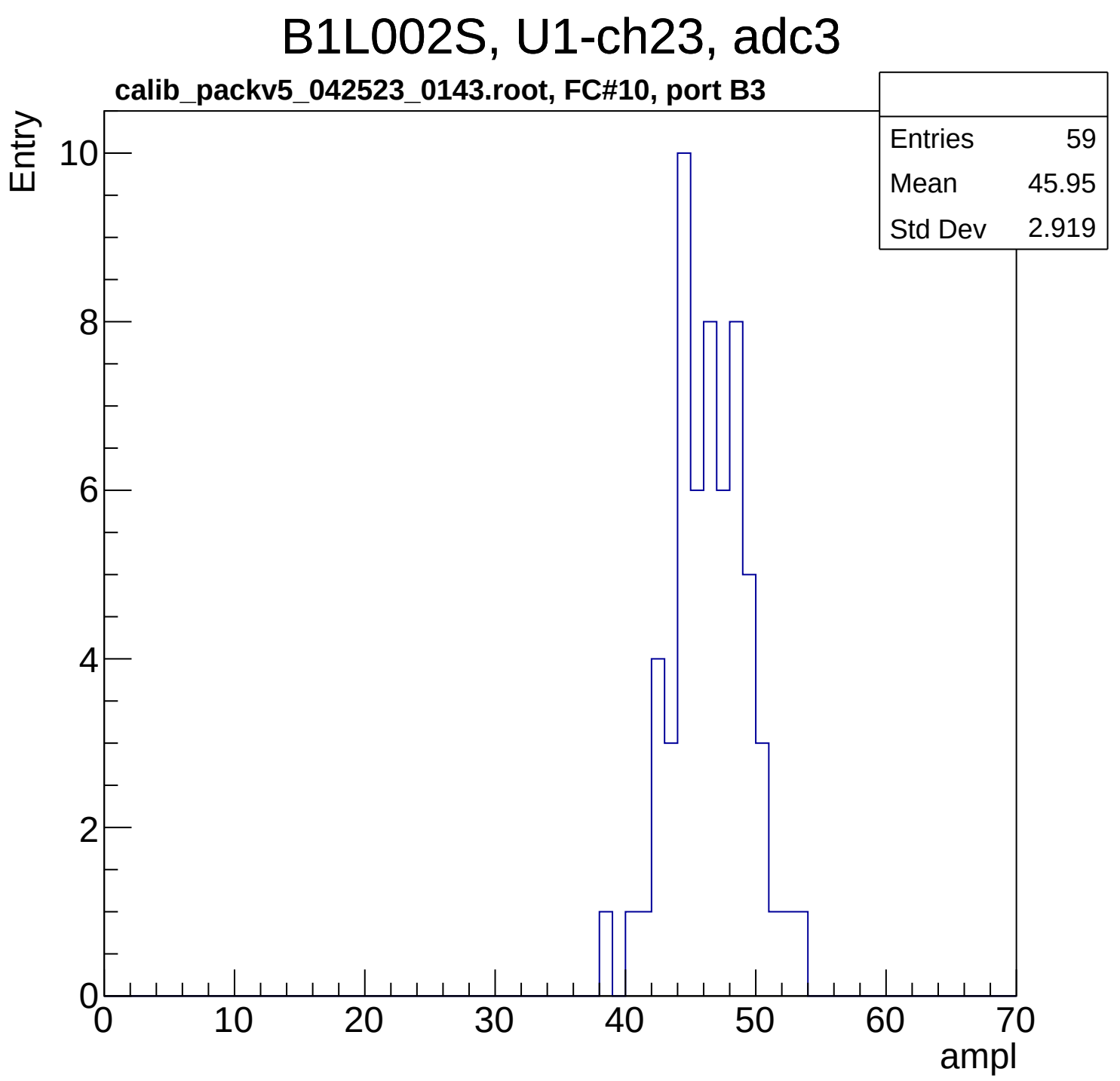
B1L002S, U1-ch23, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 45.95
Std Dev 2.919
Entry
0
2
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10
0
10
20
30
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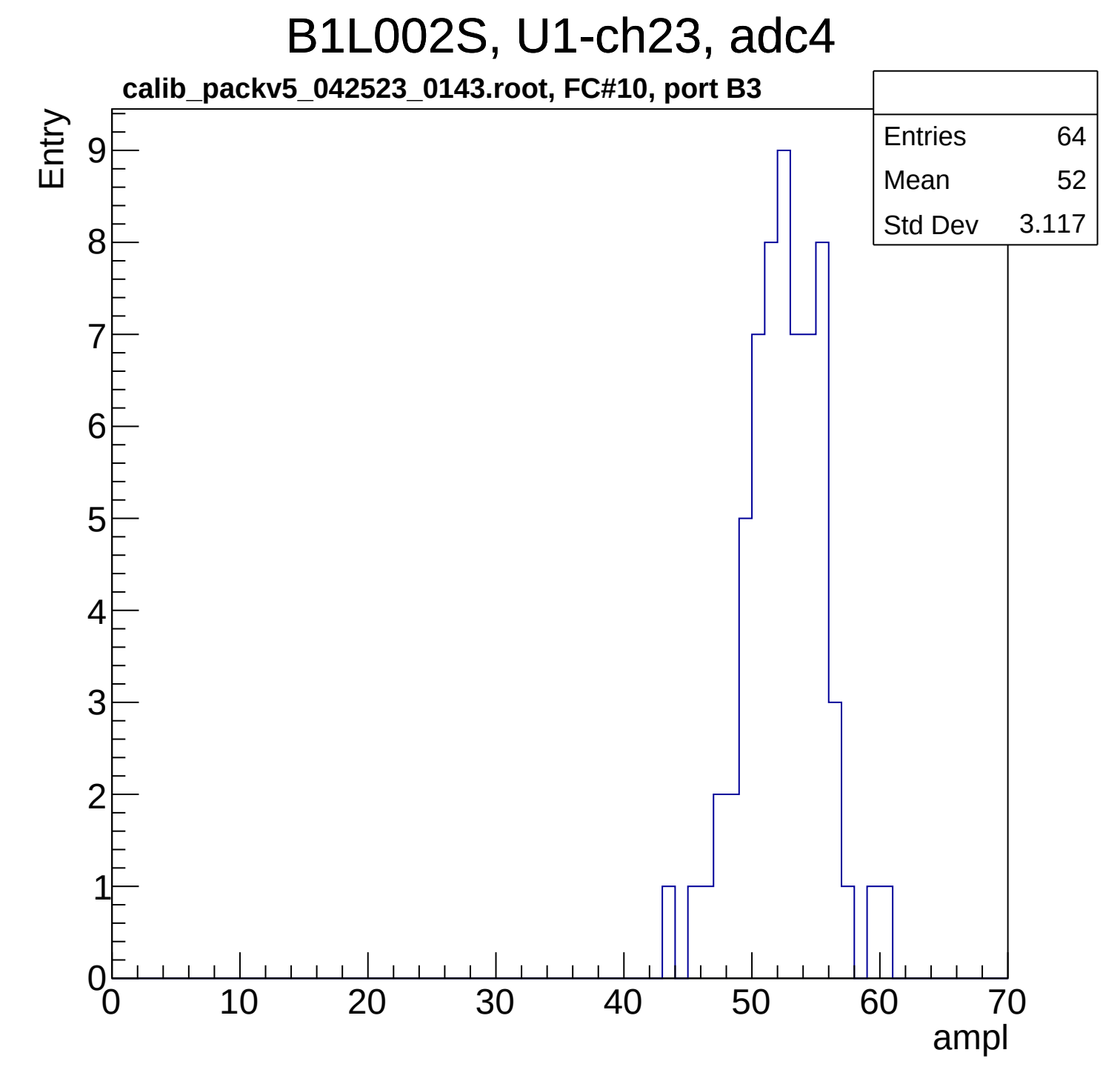
50
60
70
ampl


B1L002S, U1-ch23, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52
Std Dev 3.117
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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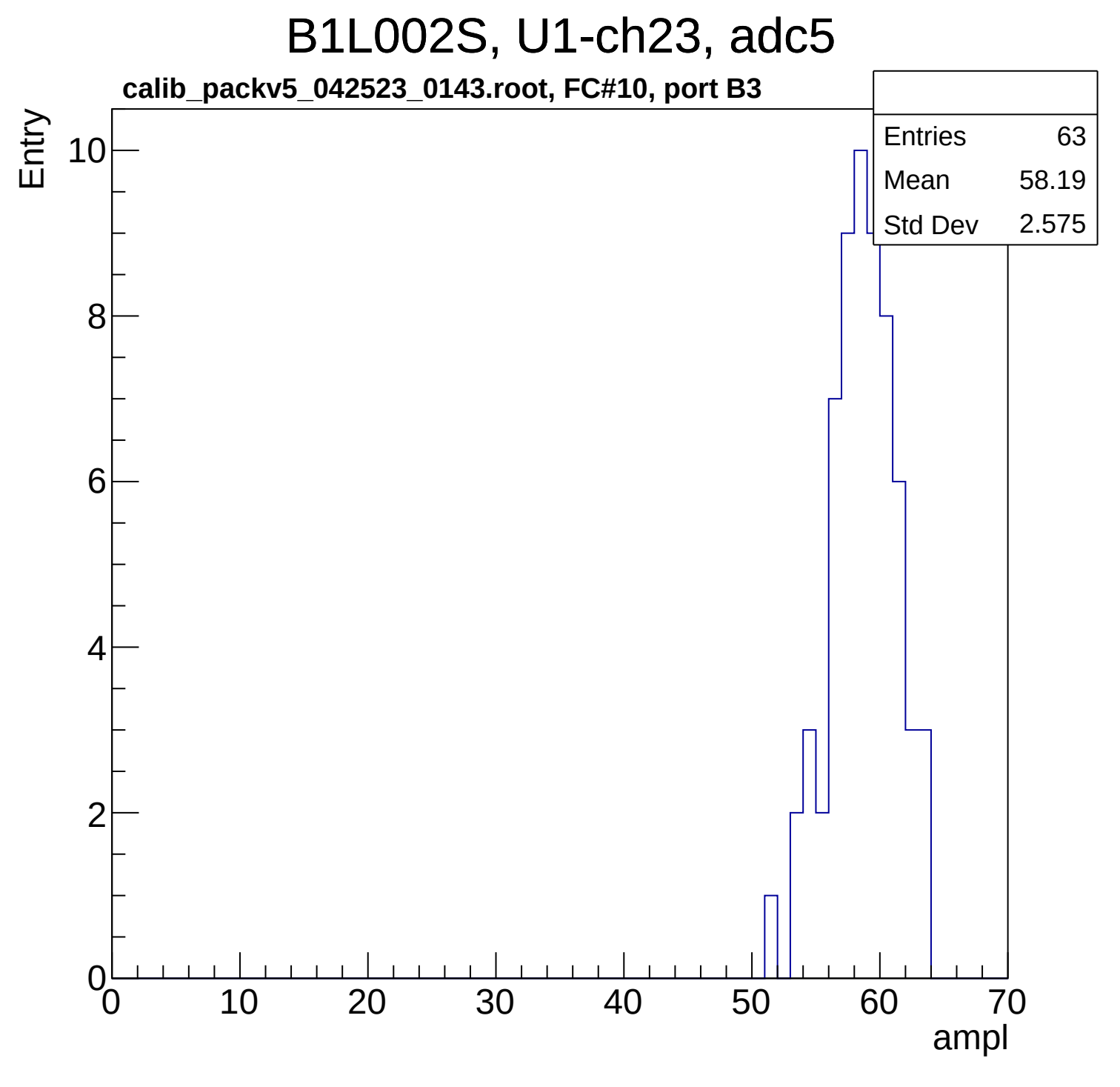
B1L002S, U1-ch23, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.19
Std Dev 2.575
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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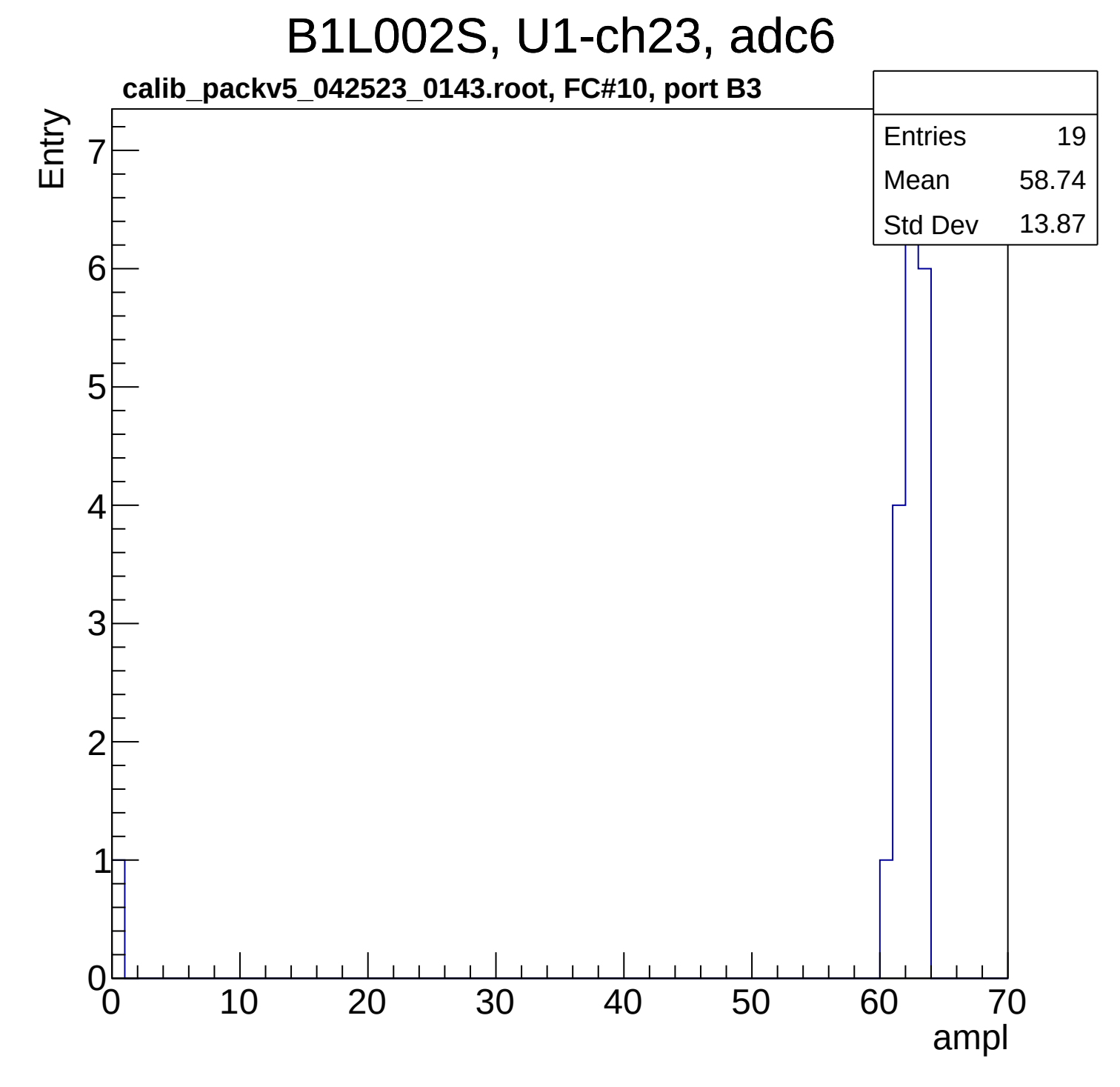
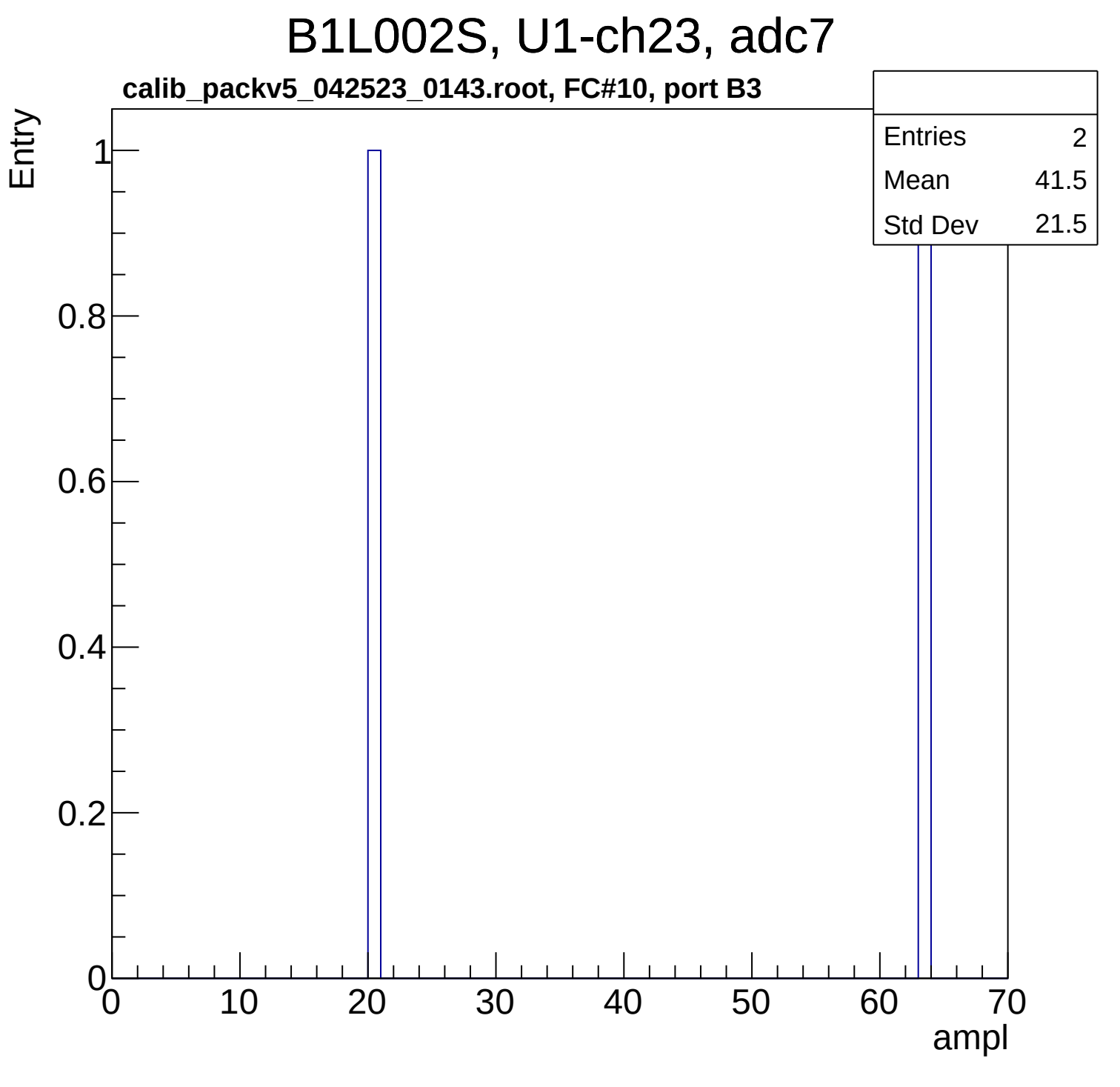
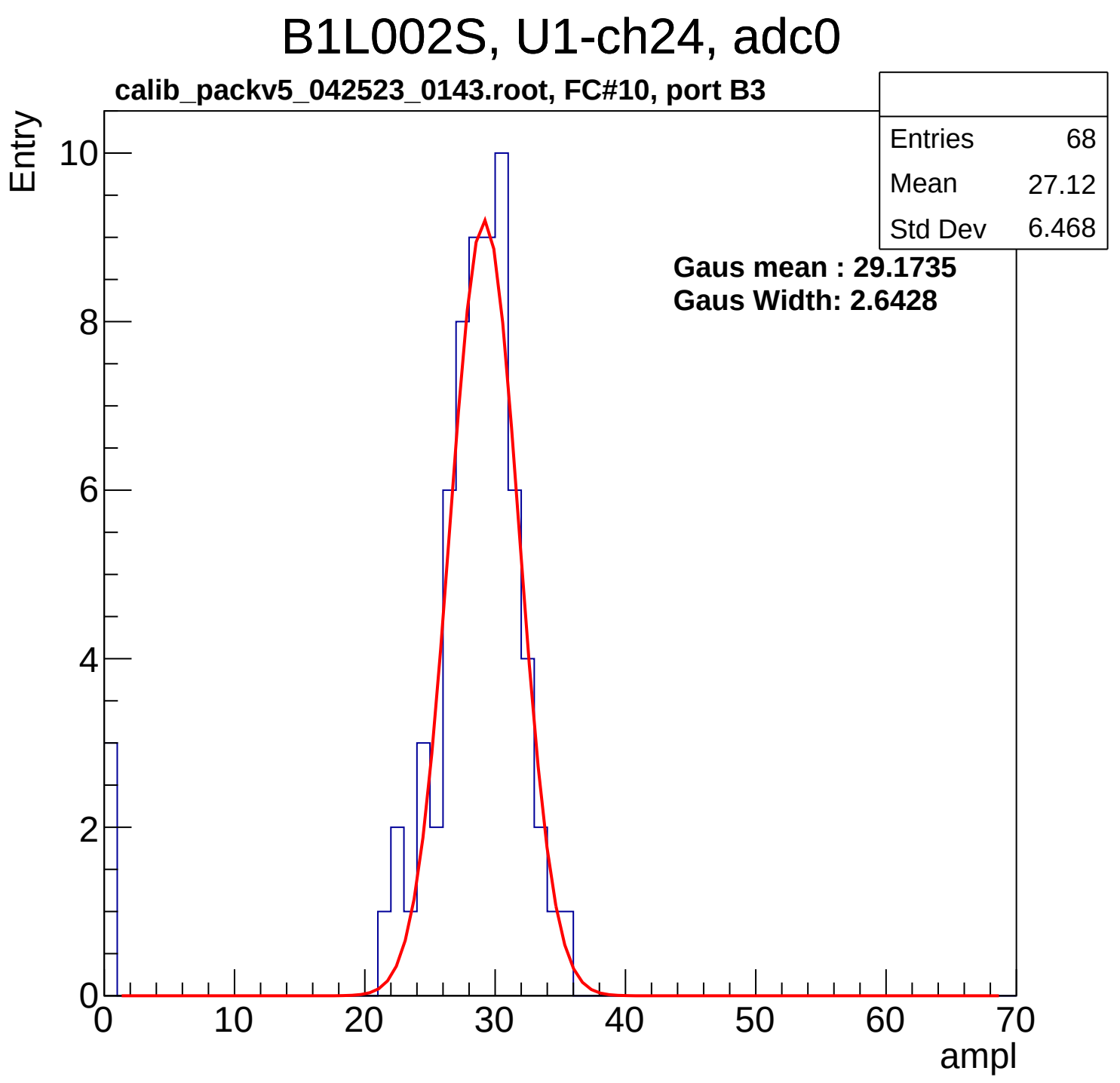
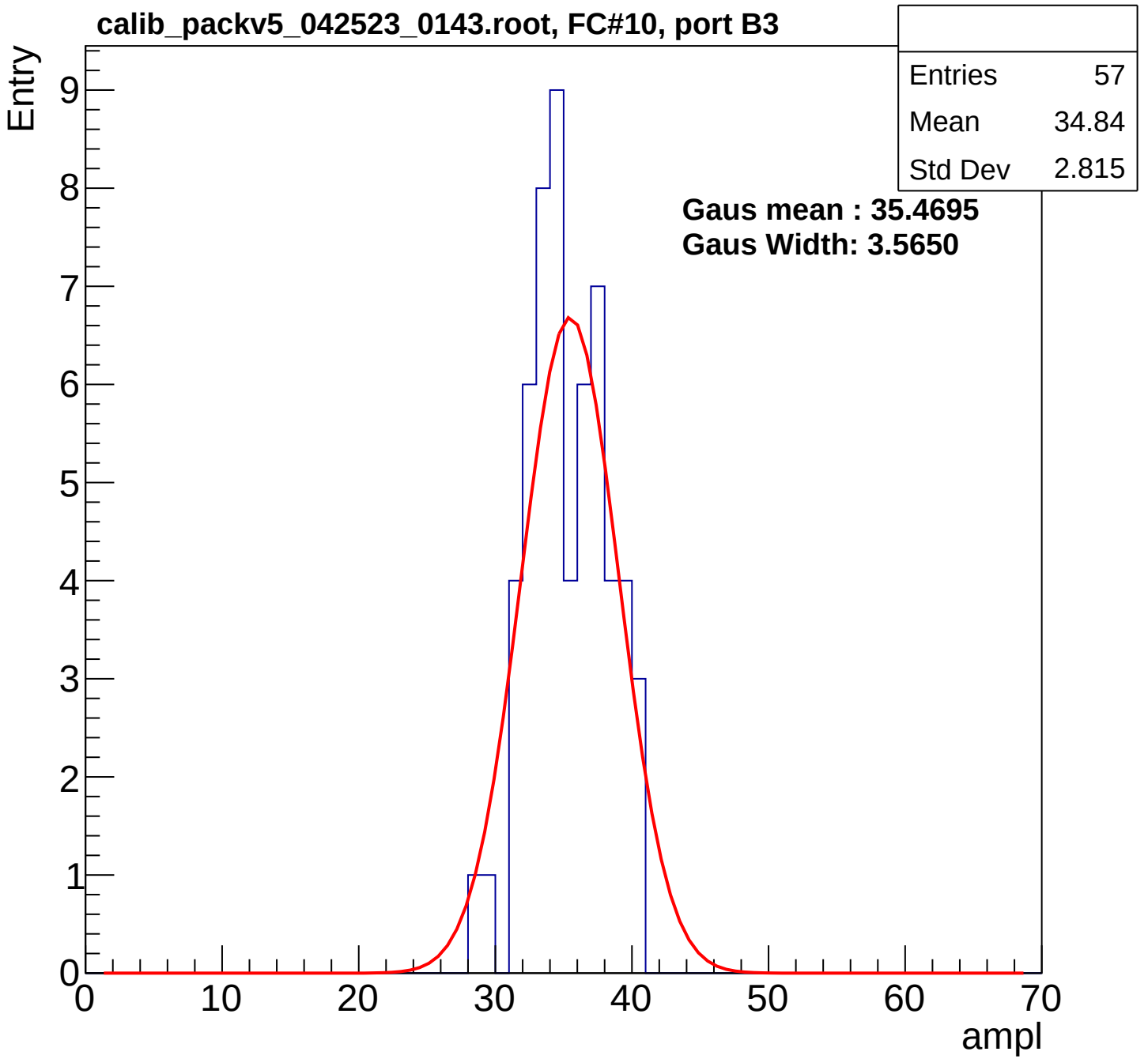

B1L002S, U1-ch23, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.74
Std Dev 13.87
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch23, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41.5
Std Dev 21.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch24, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.12
Std Dev 6.468
Gaus mean : 29.1735
Gaus Width: 2.6428
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch24, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.84
Std Dev 2.815
Gaus mean : 35.4695
Gaus Width: 3.5650
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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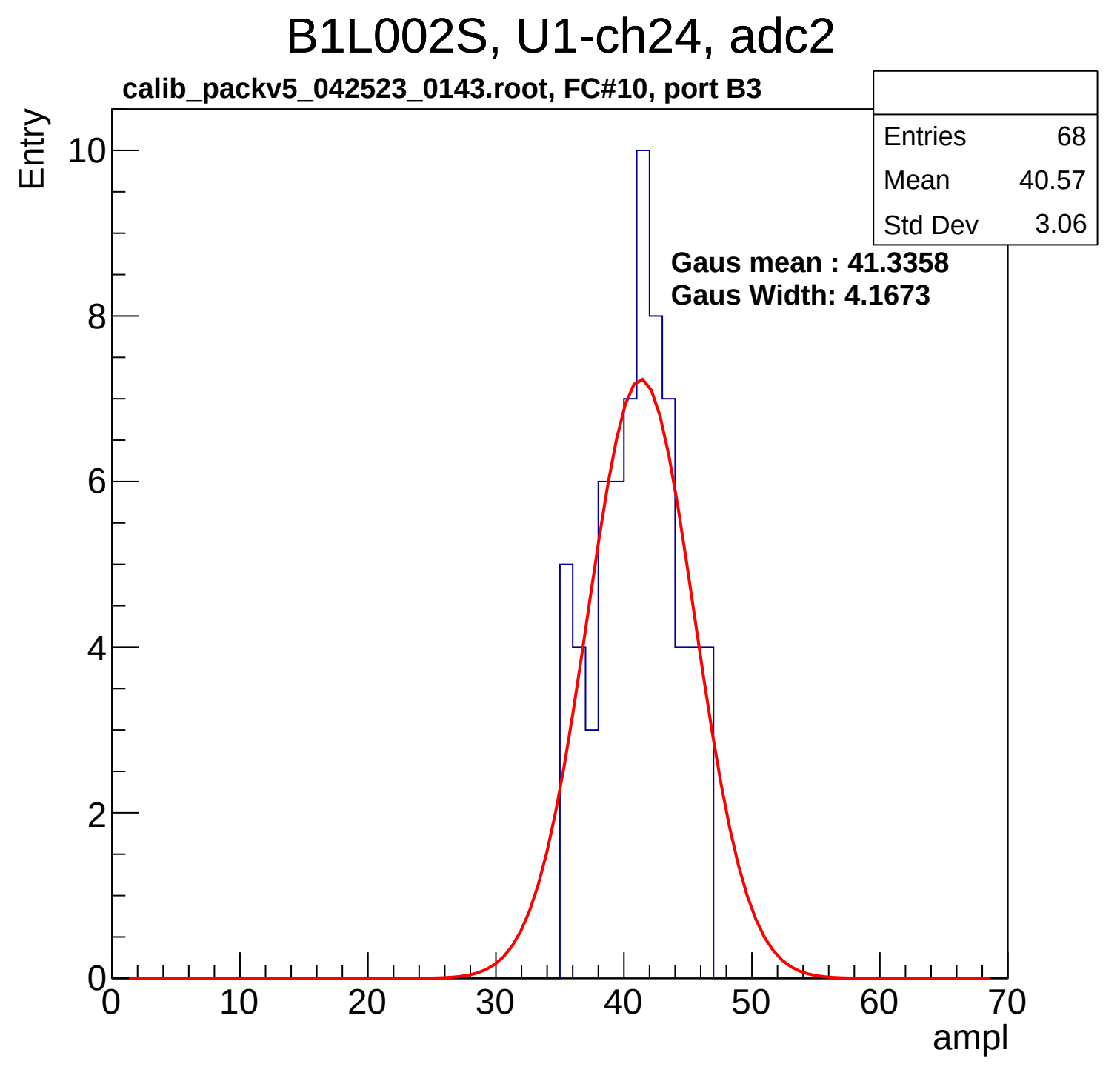
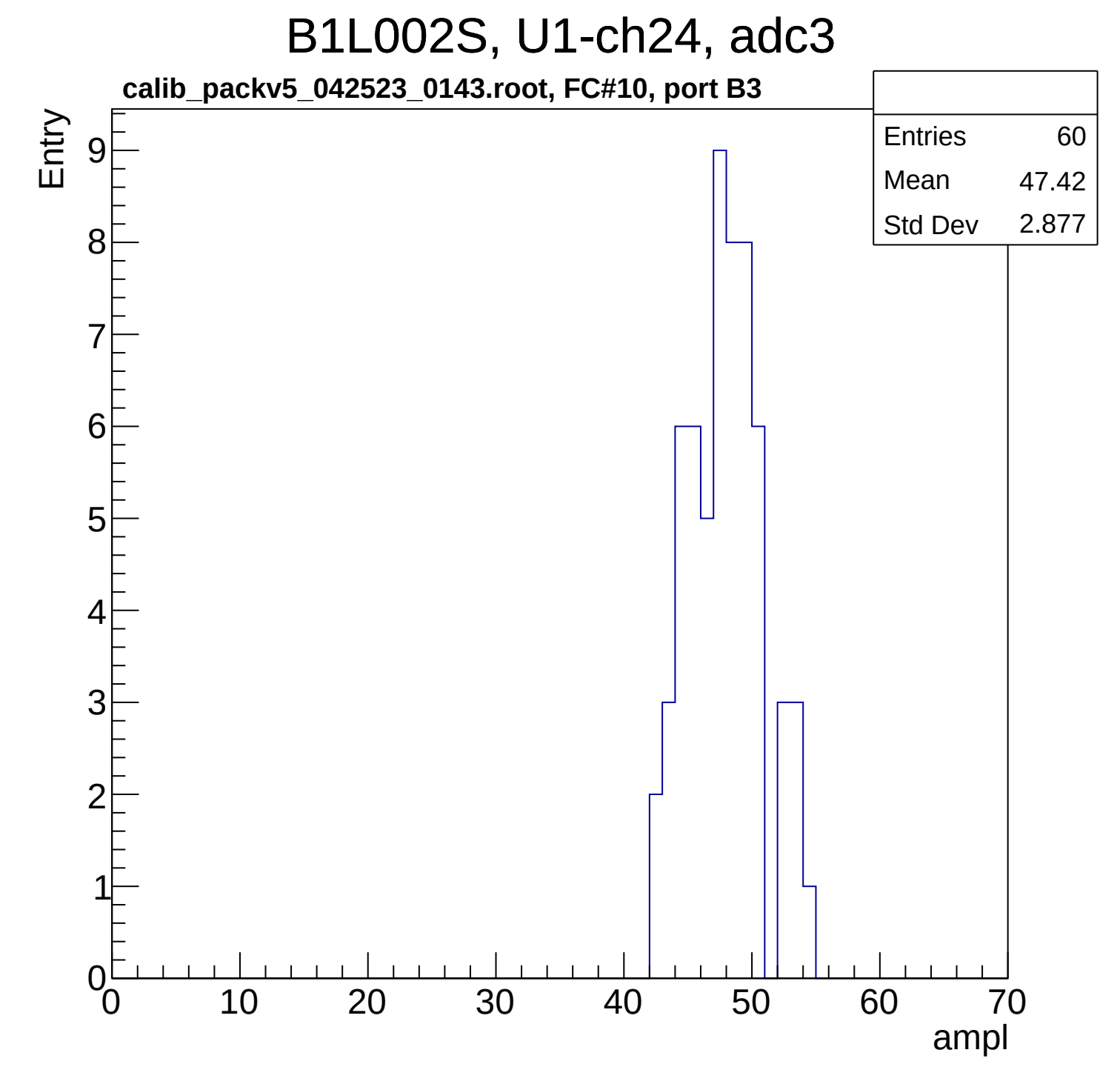
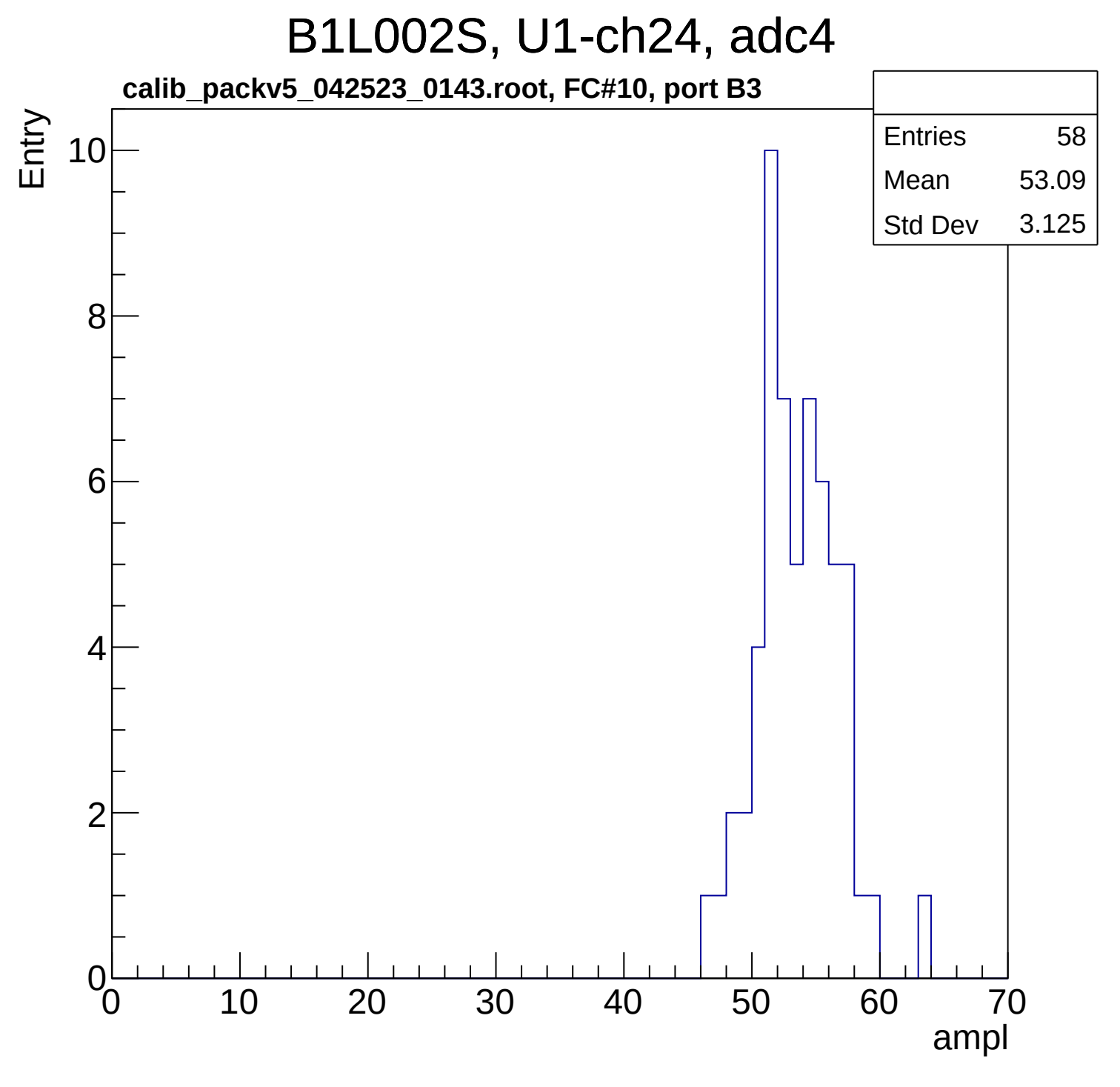
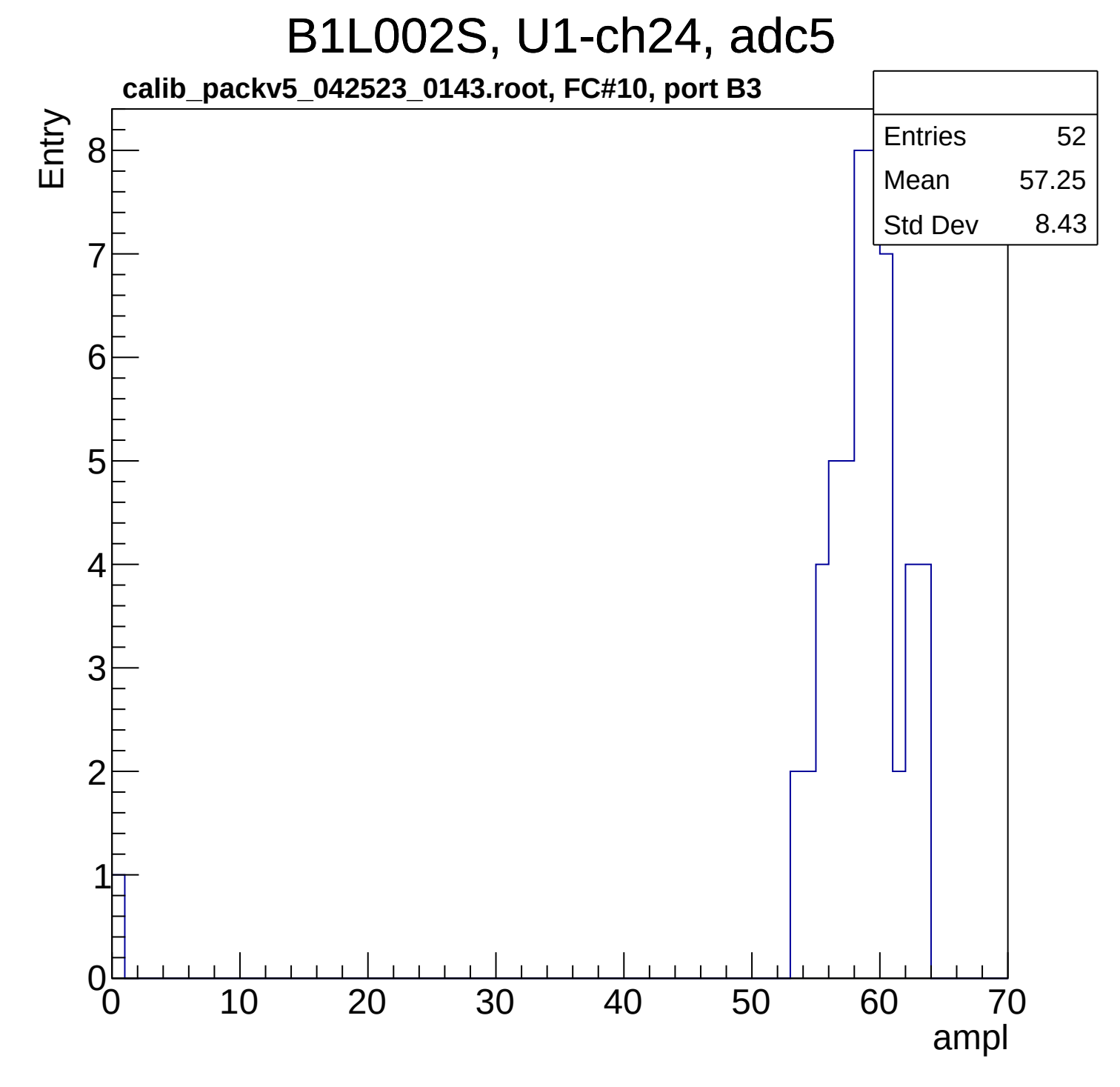

B1L002S, U1-ch24, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 40.57
Std Dev 3.06
Gaus mean : 41.3358
Gaus Width: 4.1673
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch24, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.42
Std Dev 2.877
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch24, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.09
Std Dev 3.125
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch24, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.25
Std Dev 8.43
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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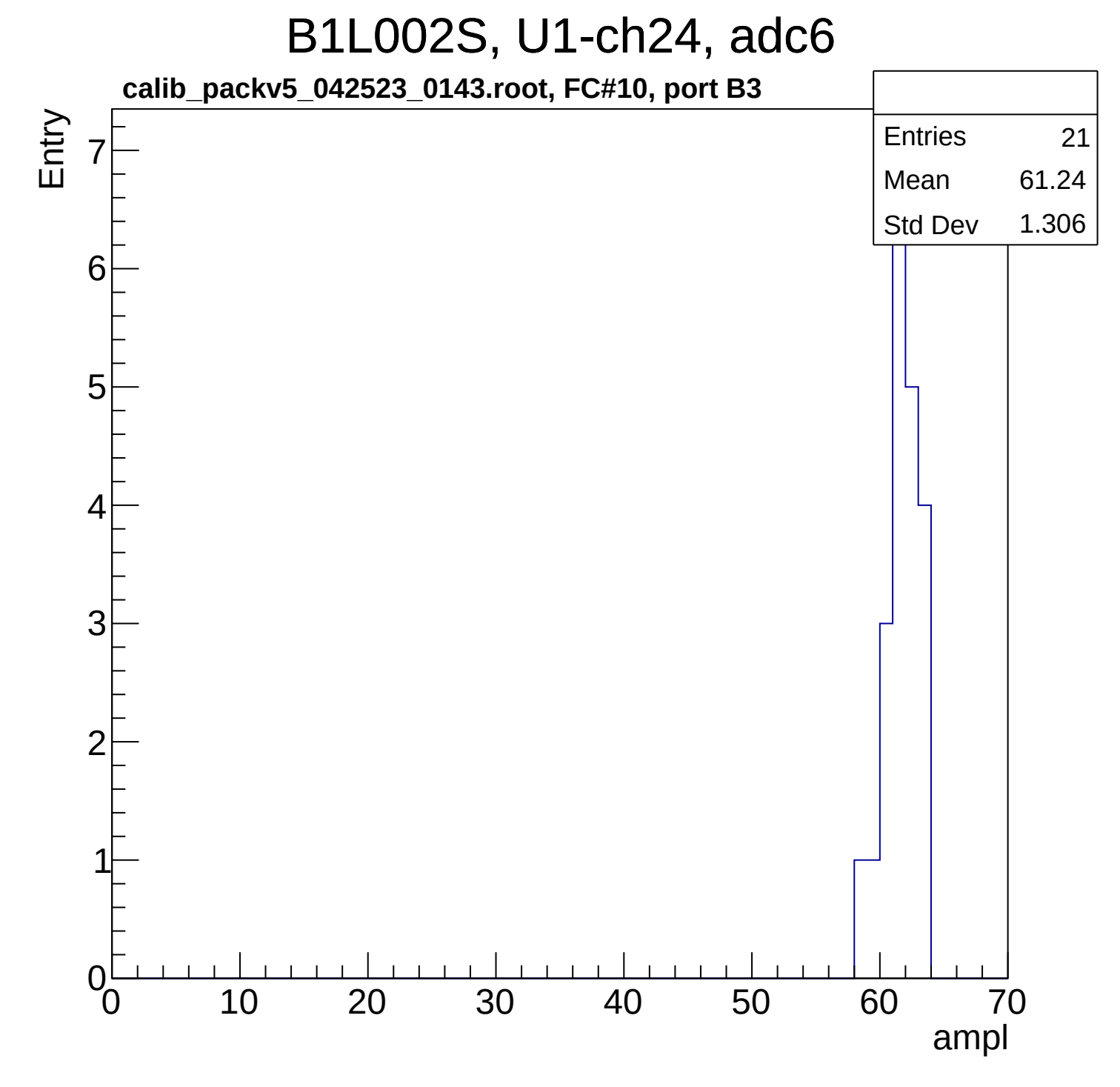
B1L002S, U1-ch24, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.24
Std Dev 1.306
Entry
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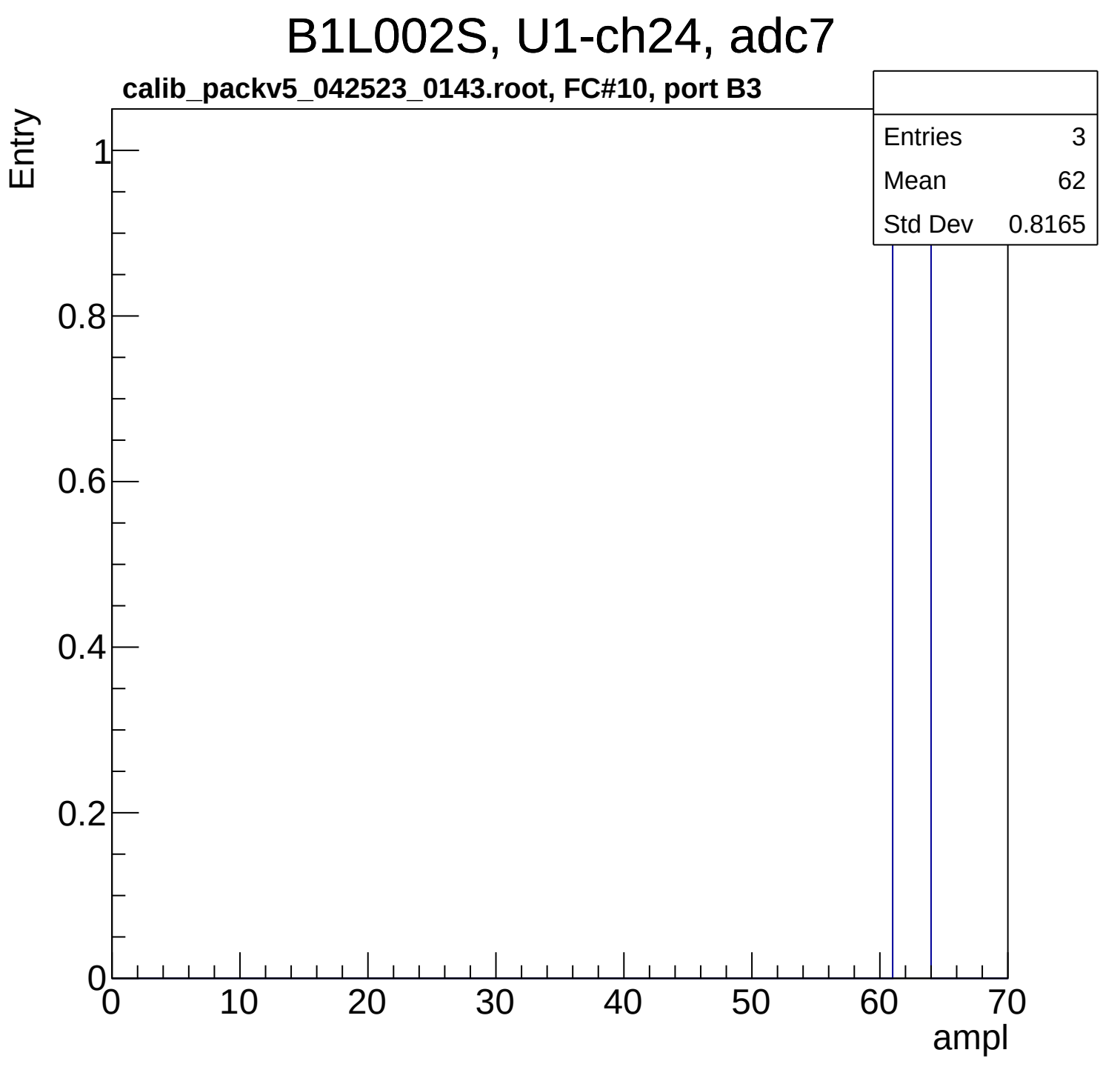
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch24, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62
Std Dev 0.8165
Entry
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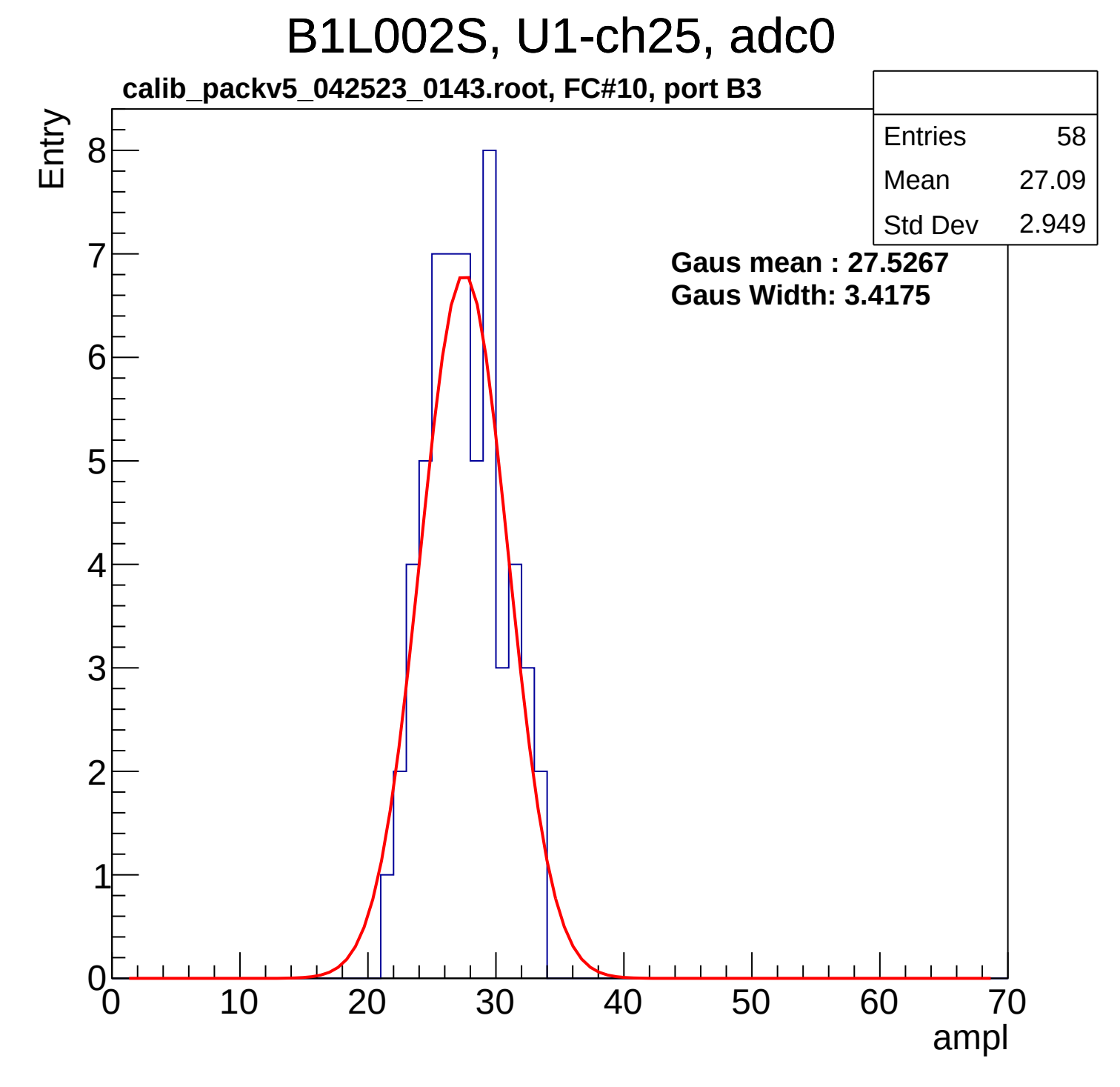
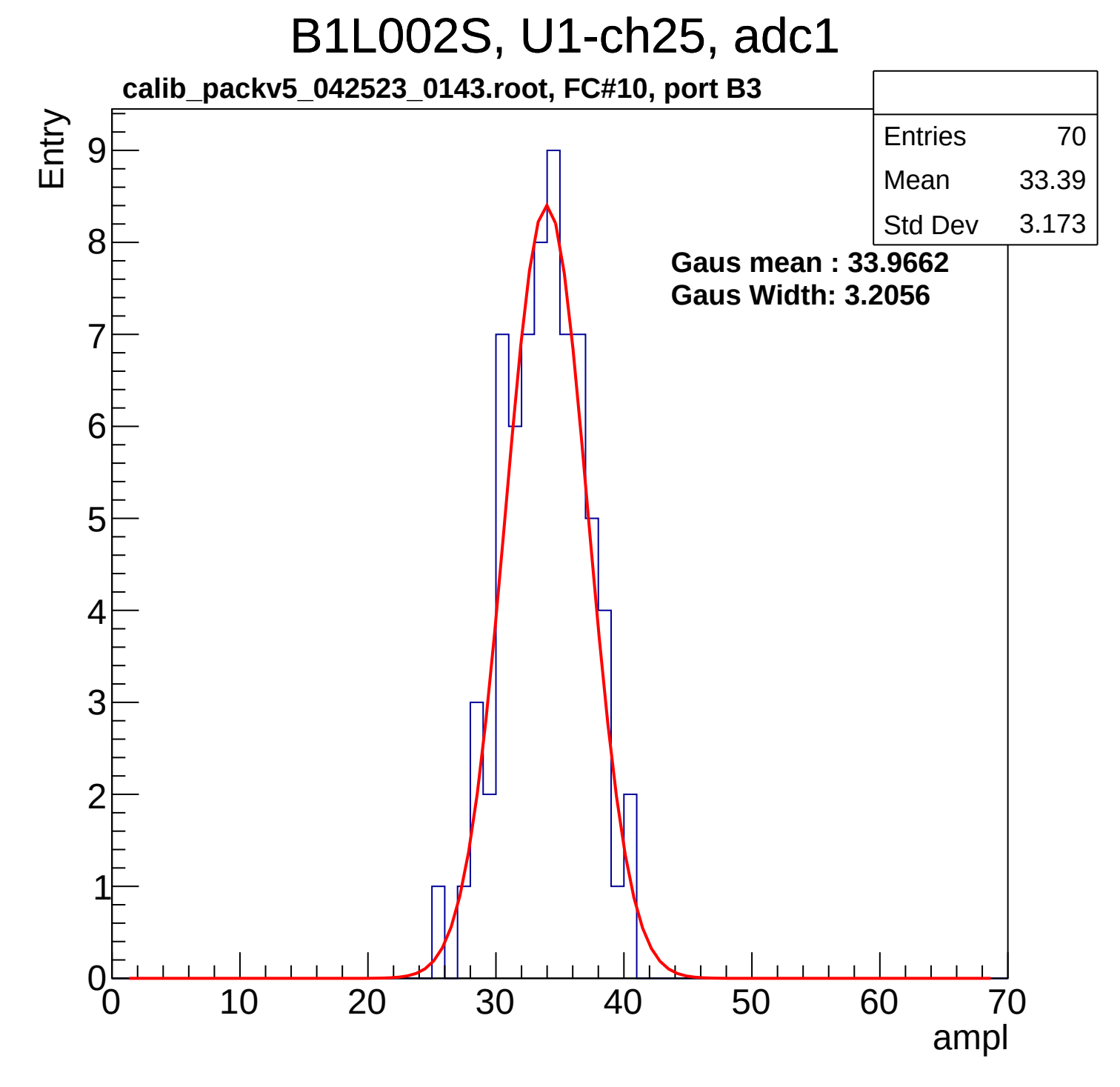
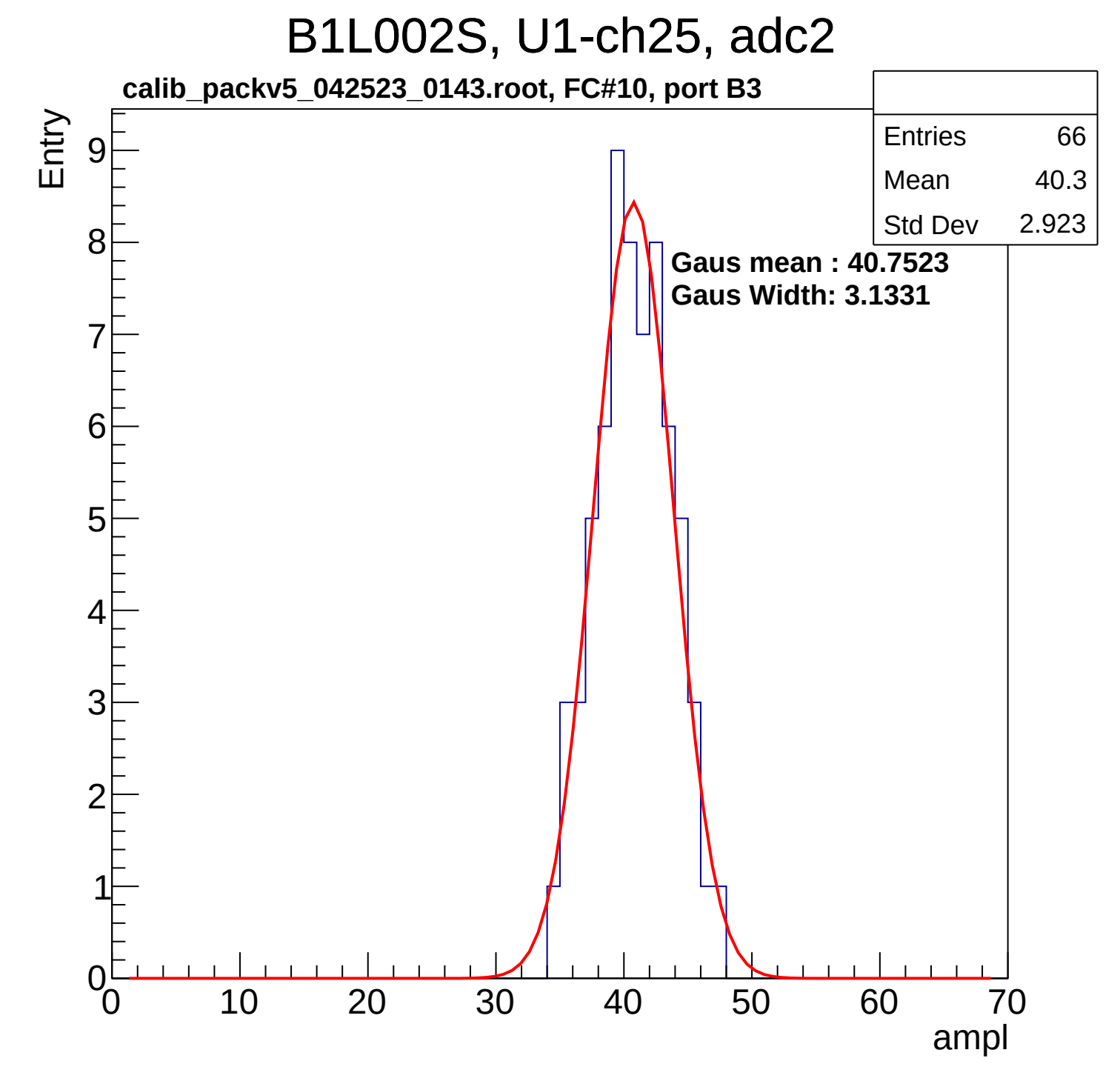
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch25, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.09
Std Dev 2.949
Gaus mean : 27.5267
Gaus Width: 3.4175
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch25, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 33.39
Std Dev 3.173
Gaus mean : 33.9662
Gaus Width: 3.2056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch25, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 40.3
Std Dev 2.923
Gaus mean : 40.7523
Gaus Width: 3.1331
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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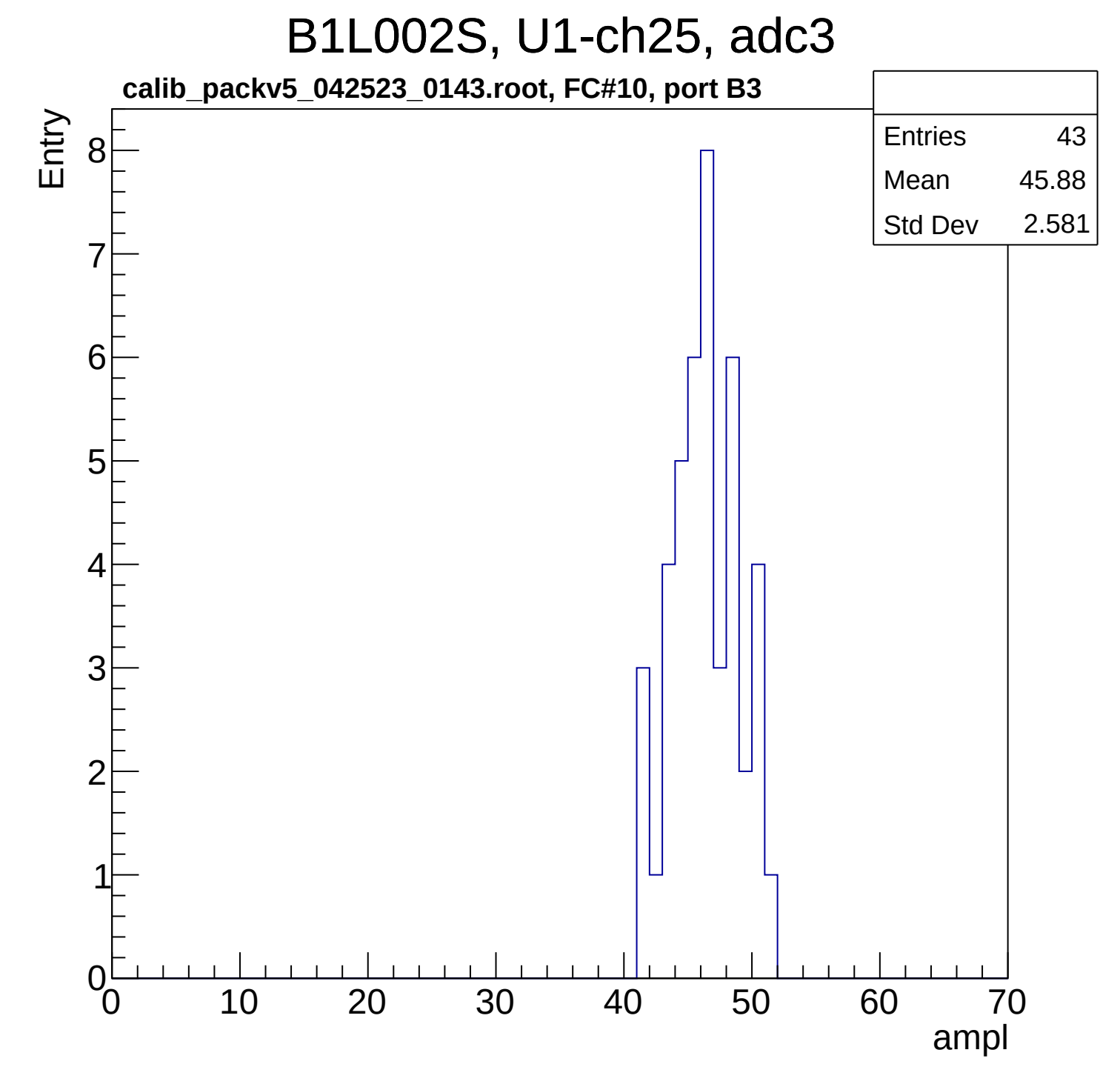
B1L002S, U1-ch25, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 45.88
Std Dev 2.581
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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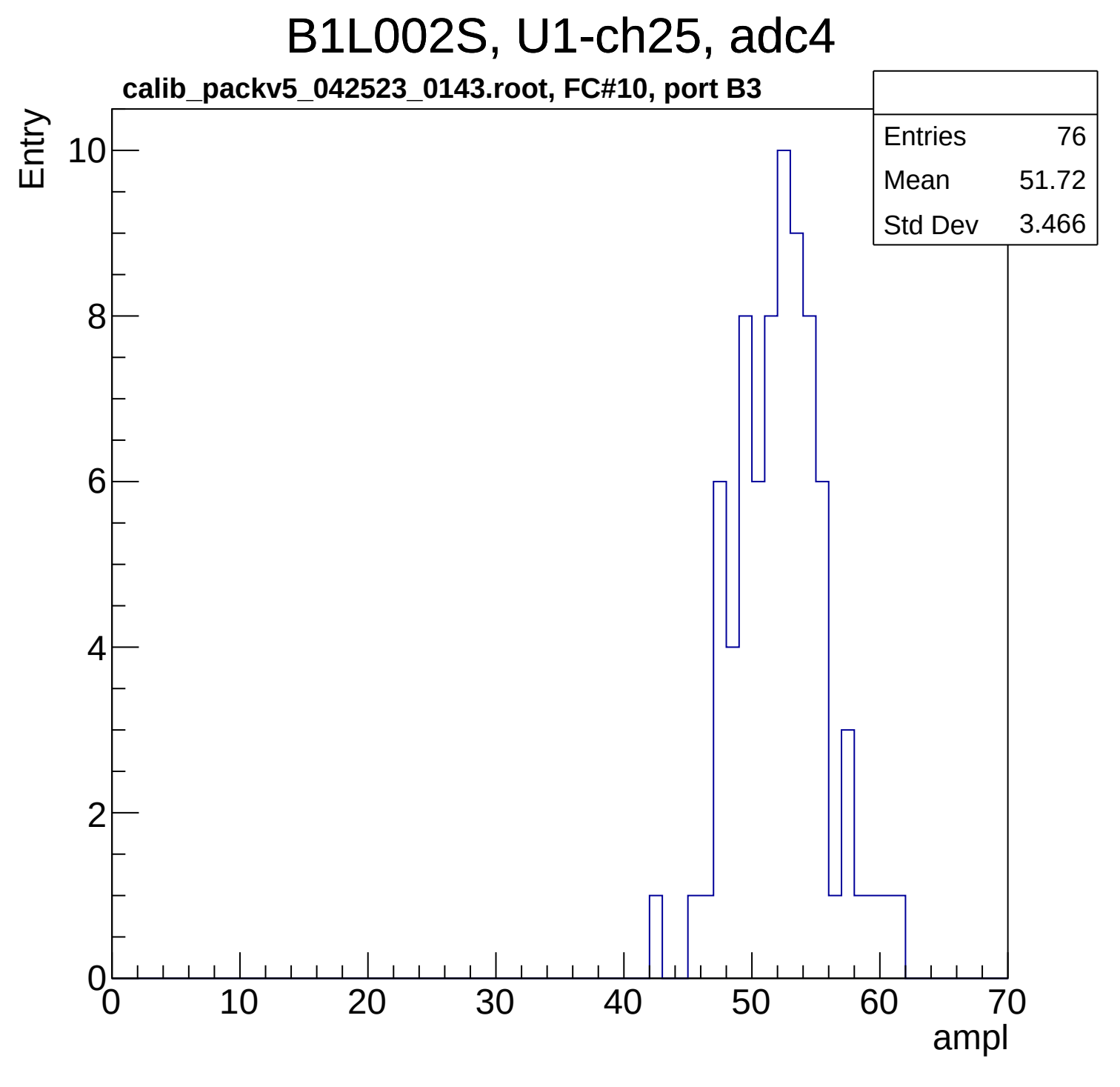
60
70


B1L002S, U1-ch25, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 51.72
Std Dev 3.466
Entry
ampl
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2
4
6
8
10
0
10
20
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40
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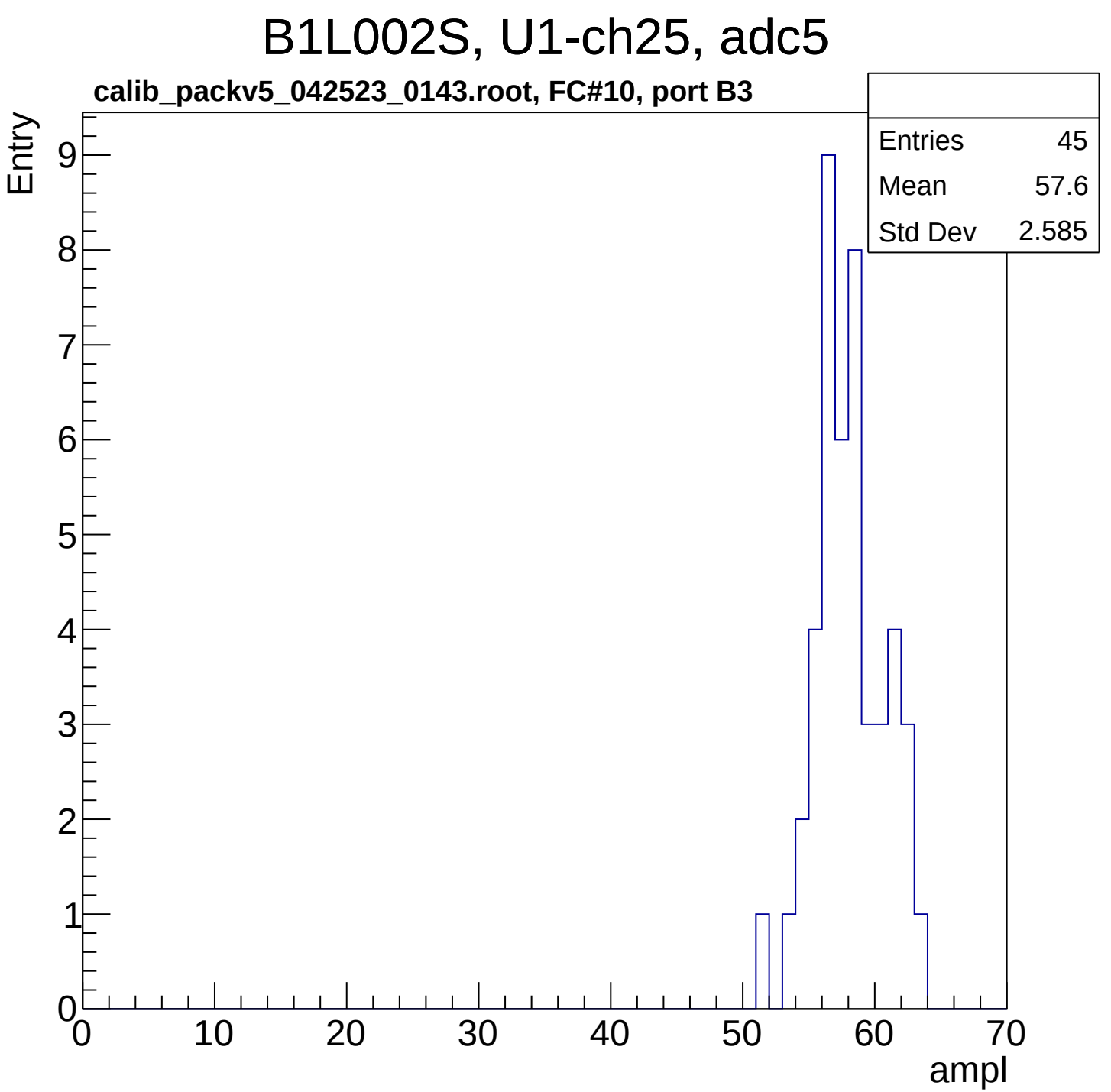
60
70


B1L002S, U1-ch25, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 57.6
Std Dev 2.585
Entry
ampl
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6
7
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0
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60
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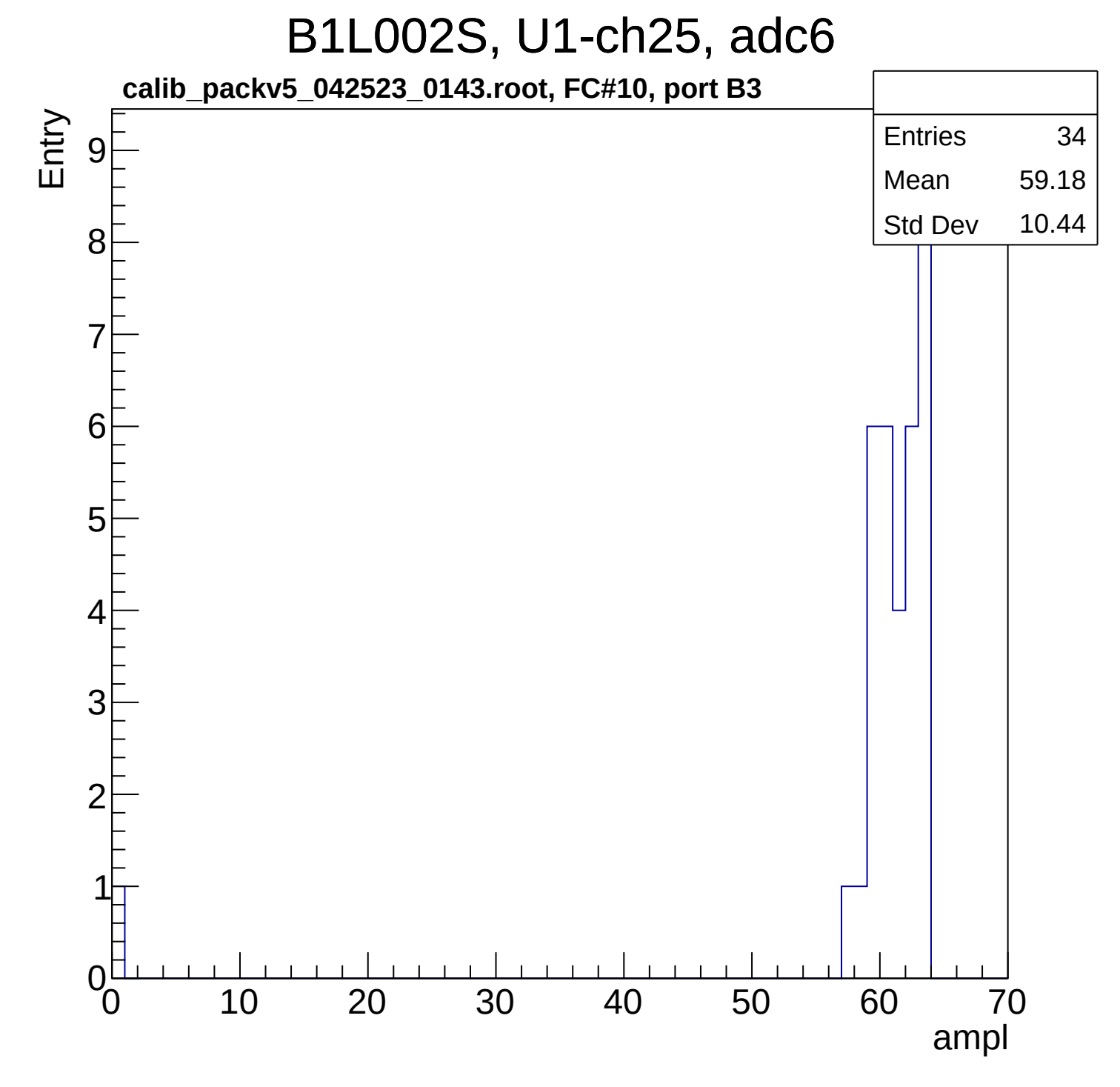
B1L002S, U1-ch25, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 59.18
Std Dev 10.44
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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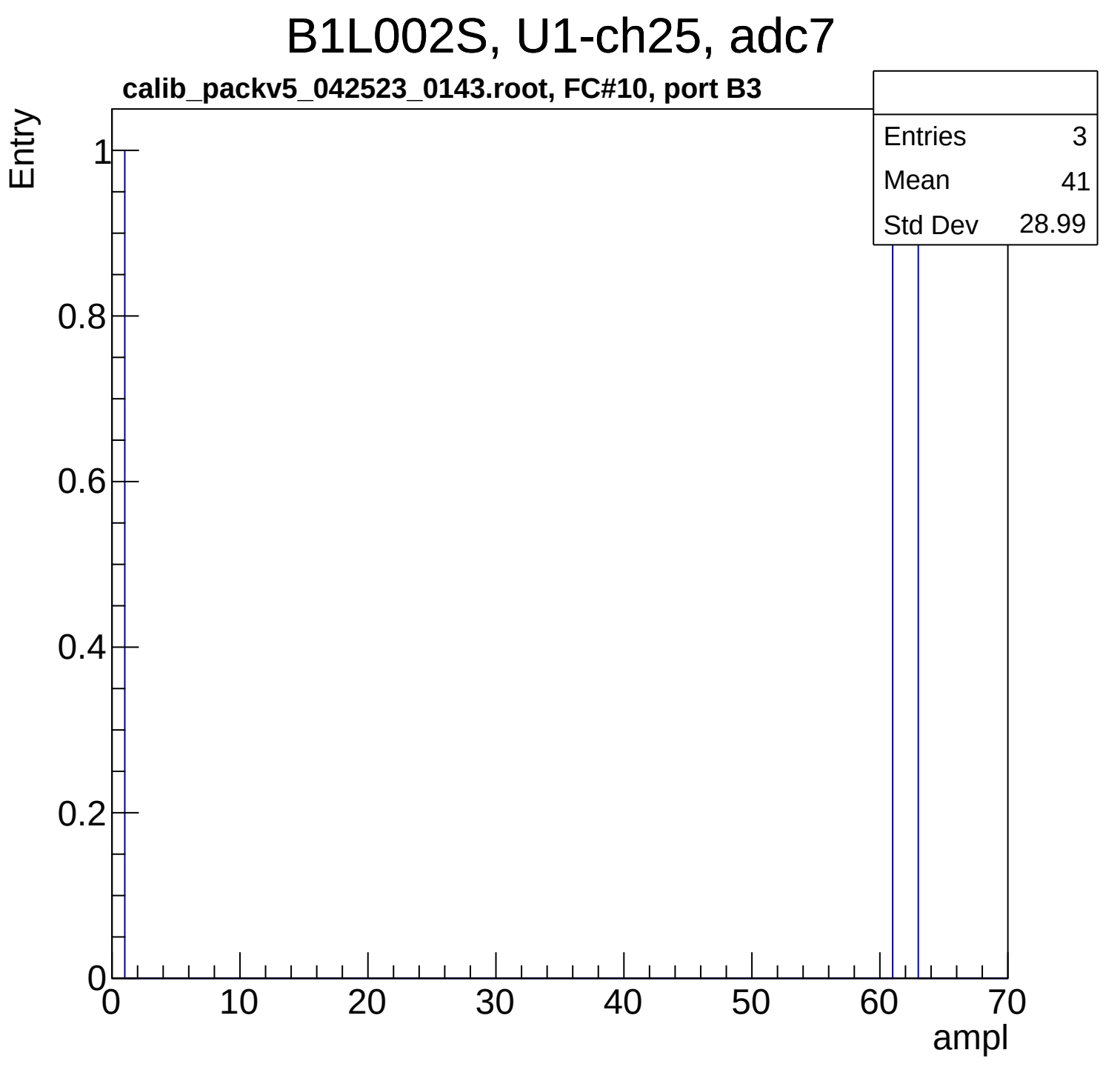
40
50
60
70


B1L002S, U1-ch25, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41
Std Dev 28.99
Entry
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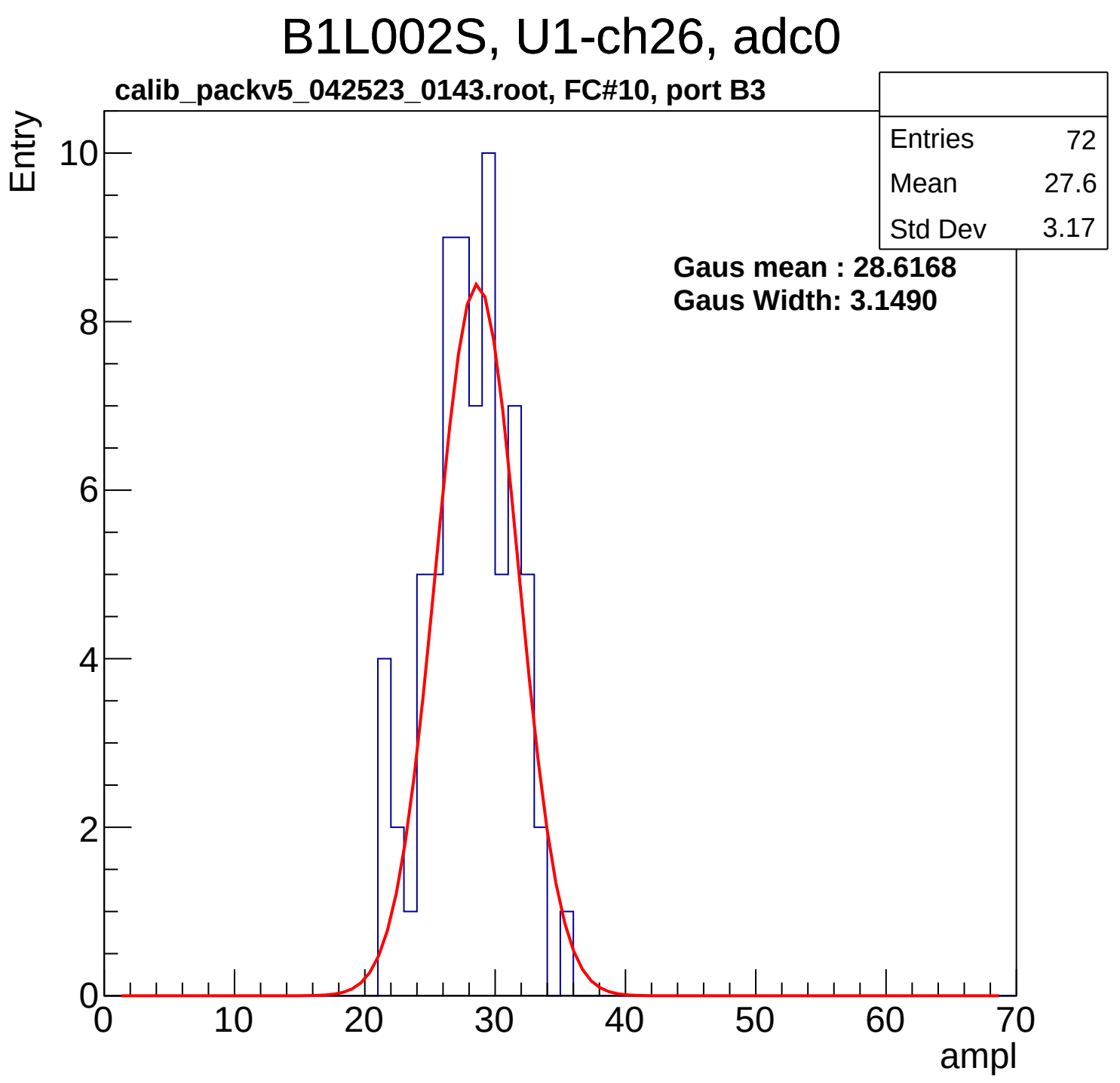
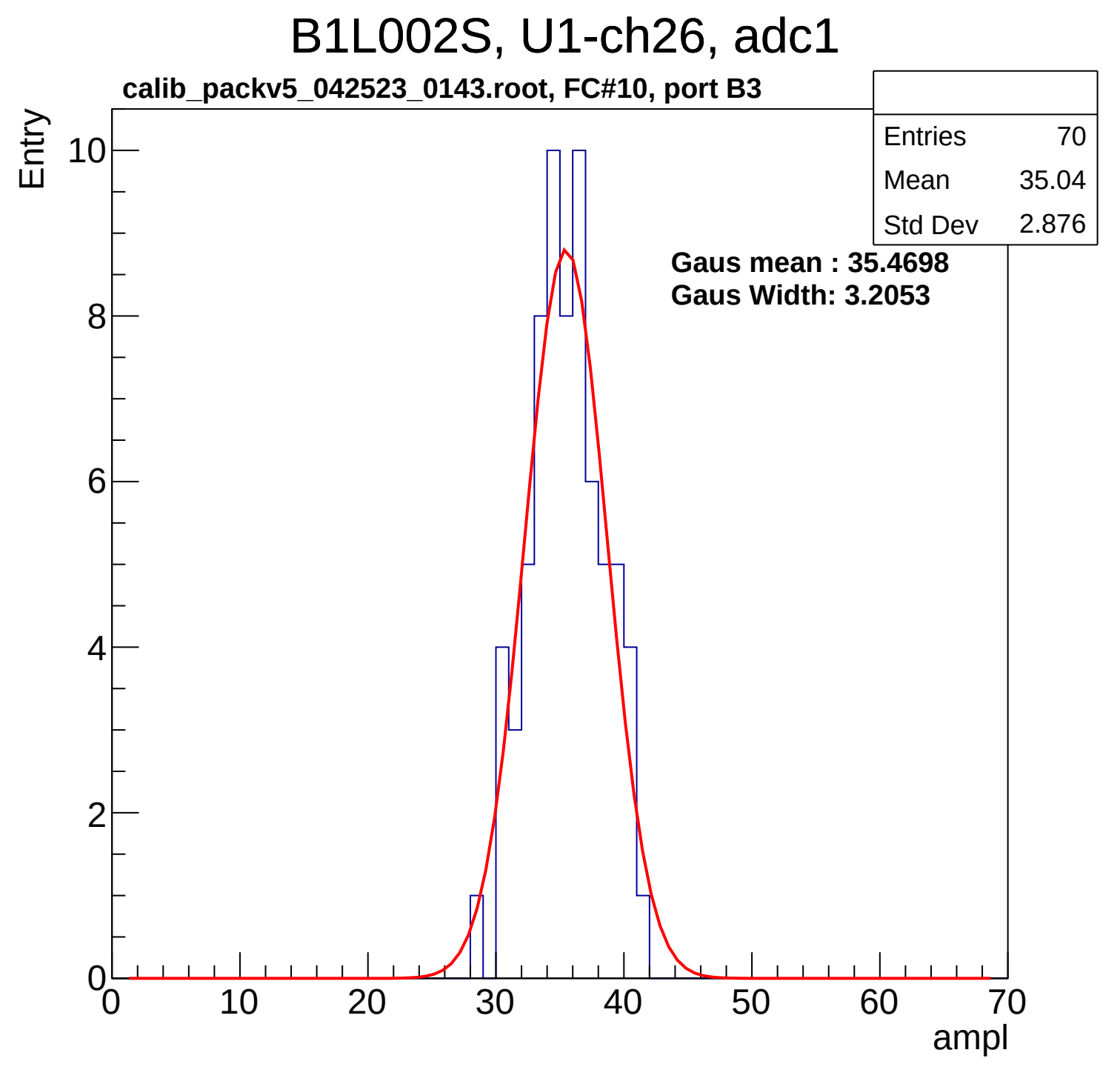
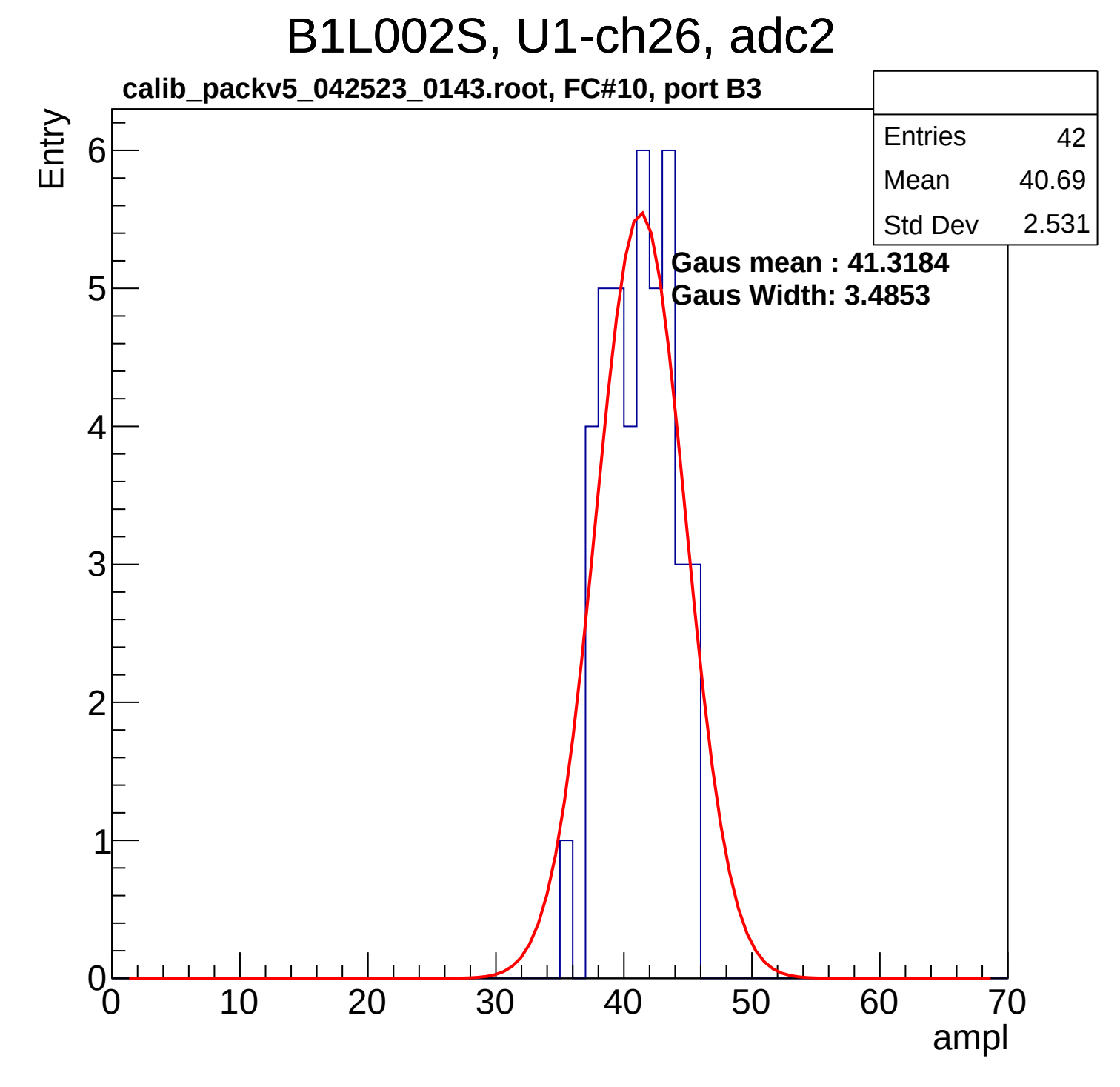
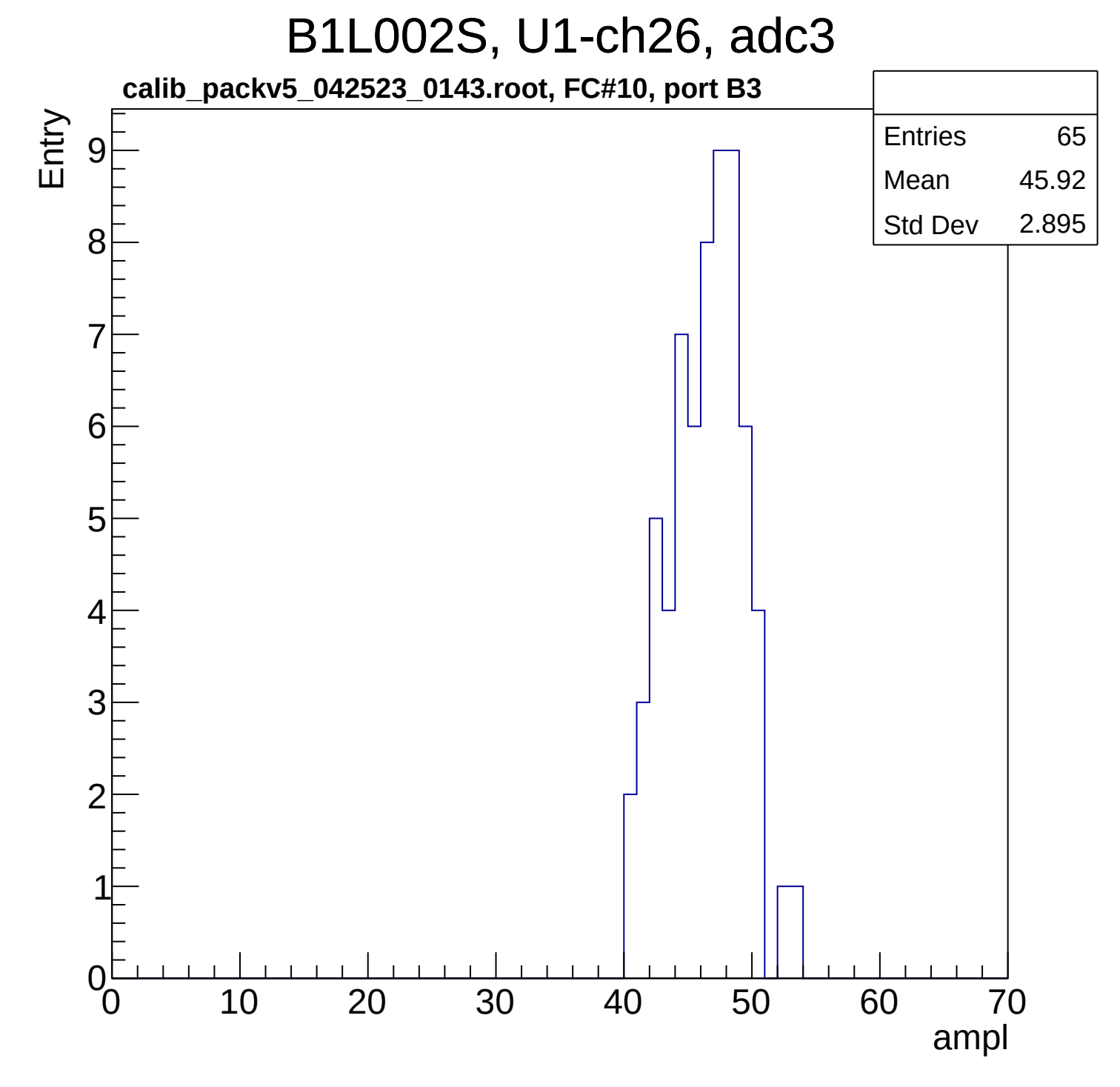
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch26, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 27.6
Std Dev 3.17
Gaus mean : 28.6168
Gaus Width: 3.1490
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch26, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.04
Std Dev 2.876
Gaus mean : 35.4698
Gaus Width: 3.2053
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch26, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 40.69
Std Dev 2.531
Gaus mean : 41.3184
Gaus Width: 3.4853
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U1-ch26, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.92
Std Dev 2.895
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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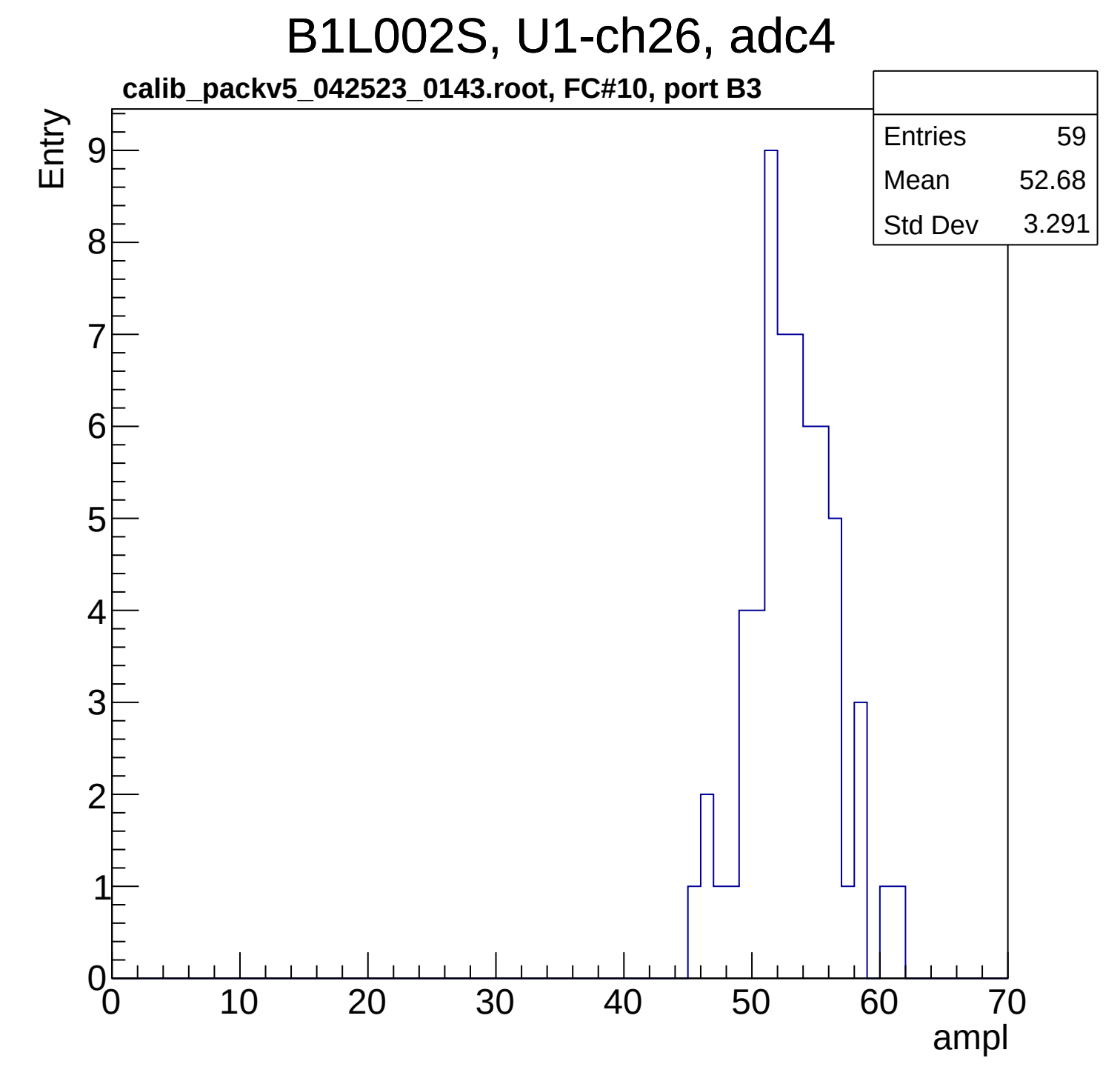
B1L002S, U1-ch26, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.68
Std Dev 3.291
Entry
ampl
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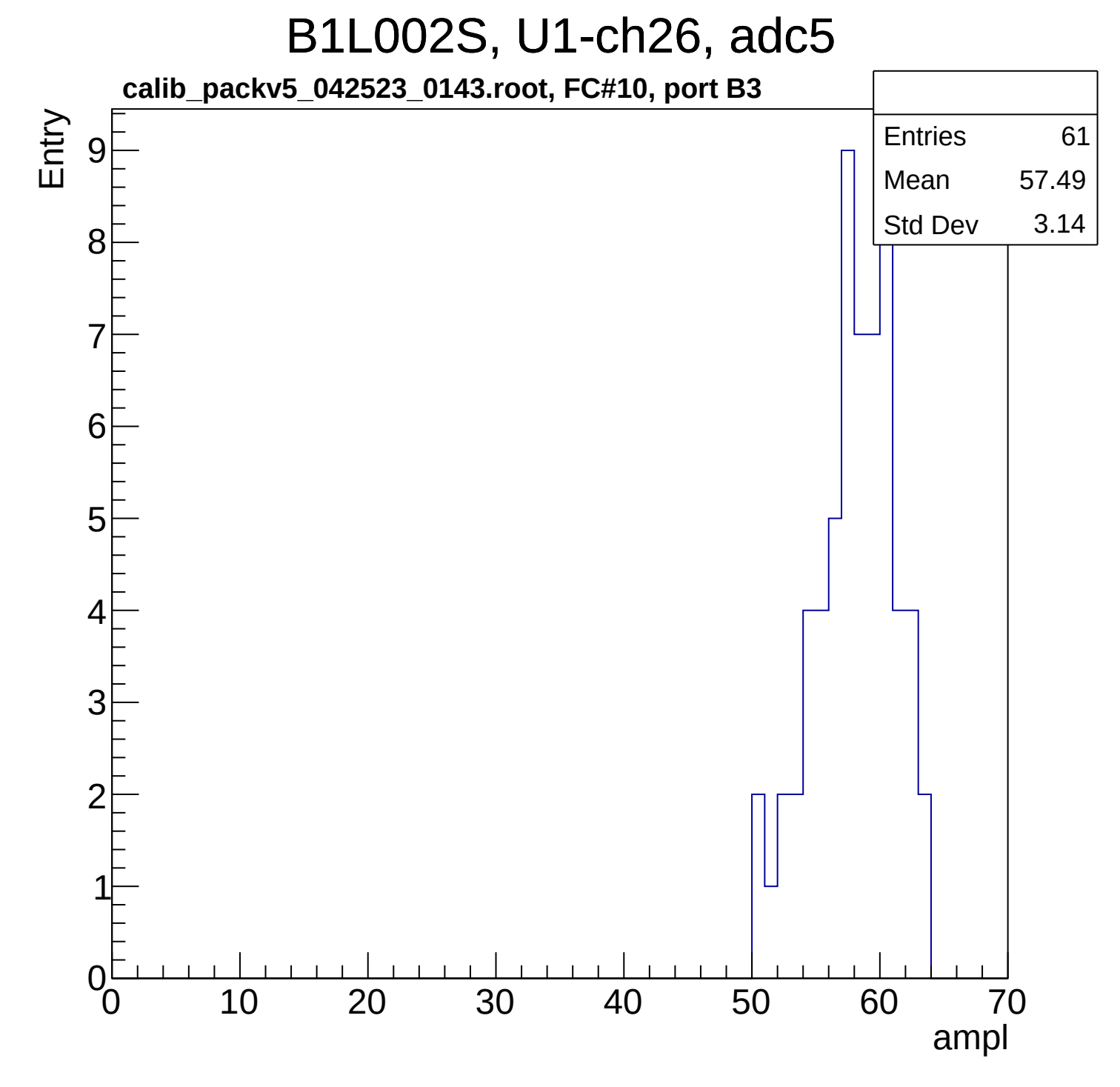
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch26, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.49
Std Dev 3.14
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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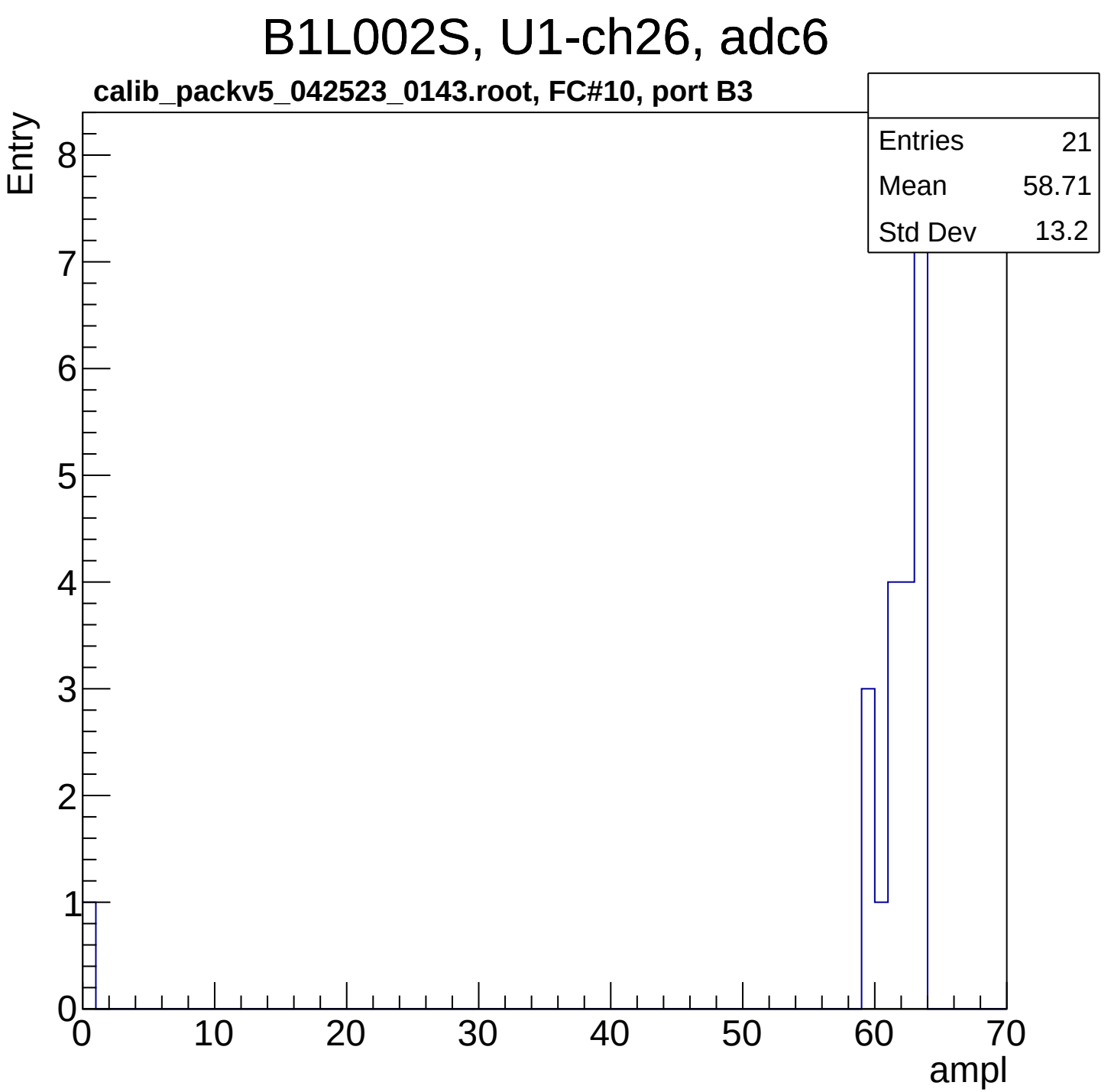
B1L002S, U1-ch26, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.71
Std Dev 13.2
Entry
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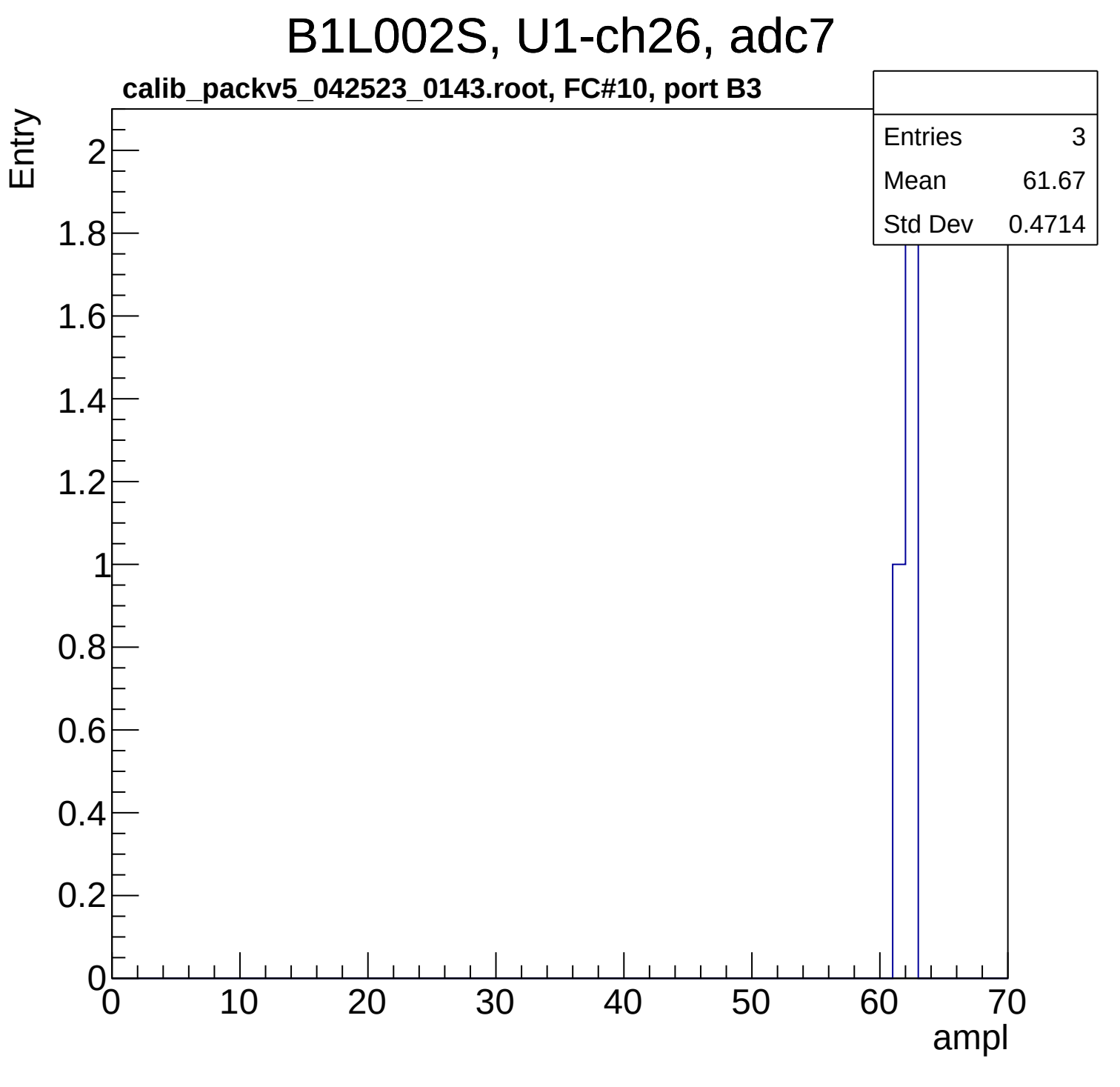
50
60
70
ampl


B1L002S, U1-ch26, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
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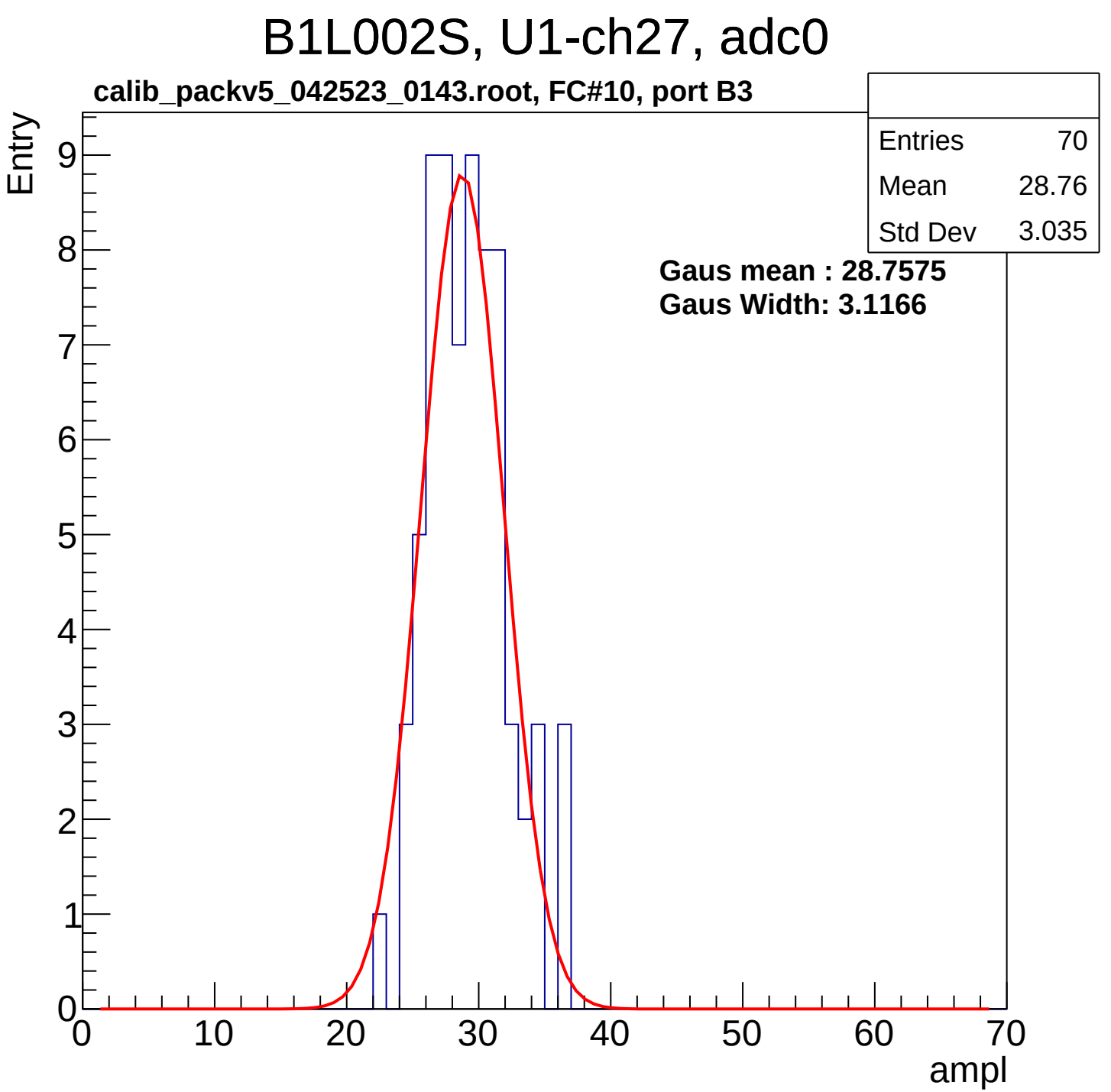
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch27, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.76
Std Dev 3.035
Gaus mean : 28.7575
Gaus Width: 3.1166
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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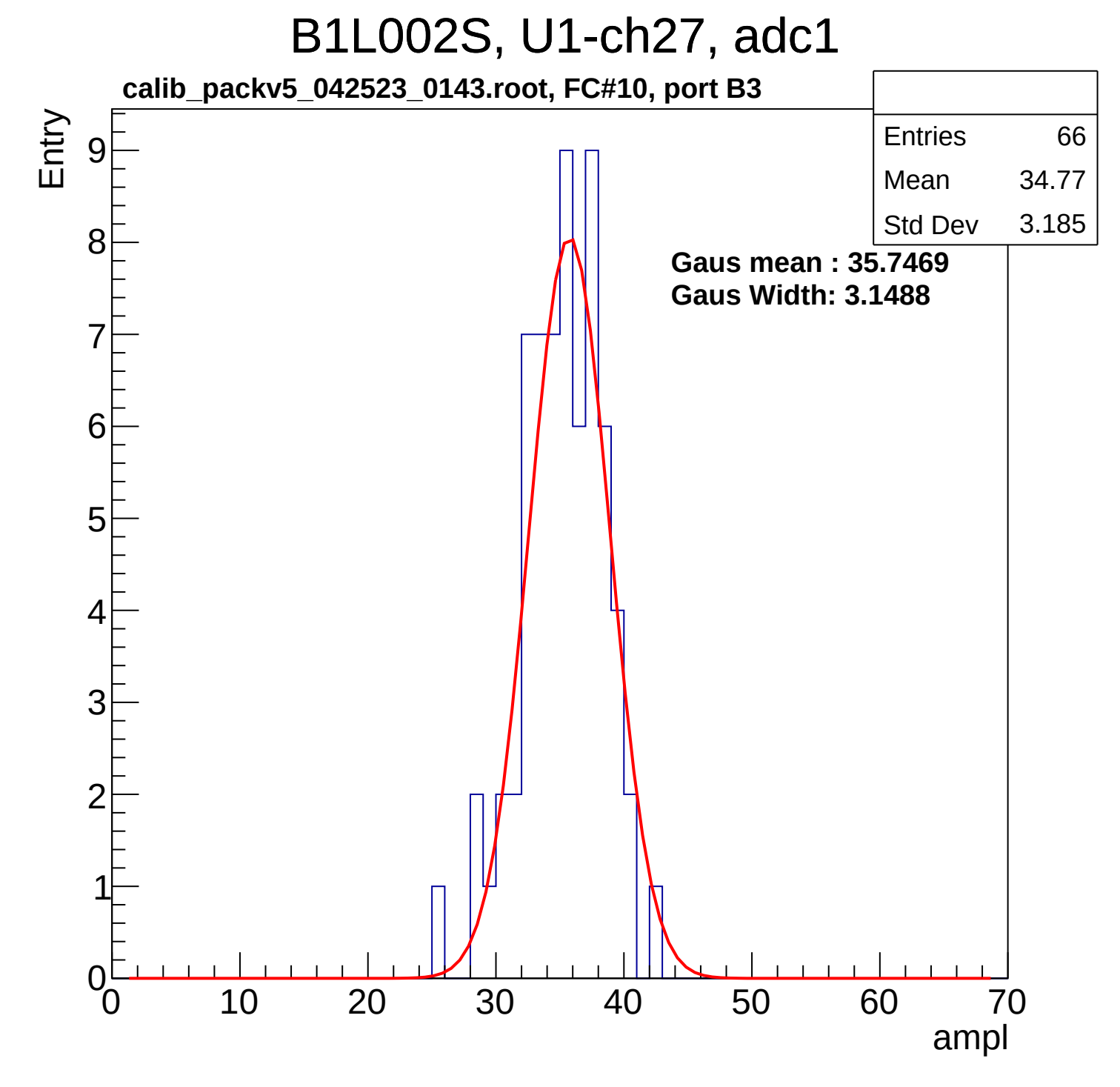
B1L002S, U1-ch27, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.77
Std Dev 3.185
Gaus mean : 35.7469
Gaus Width: 3.1488
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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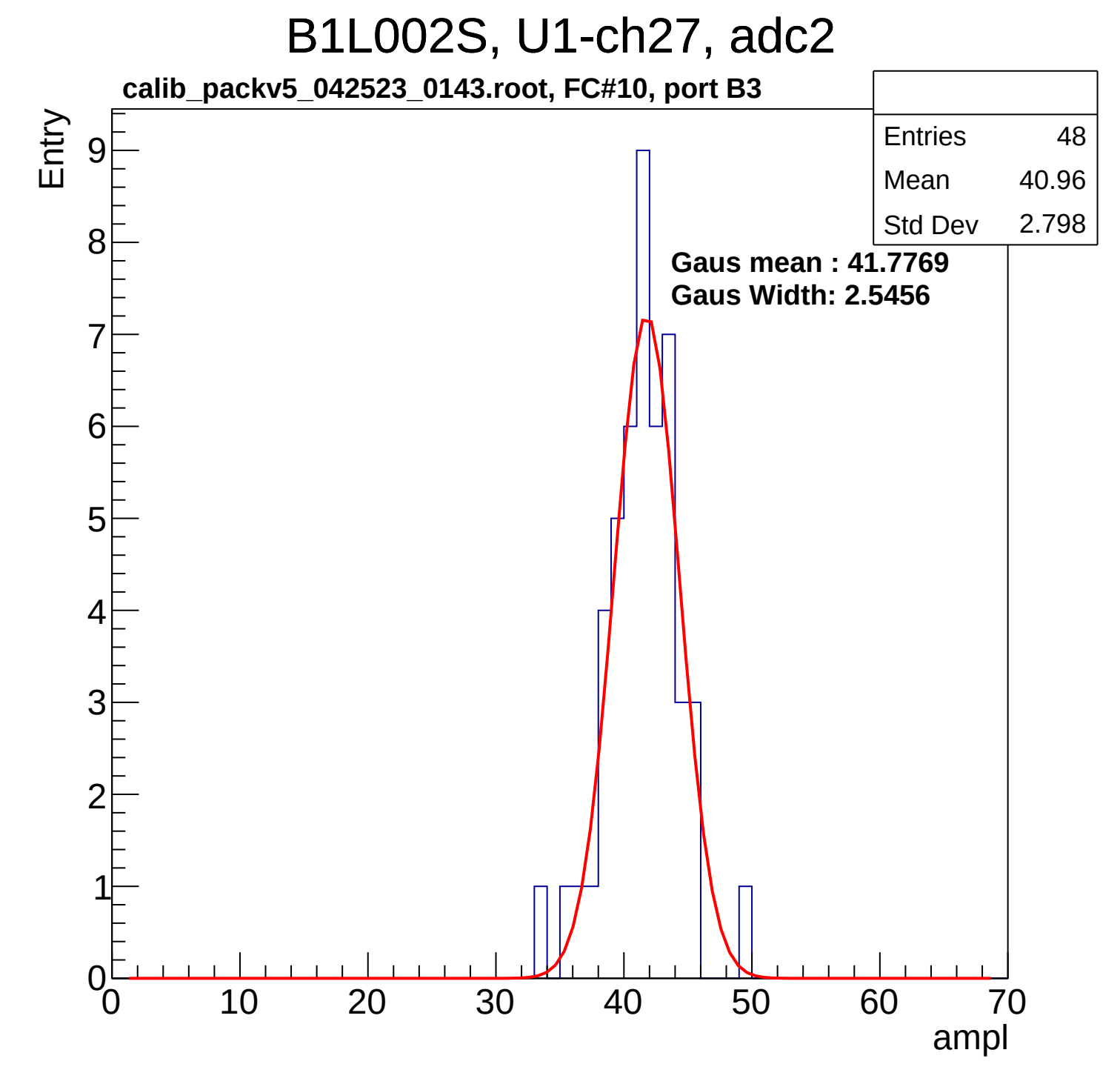
B1L002S, U1-ch27, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 40.96
Std Dev 2.798
Gaus mean : 41.7769
Gaus Width: 2.5456
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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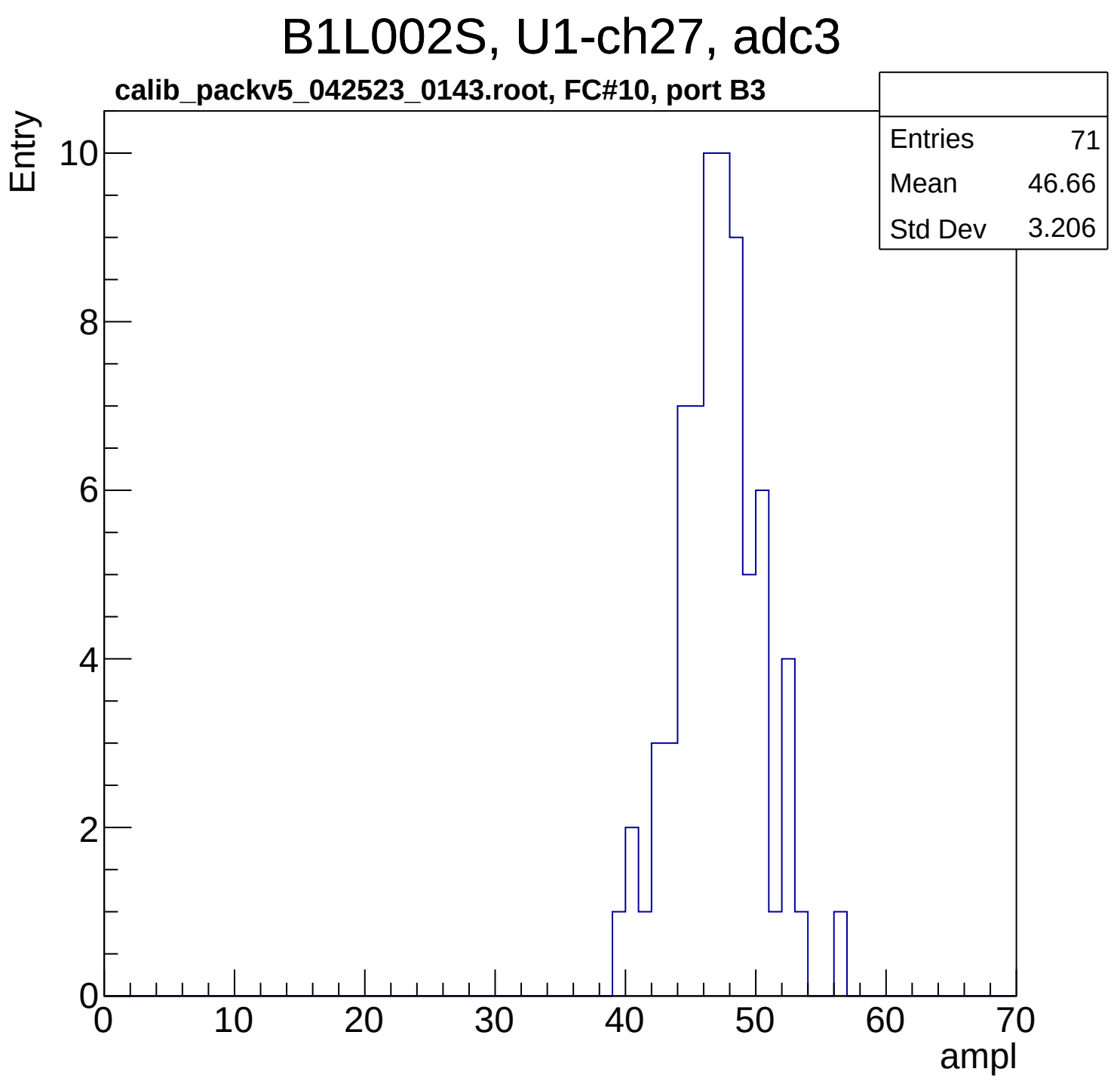
B1L002S, U1-ch27, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 46.66
Std Dev 3.206
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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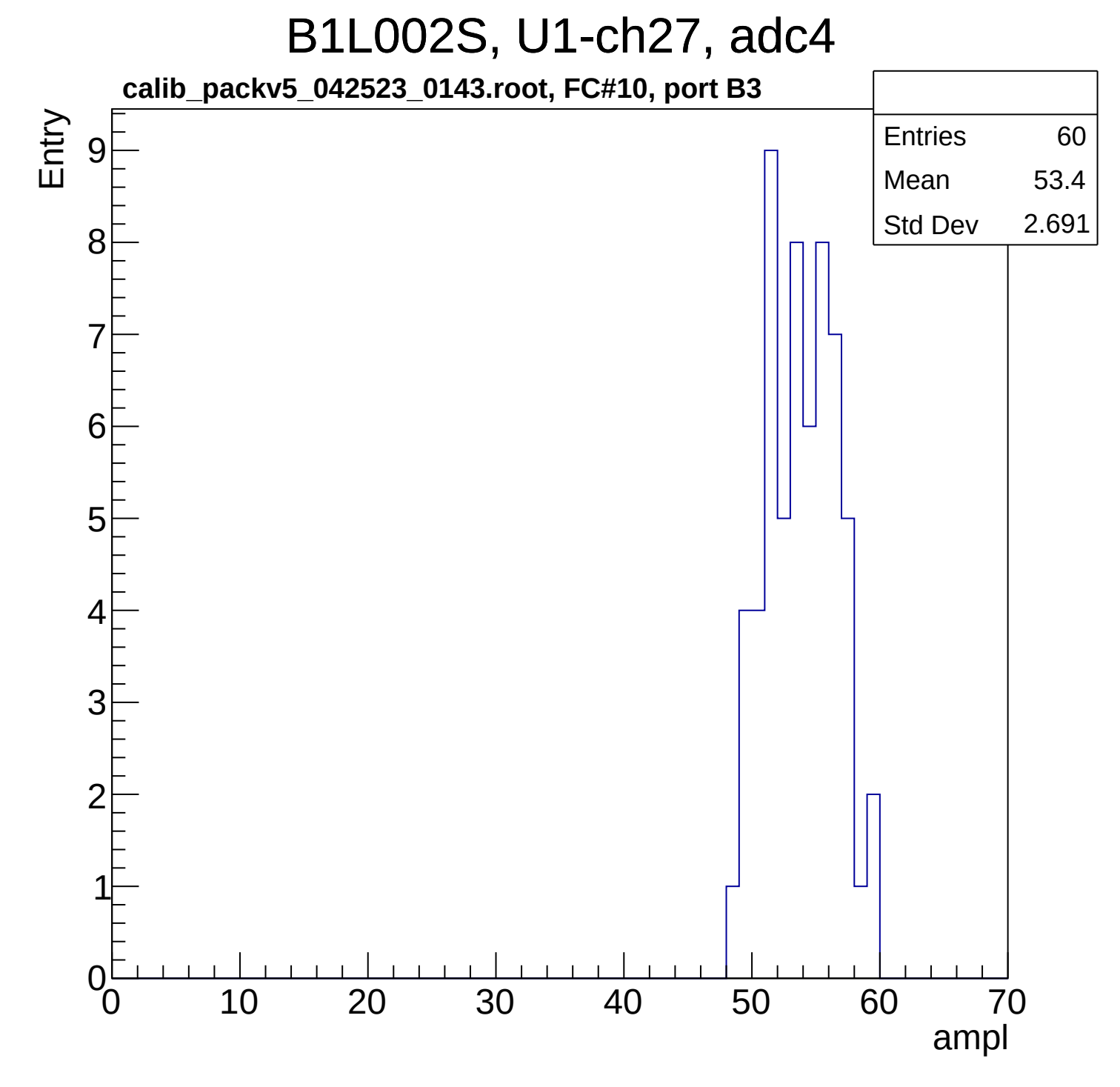
B1L002S, U1-ch27, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.4
Std Dev 2.691
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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30
40
50
60
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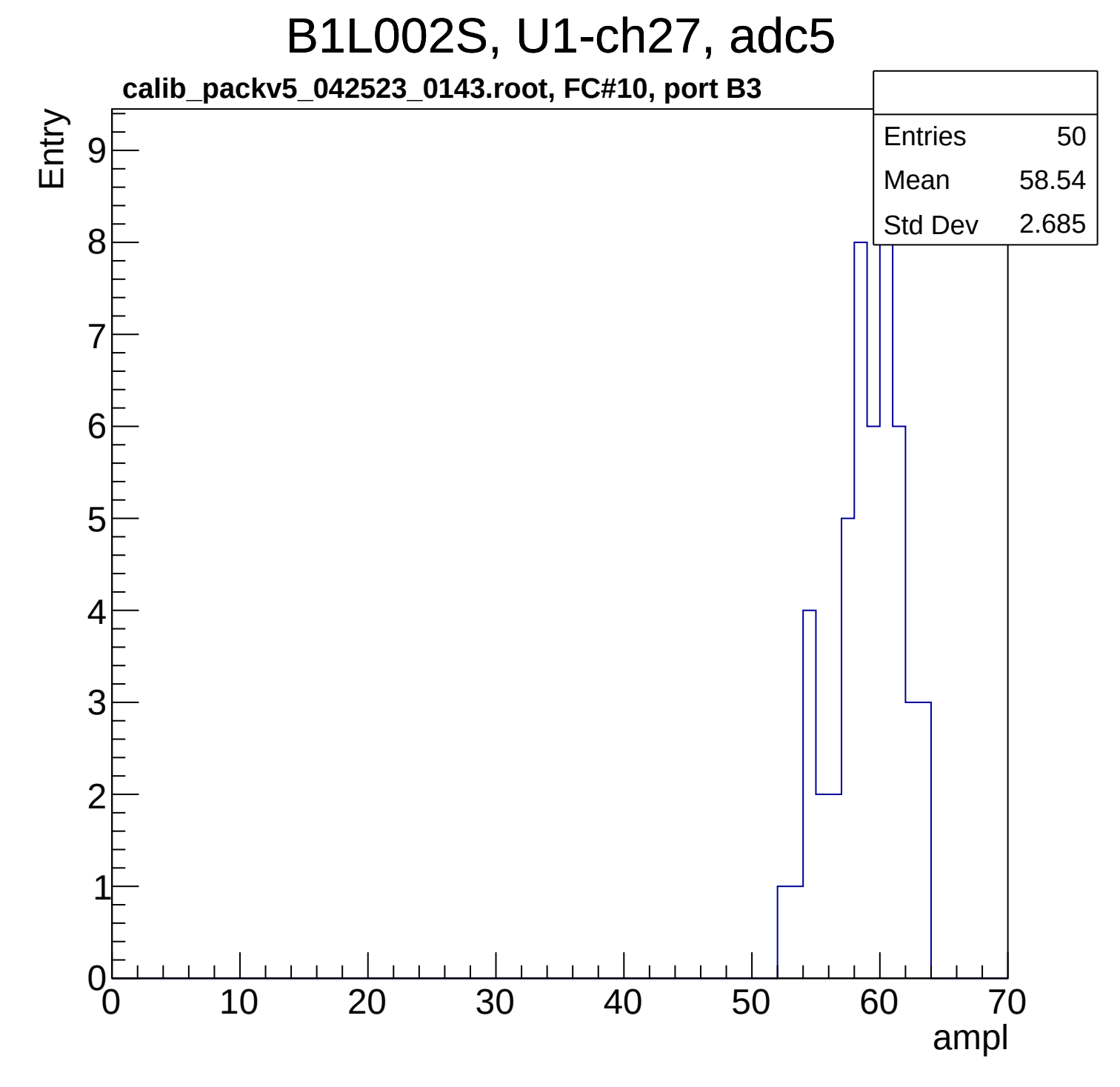
B1L002S, U1-ch27, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.54
Std Dev 2.685
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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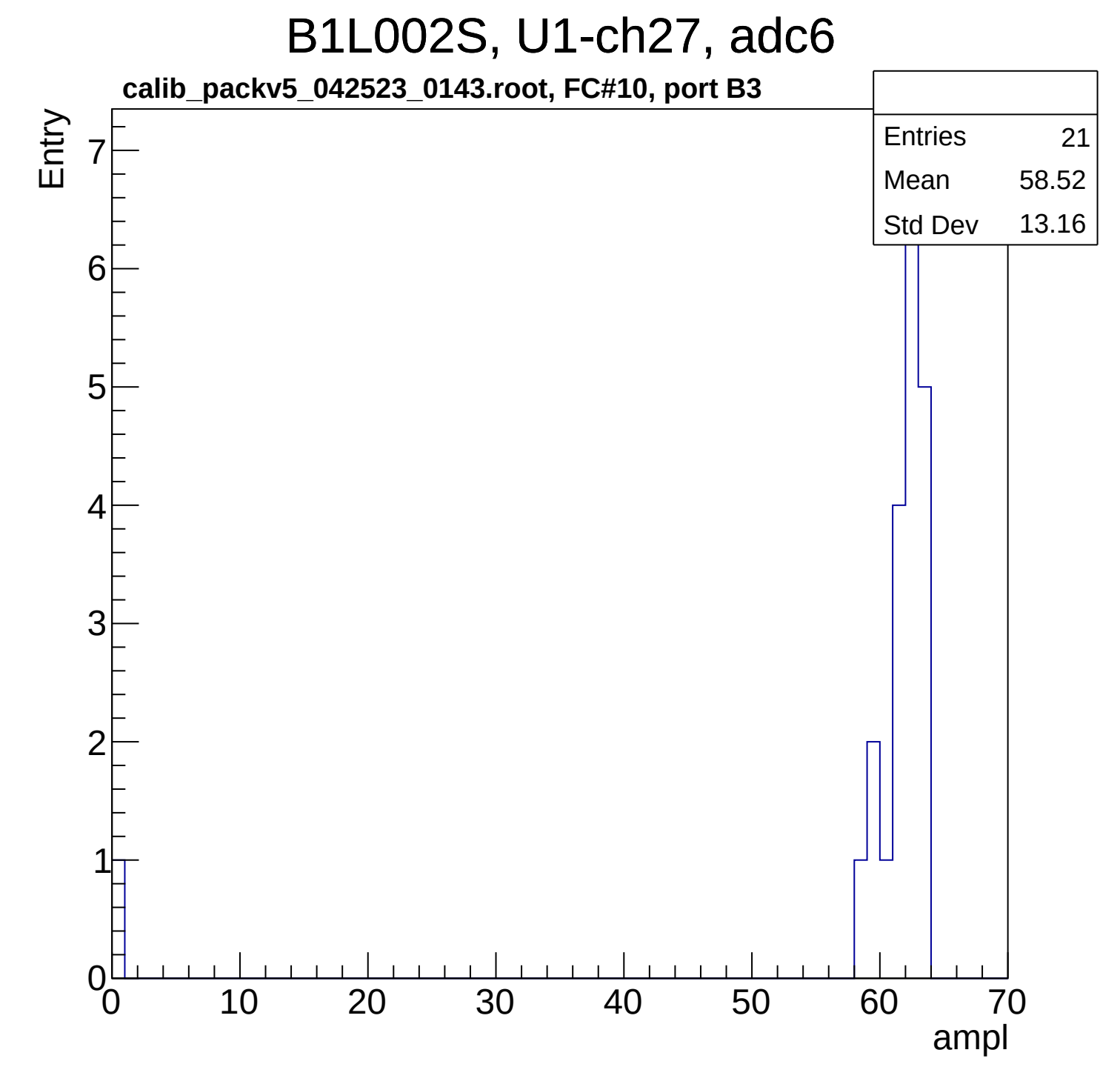
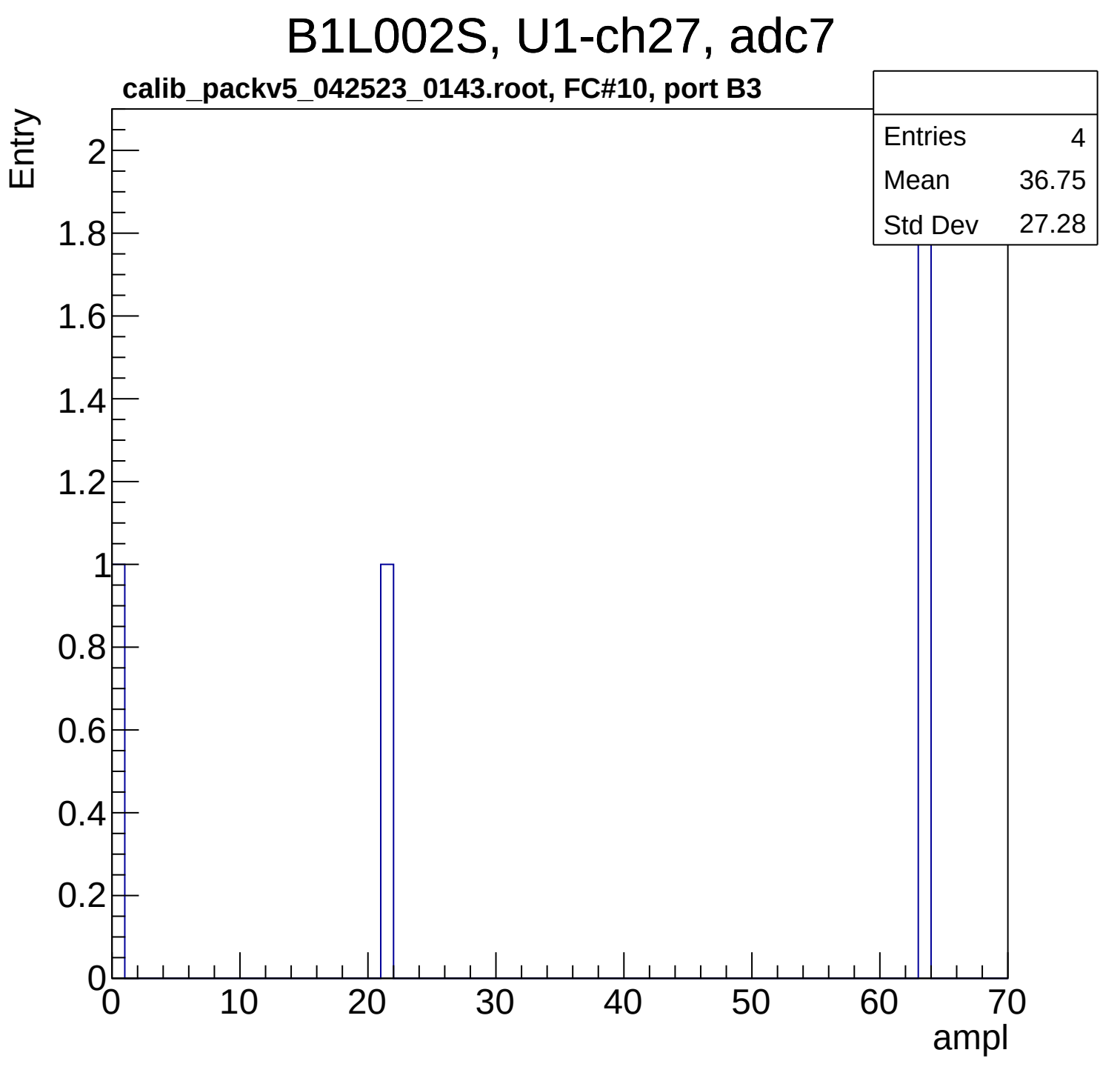
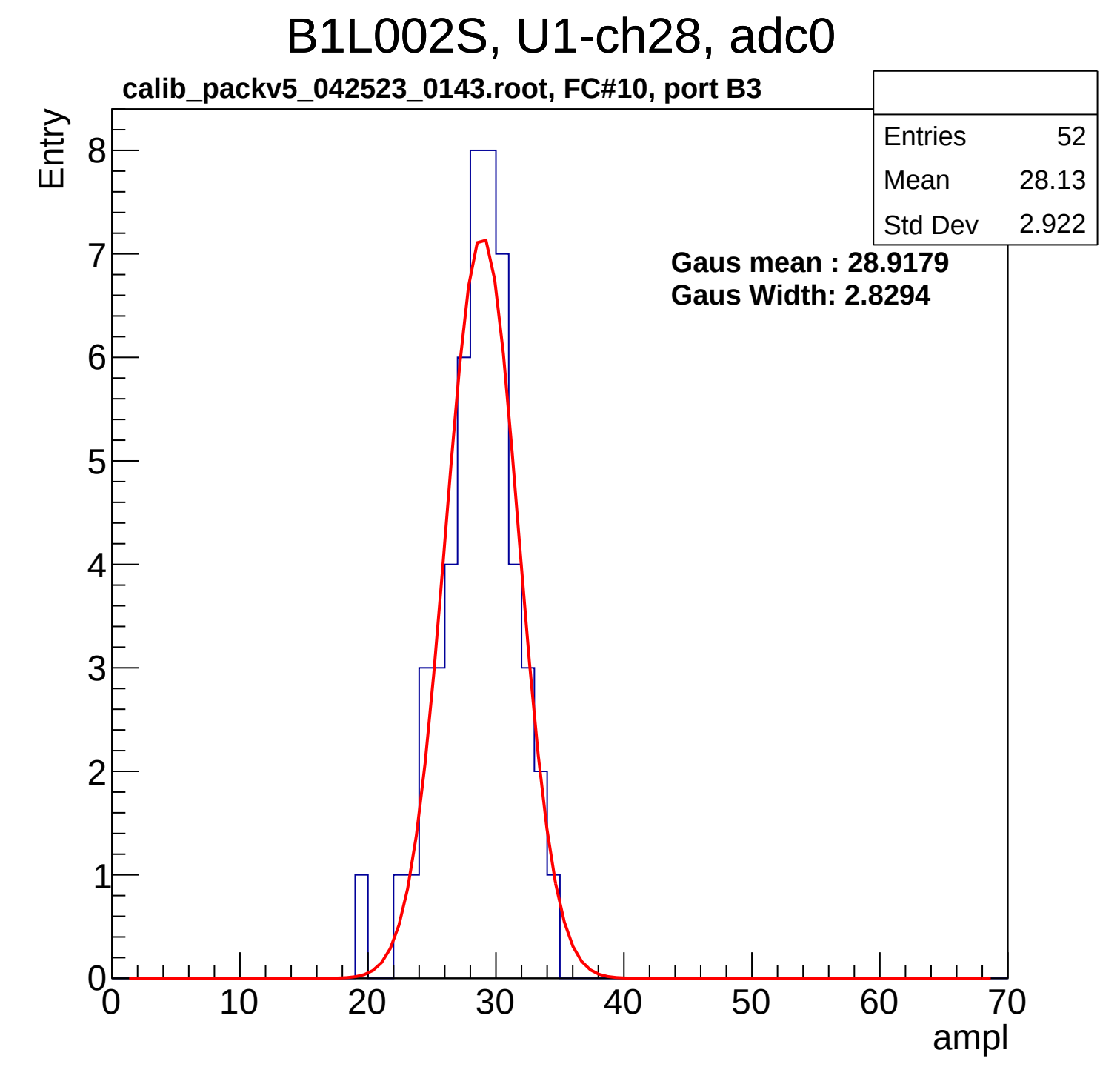
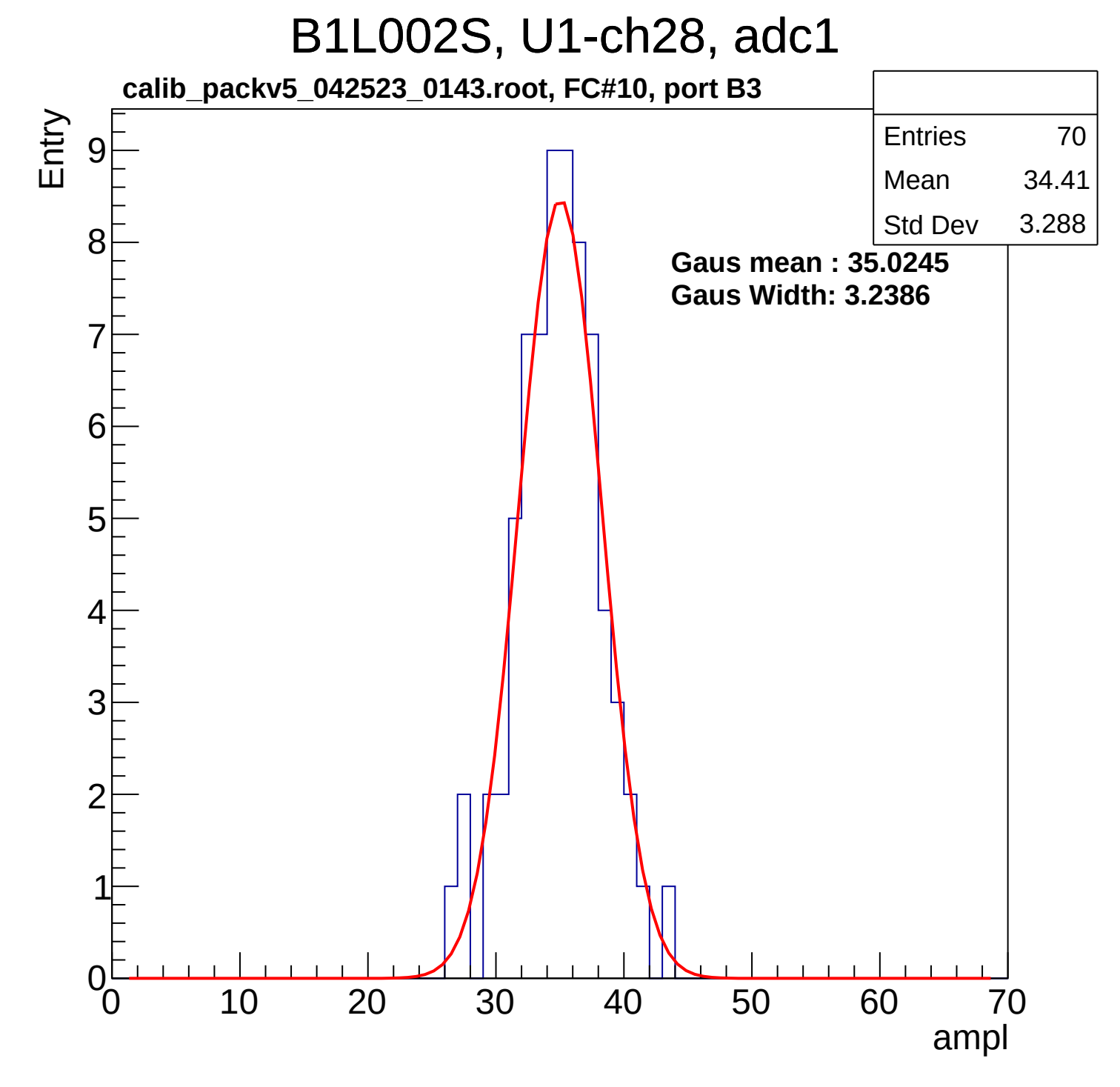

B1L002S, U1-ch27, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.52
Std Dev 13.16
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch27, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.75
Std Dev 27.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch28, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 28.13
Std Dev 2.922
Gaus mean : 28.9179
Gaus Width: 2.8294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch28, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.41
Std Dev 3.288
Gaus mean : 35.0245
Gaus Width: 3.2386
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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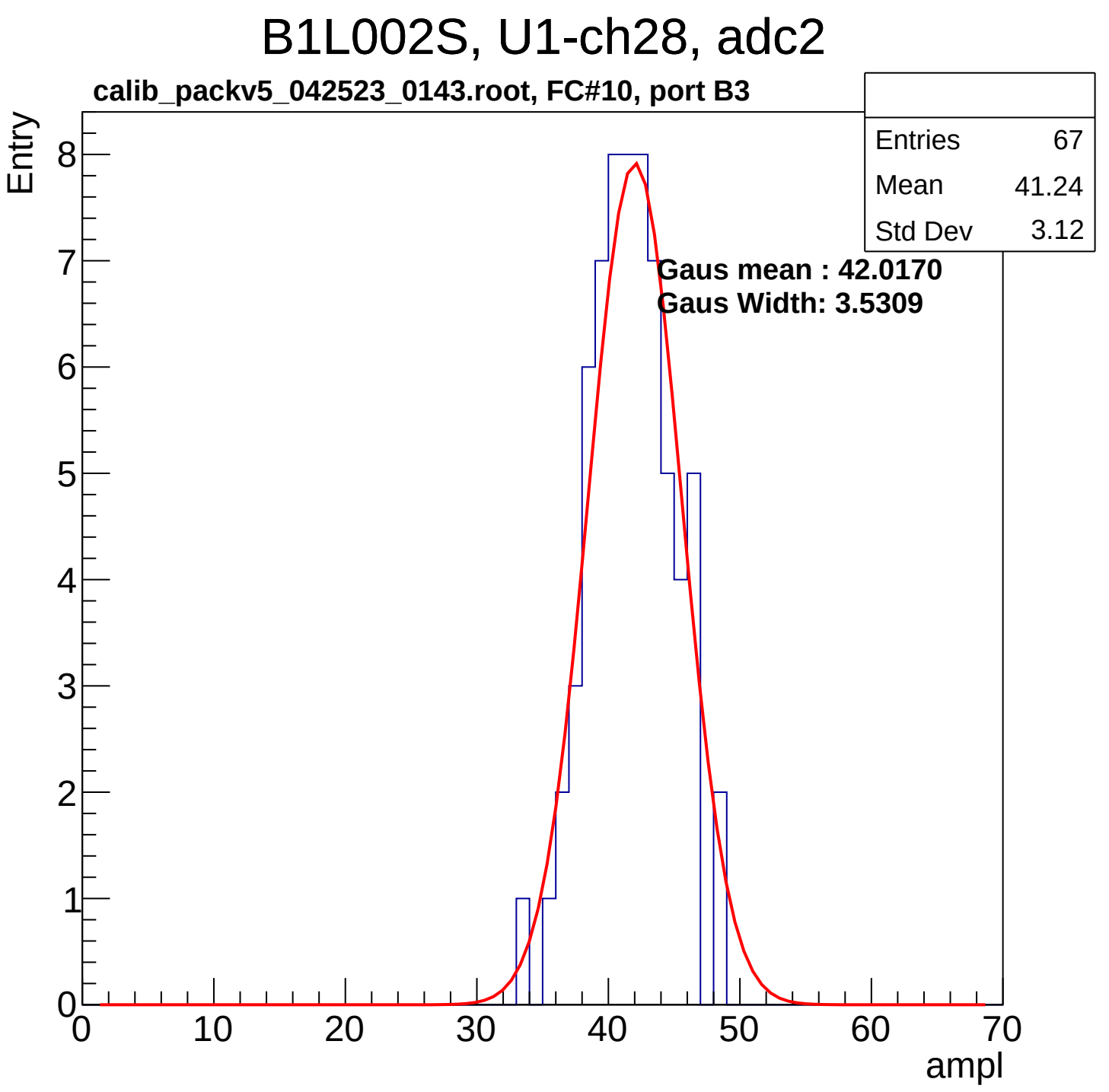
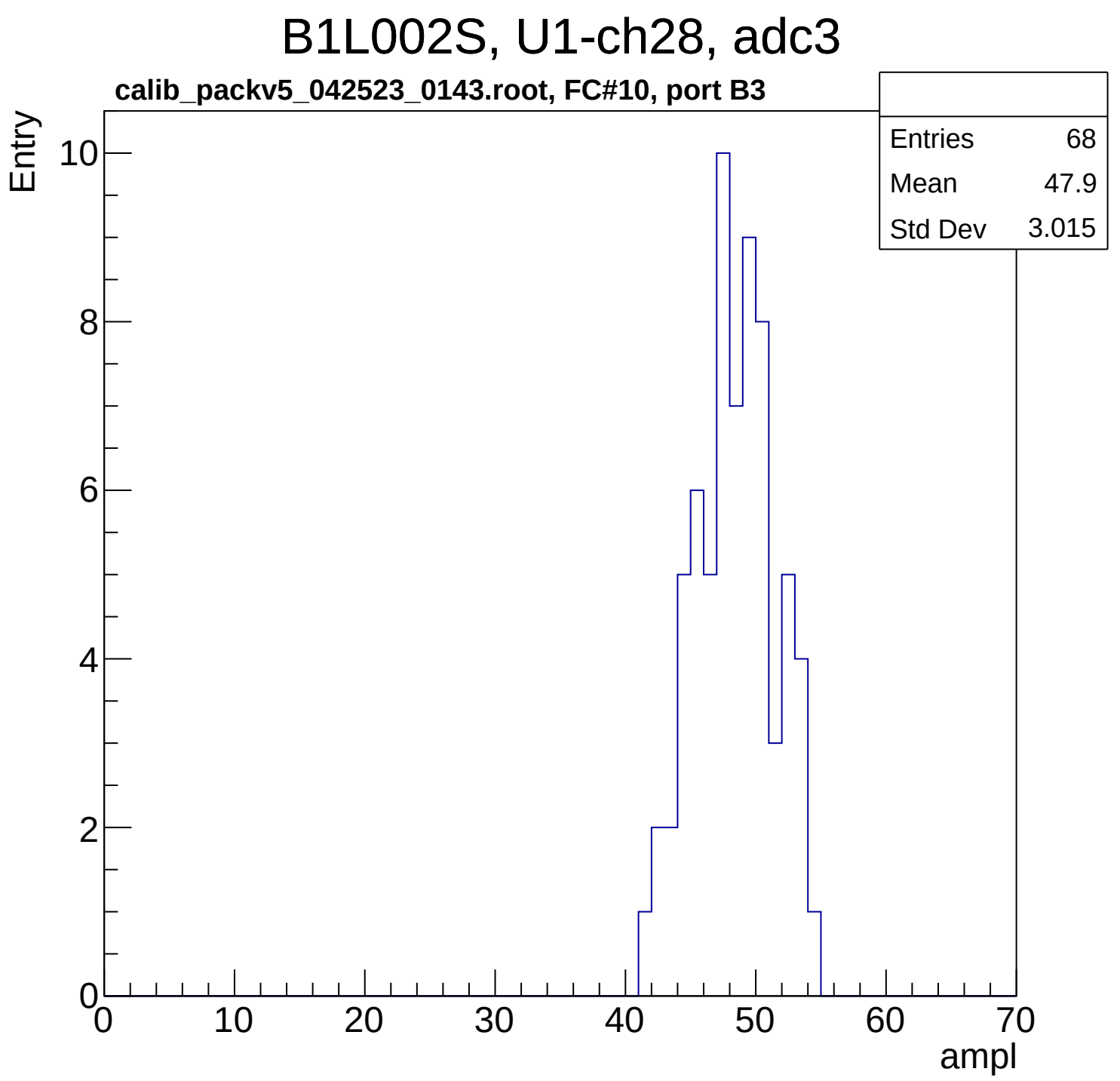
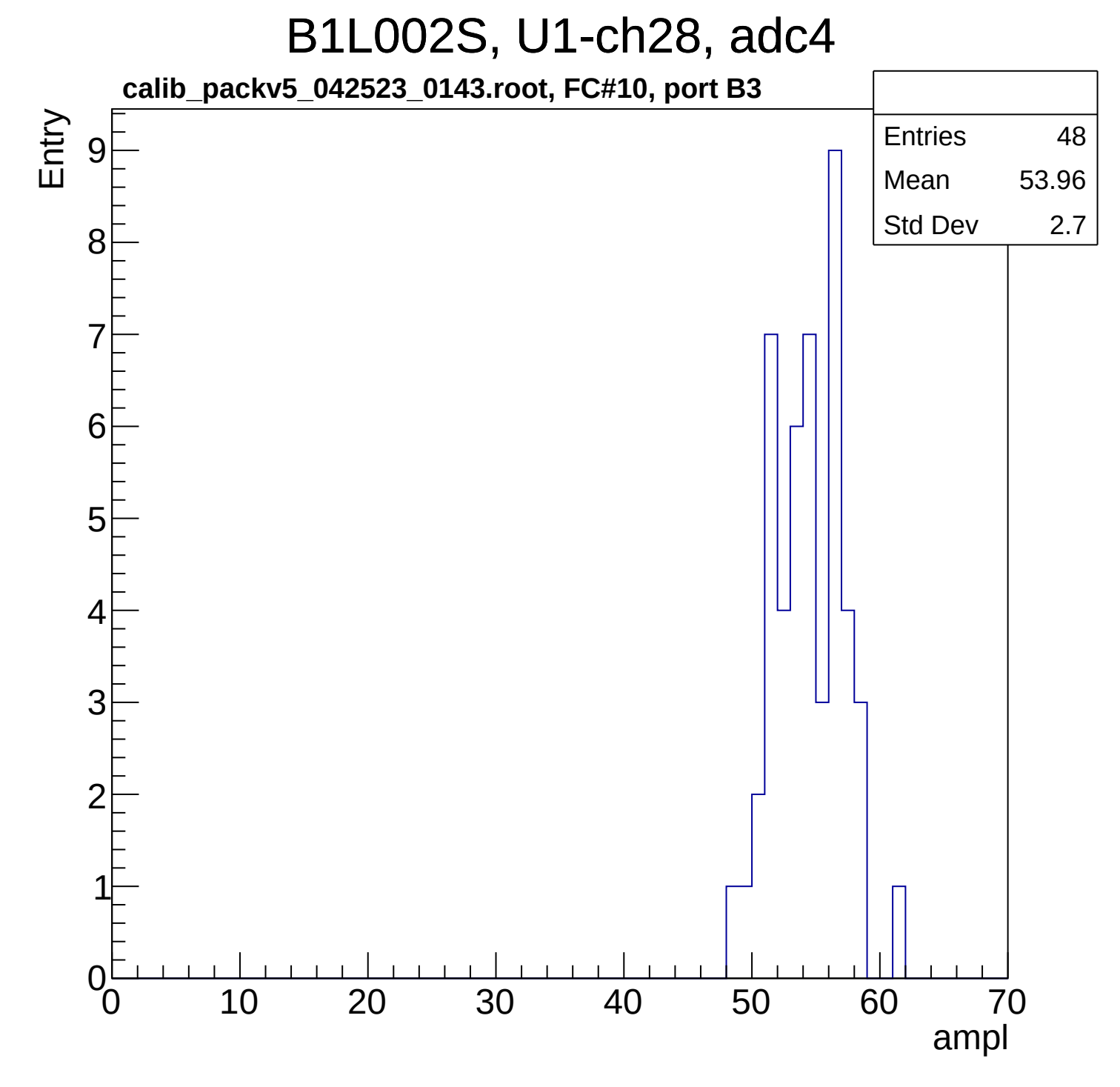

B1L002S, U1-ch28, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.24
Std Dev 3.12
Gaus mean : 42.0170
Gaus Width: 3.5309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch28, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.9
Std Dev 3.015
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch28, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.96
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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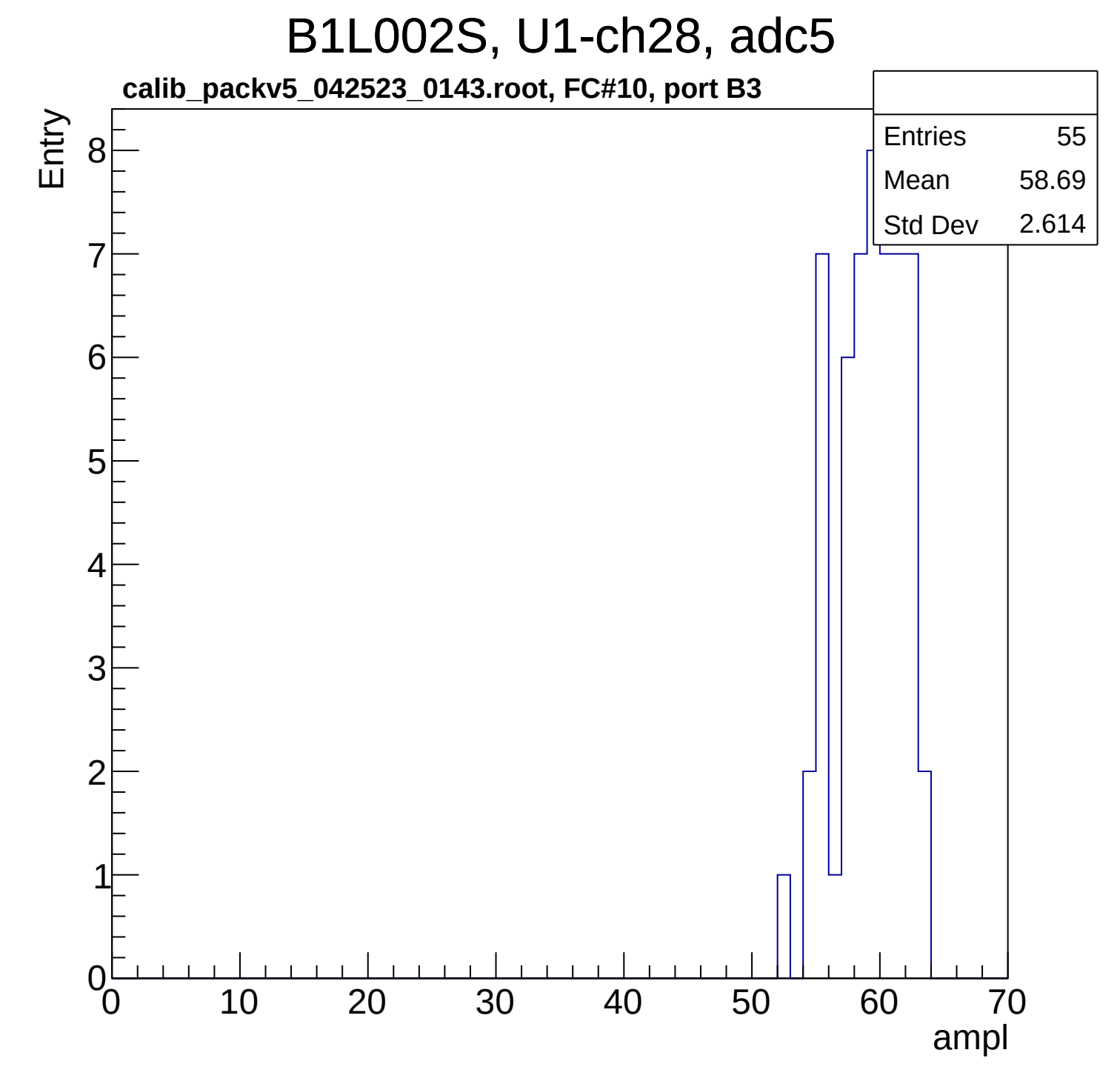
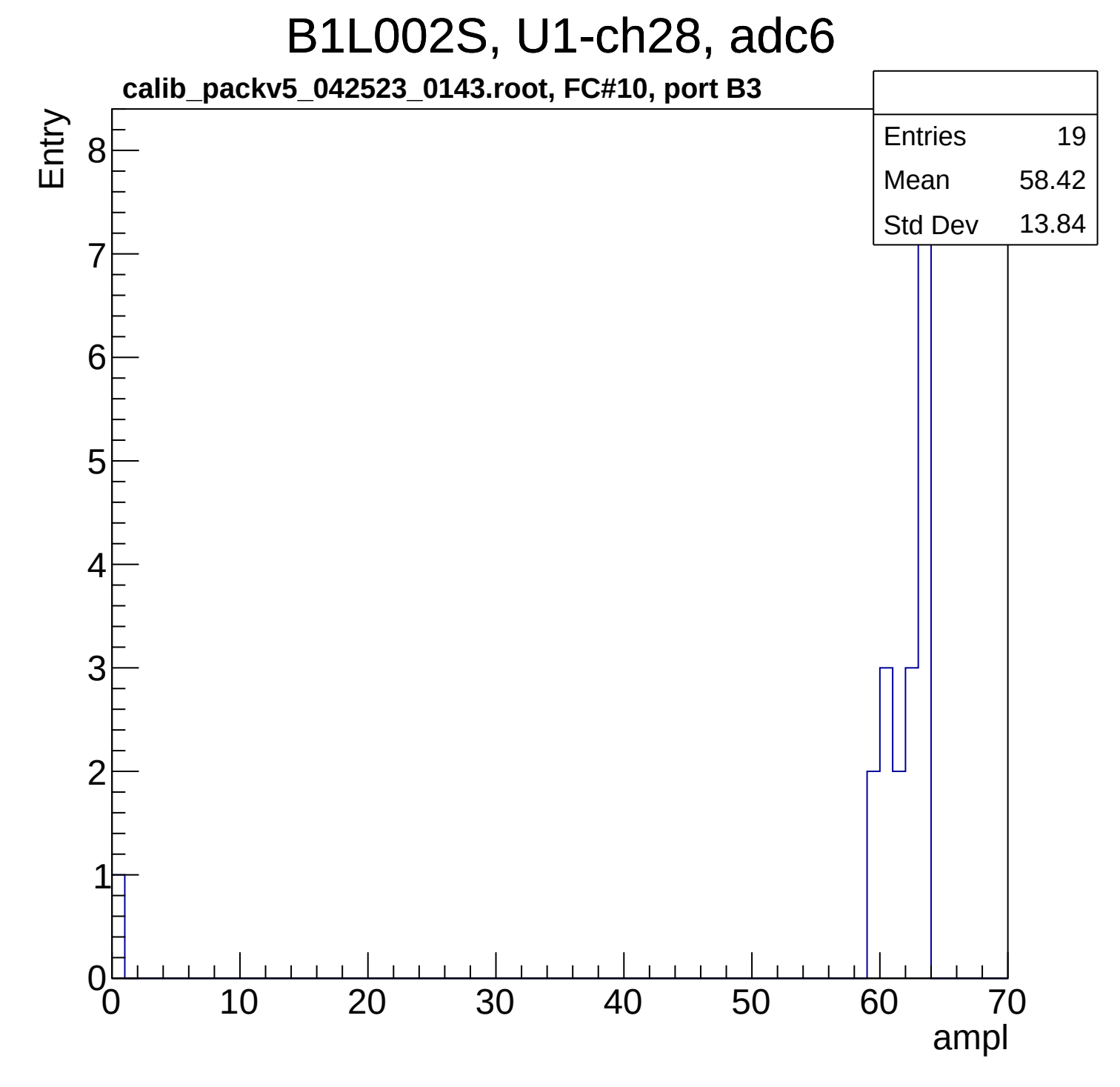

B1L002S, U1-ch28, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.69
Std Dev 2.614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch28, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.42
Std Dev 13.84
Entry
ampl
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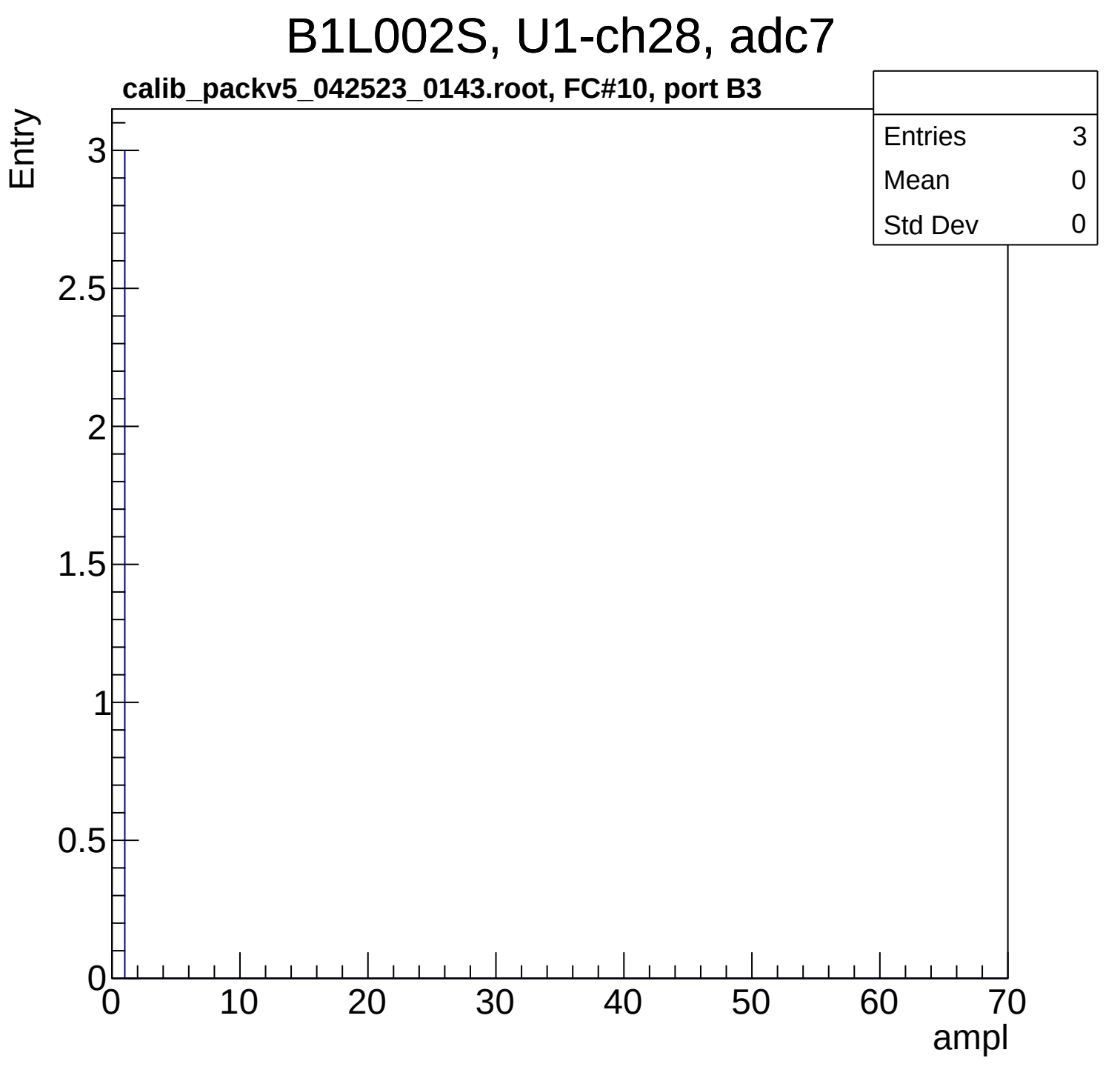
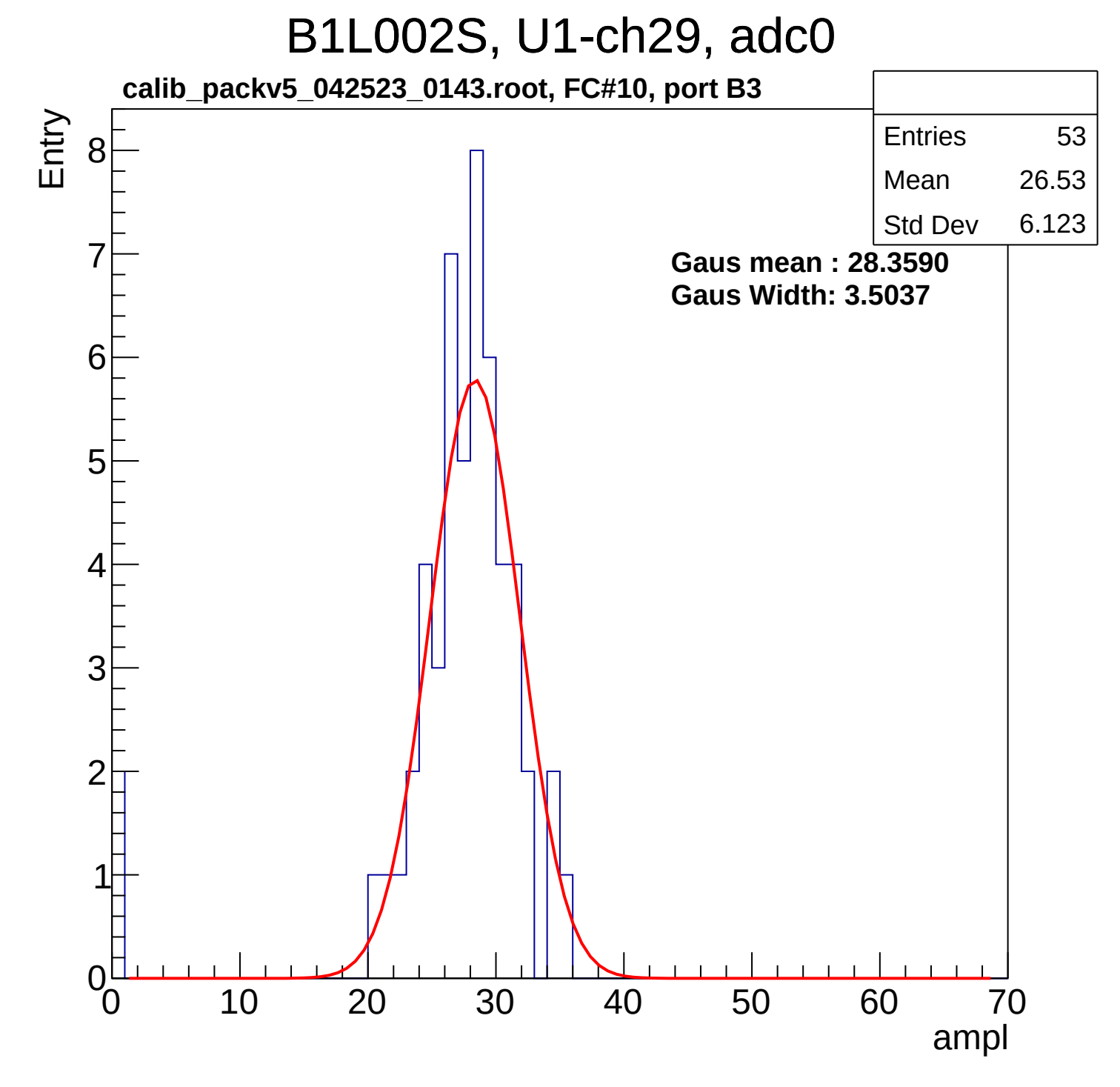
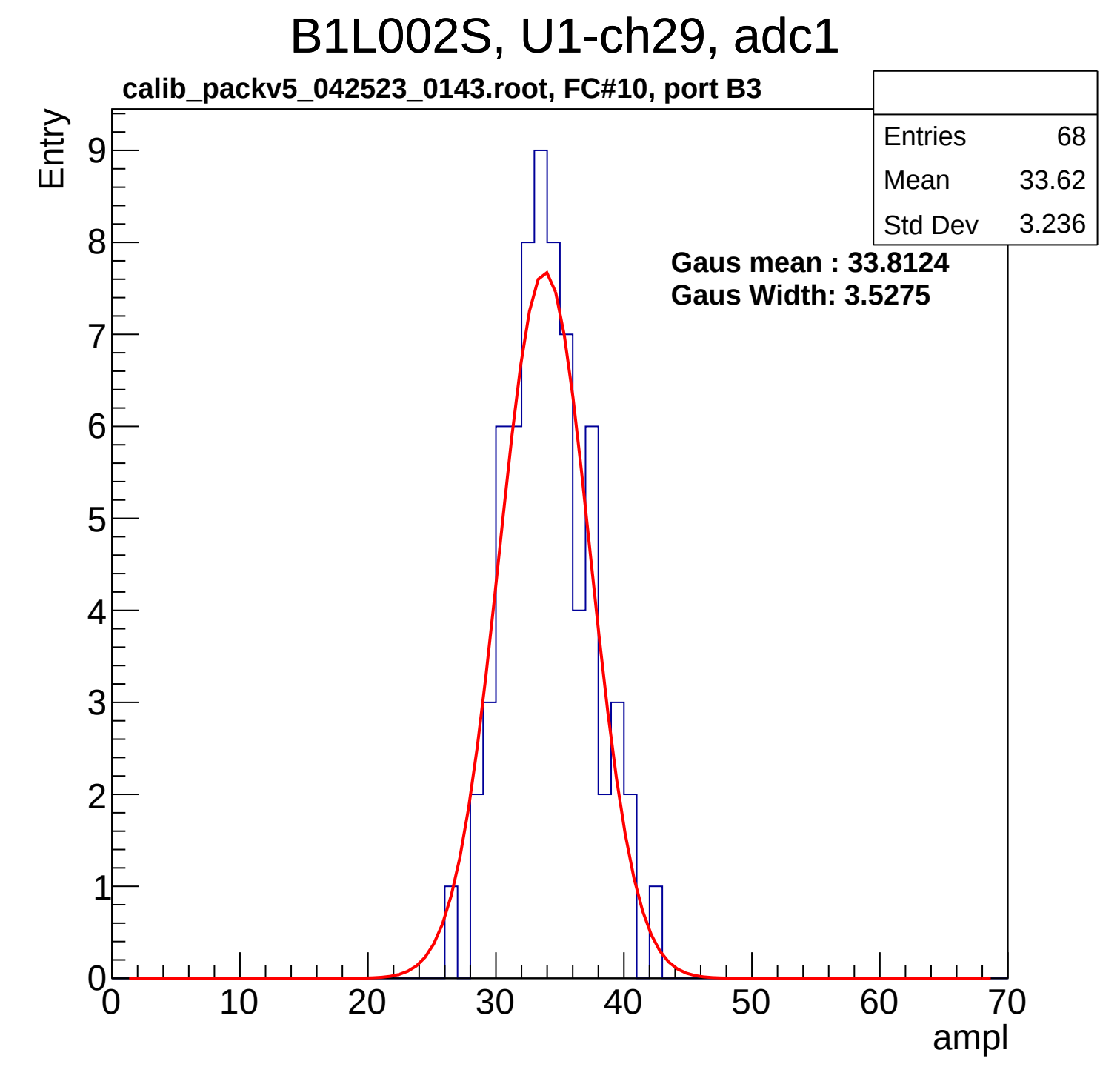
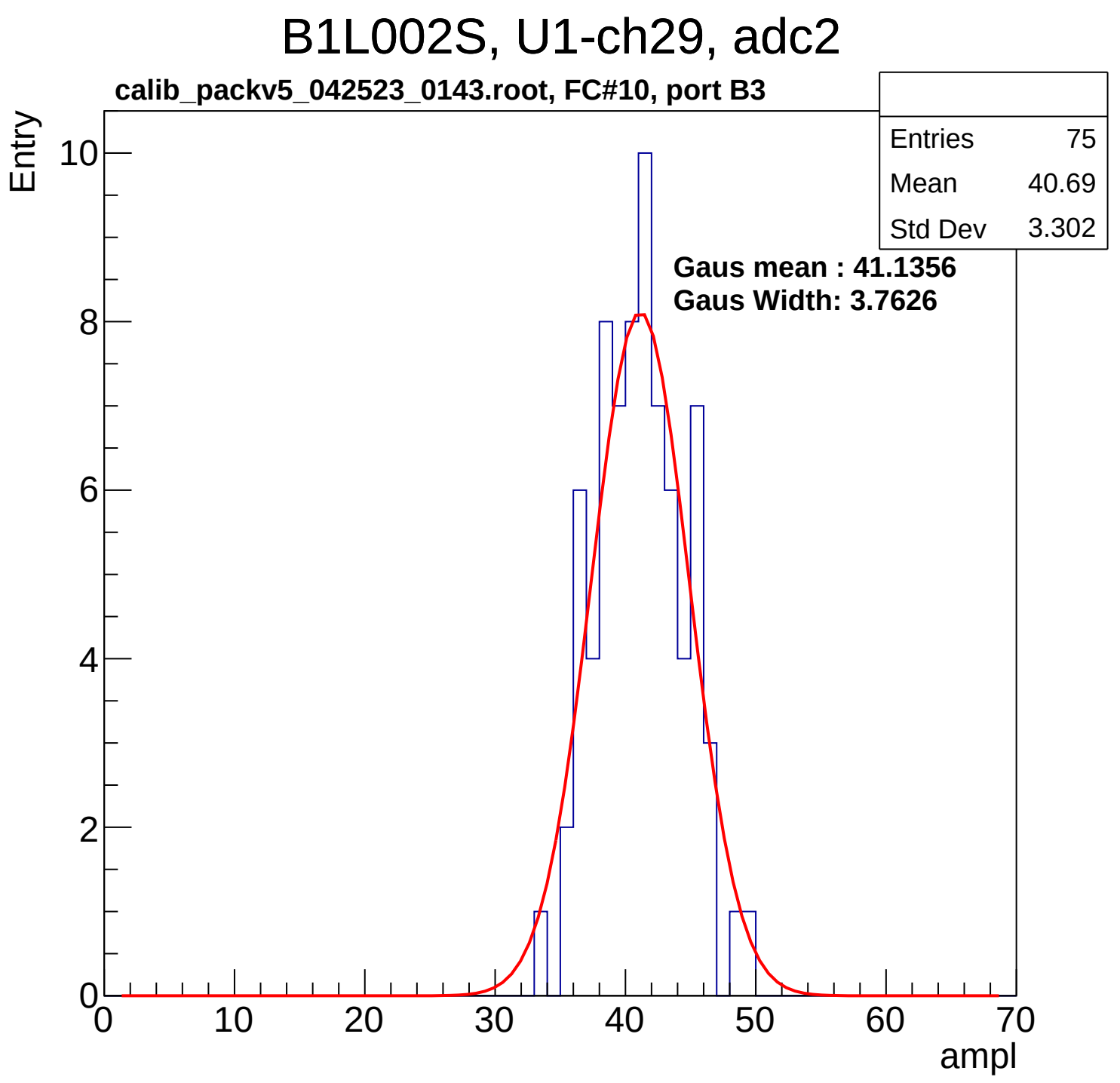
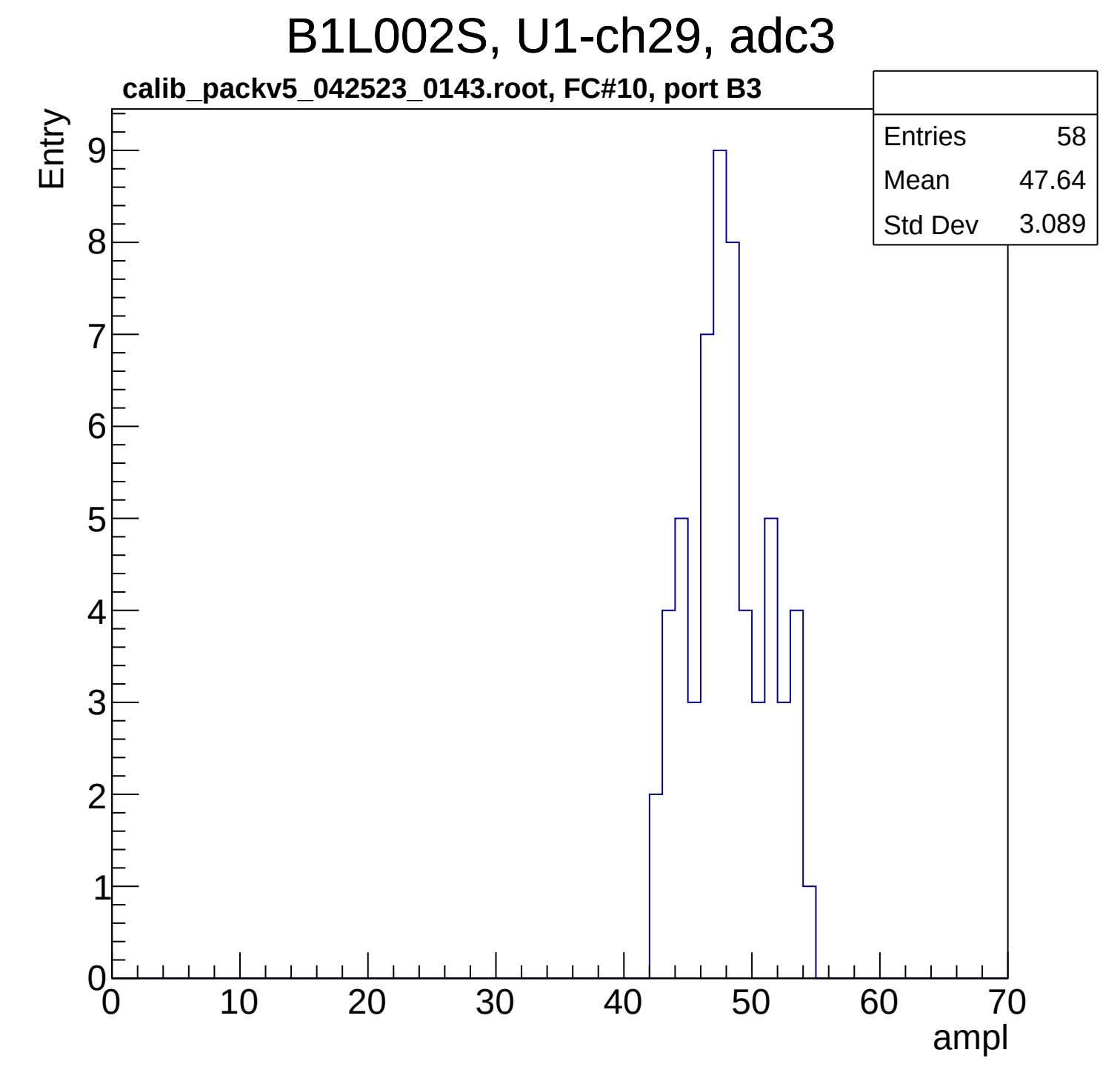
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch28, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch29, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 26.53
Std Dev 6.123
Gaus mean : 28.3590
Gaus Width: 3.5037
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch29, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.62
Std Dev 3.236
Gaus mean : 33.8124
Gaus Width: 3.5275
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch29, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 40.69
Std Dev 3.302
Gaus mean : 41.1356
Gaus Width: 3.7626
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch29, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.64
Std Dev 3.089
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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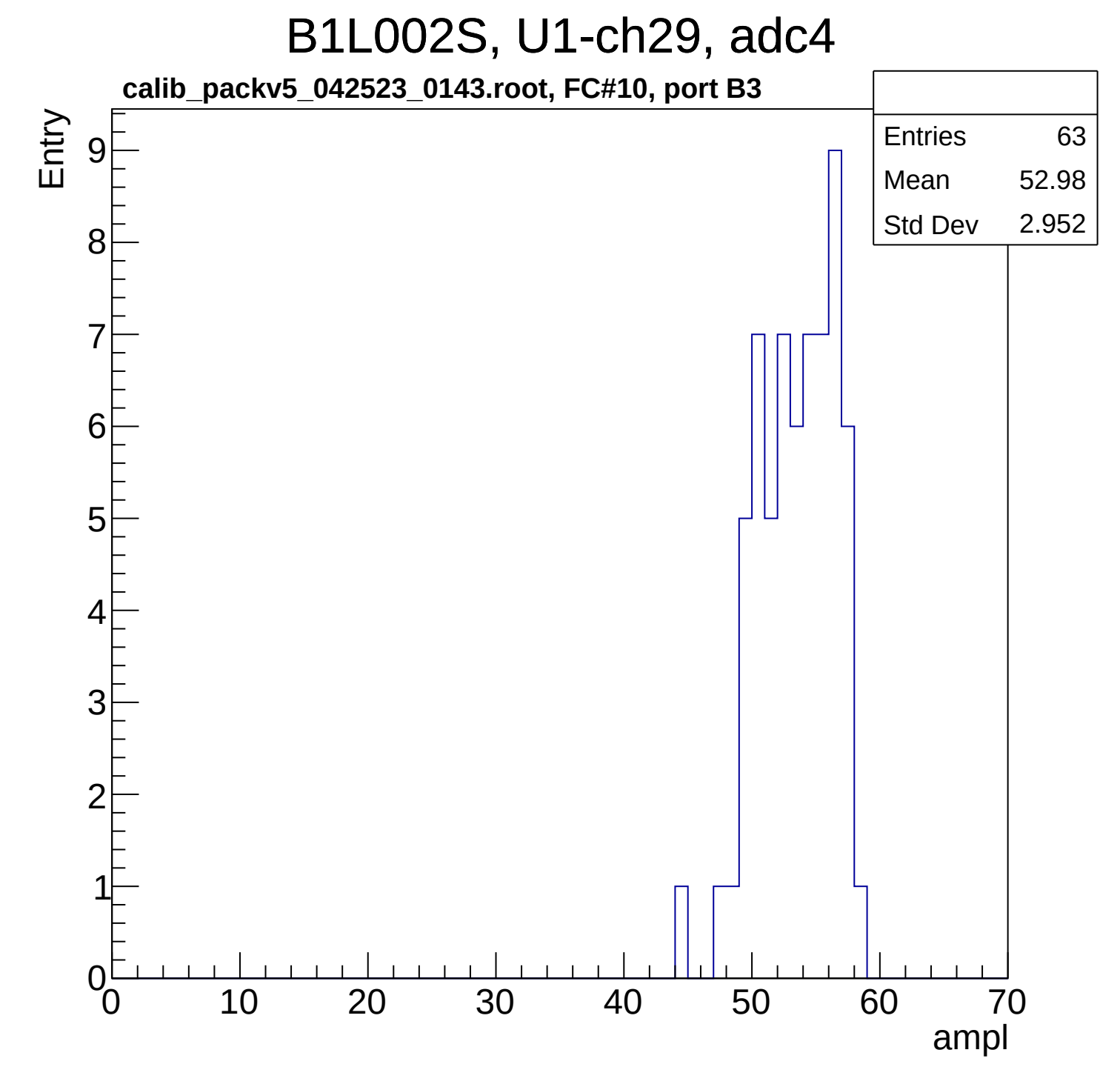
B1L002S, U1-ch29, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 52.98
Std Dev 2.952
Entry
ampl
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2
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4
5
6
7
8
9
0
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60
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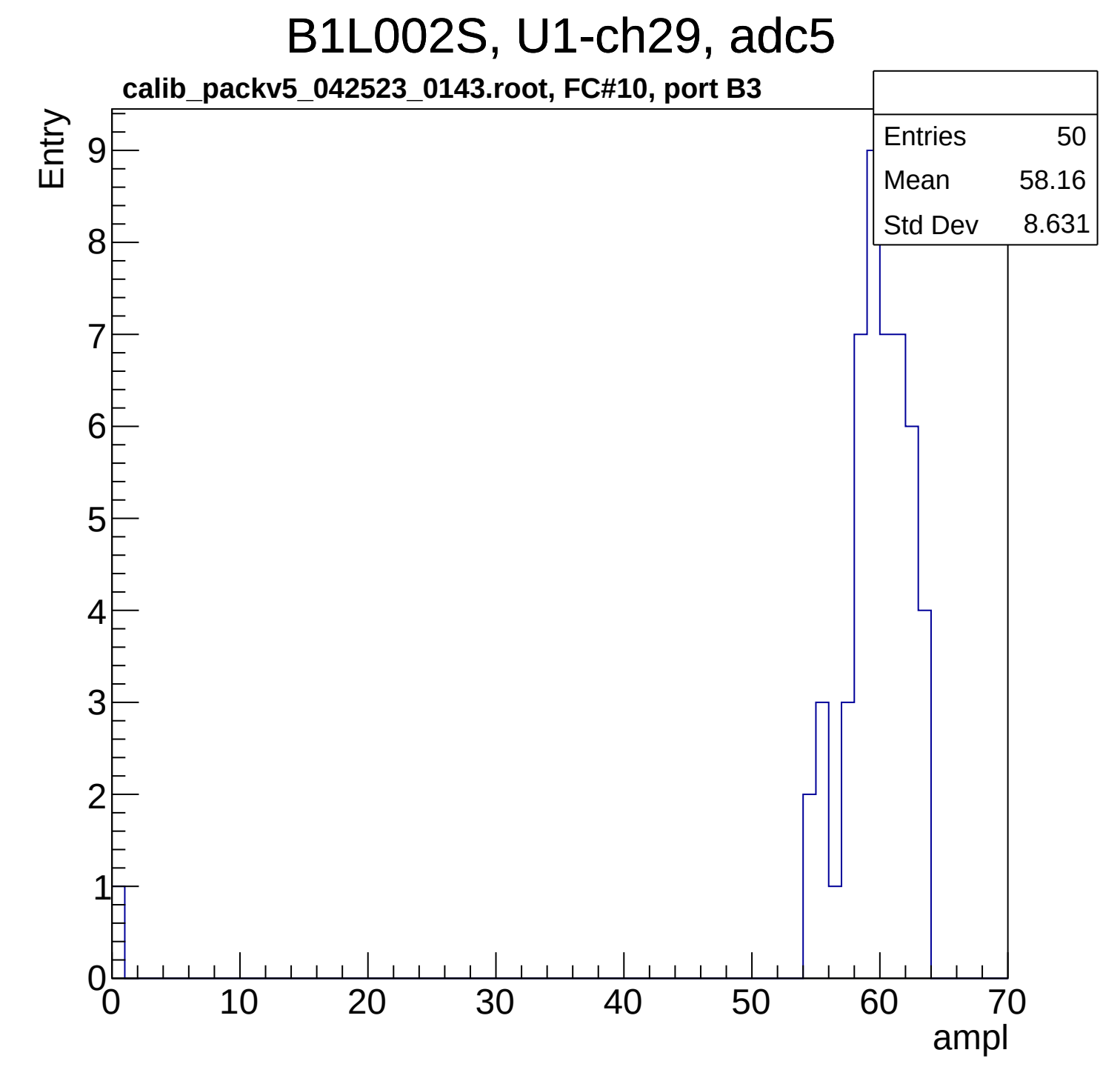
B1L002S, U1-ch29, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.16
Std Dev 8.631
Entry
ampl
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2
3
4
5
6
7
8
9
0
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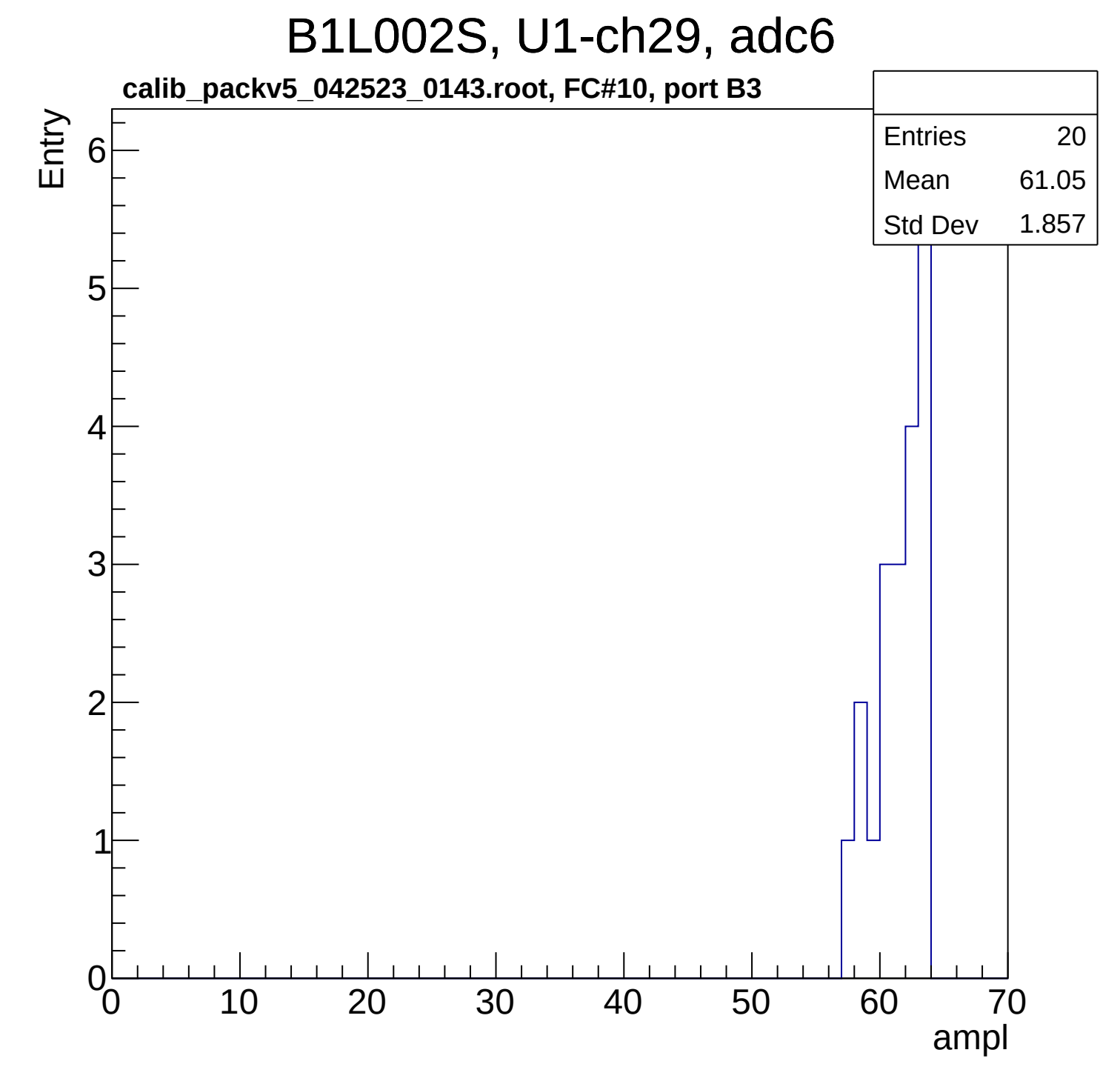
20
30
40
50
60
70


B1L002S, U1-ch29, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.05
Std Dev 1.857
Entry
0
1
2
3
4
5
6
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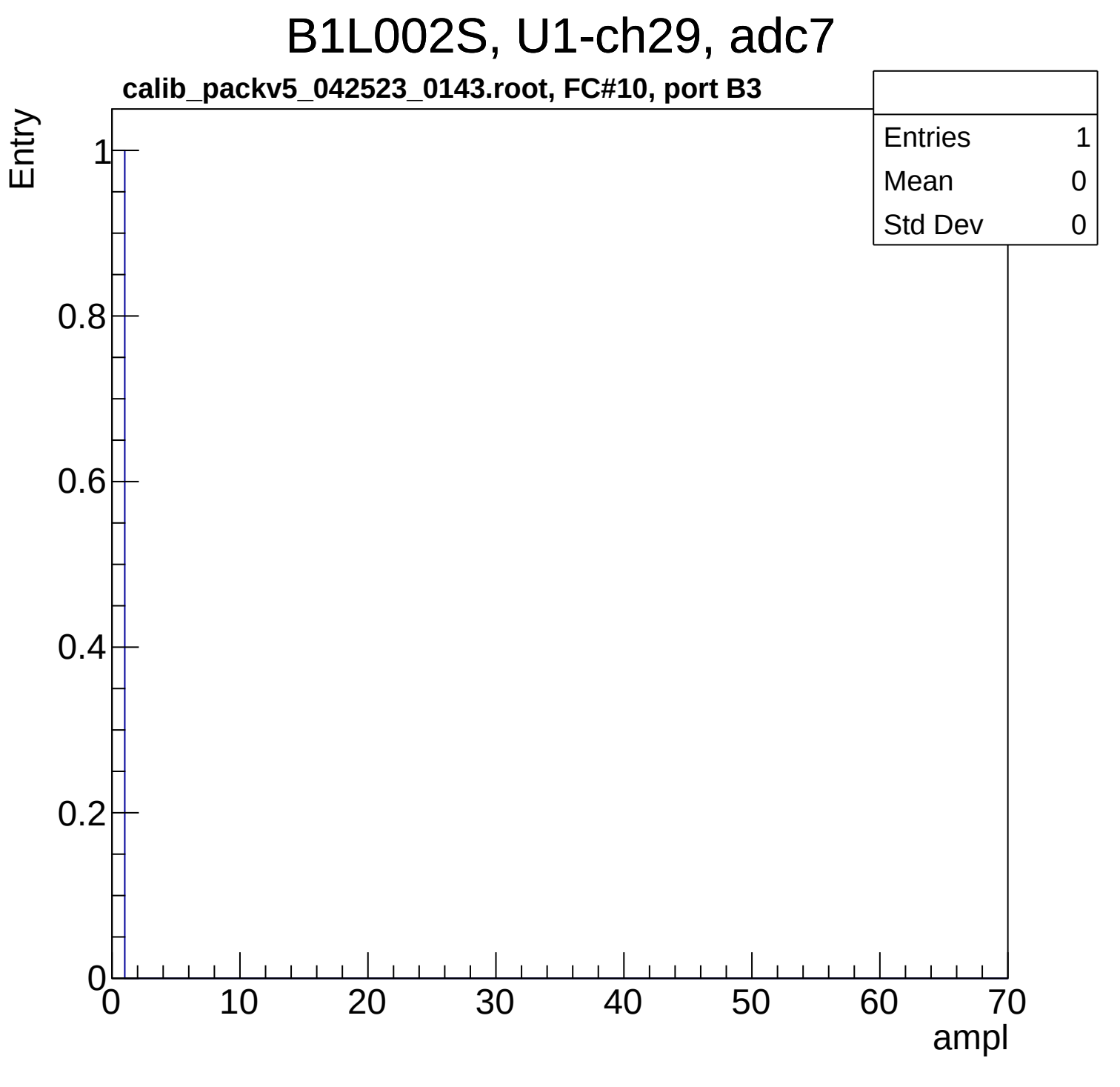
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch29, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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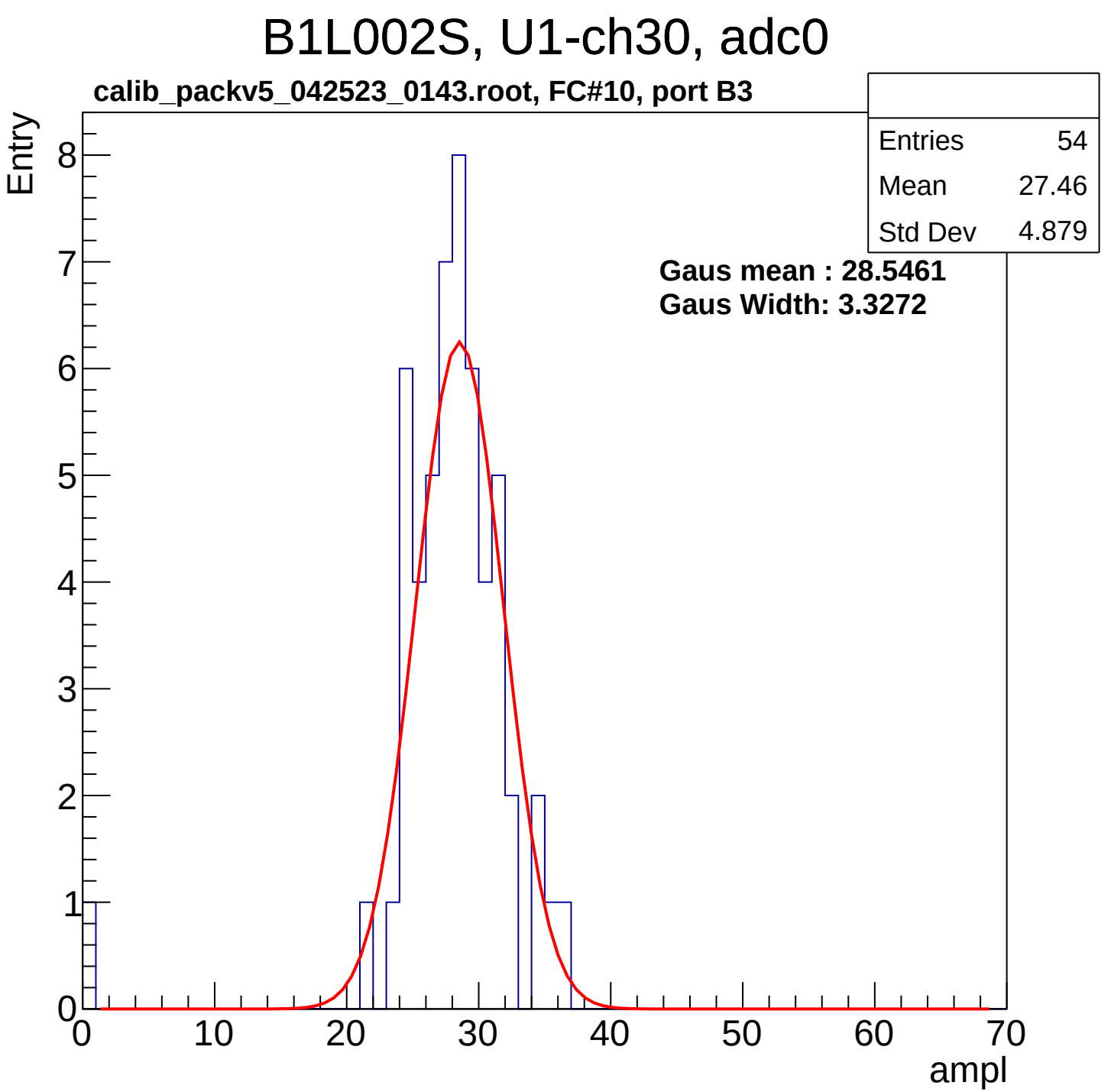
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch30, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.46
Std Dev 4.879
Gaus mean : 28.5461
Gaus Width: 3.3272
Entry
ampl
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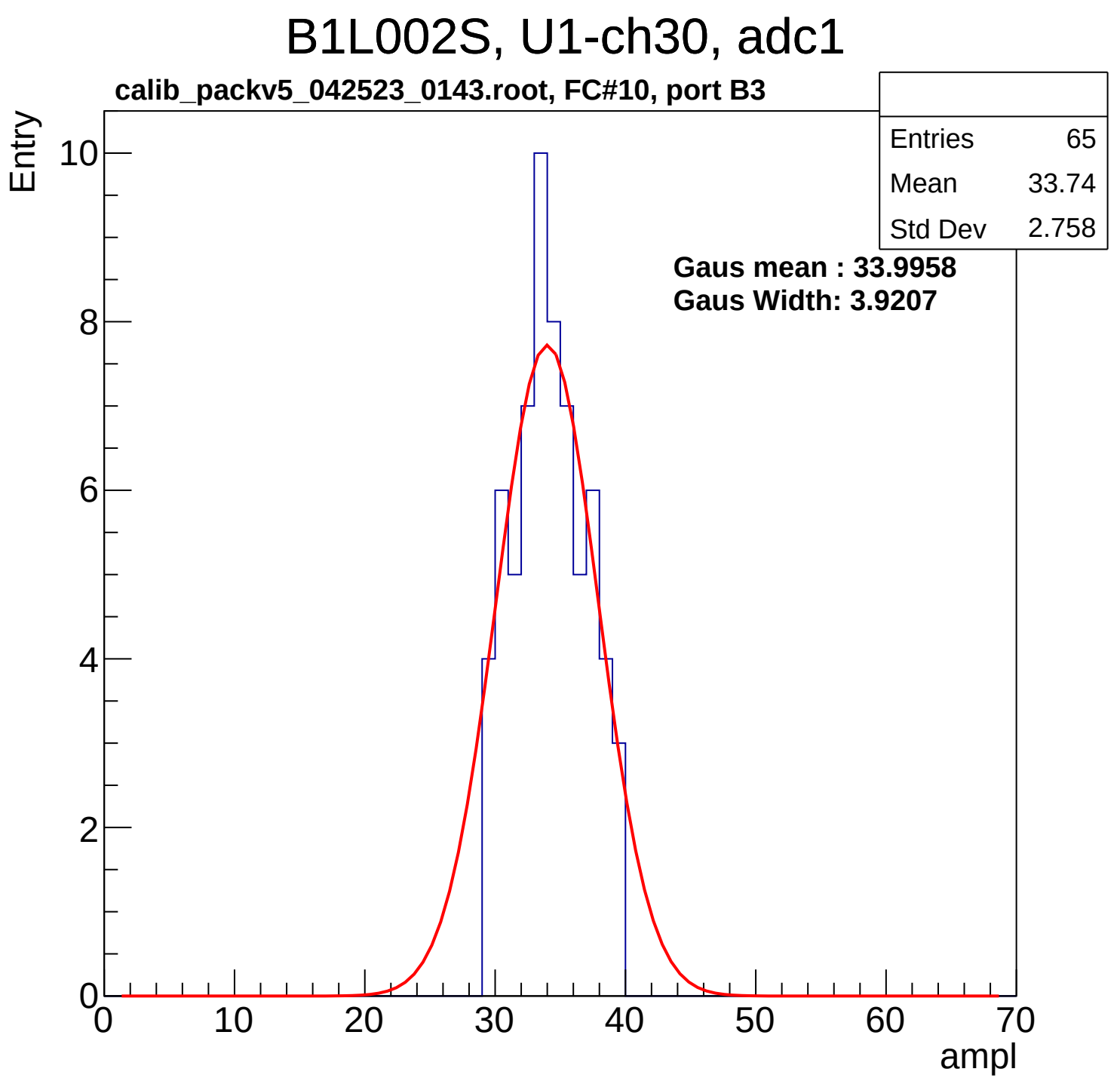
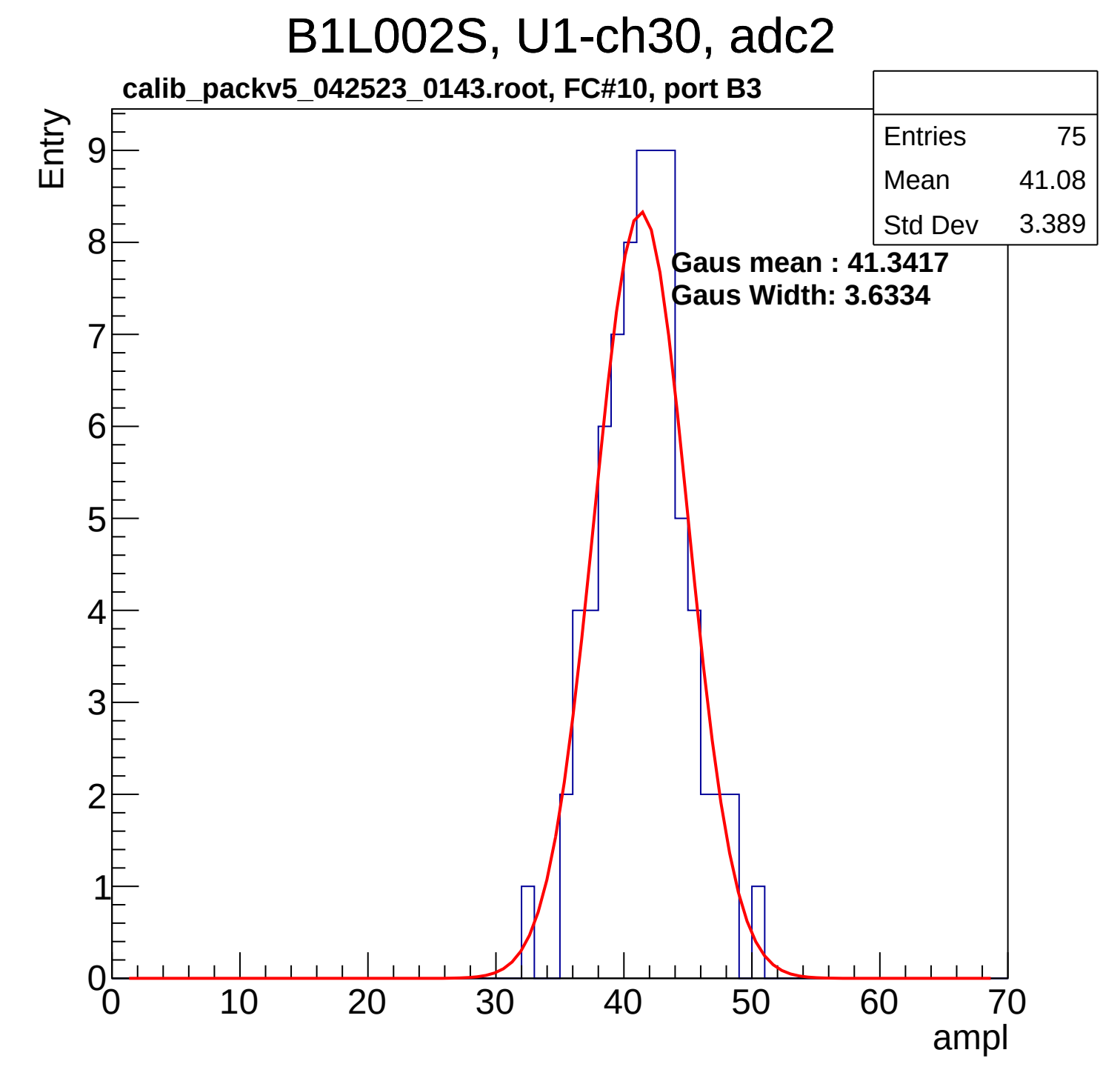
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch30, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.74
Std Dev 2.758
Gaus mean : 33.9958
Gaus Width: 3.9207
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch30, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 41.08
Std Dev 3.389
Gaus mean : 41.3417
Gaus Width: 3.6334
Entry
ampl
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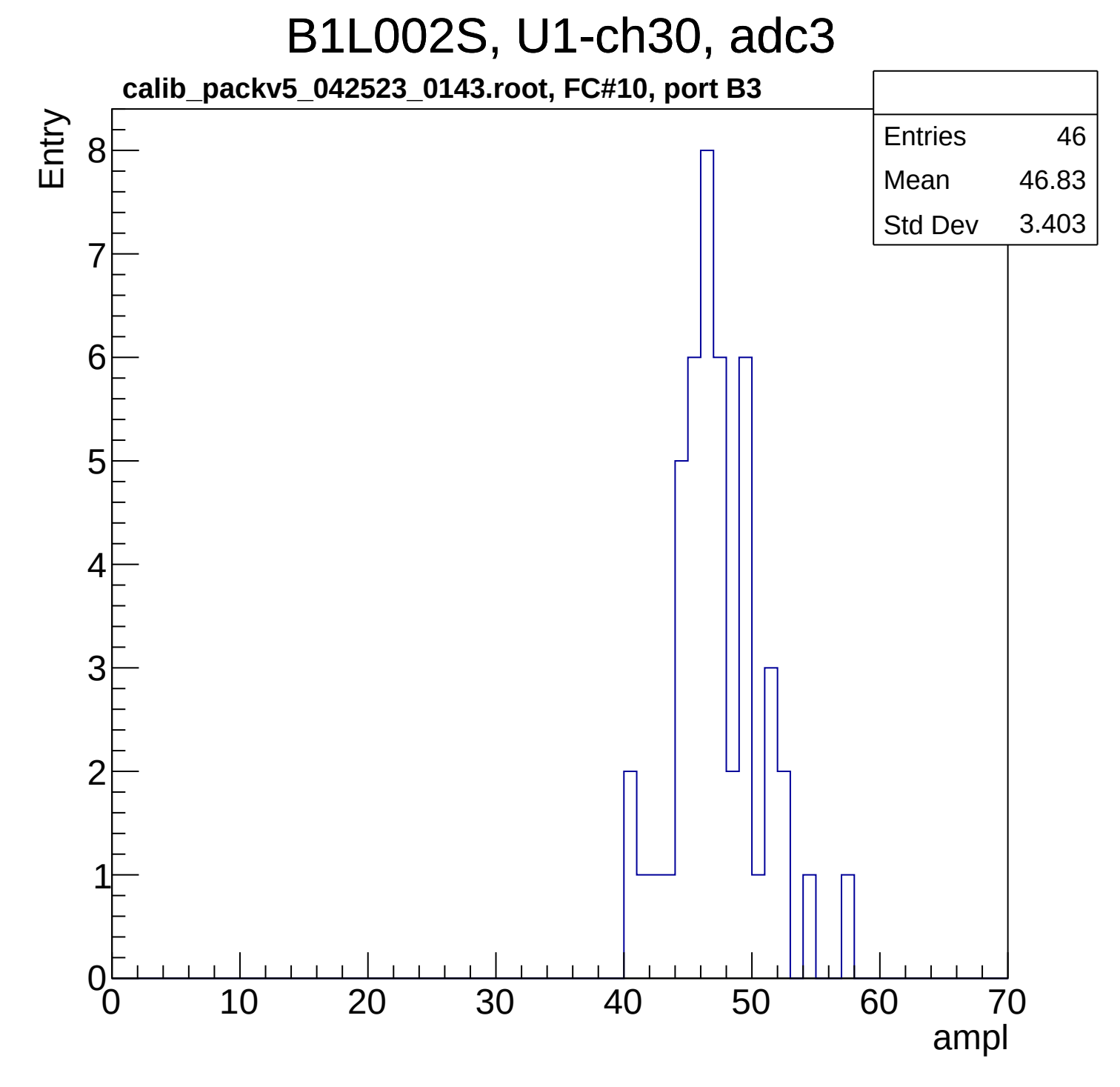
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch30, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 46.83
Std Dev 3.403
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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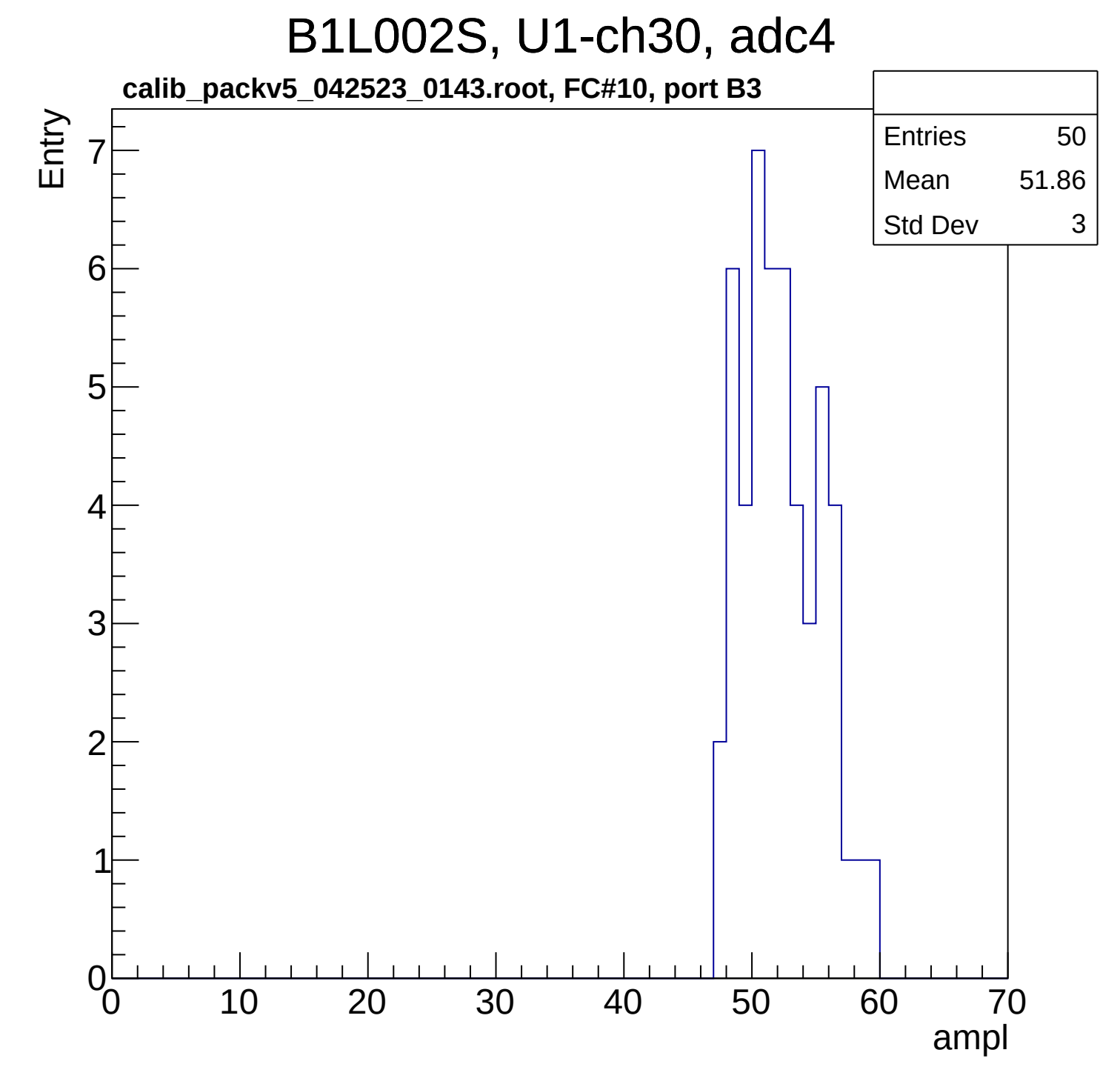
B1L002S, U1-ch30, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.86
Std Dev 3
Entry
ampl
0
1
2
3
4
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0
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60
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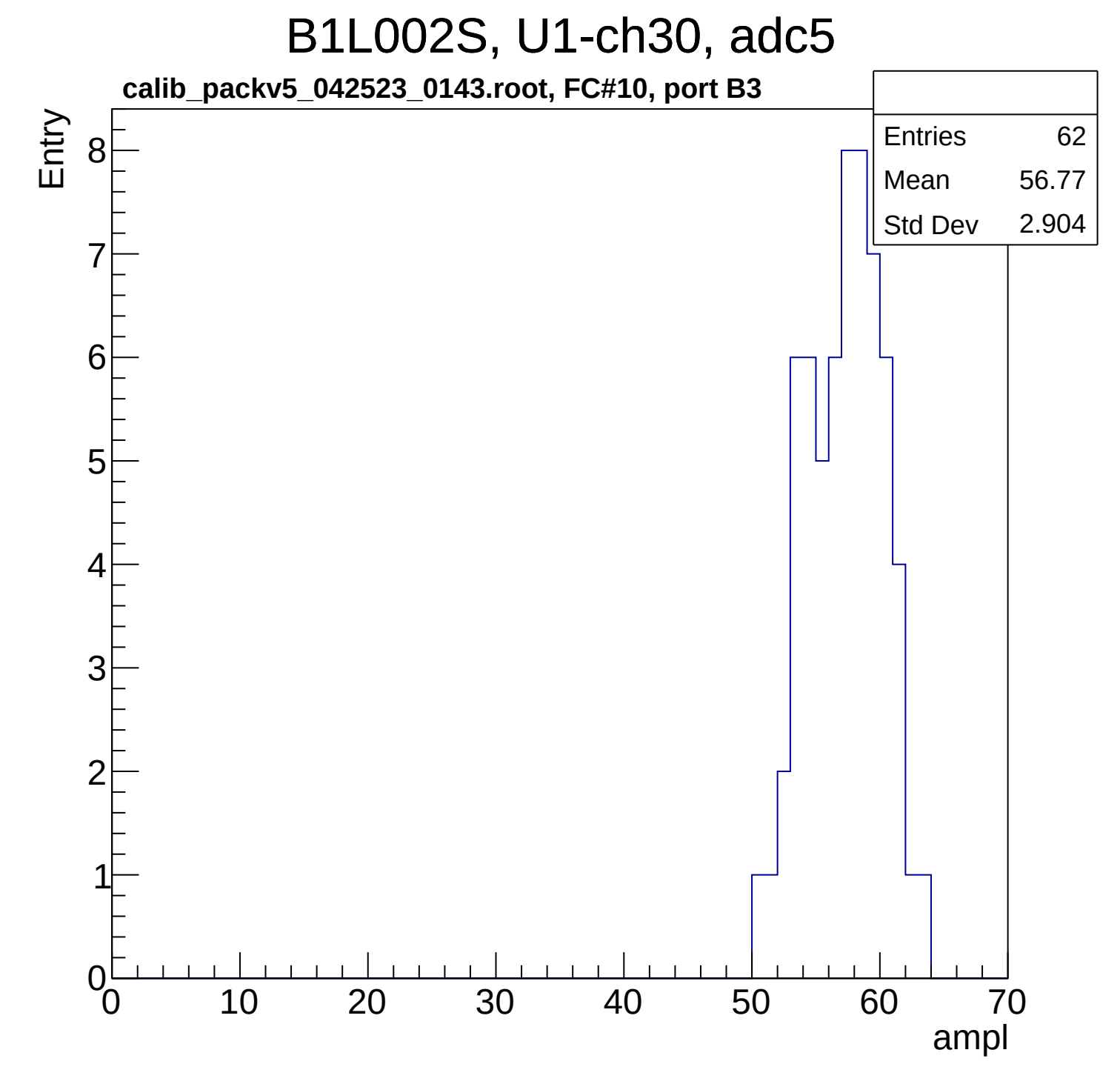
B1L002S, U1-ch30, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 56.77
Std Dev 2.904
Entry
ampl
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1
2
3
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5
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60
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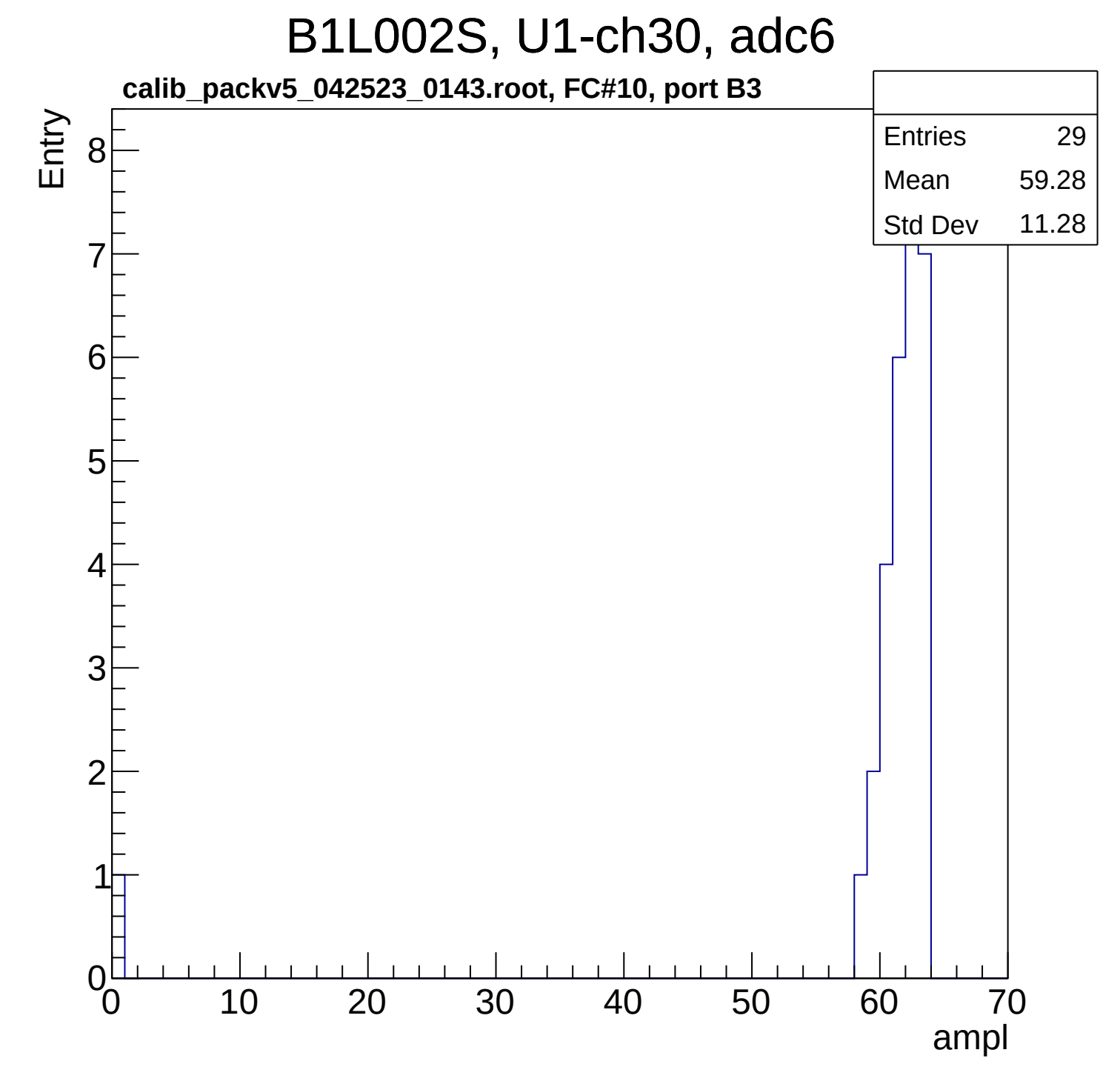
B1L002S, U1-ch30, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.28
Std Dev 11.28
Entry
ampl
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1
2
3
4
5
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7
8
0
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20
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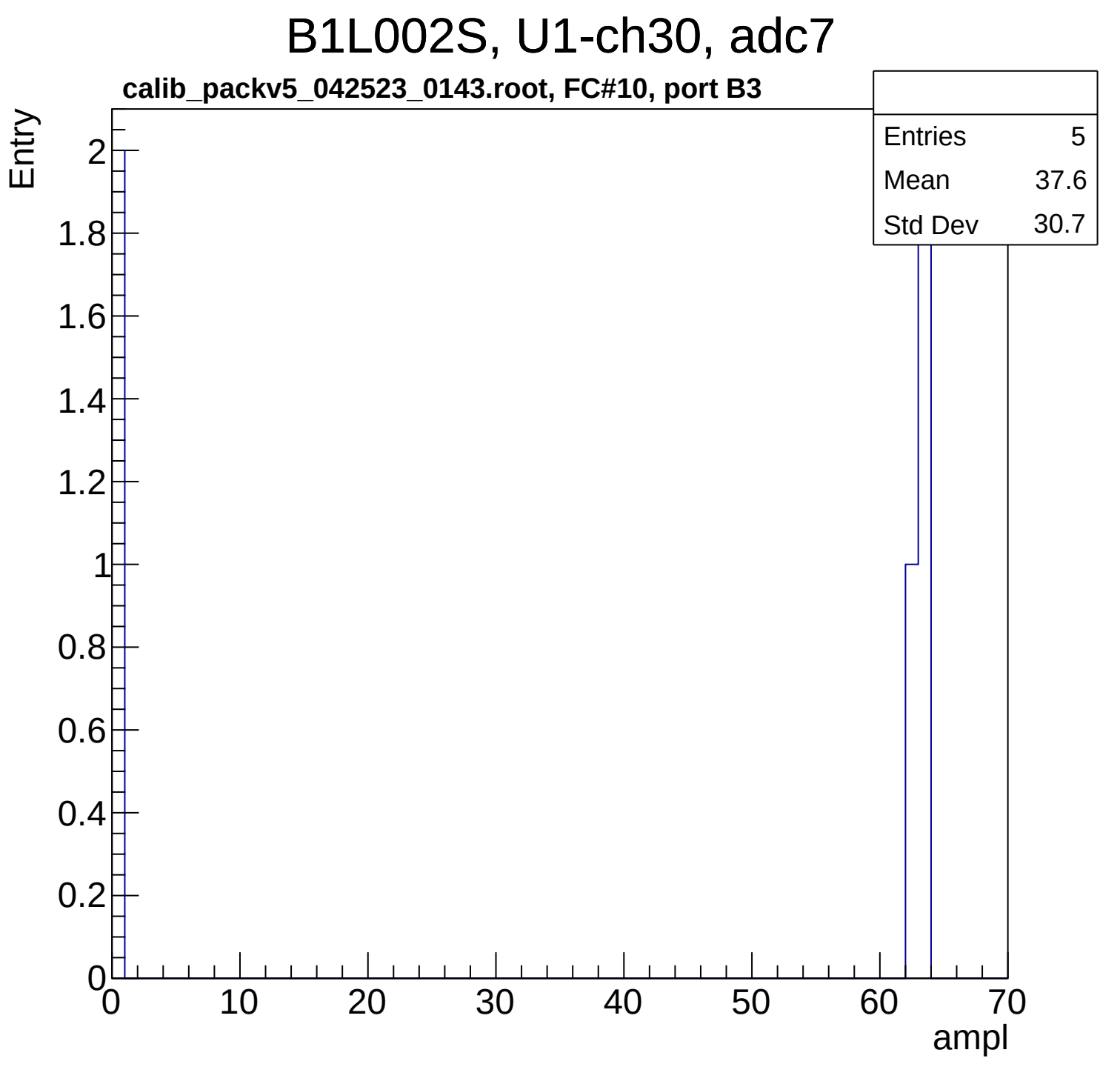
40
50
60
70


B1L002S, U1-ch30, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.6
Std Dev 30.7
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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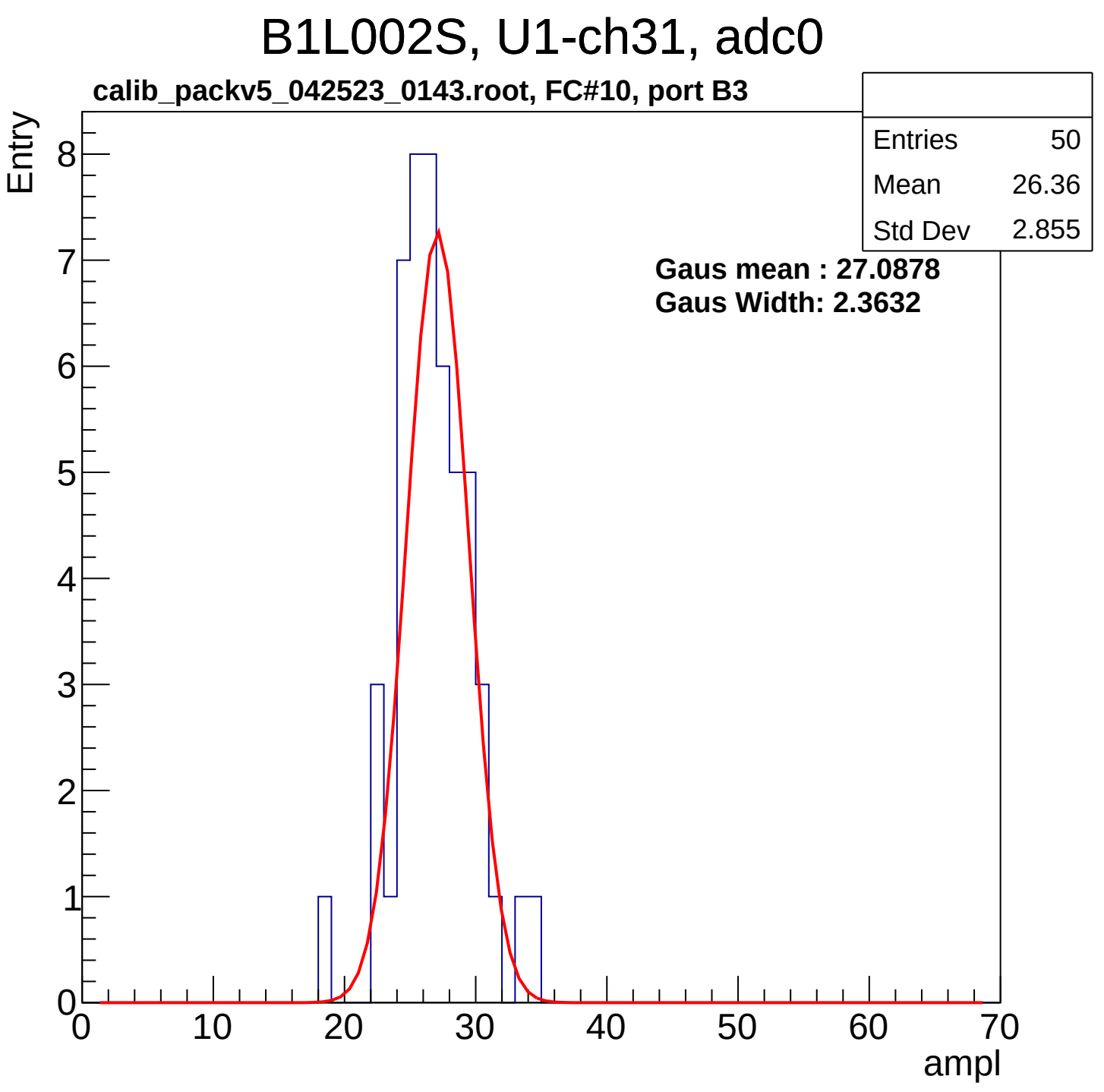
B1L002S, U1-ch31, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 26.36
Std Dev 2.855
Gaus mean : 27.0878
Gaus Width: 2.3632
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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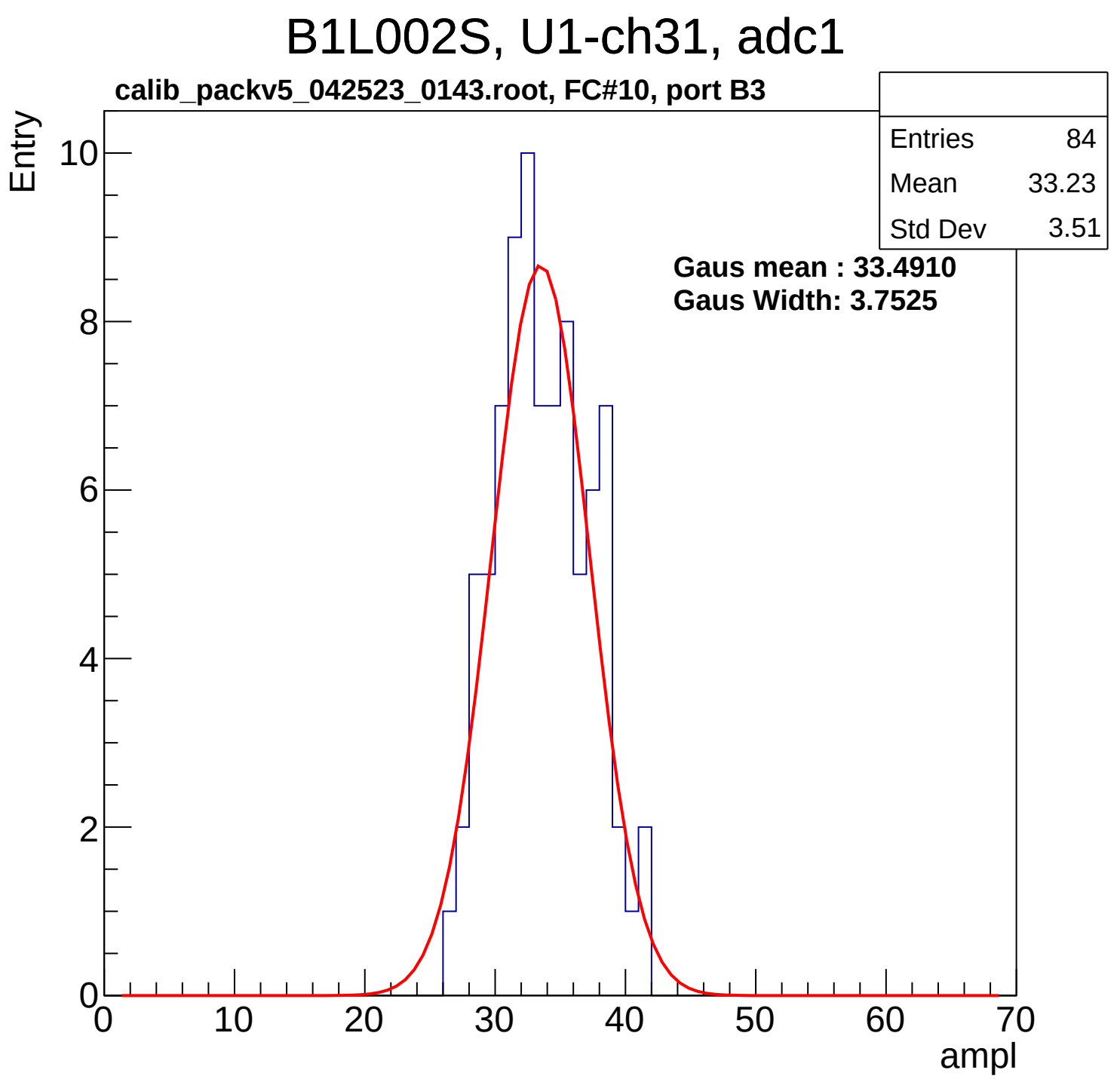
B1L002S, U1-ch31, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 33.23
Std Dev 3.51
Gaus mean : 33.4910
Gaus Width: 3.7525
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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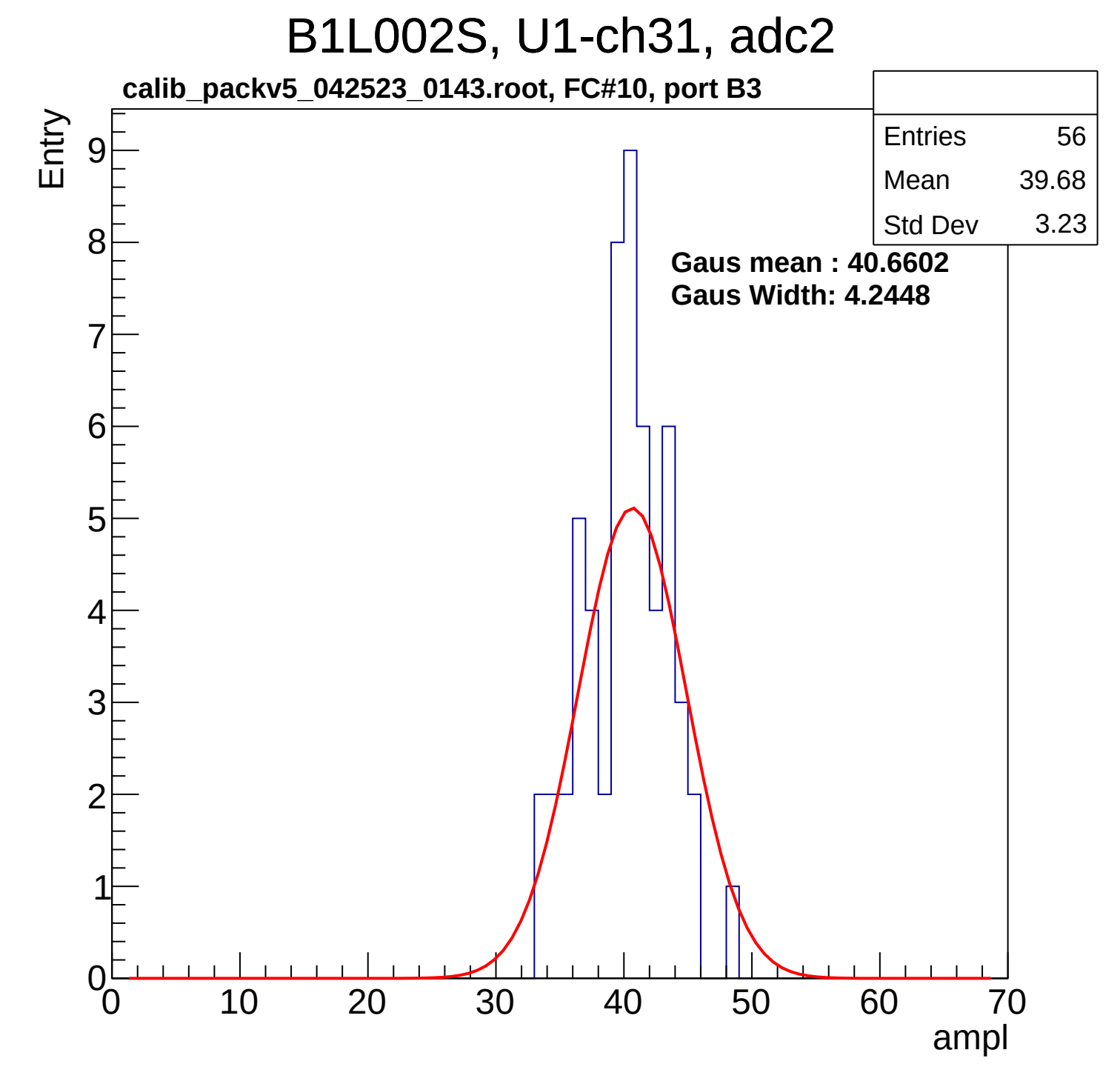
B1L002S, U1-ch31, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 39.68
Std Dev 3.23
Gaus mean : 40.6602
Gaus Width: 4.2448
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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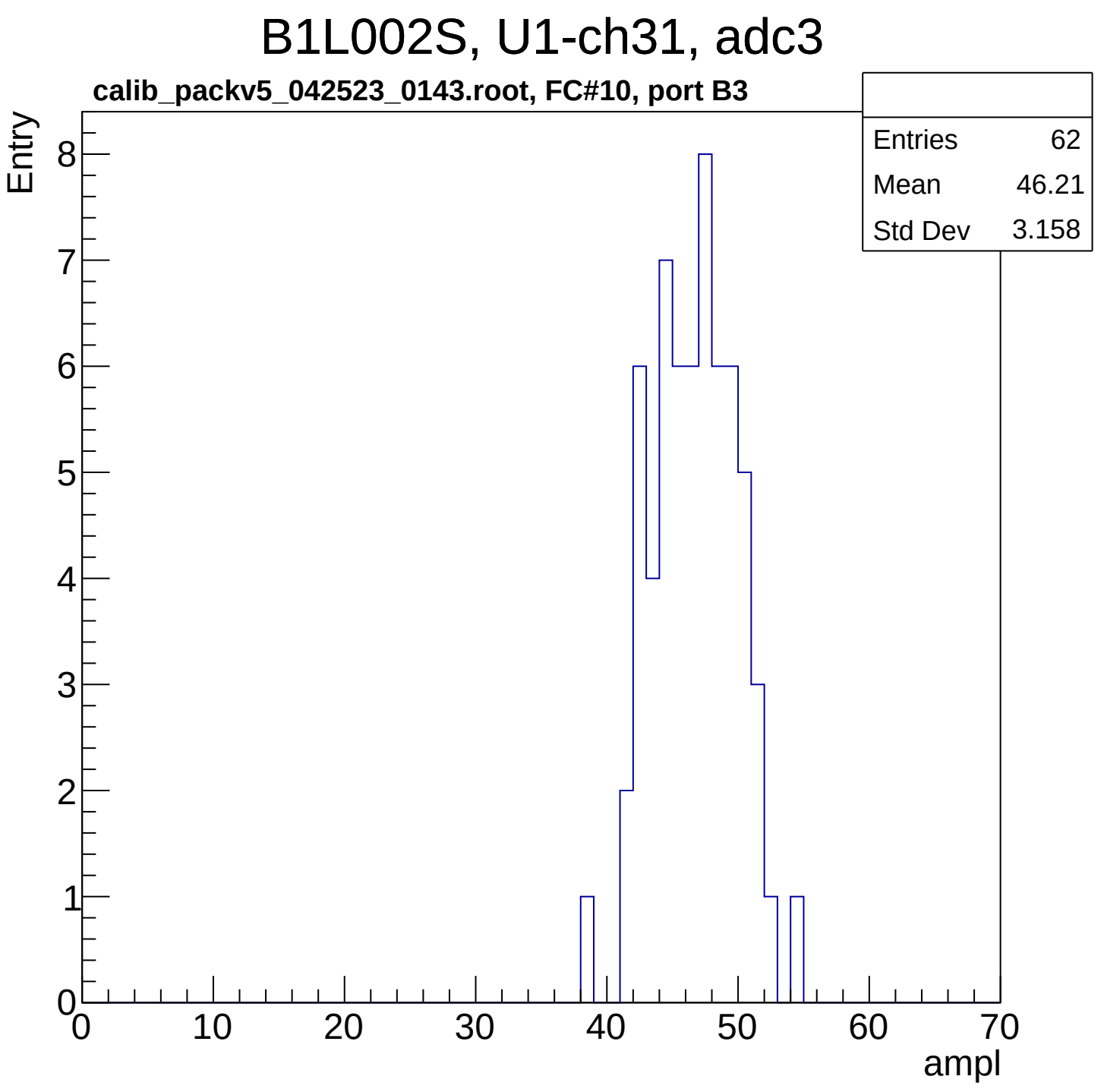
B1L002S, U1-ch31, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.21
Std Dev 3.158
Entry
ampl
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1
2
3
4
5
6
7
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0
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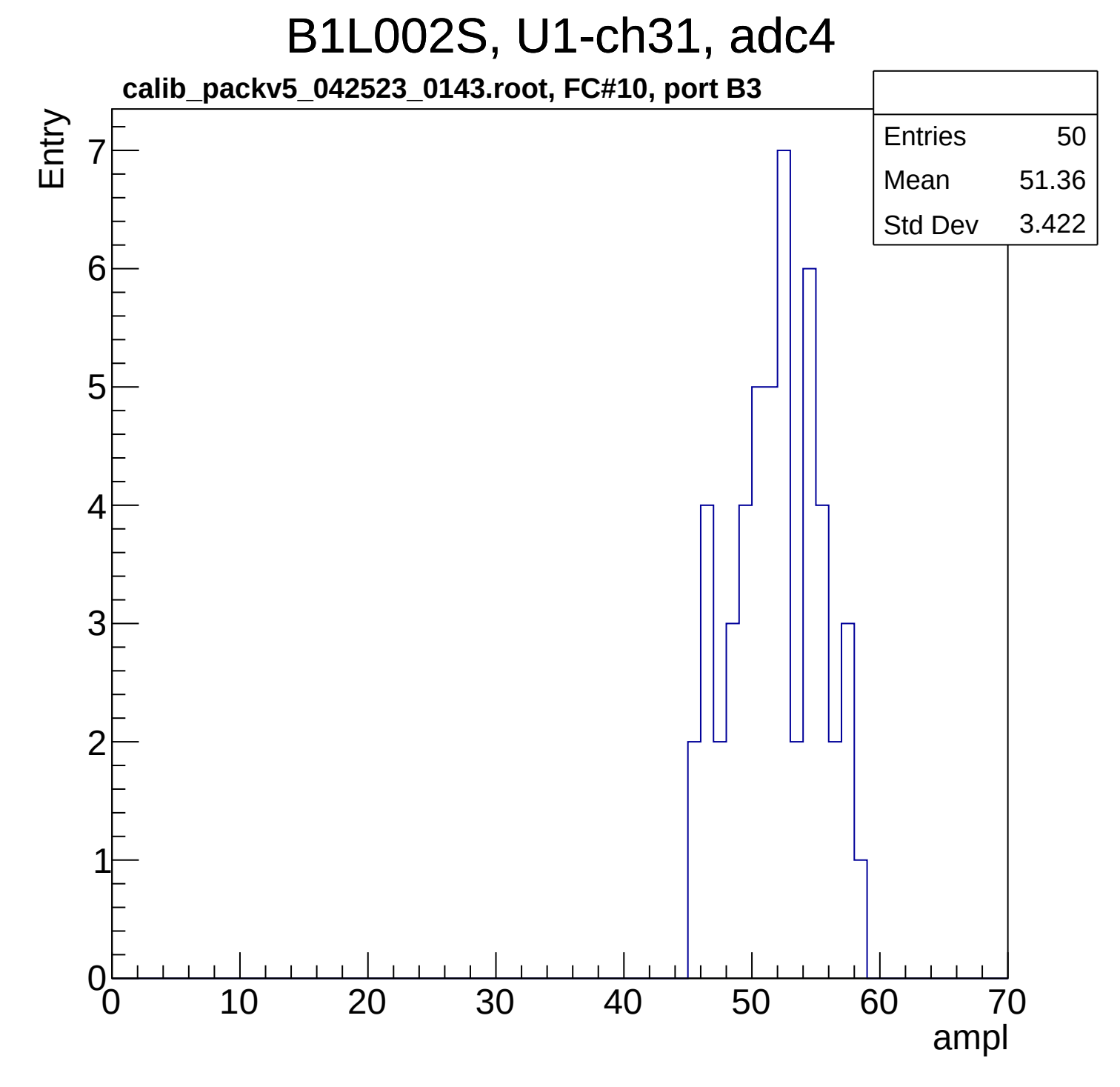
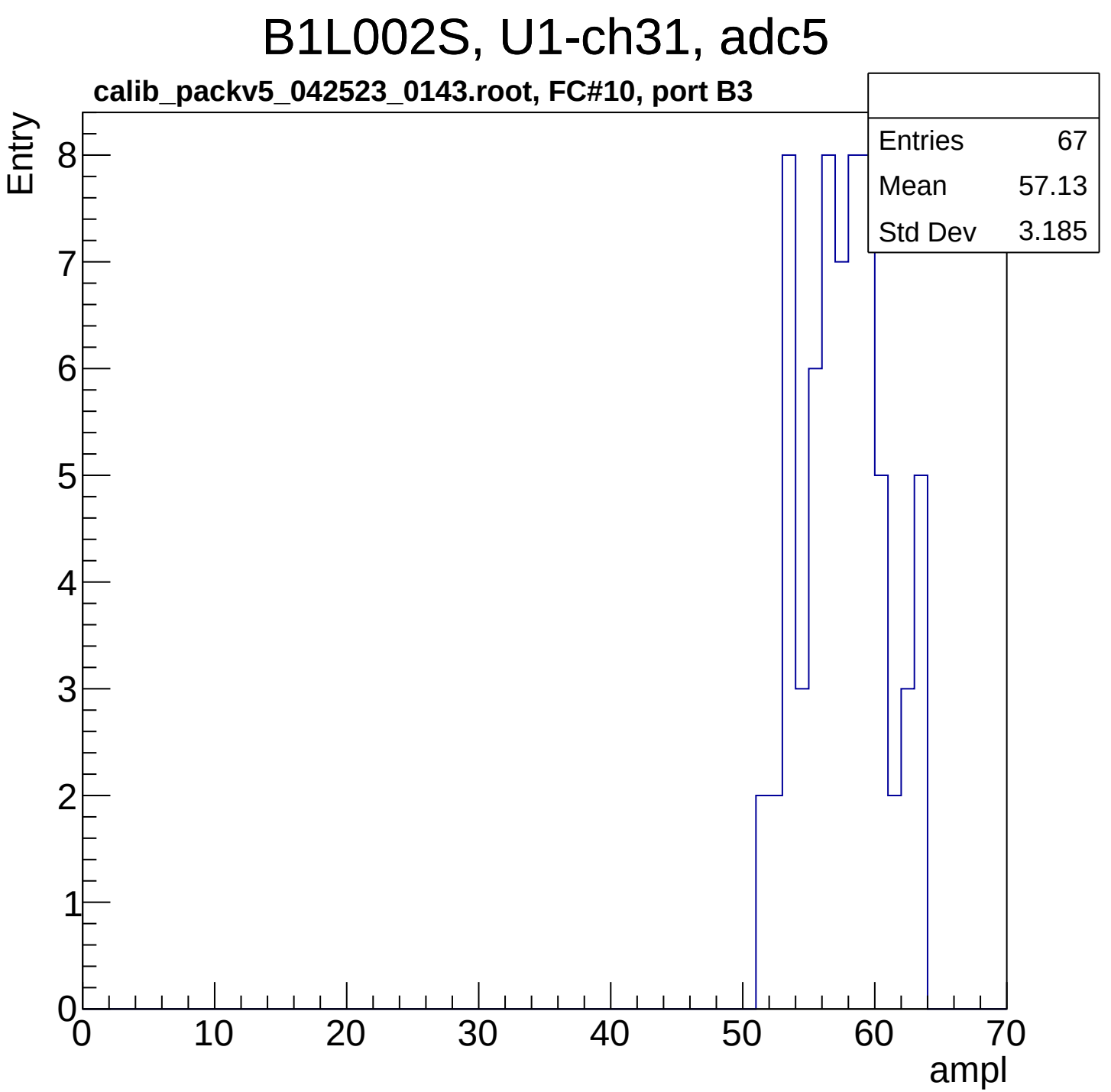

B1L002S, U1-ch31, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.36
Std Dev 3.422
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch31, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 57.13
Std Dev 3.185
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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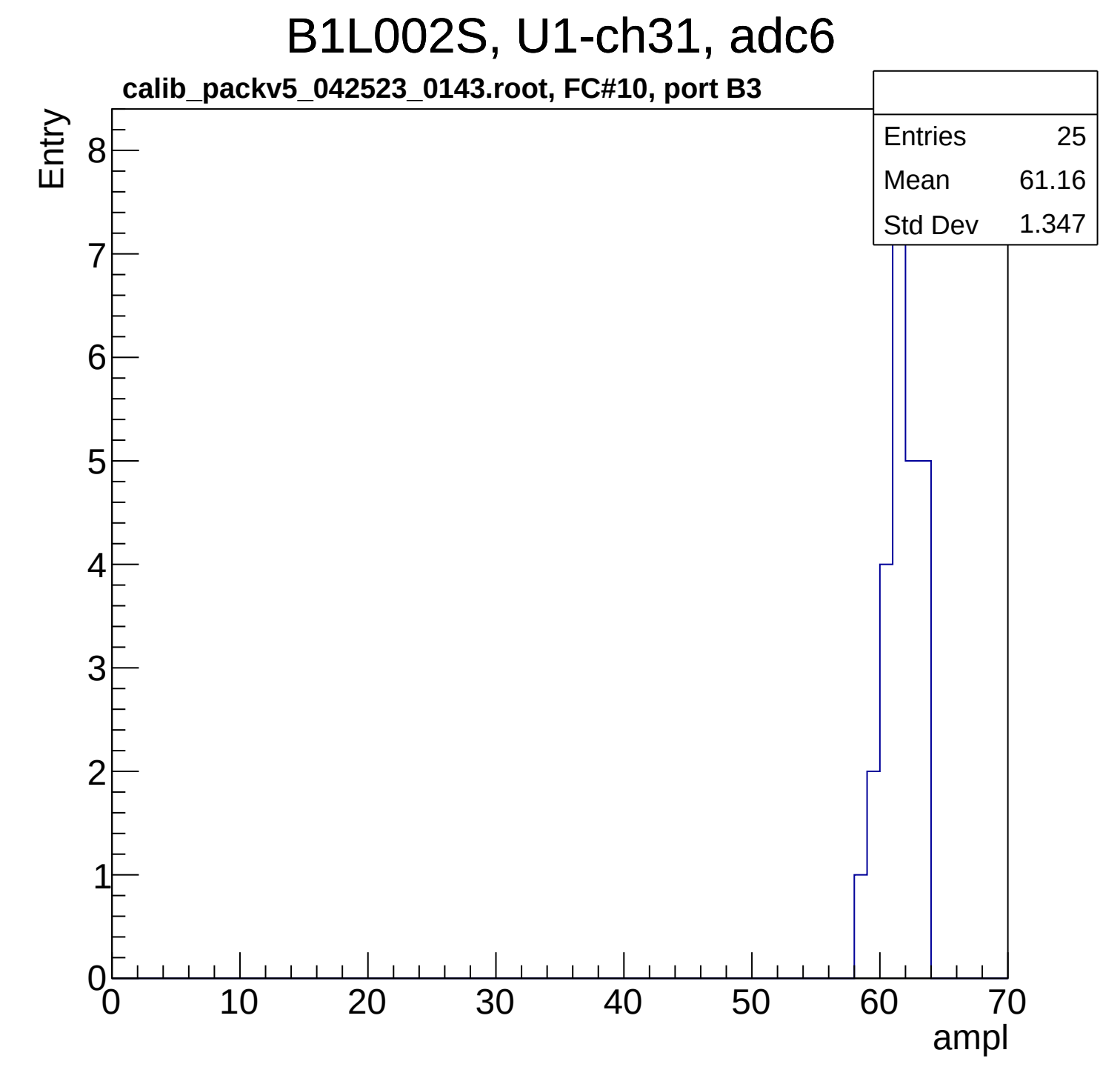
B1L002S, U1-ch31, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 61.16
Std Dev 1.347
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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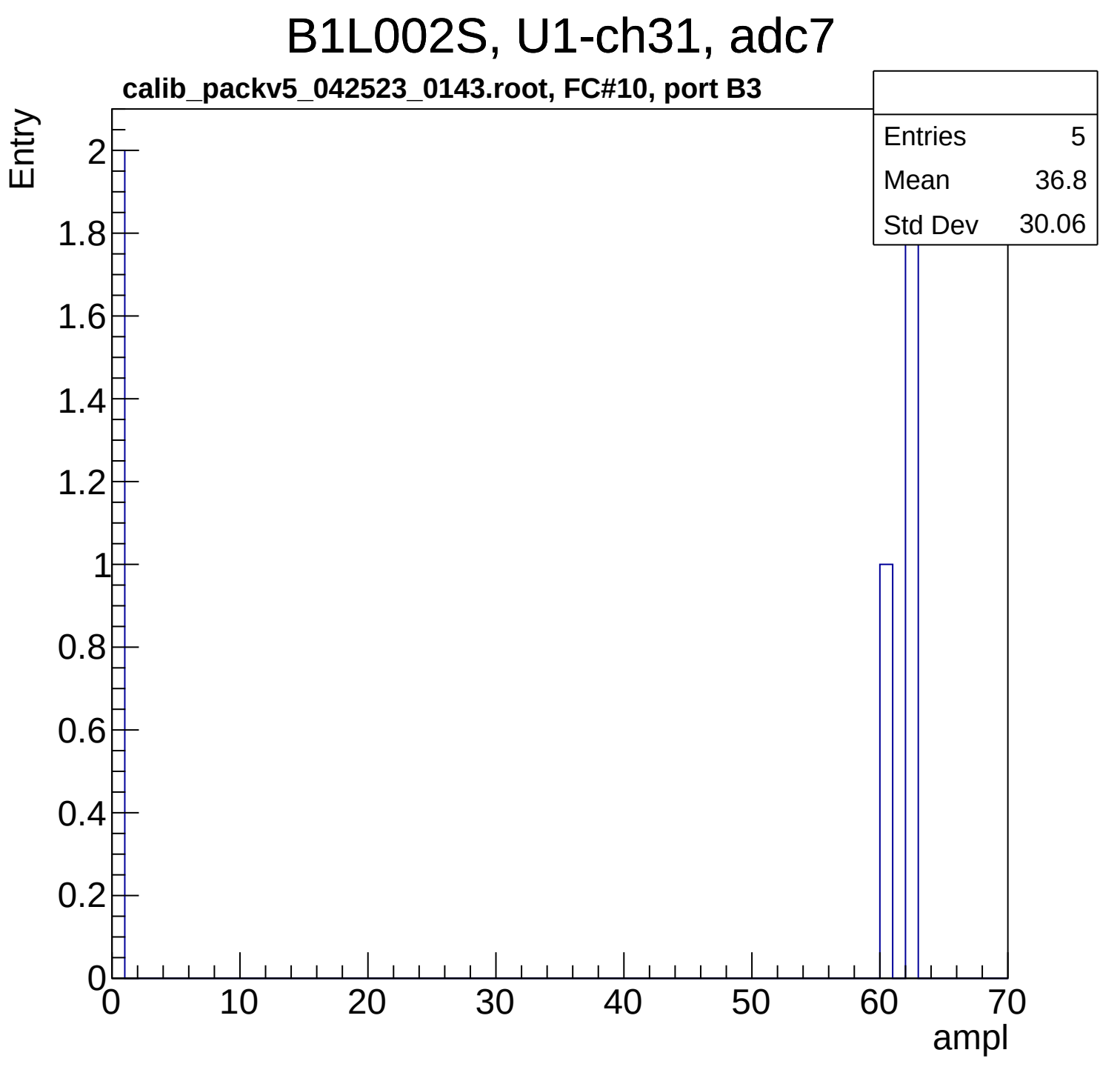
B1L002S, U1-ch31, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 36.8
Std Dev 30.06
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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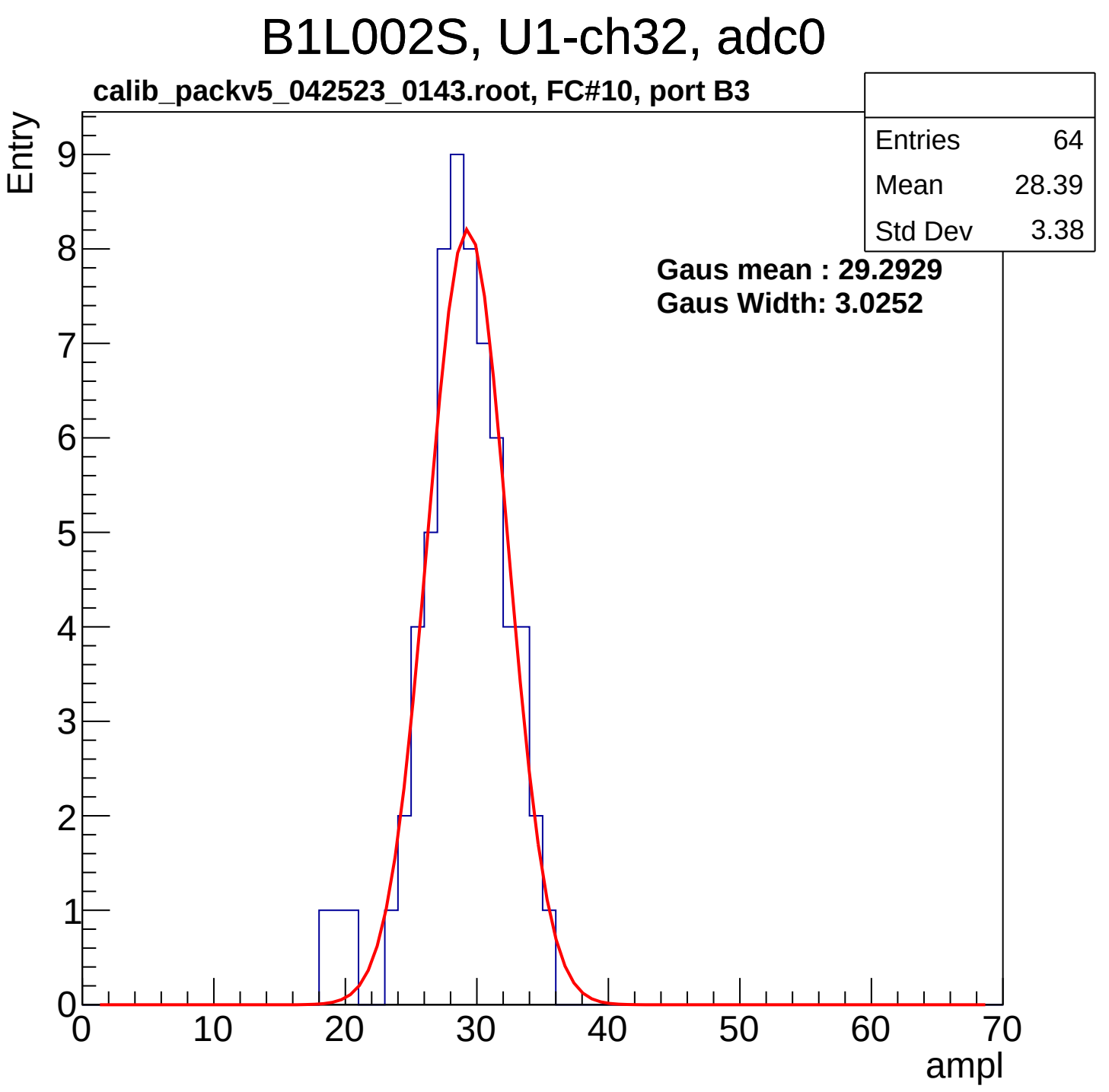
B1L002S, U1-ch32, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.39
Std Dev 3.38
Gaus mean : 29.2929
Gaus Width: 3.0252
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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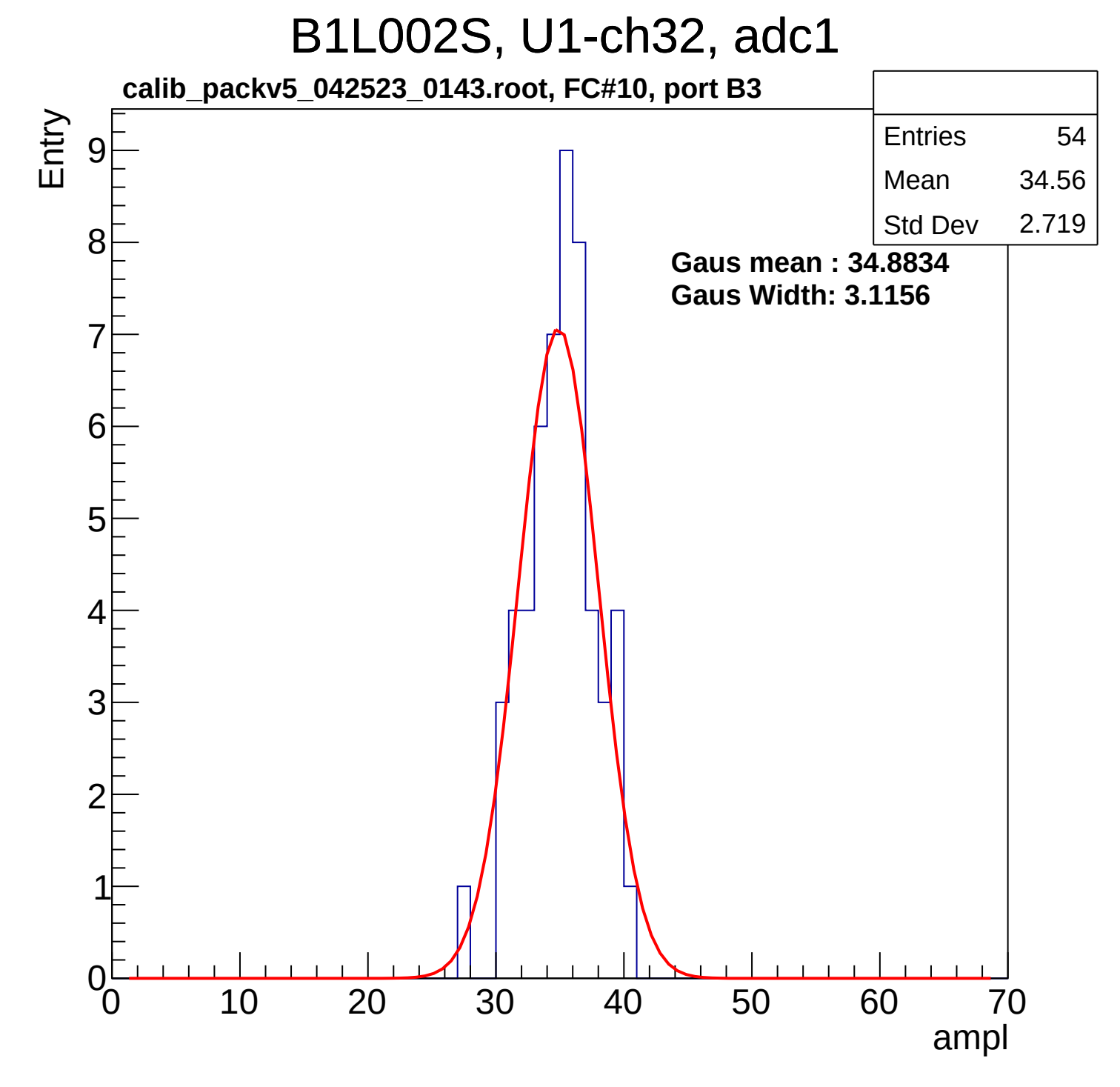
B1L002S, U1-ch32, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 34.56
Std Dev 2.719
Gaus mean : 34.8834
Gaus Width: 3.1156
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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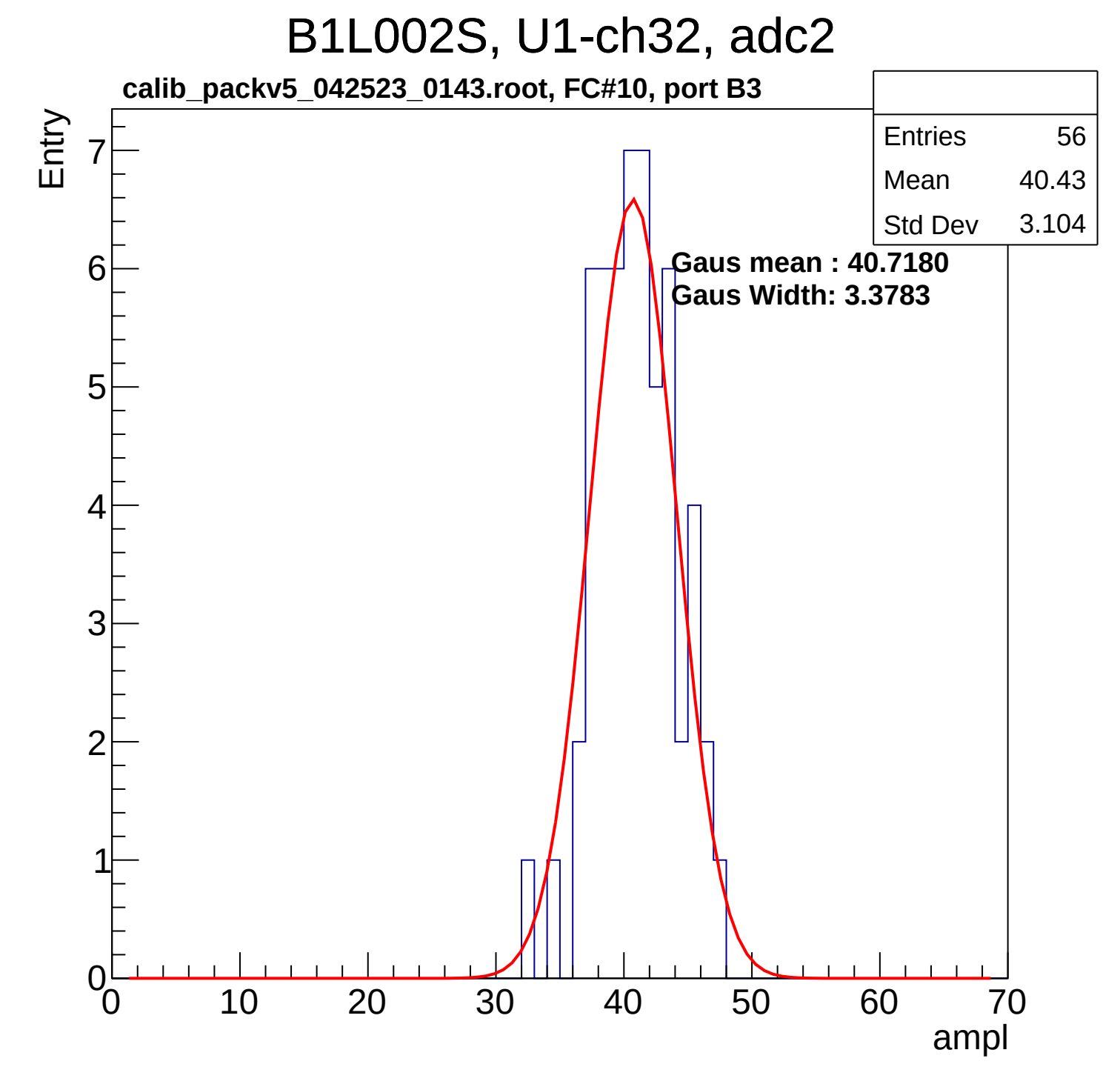
B1L002S, U1-ch32, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.43
Std Dev 3.104
Gaus mean : 40.7180
Gaus Width: 3.3783
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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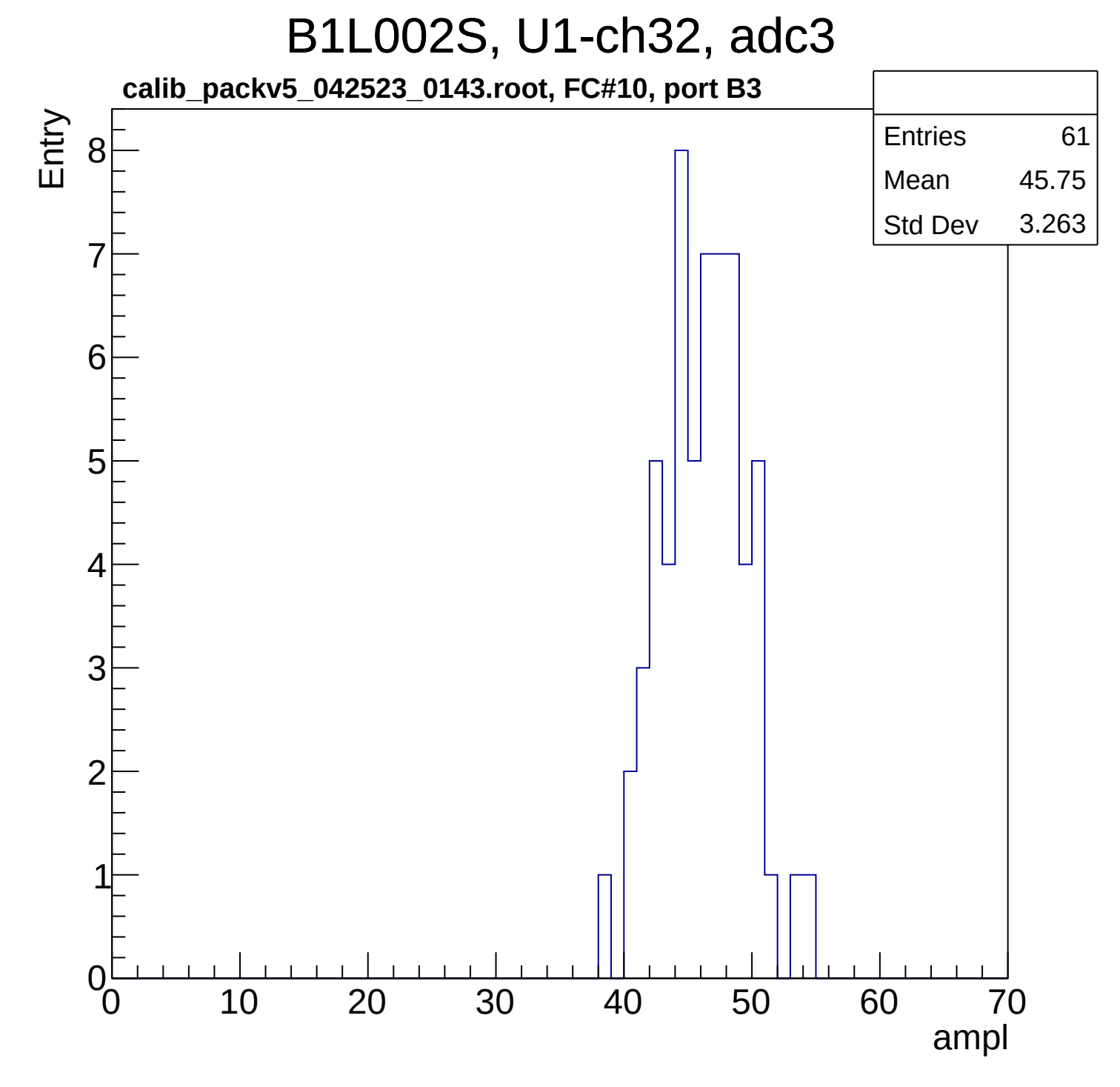
B1L002S, U1-ch32, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 45.75
Std Dev 3.263
Entry
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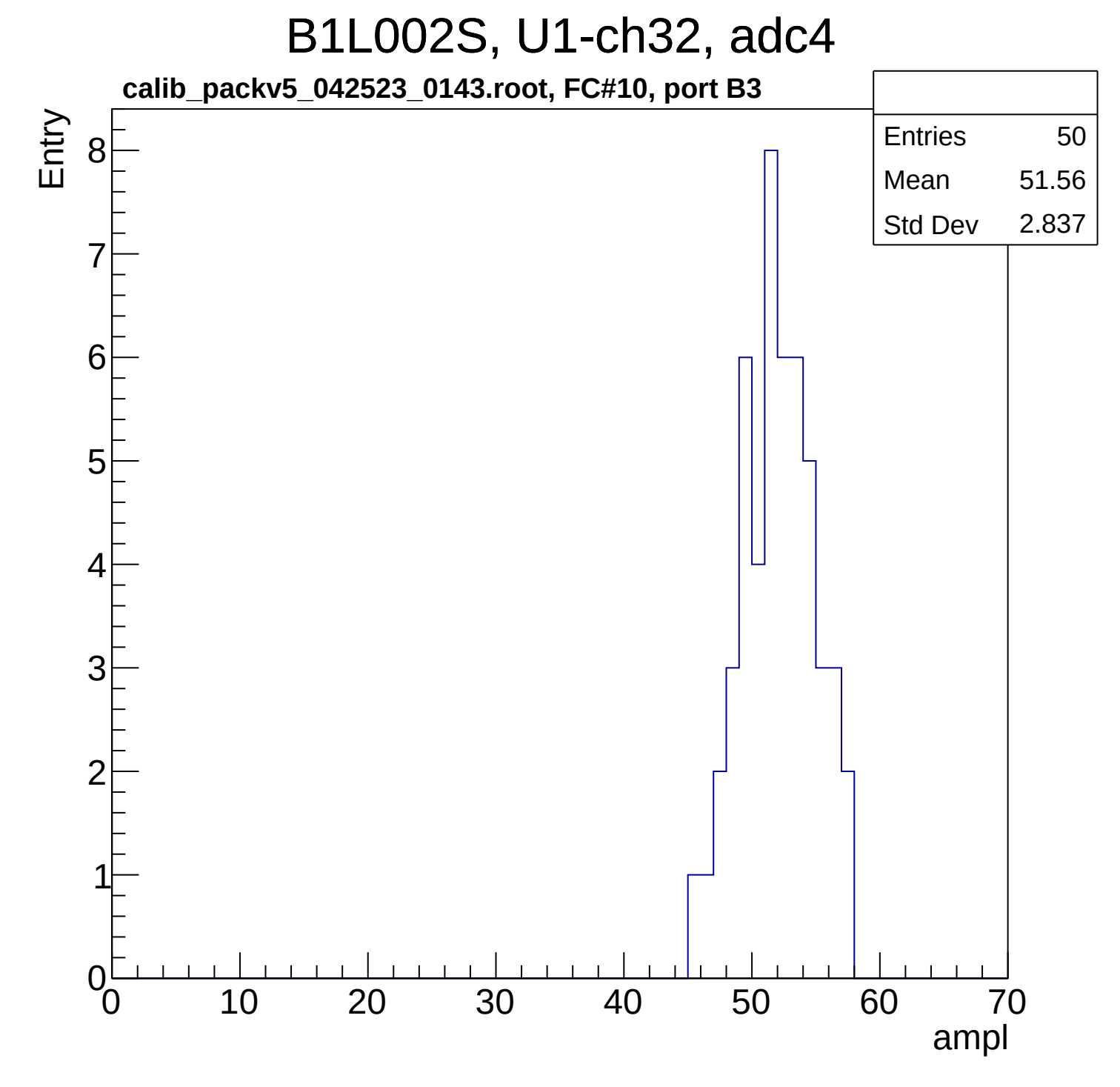
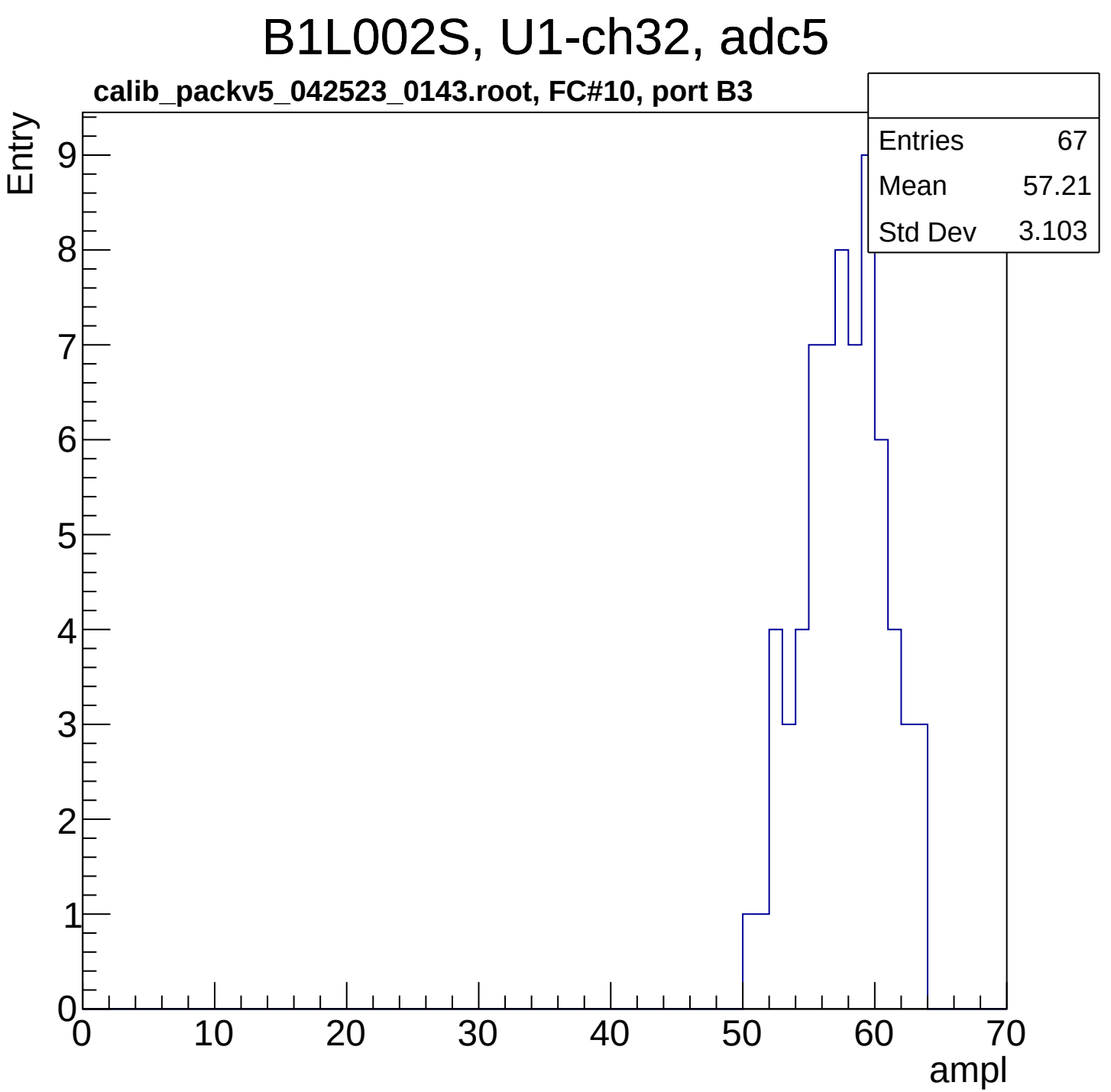
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch32, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.56
Std Dev 2.837
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch32, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 57.21
Std Dev 3.103
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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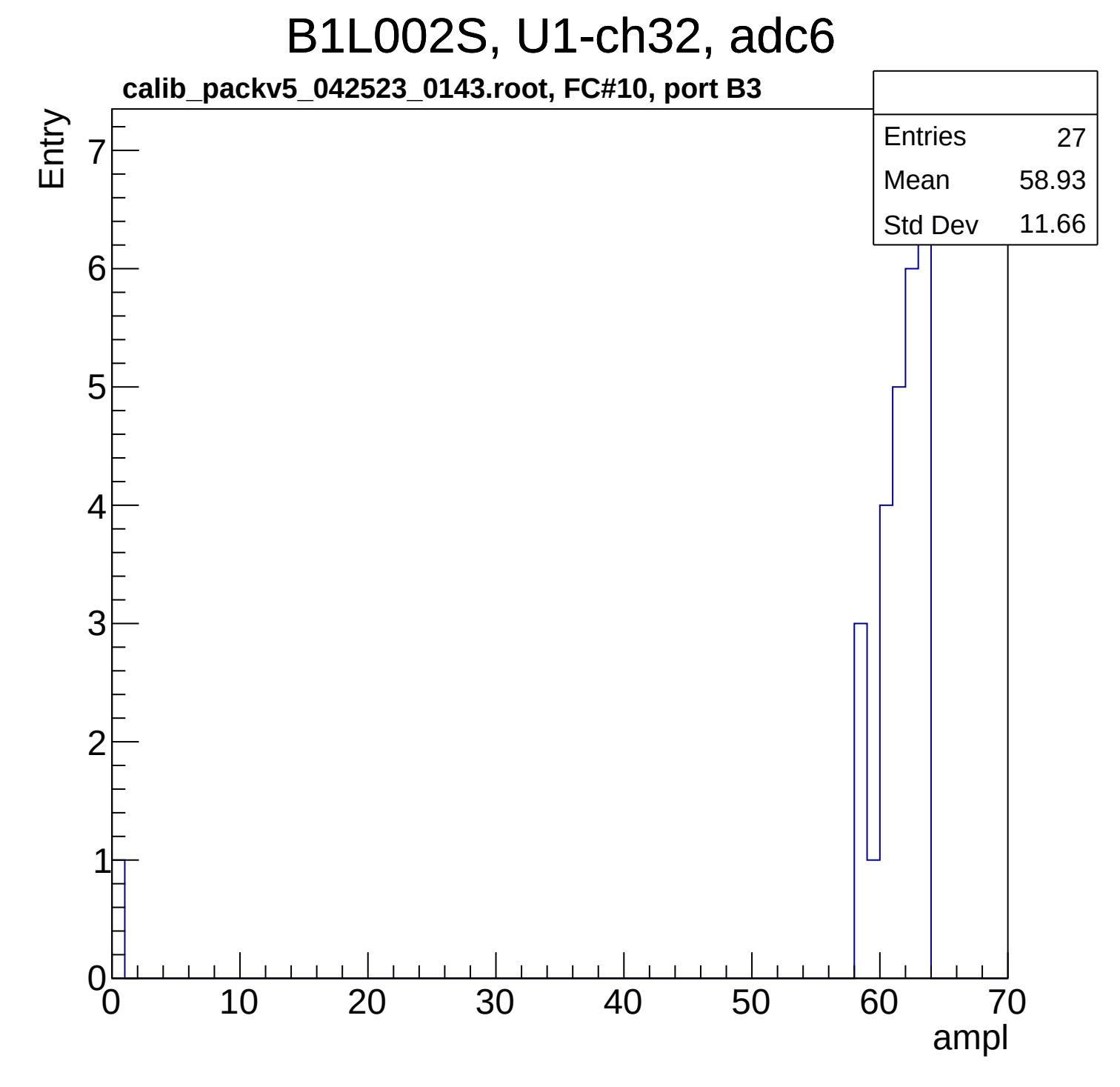
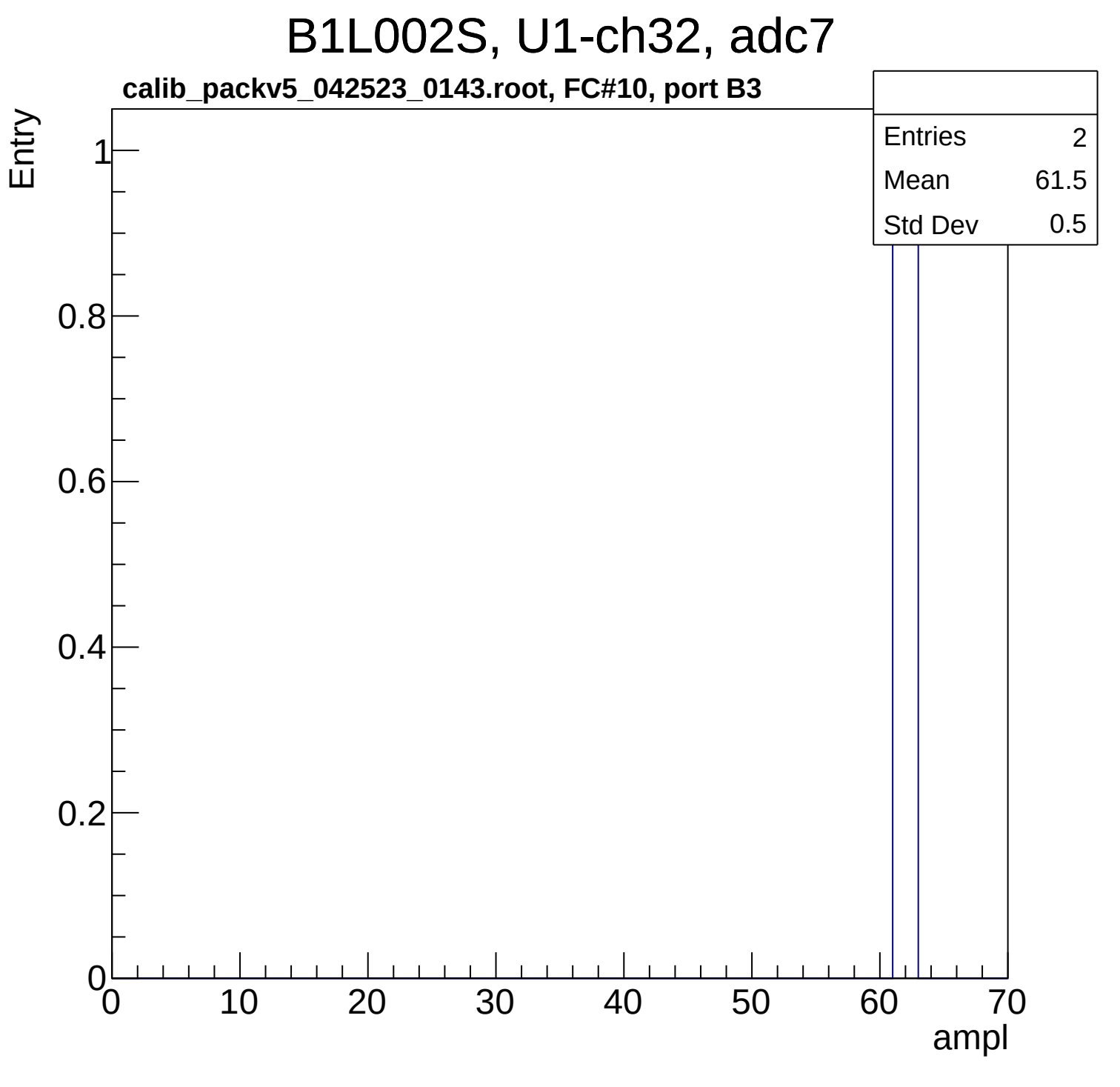

B1L002S, U1-ch32, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.93
Std Dev 11.66
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch32, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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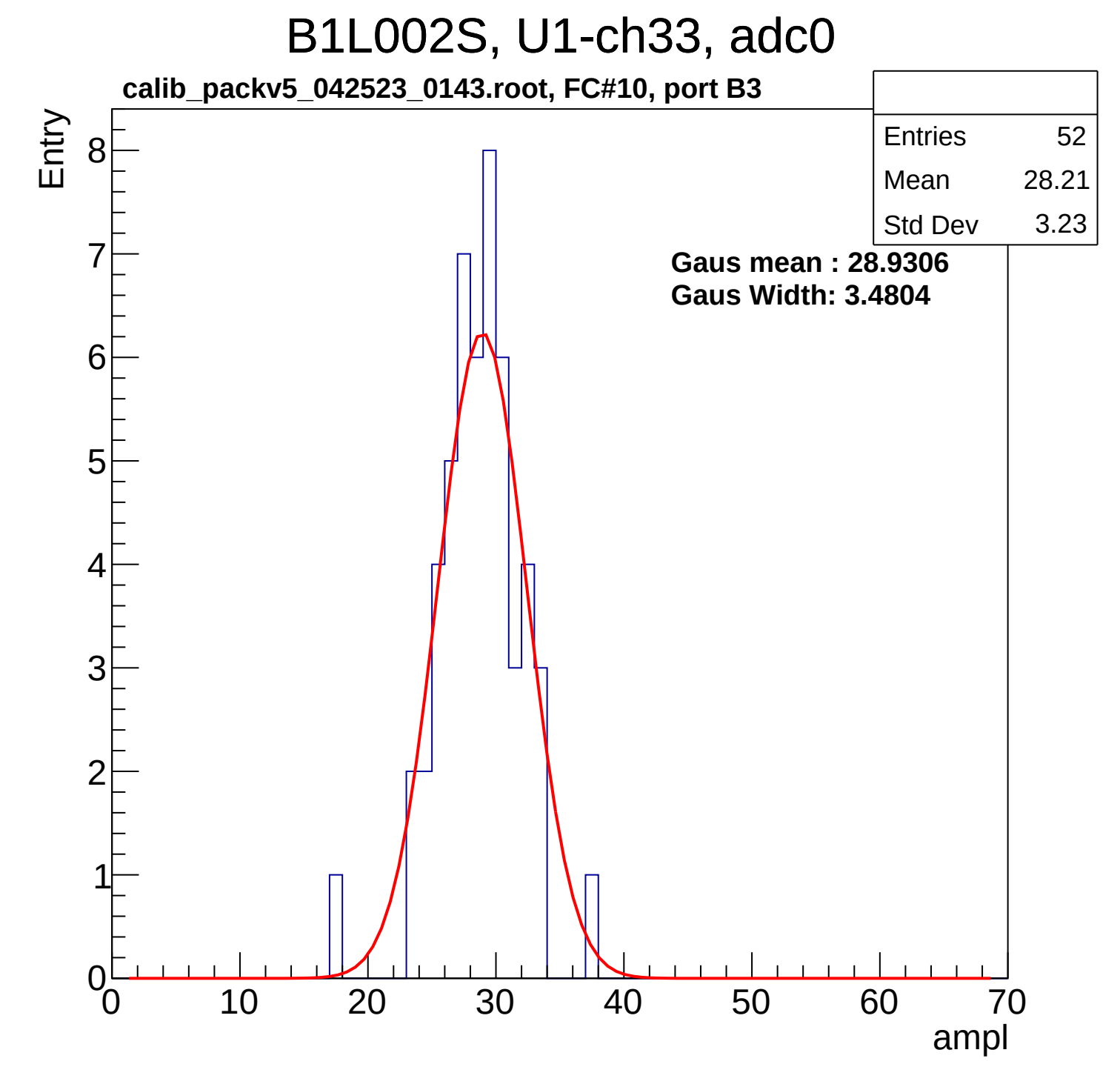
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch33, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 28.21
Std Dev 3.23
Gaus mean : 28.9306
Gaus Width: 3.4804
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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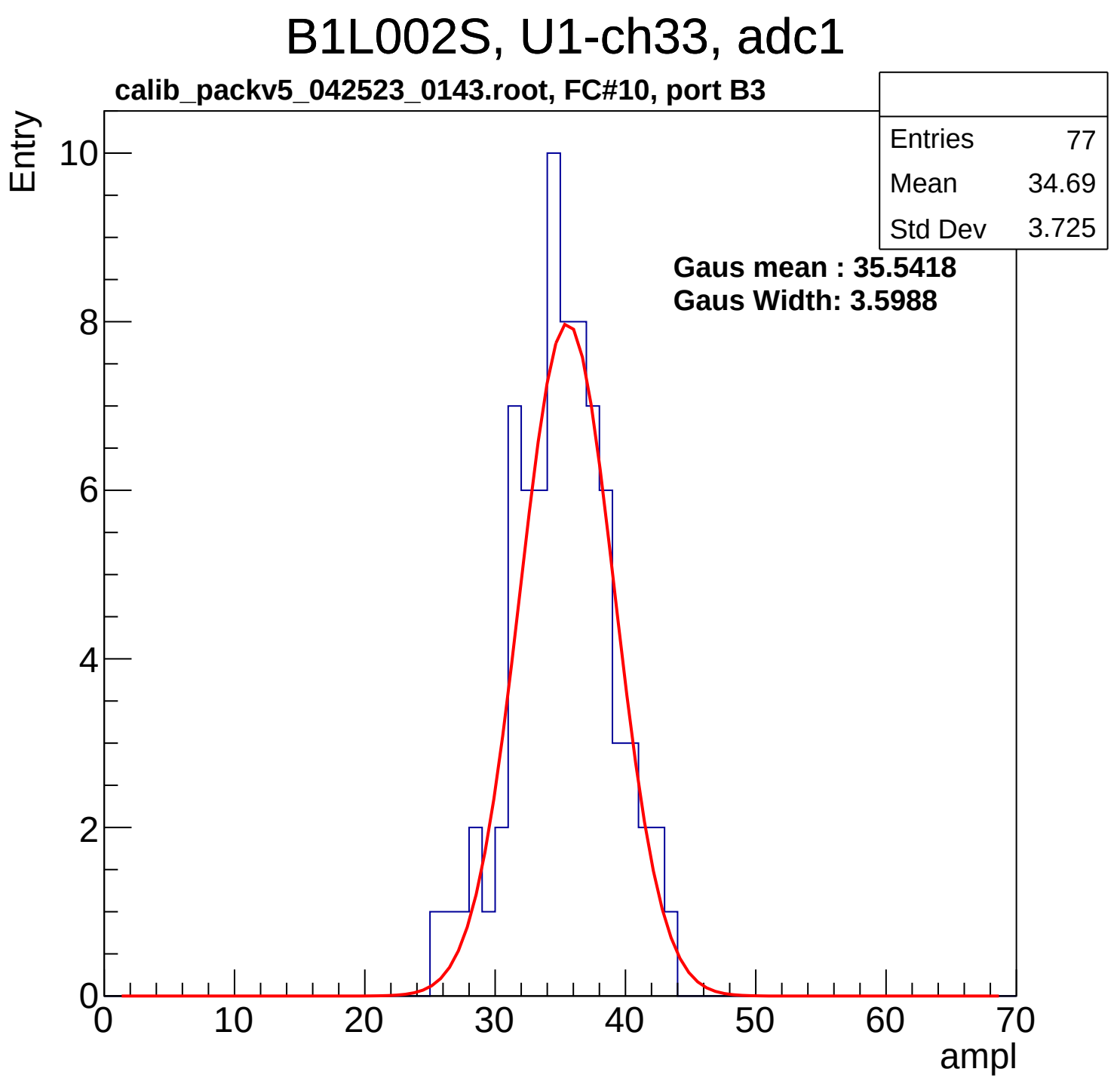
60
70


B1L002S, U1-ch33, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.69
Std Dev 3.725
Gaus mean : 35.5418
Gaus Width: 3.5988
Entry
ampl
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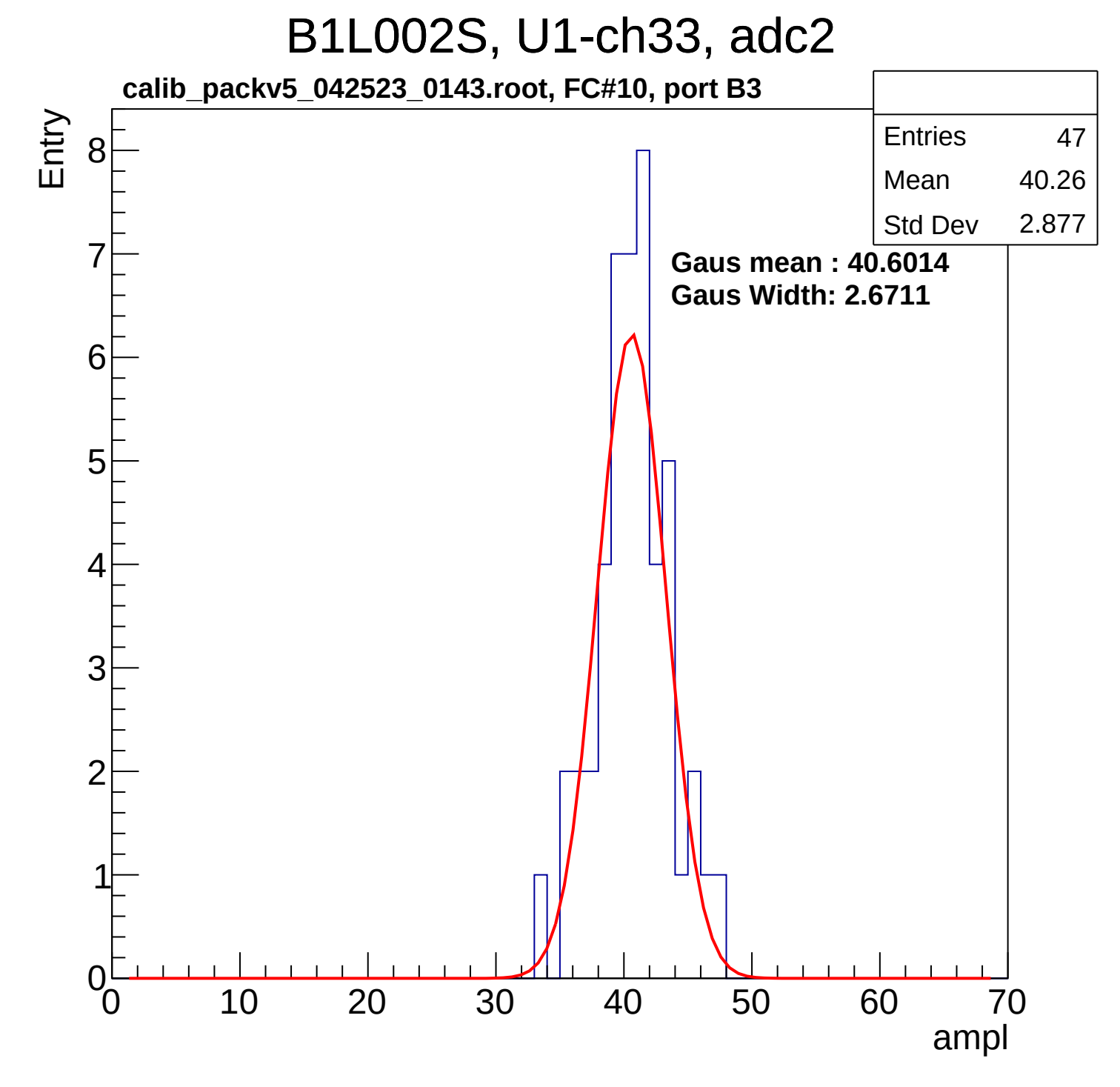
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch33, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 40.26
Std Dev 2.877
Gaus mean : 40.6014
Gaus Width: 2.6711
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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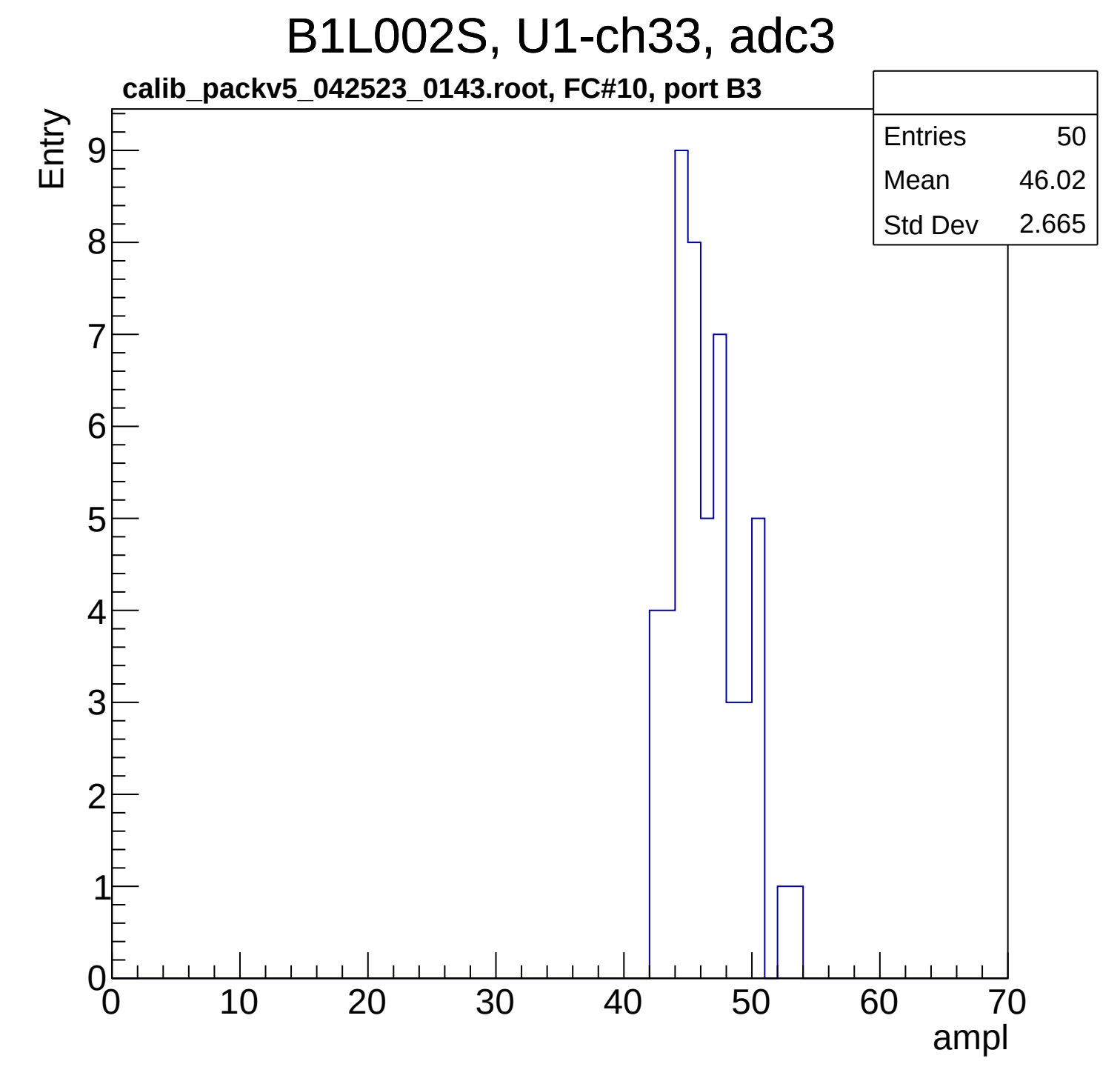
B1L002S, U1-ch33, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 46.02
Std Dev 2.665
Entry
ampl
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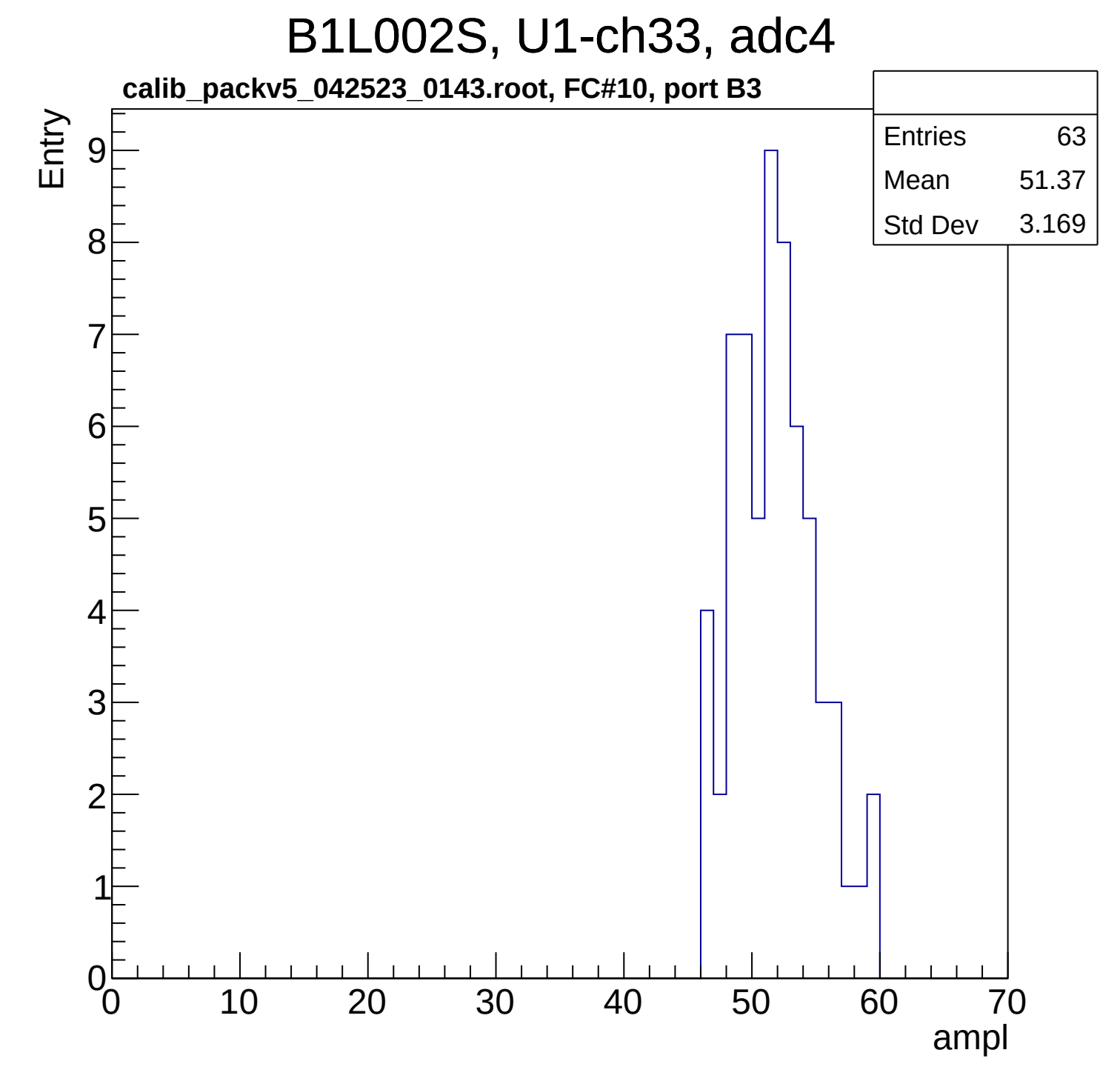
B1L002S, U1-ch33, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 51.37
Std Dev 3.169
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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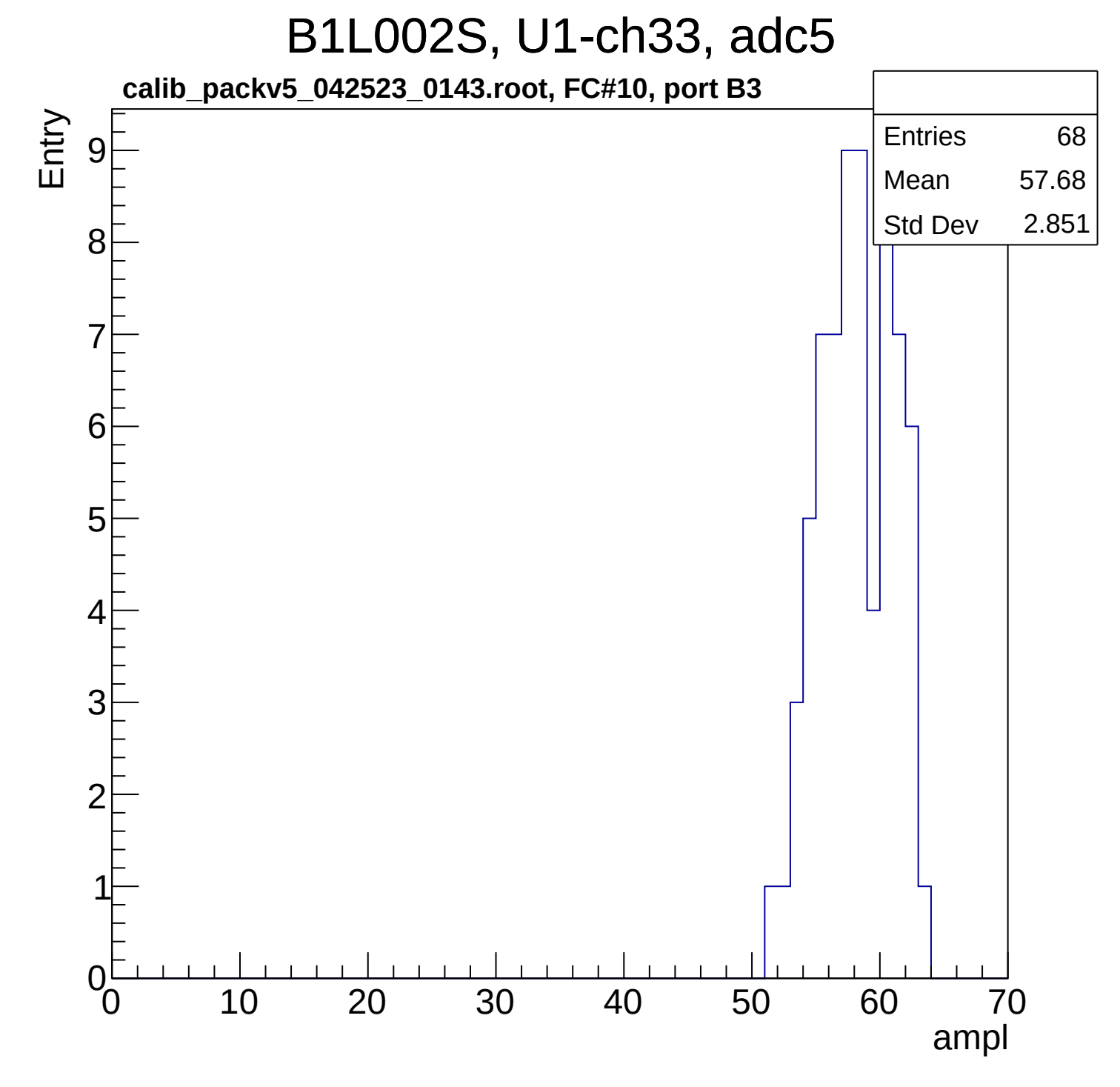
B1L002S, U1-ch33, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.68
Std Dev 2.851
Entry
ampl
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0
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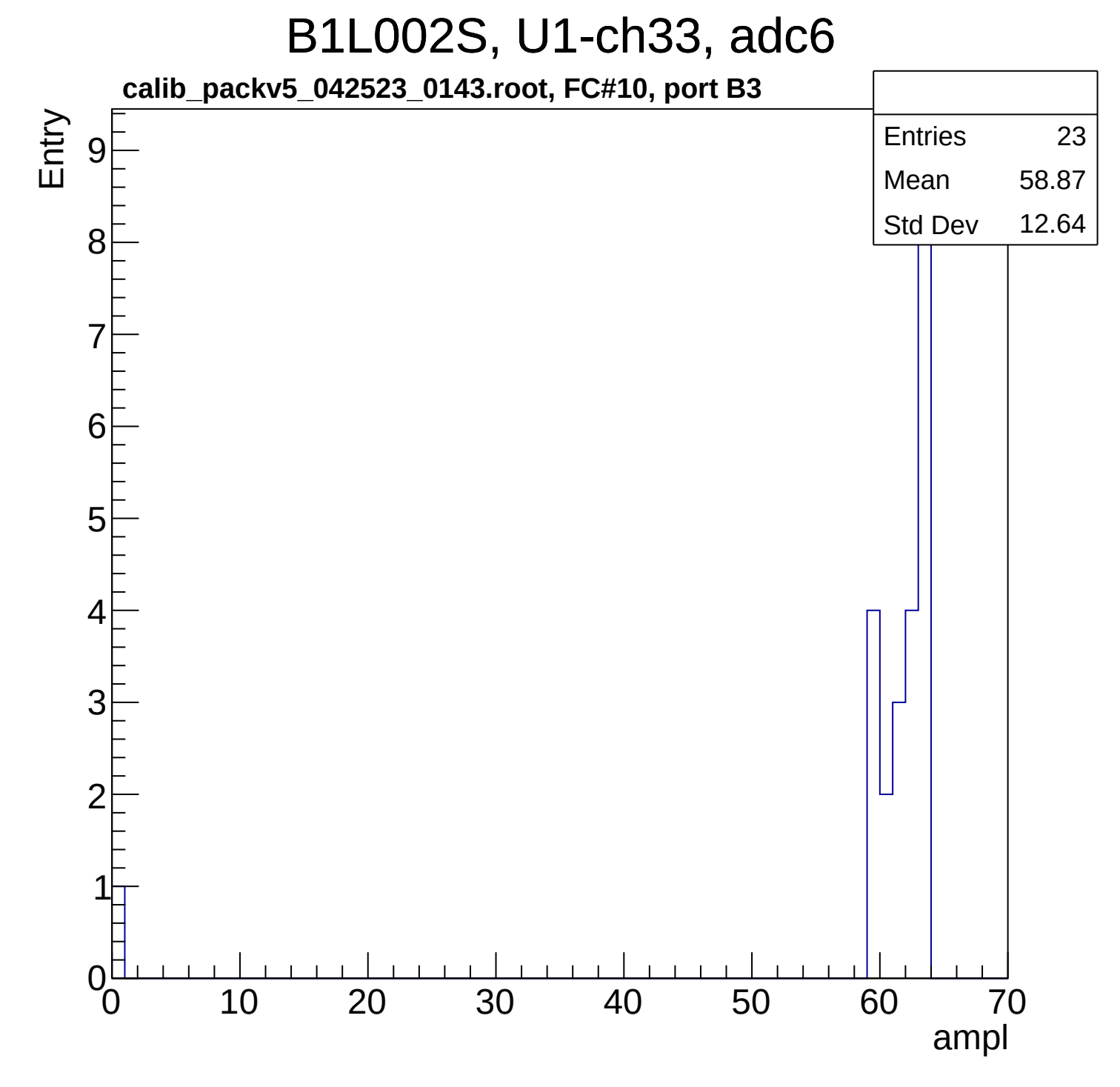
B1L002S, U1-ch33, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.87
Std Dev 12.64
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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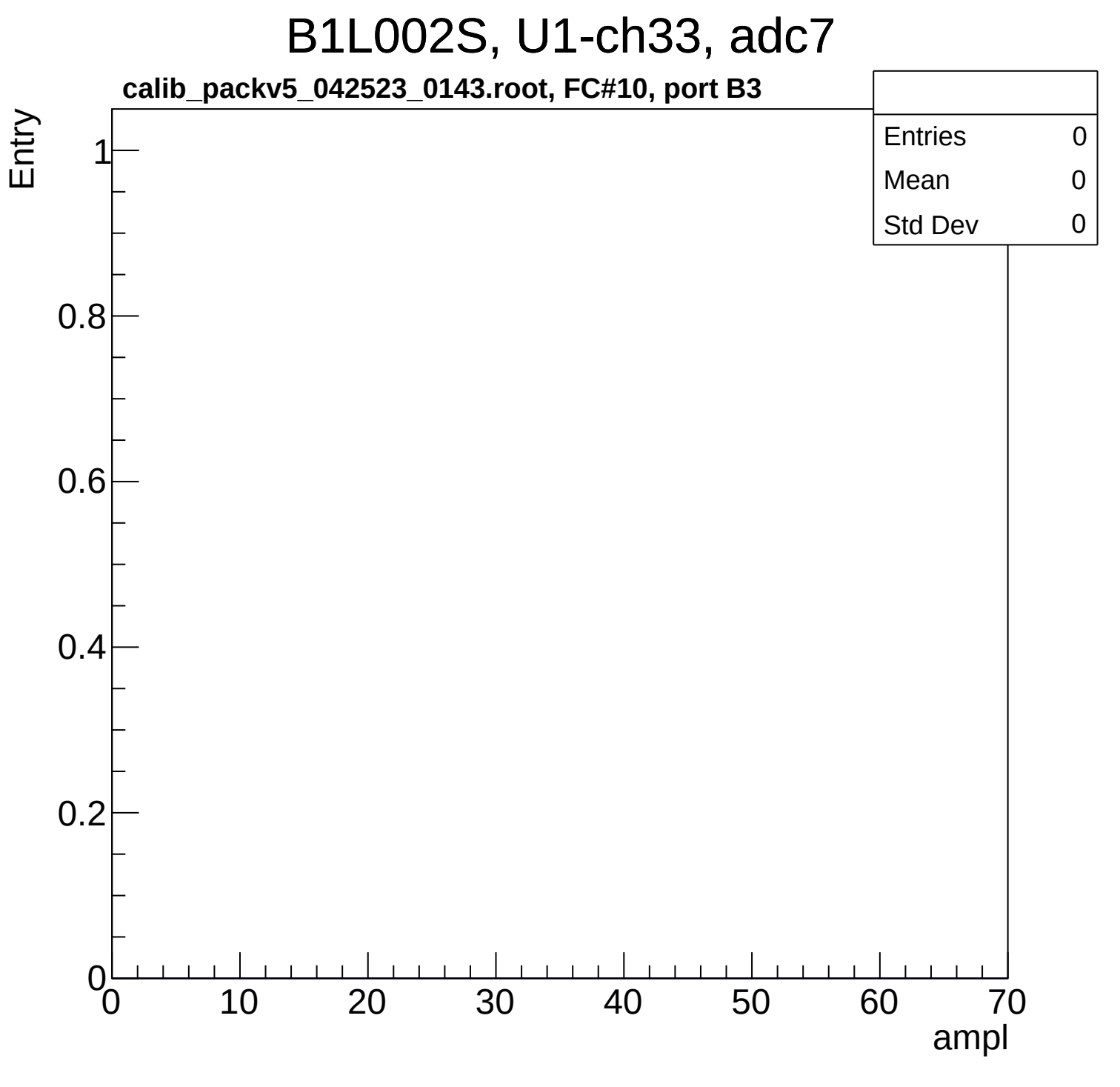
B1L002S, U1-ch33, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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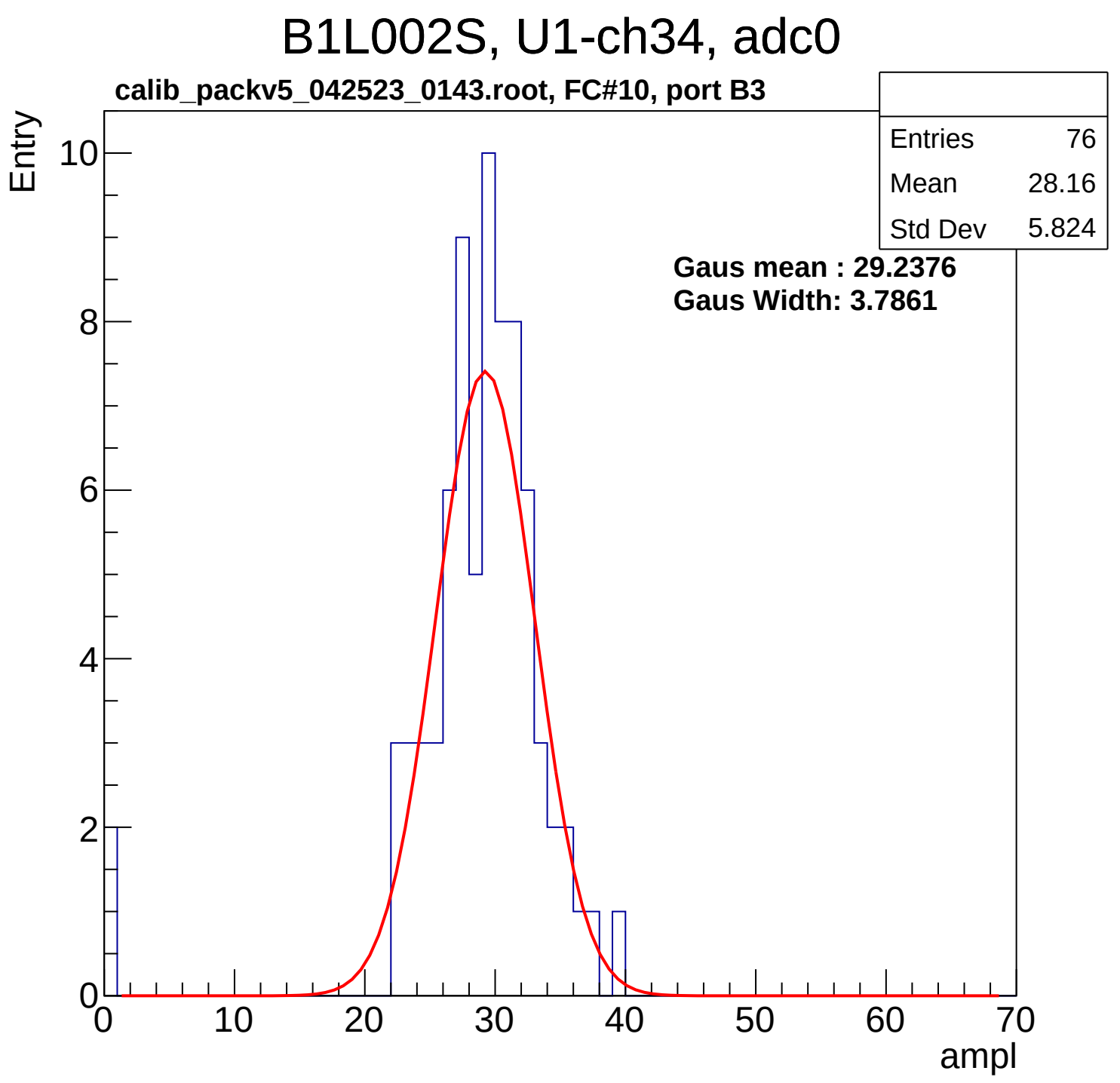
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch34, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 28.16
Std Dev 5.824
Gaus mean : 29.2376
Gaus Width: 3.7861
Entry
ampl
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2
4
6
8
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0
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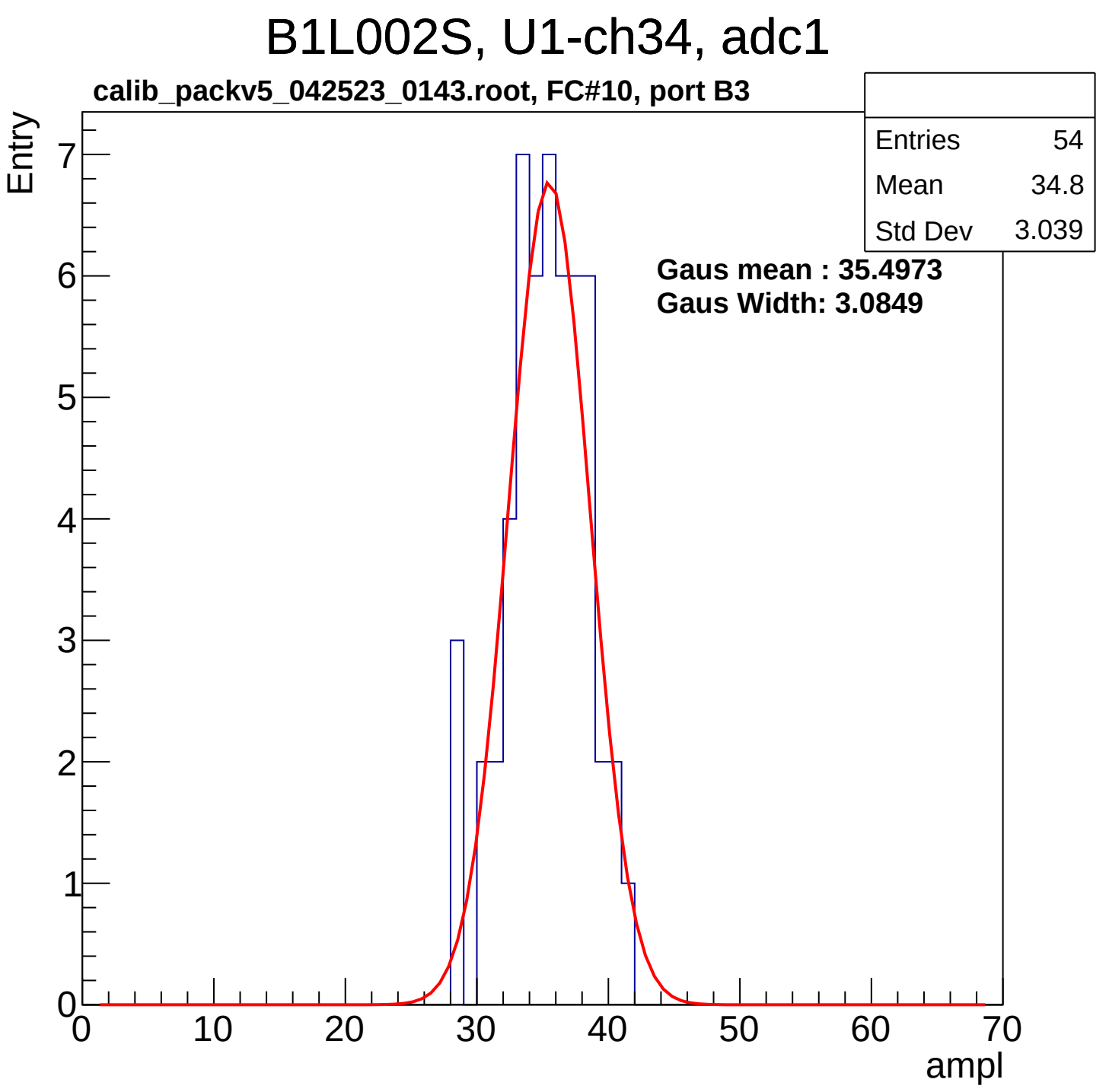
B1L002S, U1-ch34, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 34.8
Std Dev 3.039
Gaus mean : 35.4973
Gaus Width: 3.0849
Entry
ampl
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2
3
4
5
6
7
0
10
20
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40
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60
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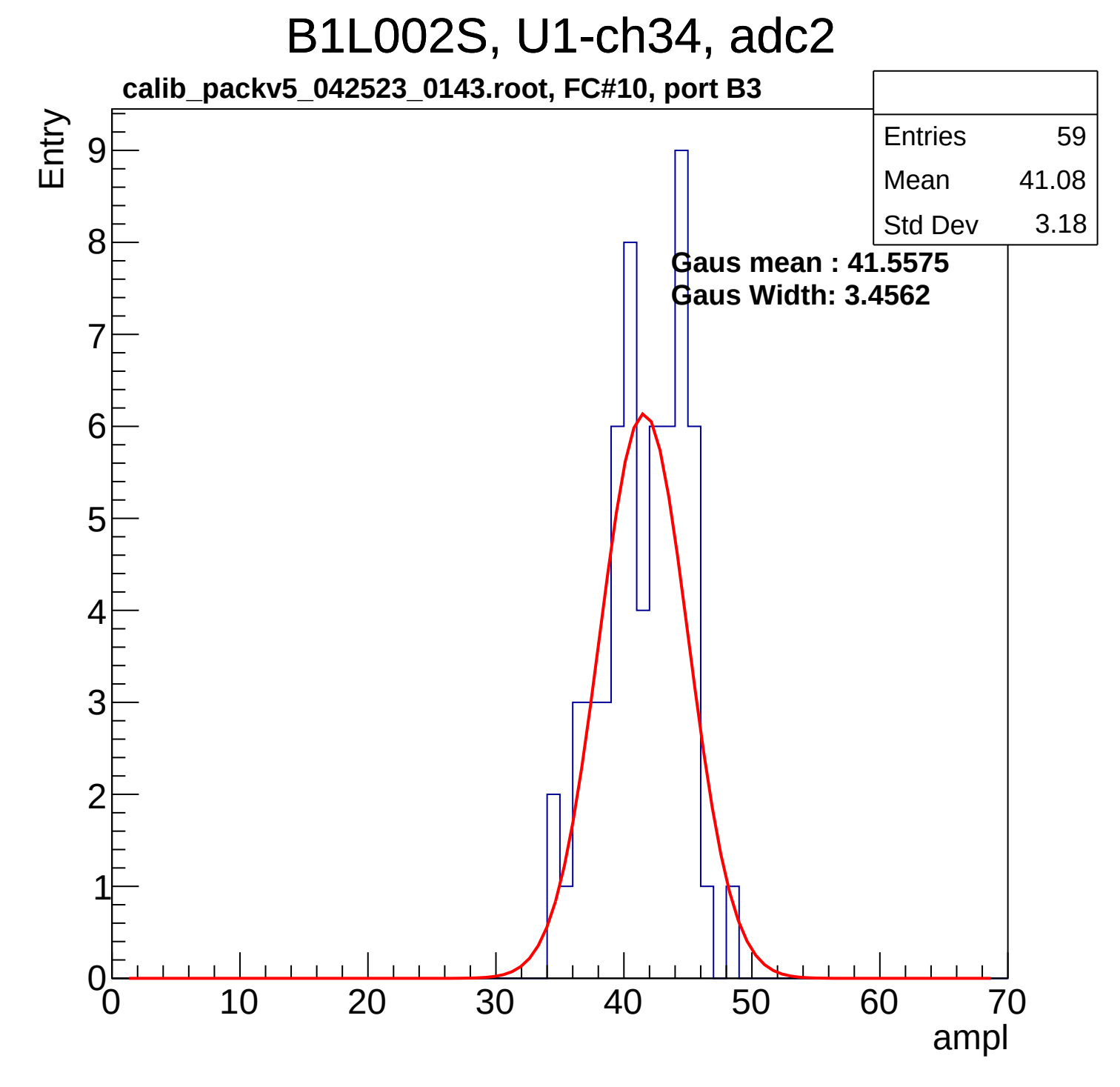
B1L002S, U1-ch34, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.08
Std Dev 3.18
Gaus mean : 41.5575
Gaus Width: 3.4562
Entry
ampl
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2
3
4
5
6
7
8
9
0
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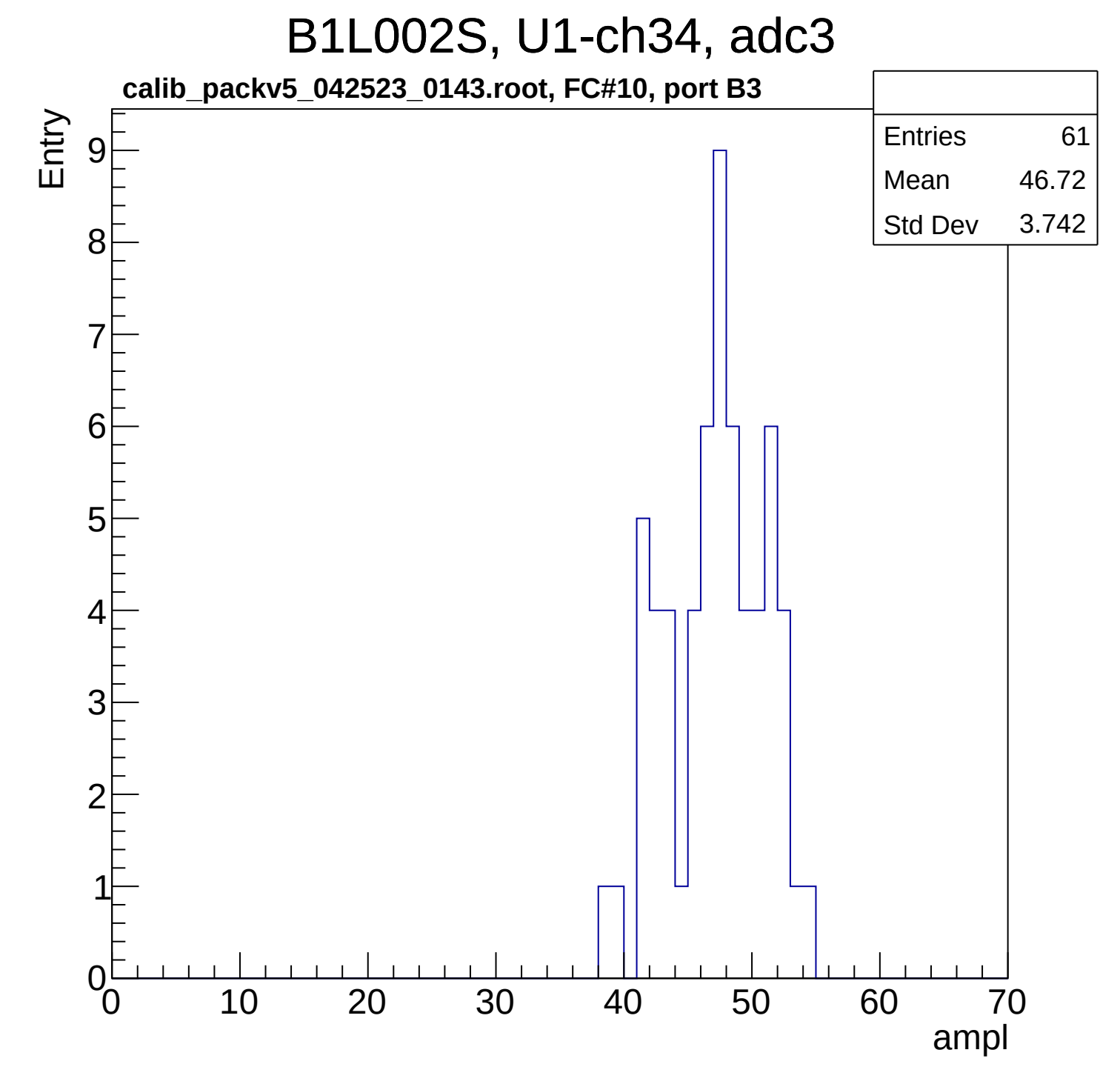
B1L002S, U1-ch34, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.72
Std Dev 3.742
Entry
ampl
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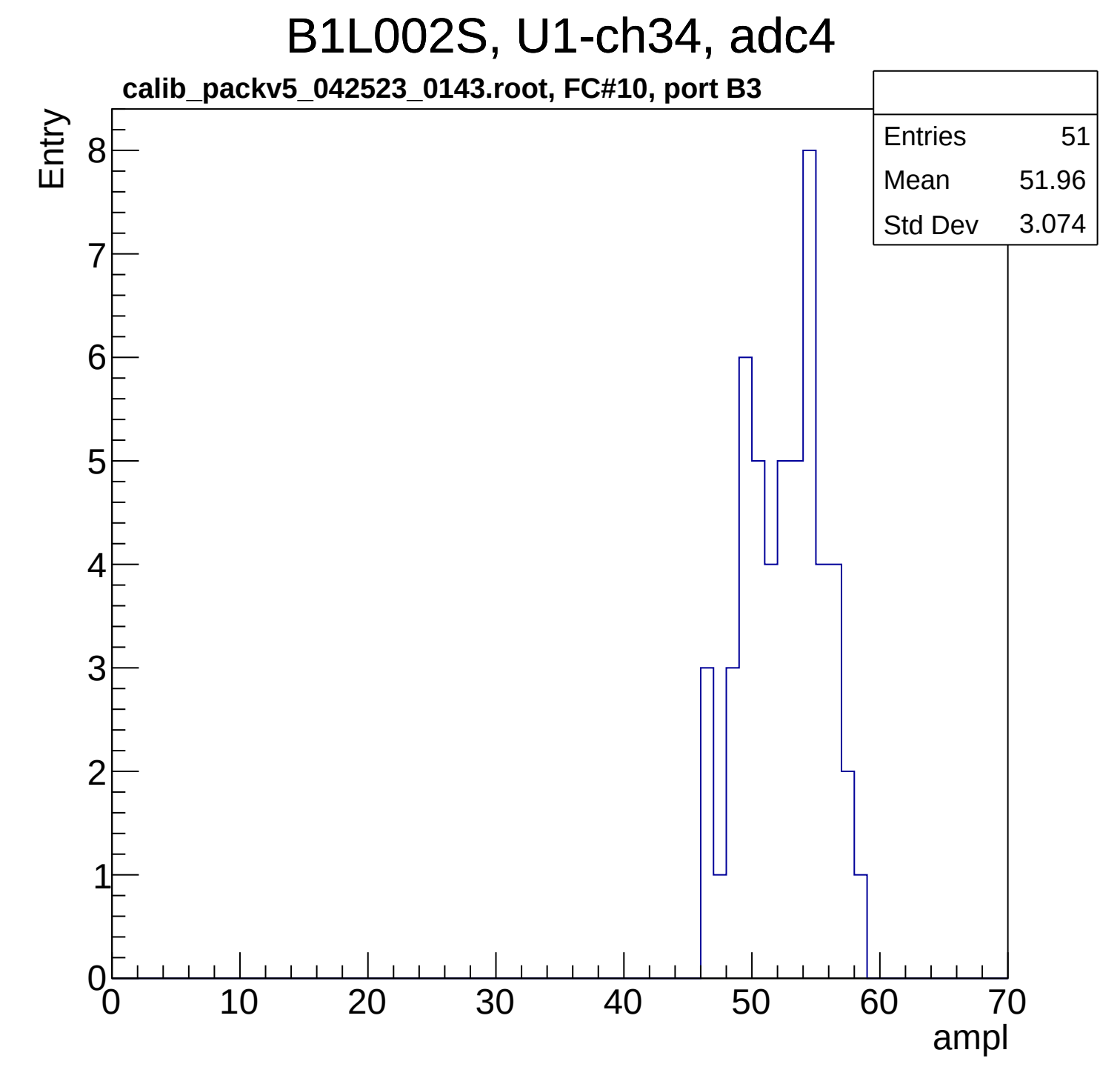
B1L002S, U1-ch34, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 51.96
Std Dev 3.074
Entry
ampl
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3
4
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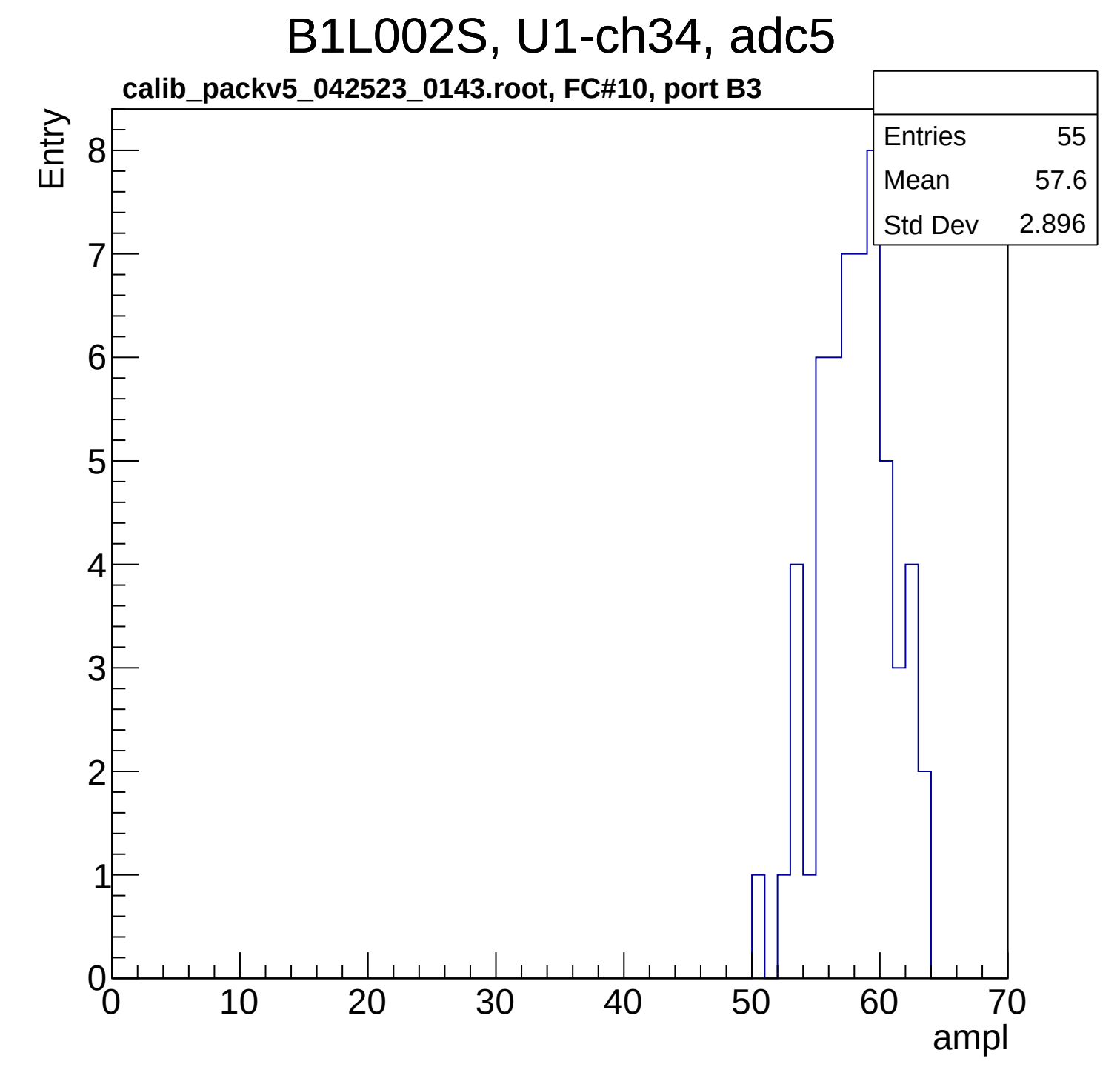
B1L002S, U1-ch34, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.6
Std Dev 2.896
Entry
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2
3
4
5
6
7
8
0
10
20
30
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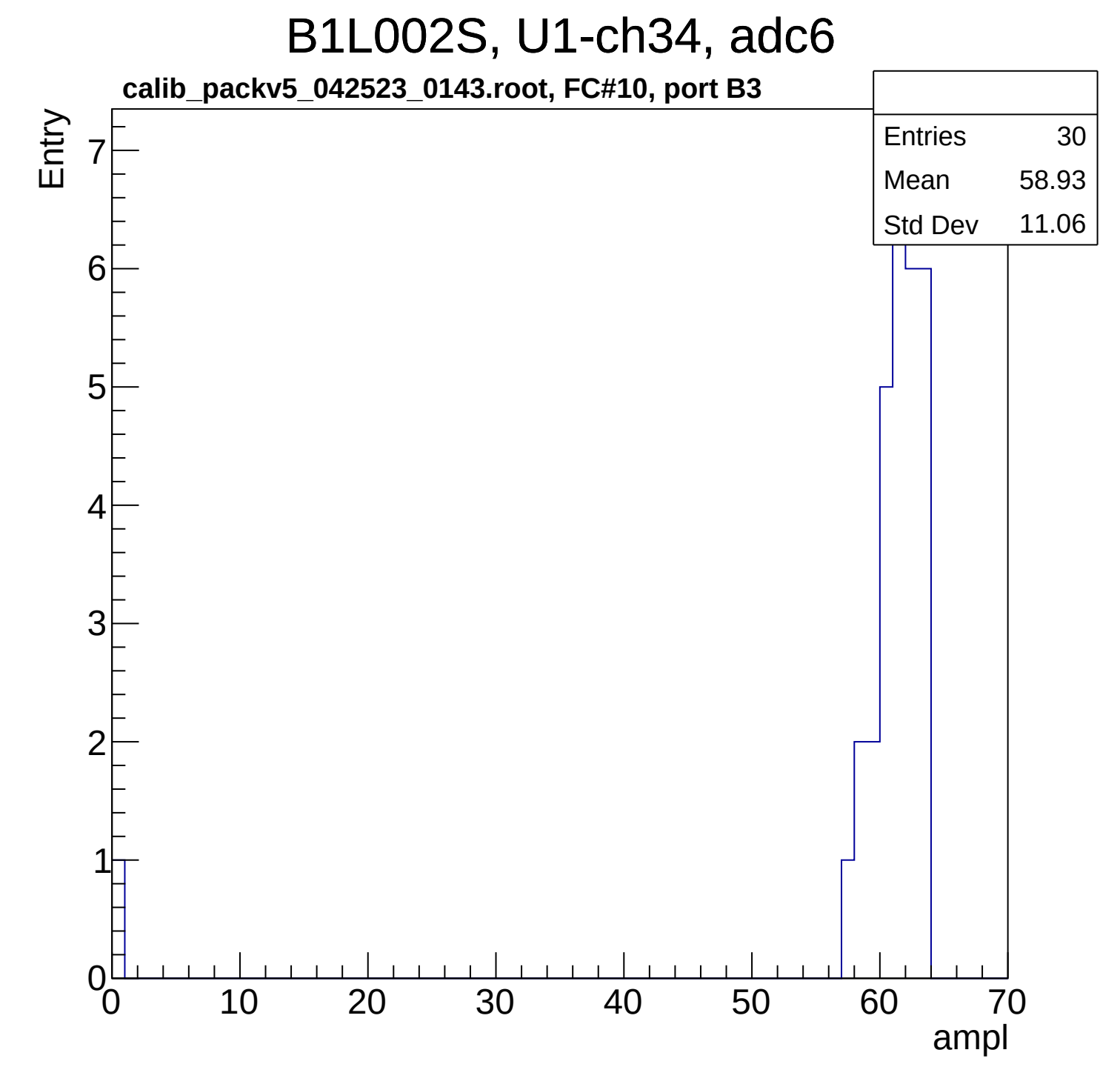
50
60
70
ampl


B1L002S, U1-ch34, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 58.93
Std Dev 11.06
Entry
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1
2
3
4
5
6
7
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20
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40
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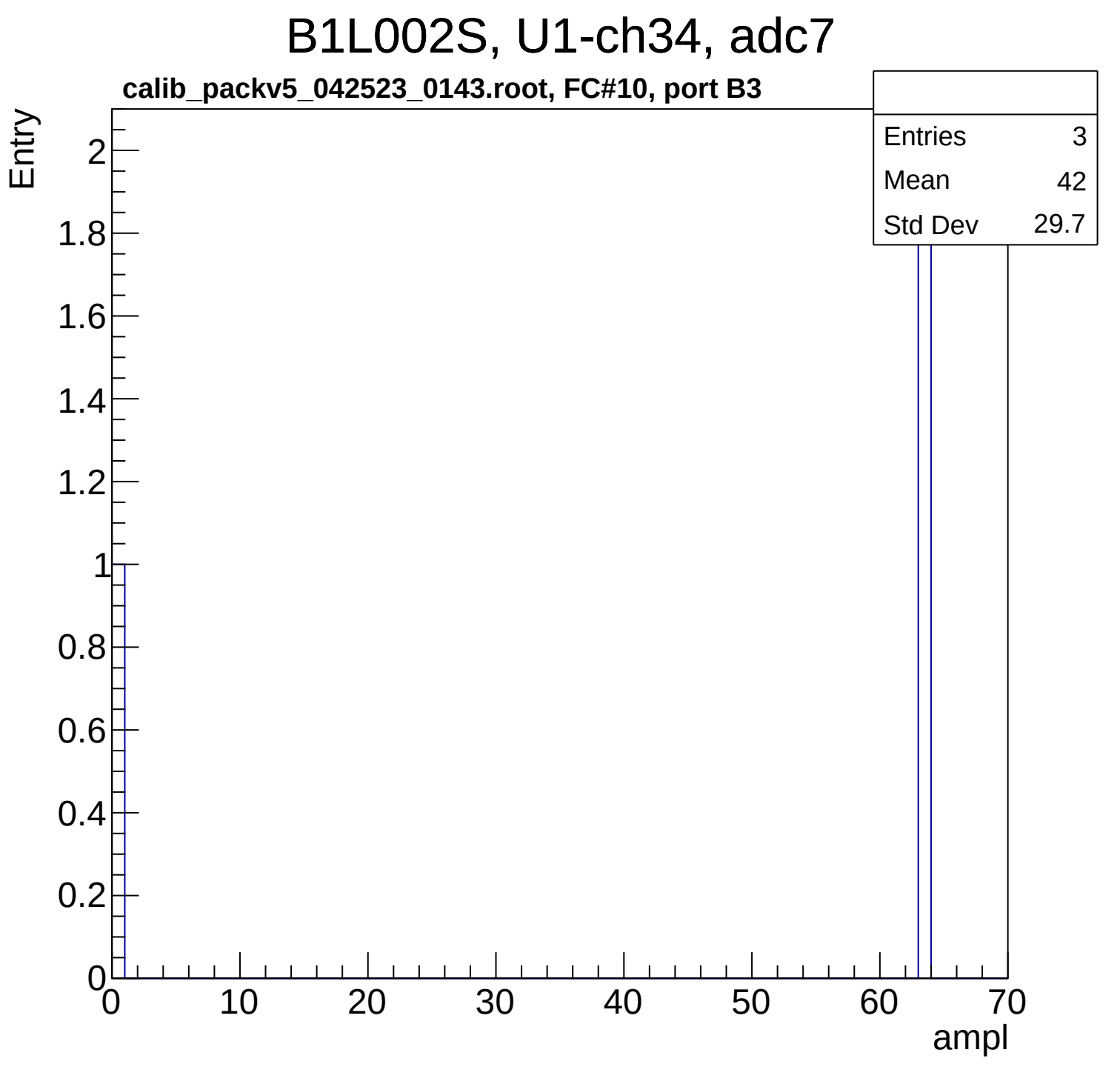
70
ampl


B1L002S, U1-ch34, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
50
60
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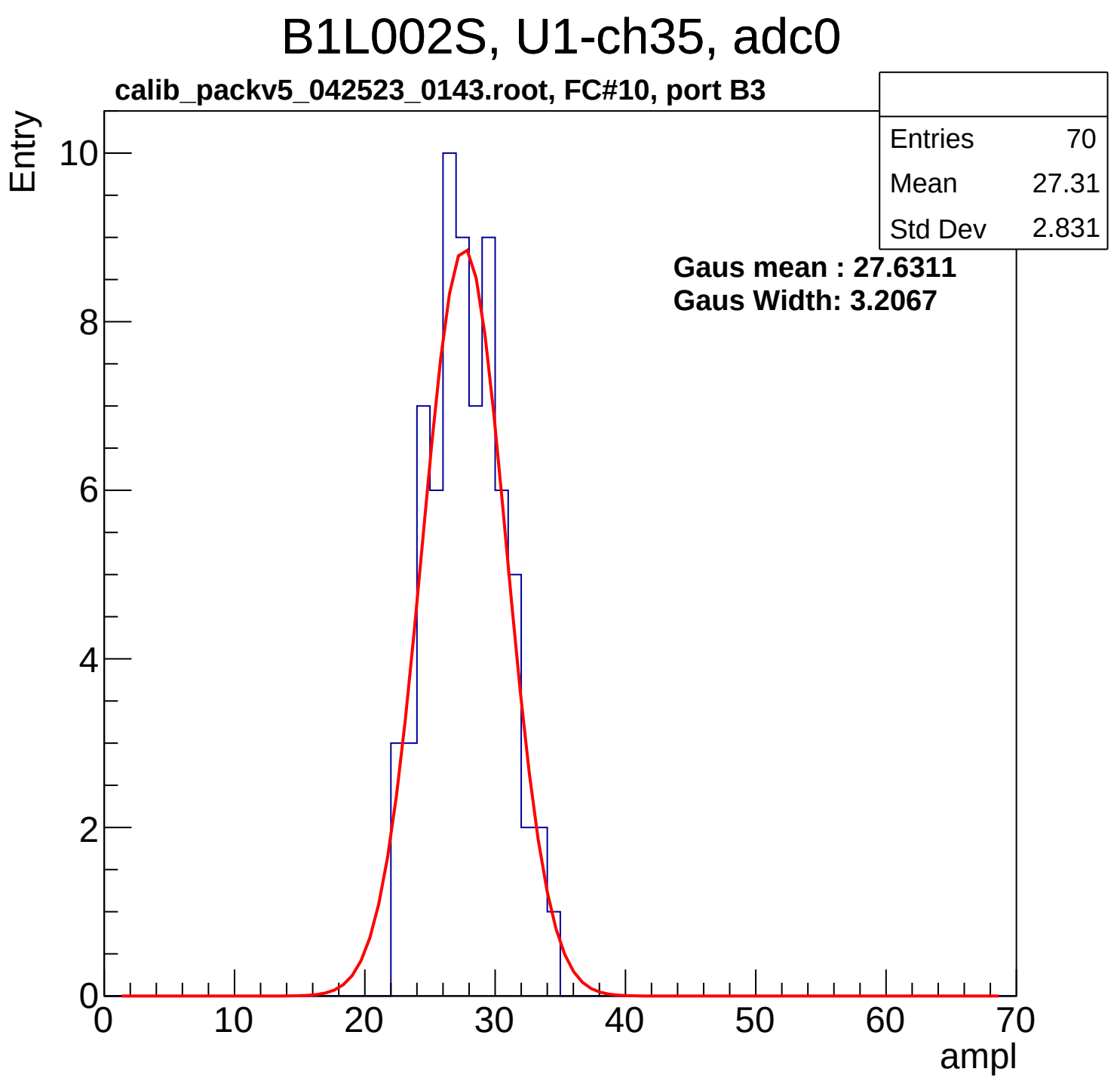
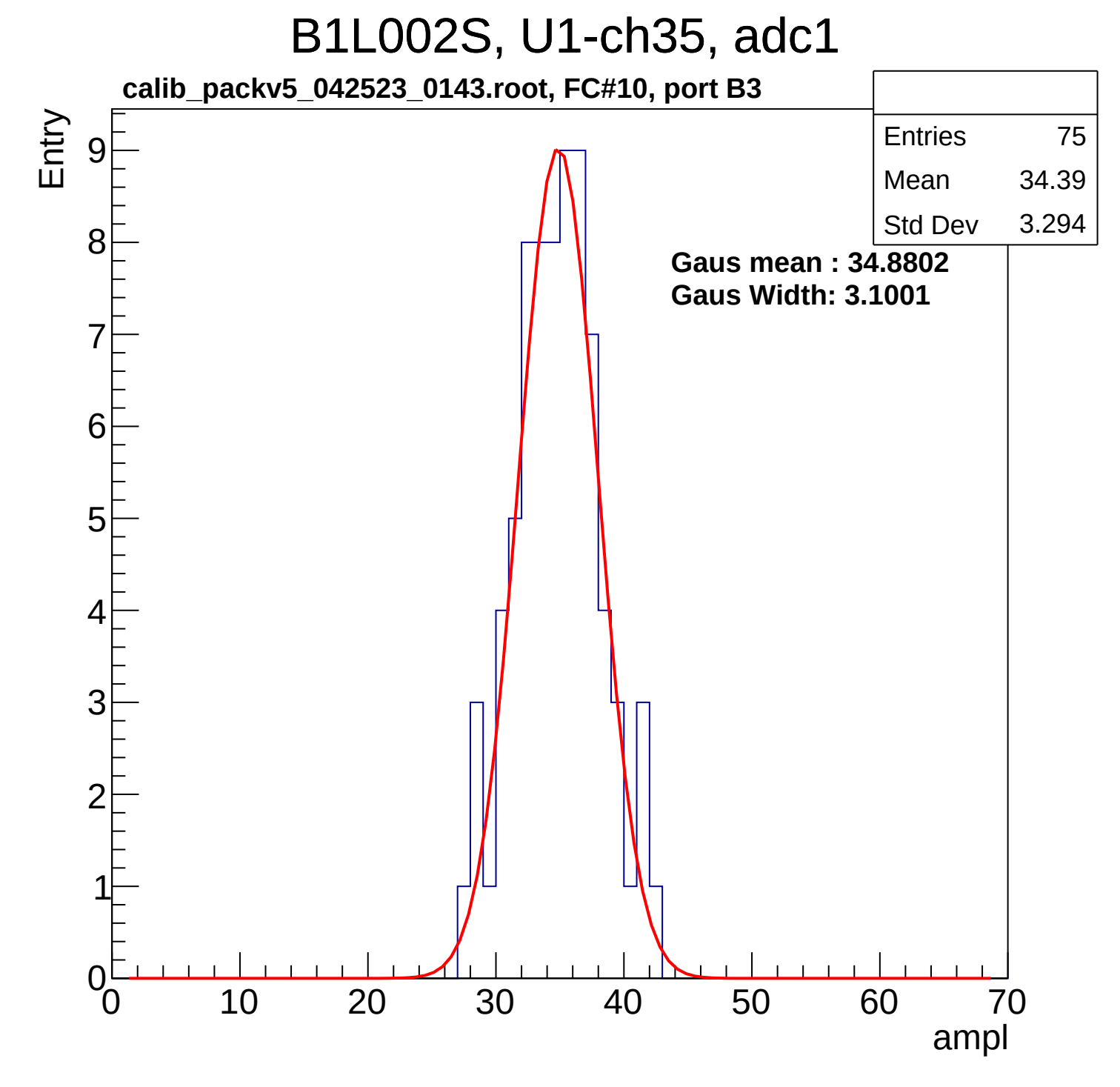

B1L002S, U1-ch35, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.31
Std Dev 2.831
Gaus mean : 27.6311
Gaus Width: 3.2067
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch35, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.39
Std Dev 3.294
Gaus mean : 34.8802
Gaus Width: 3.1001
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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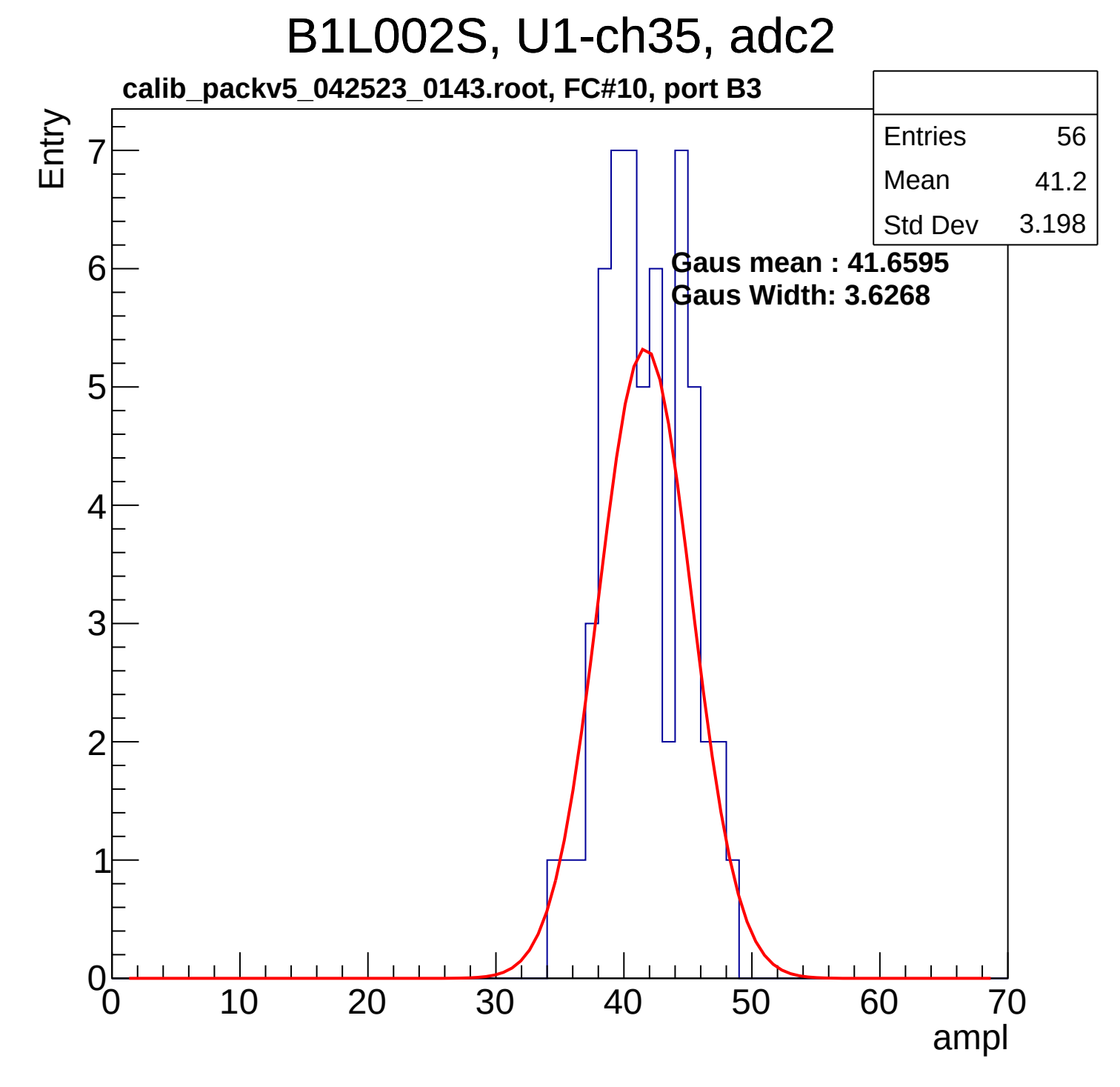
B1L002S, U1-ch35, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.2
Std Dev 3.198
Gaus mean : 41.6595
Gaus Width: 3.6268
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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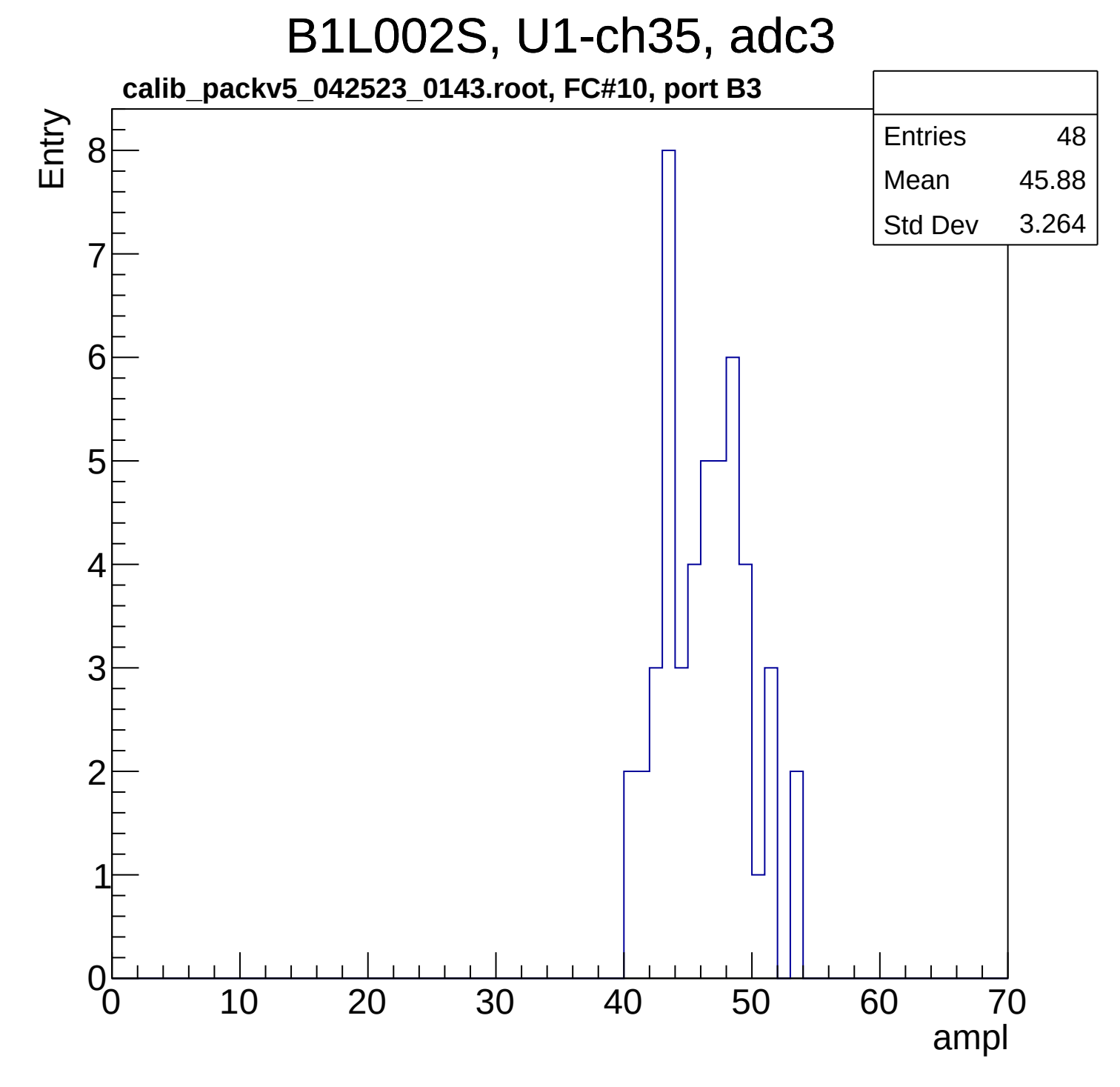
B1L002S, U1-ch35, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 45.88
Std Dev 3.264
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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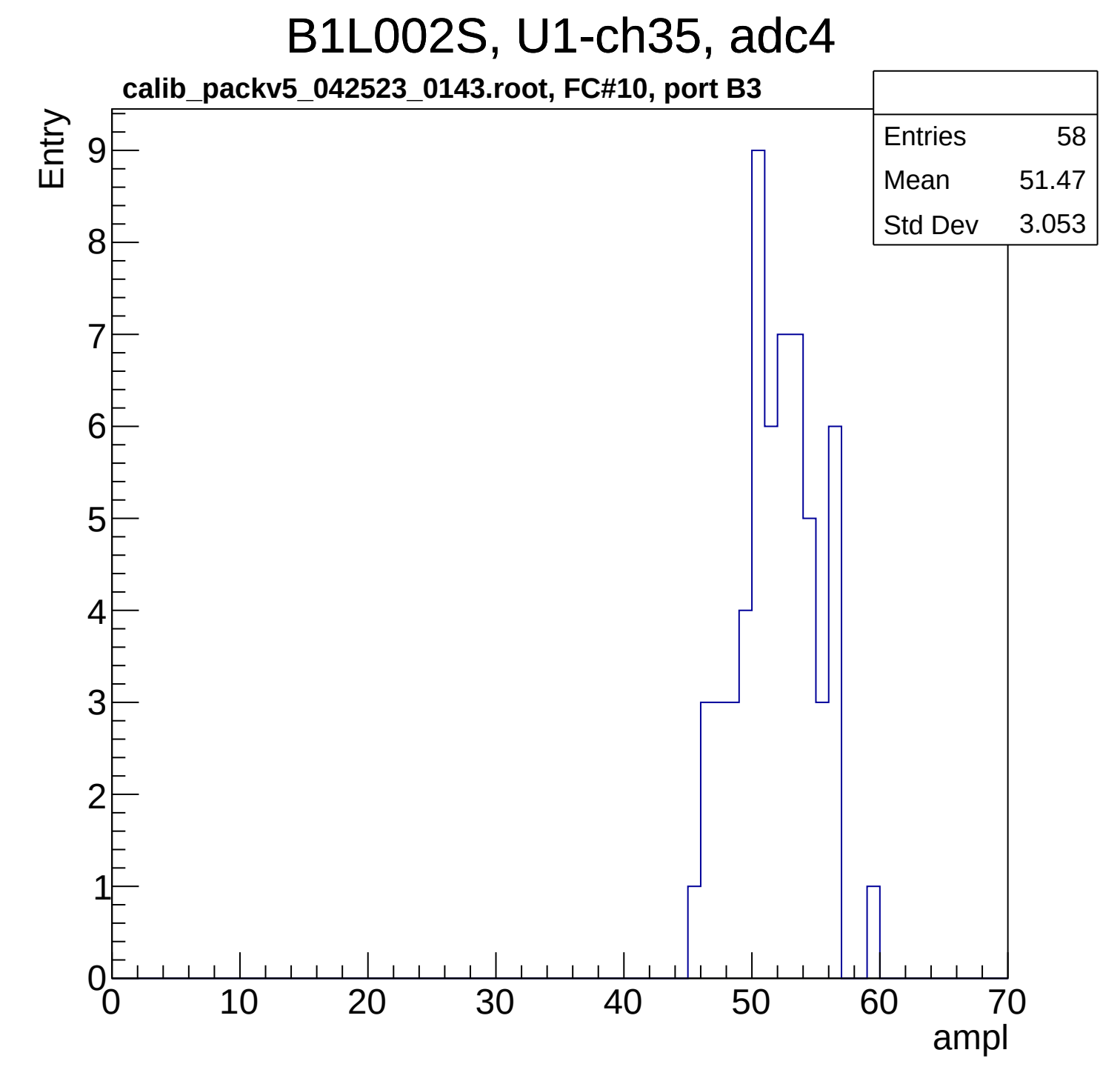
B1L002S, U1-ch35, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 51.47
Std Dev 3.053
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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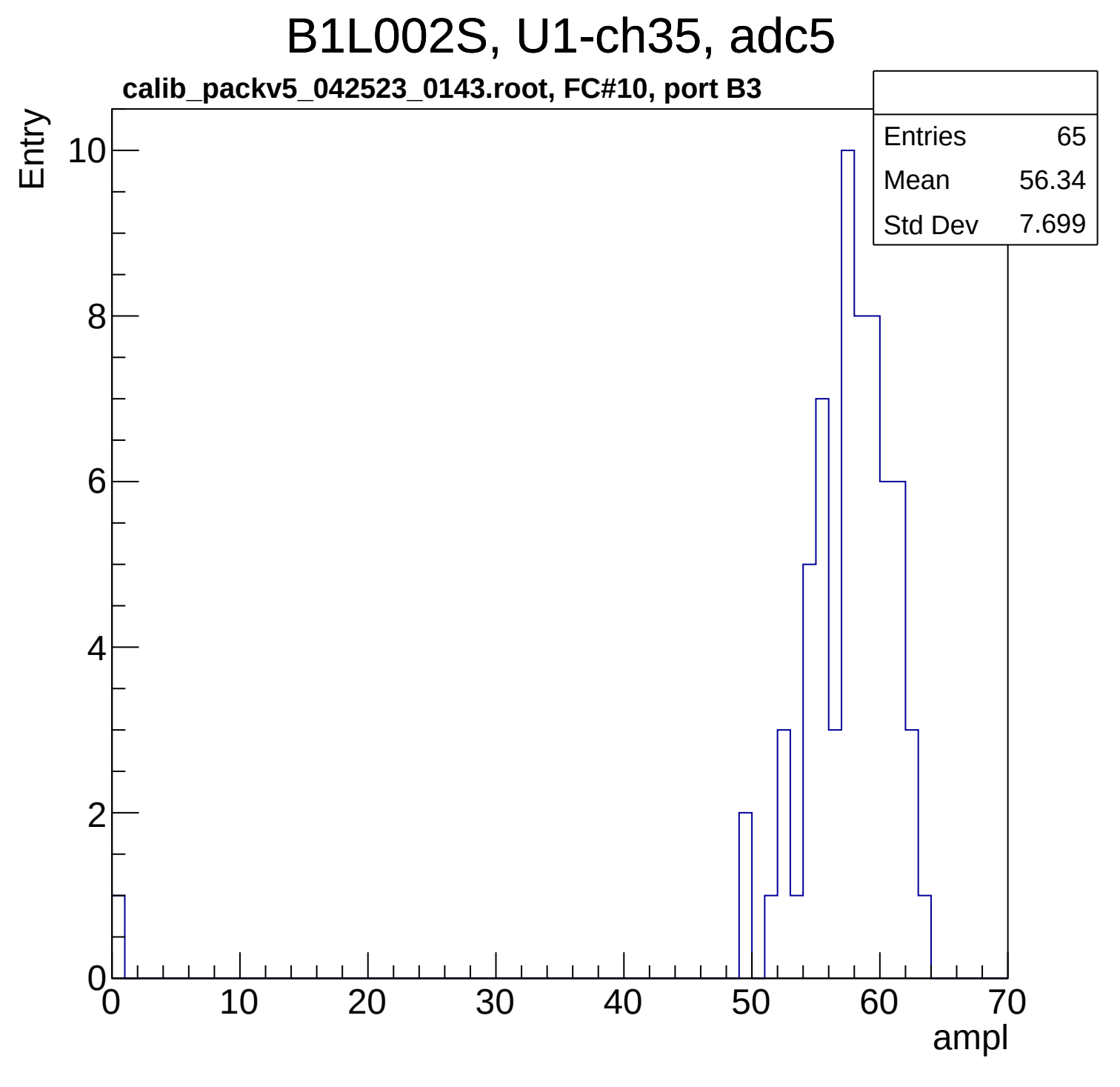
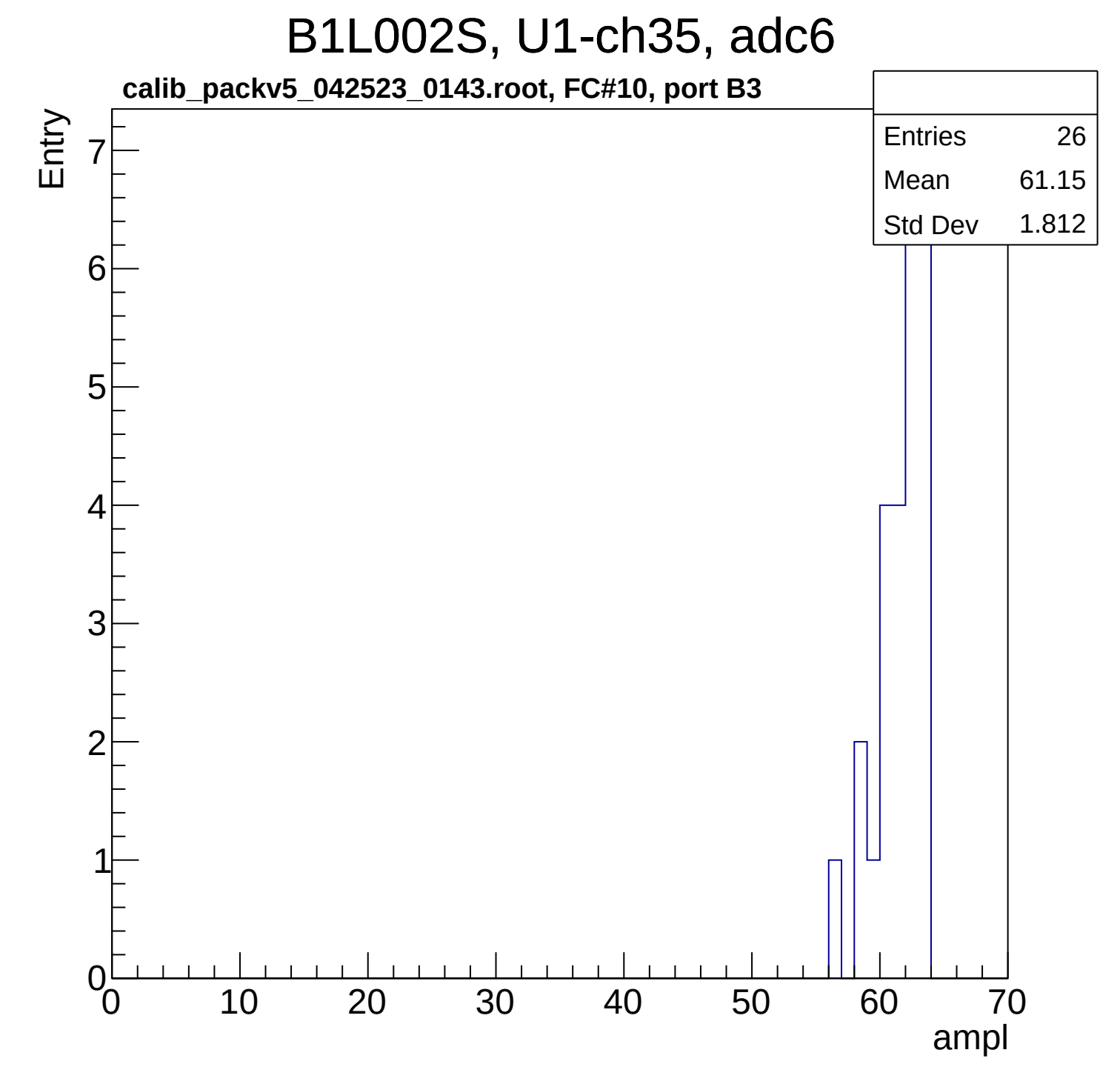

B1L002S, U1-ch35, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 56.34
Std Dev 7.699
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch35, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 61.15
Std Dev 1.812
Entry
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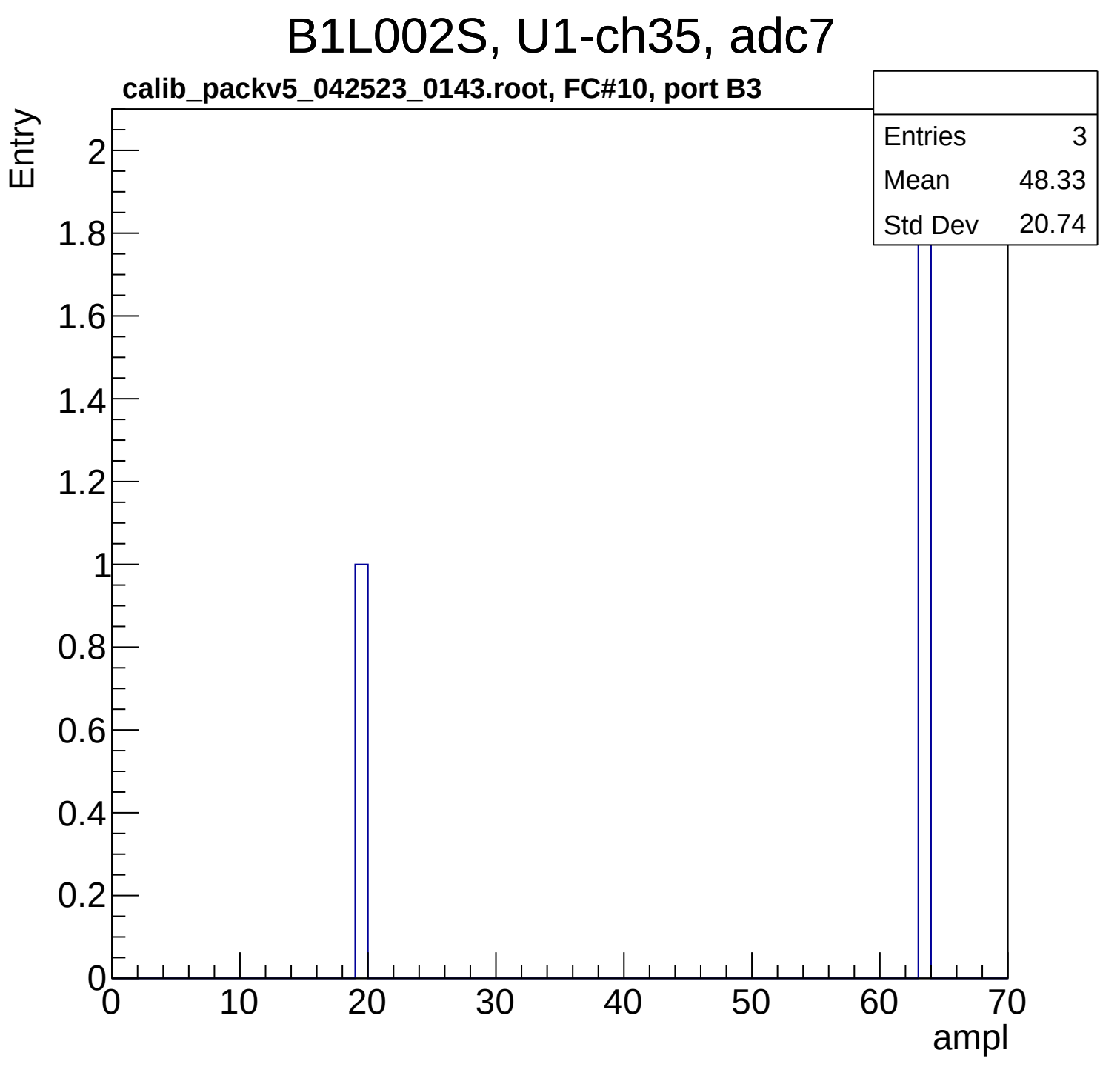
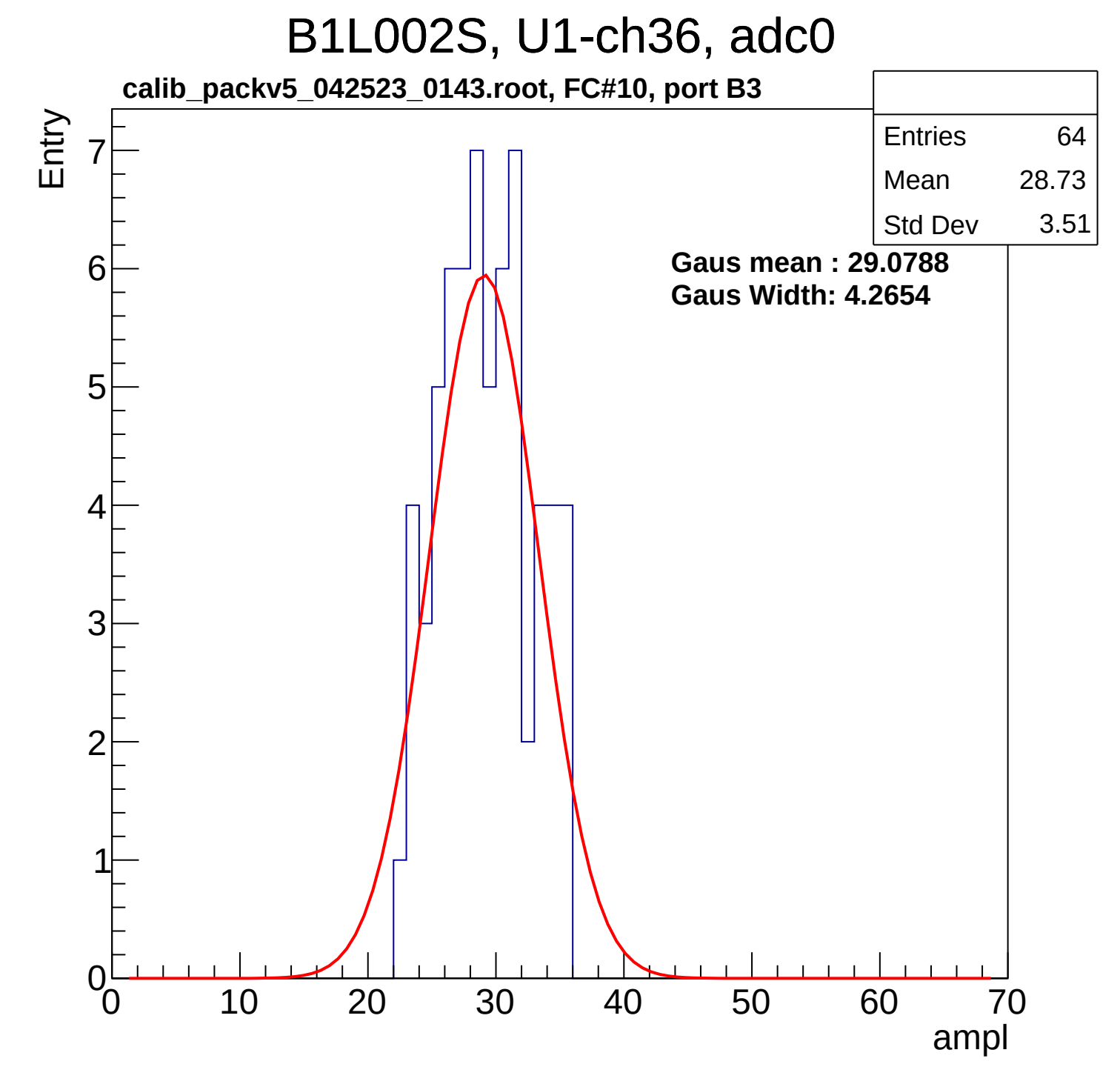
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch35, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 48.33
Std Dev 20.74
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch36, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.73
Std Dev 3.51
Gaus mean : 29.0788
Gaus Width: 4.2654
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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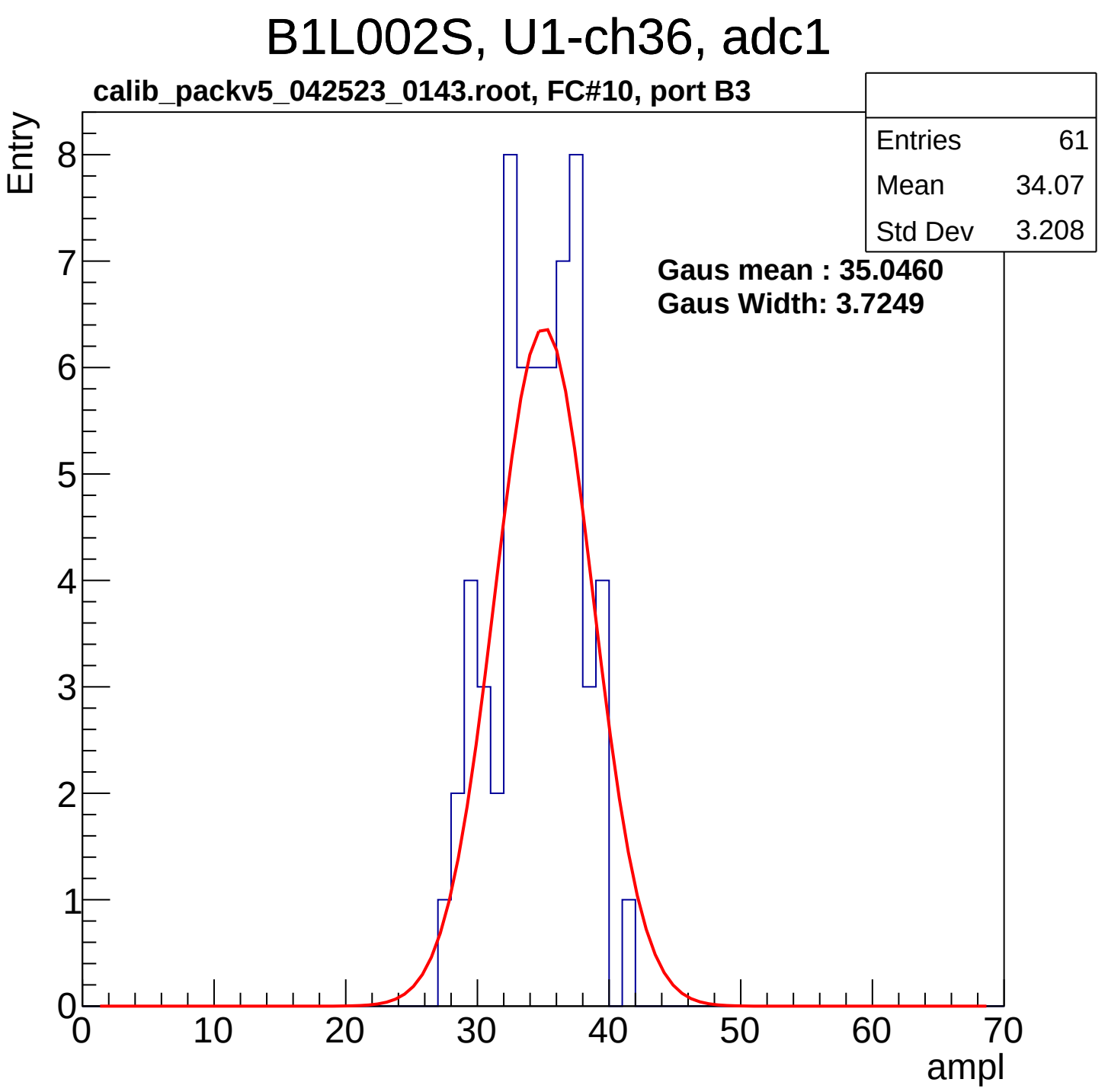
B1L002S, U1-ch36, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.07
Std Dev 3.208
Gaus mean : 35.0460
Gaus Width: 3.7249
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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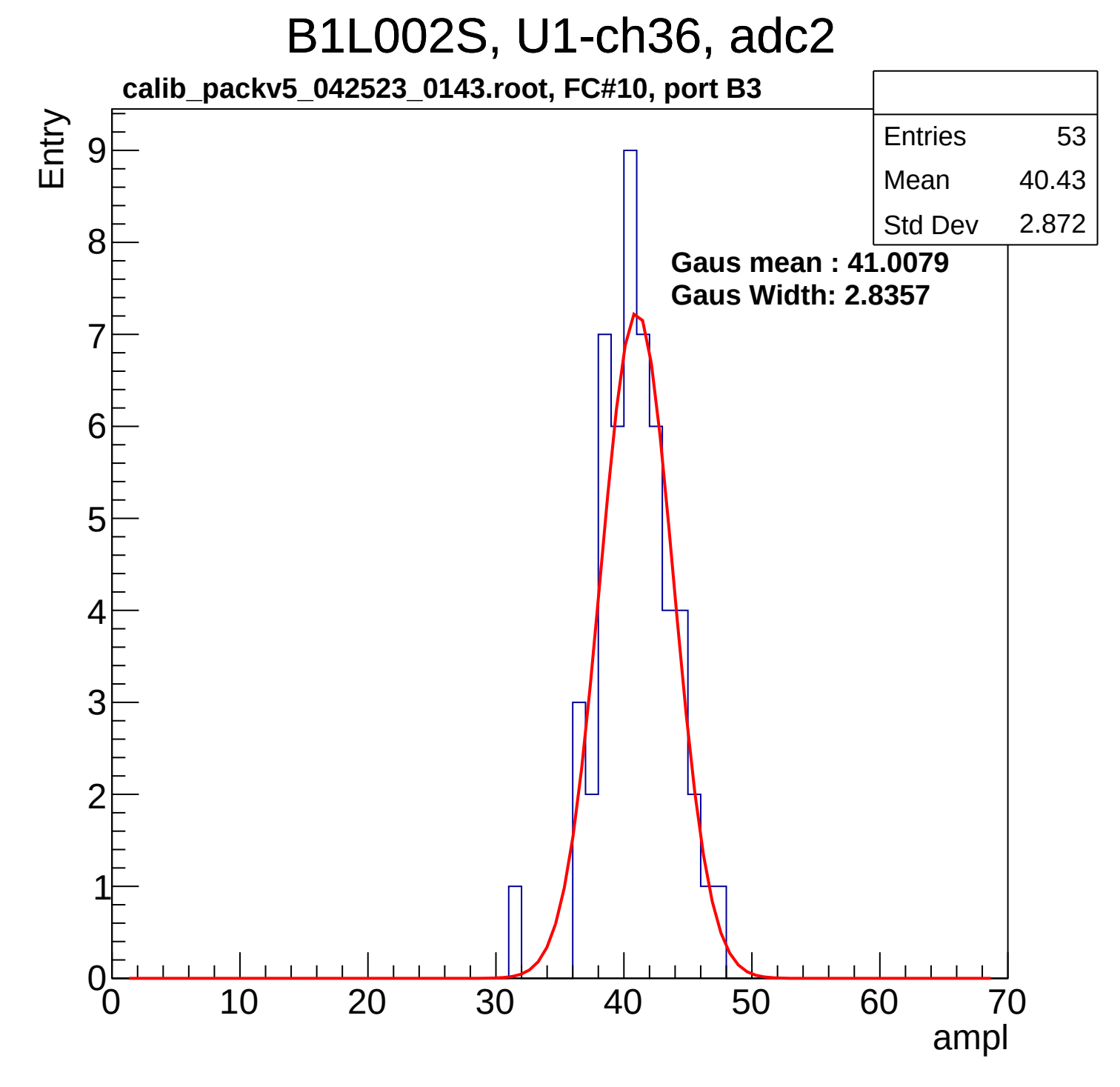
B1L002S, U1-ch36, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 40.43
Std Dev 2.872
Gaus mean : 41.0079
Gaus Width: 2.8357
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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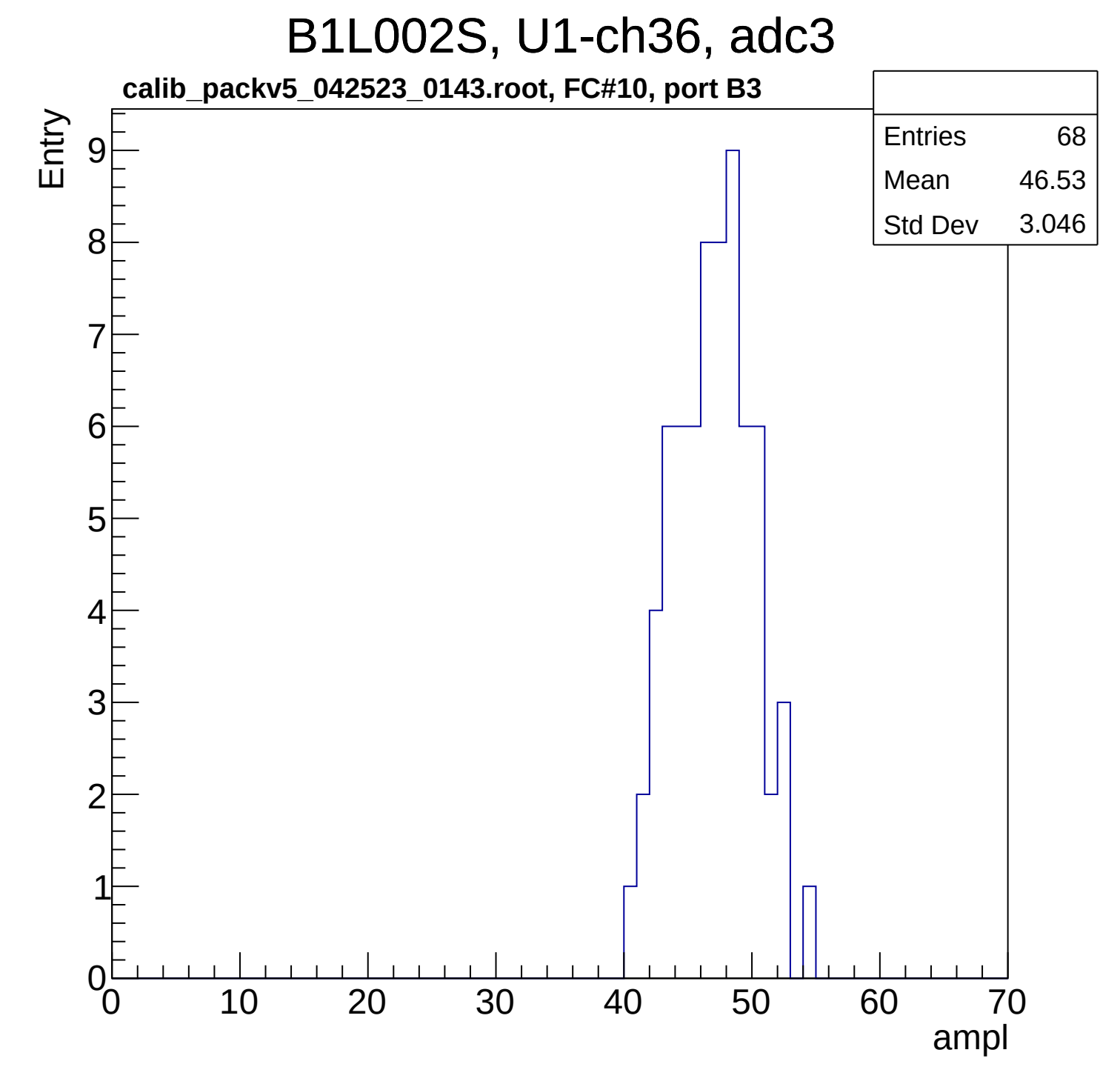
B1L002S, U1-ch36, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.53
Std Dev 3.046
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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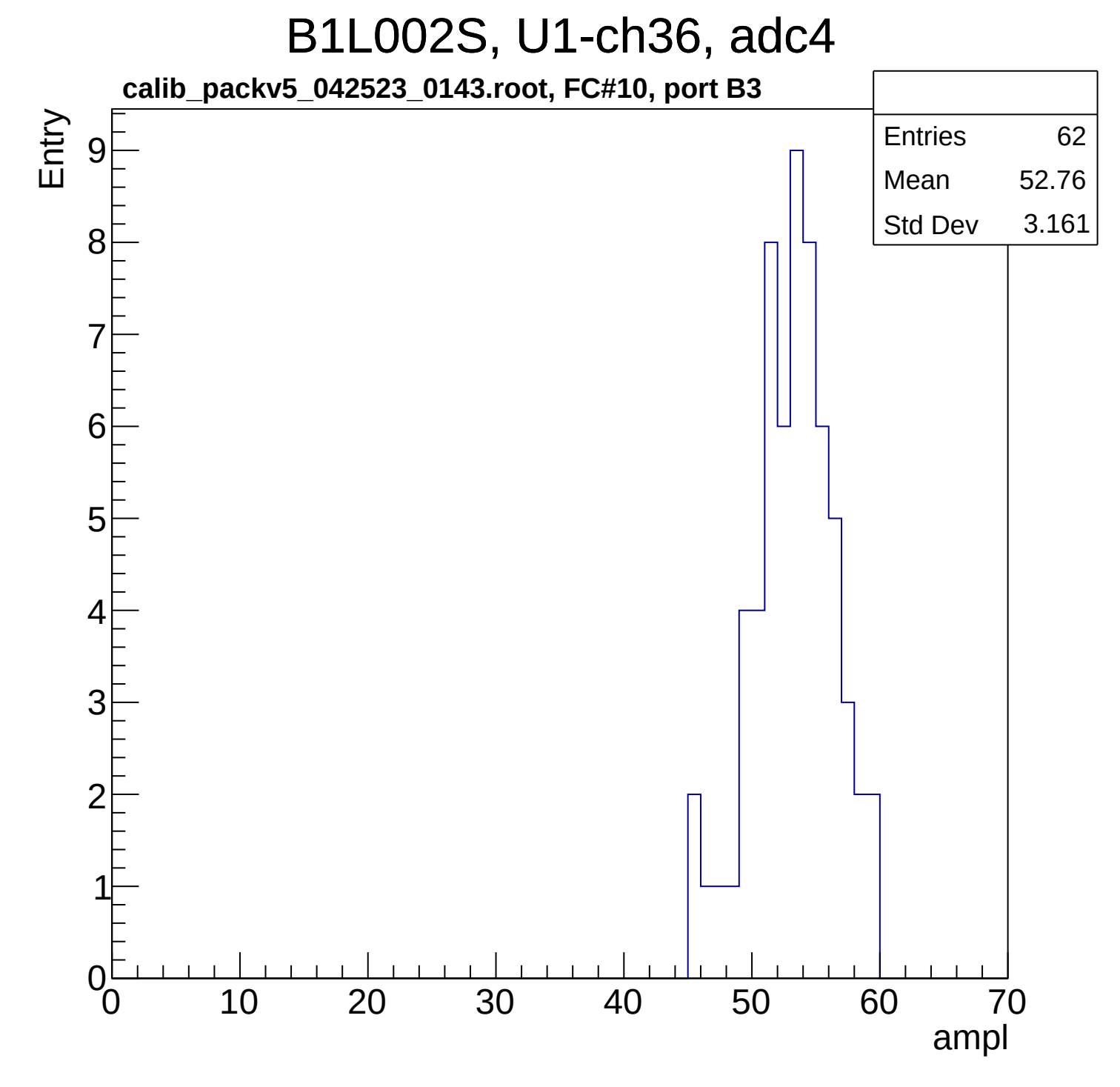
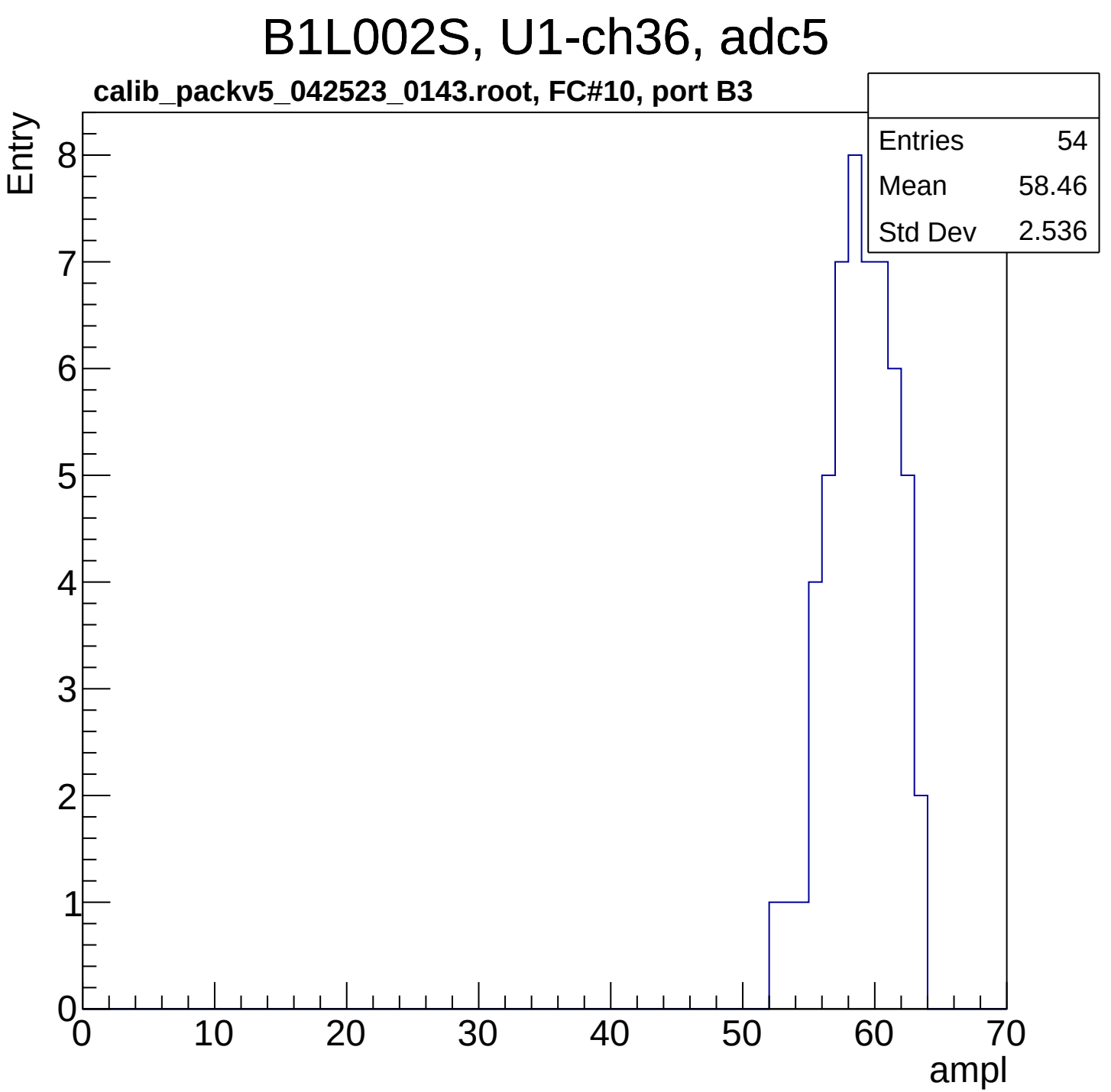

B1L002S, U1-ch36, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.76
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch36, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.46
Std Dev 2.536
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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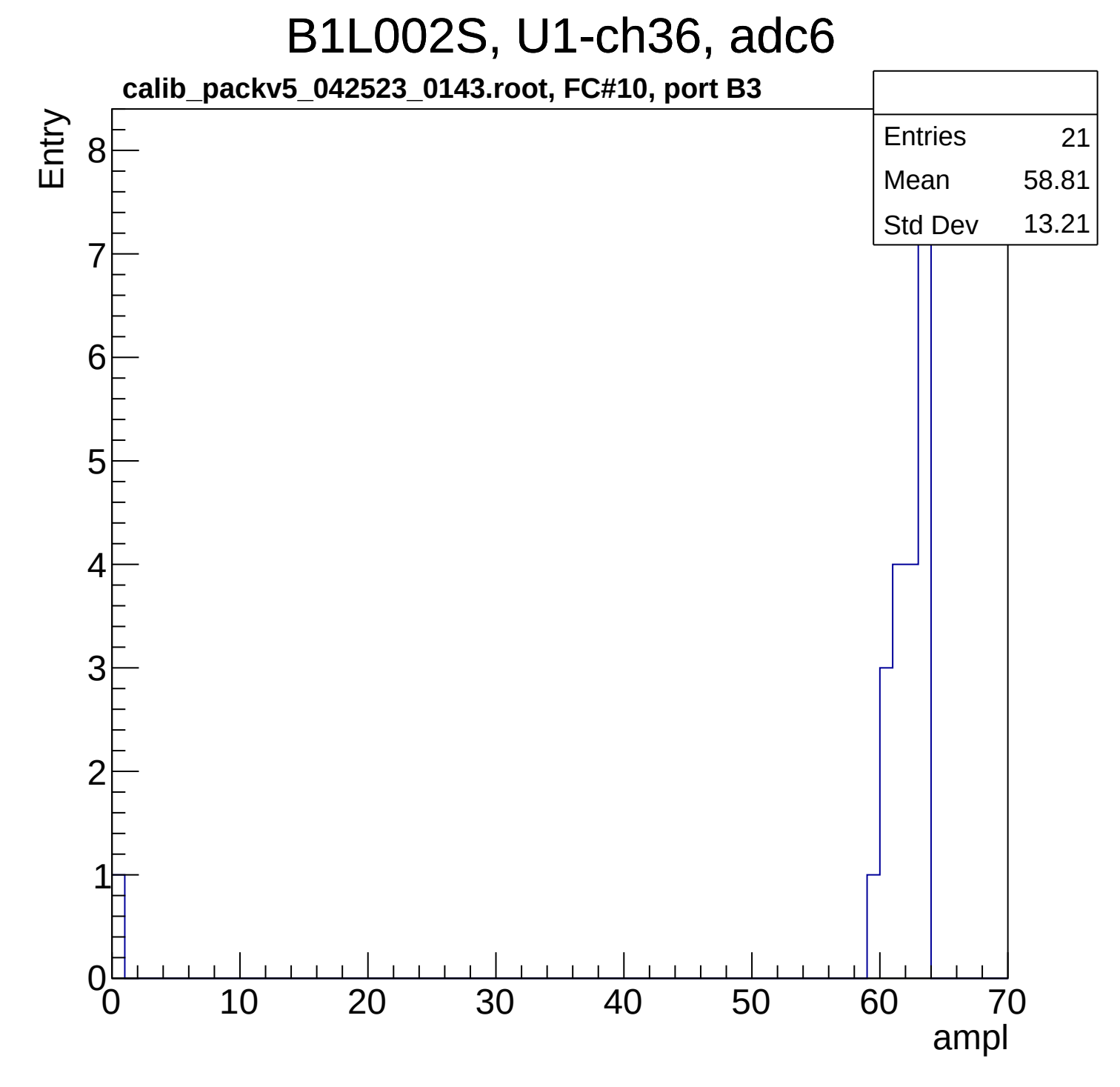
B1L002S, U1-ch36, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.81
Std Dev 13.21
Entry
ampl
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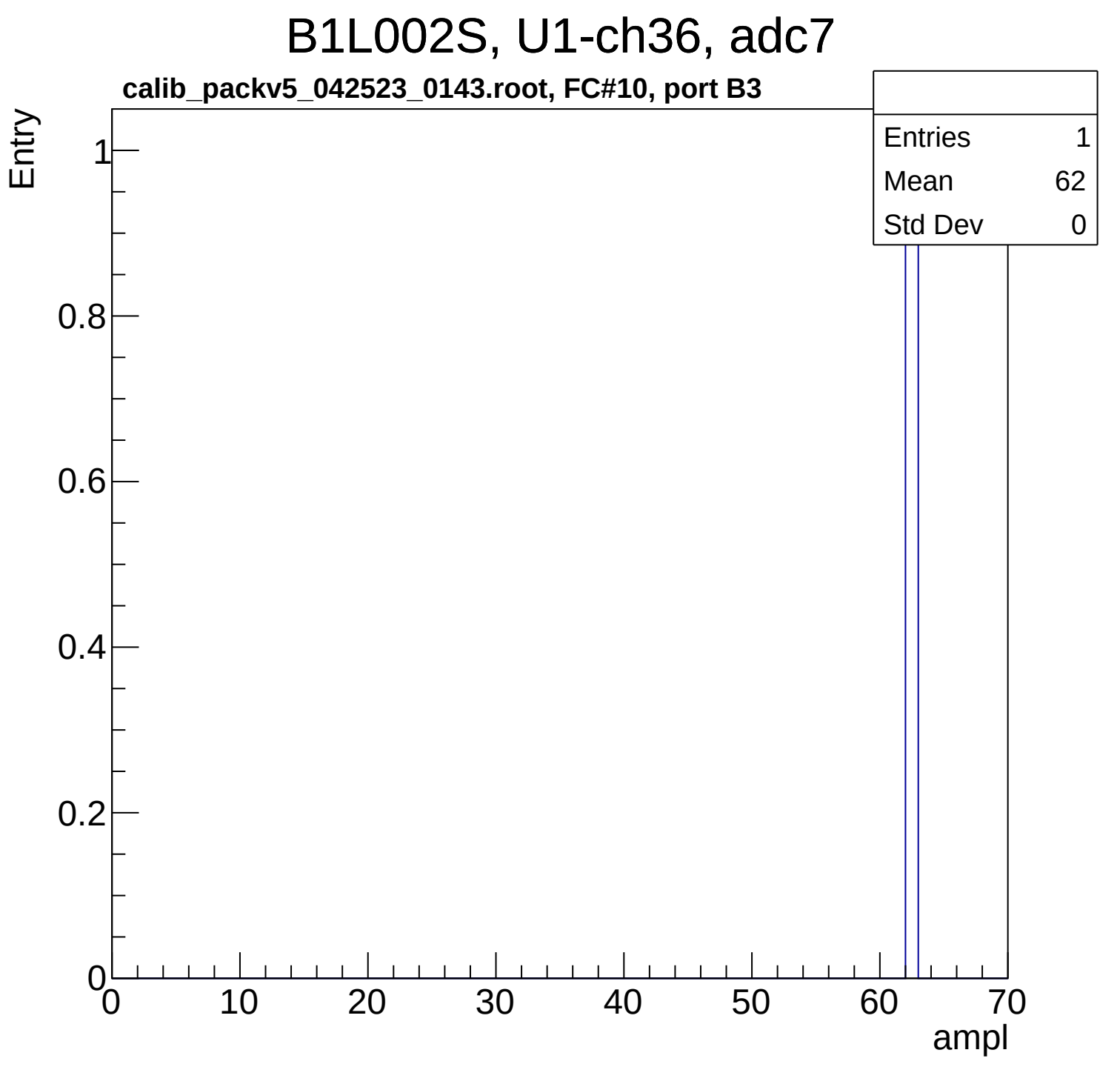
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch36, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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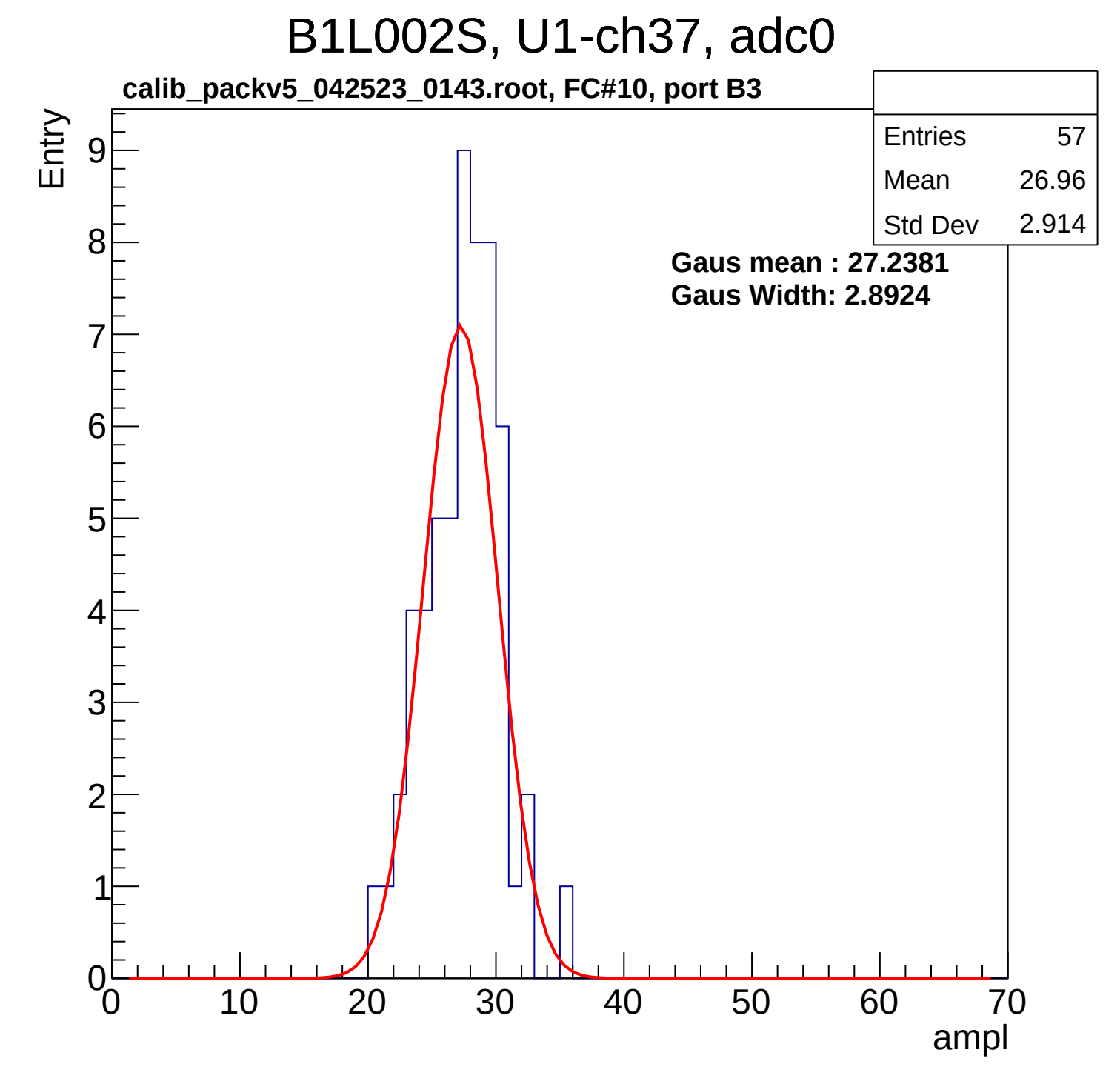
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch37, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 26.96
Std Dev 2.914
Gaus mean : 27.2381
Gaus Width: 2.8924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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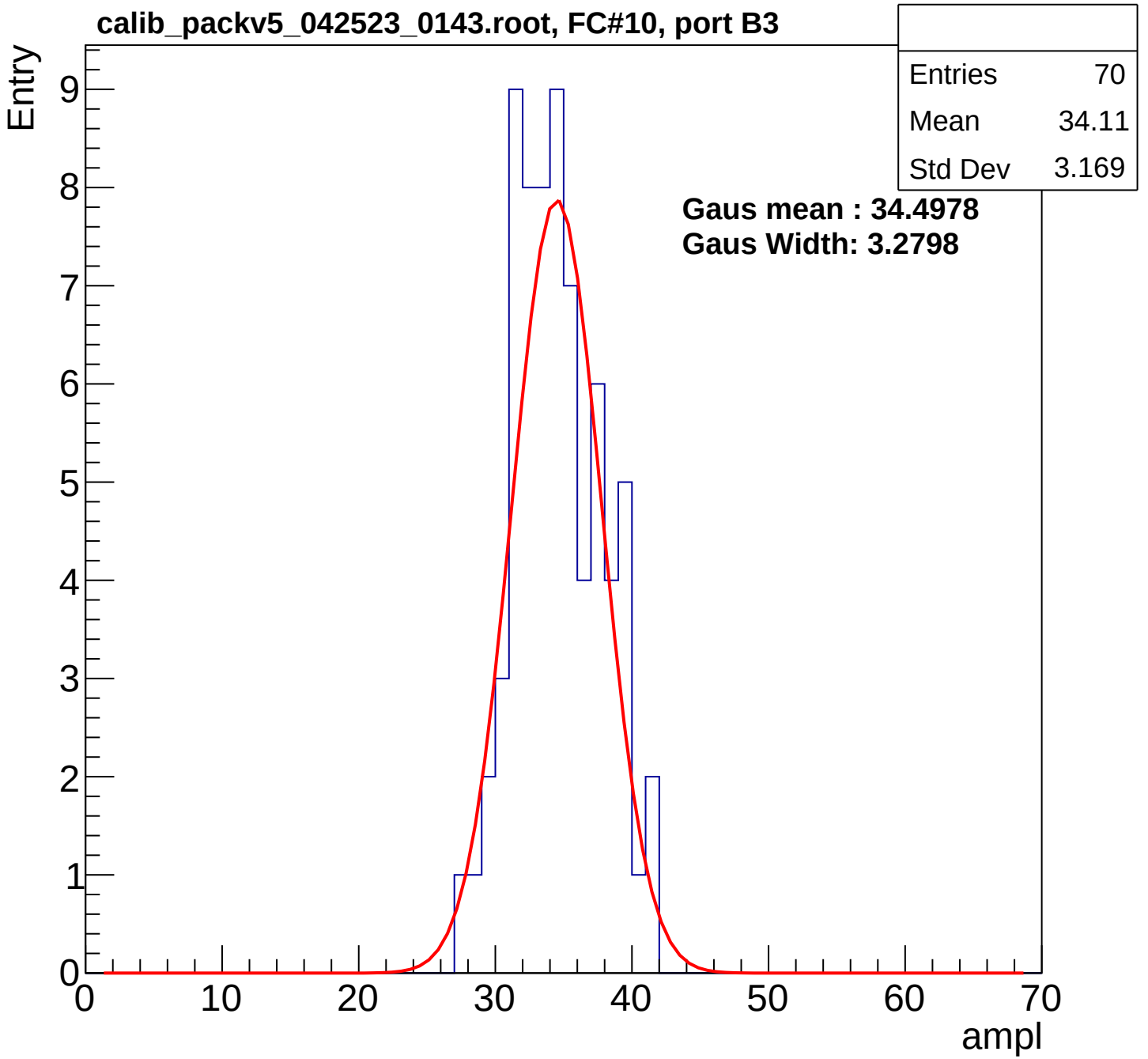
B1L002S, U1-ch37, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.11
Std Dev 3.169
Gaus mean : 34.4978
Gaus Width: 3.2798
Entry
ampl
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2
3
4
5
6
7
8
9
0
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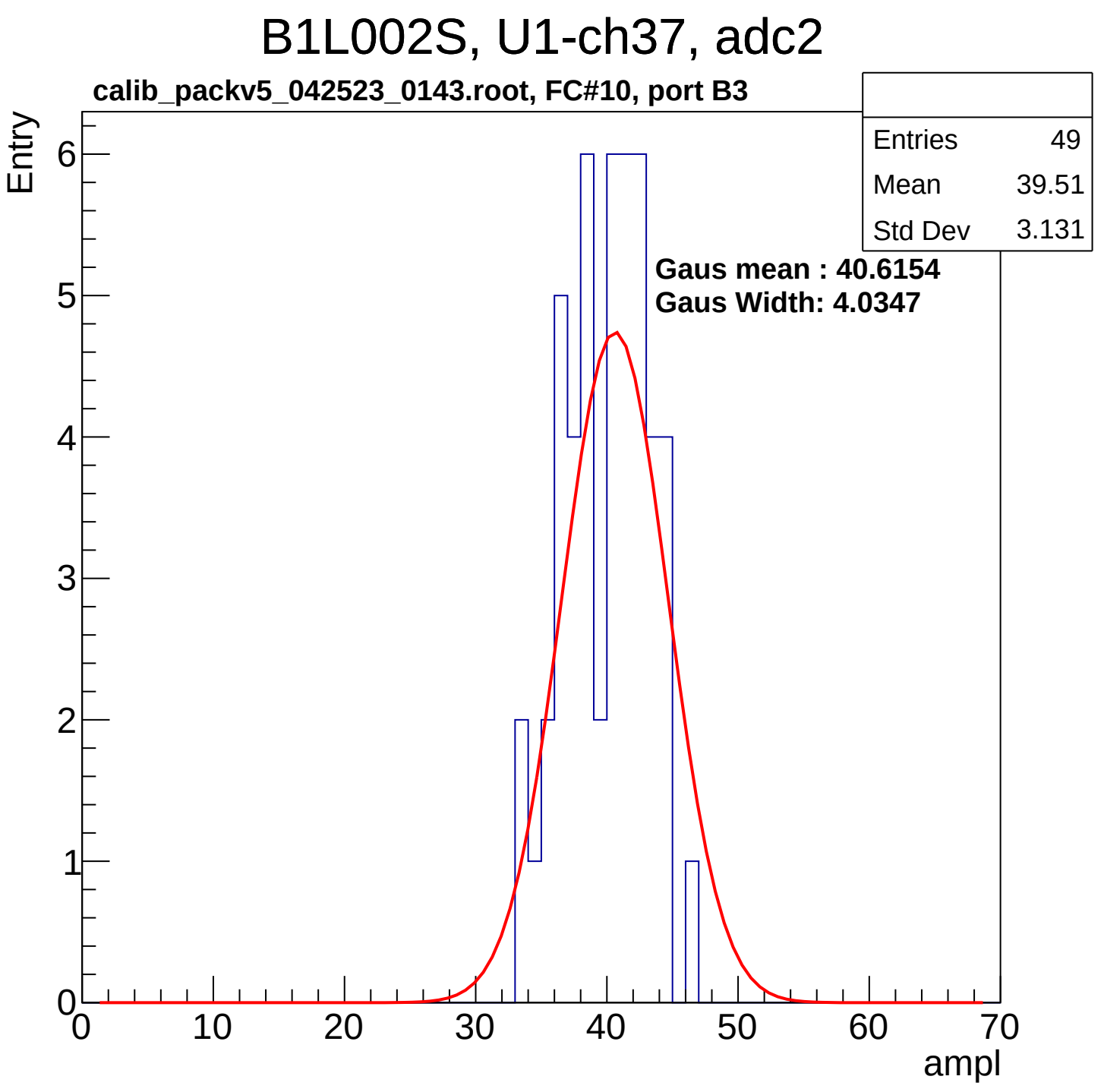
B1L002S, U1-ch37, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 39.51
Std Dev 3.131
Gaus mean : 40.6154
Gaus Width: 4.0347
Entry
ampl
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3
4
5
6
0
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40
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60
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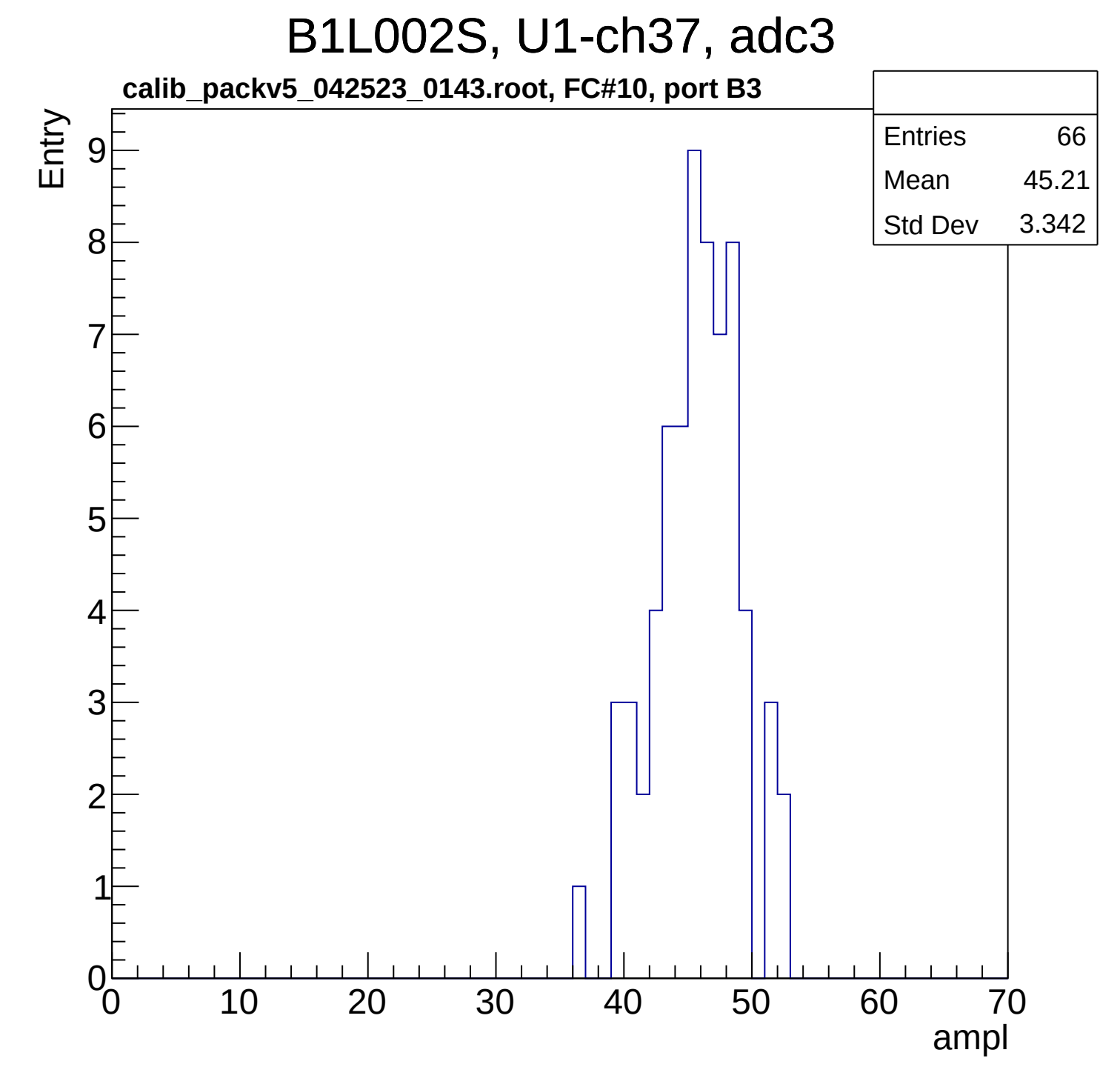
B1L002S, U1-ch37, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 45.21
Std Dev 3.342
Entry
ampl
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6
7
8
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0
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60
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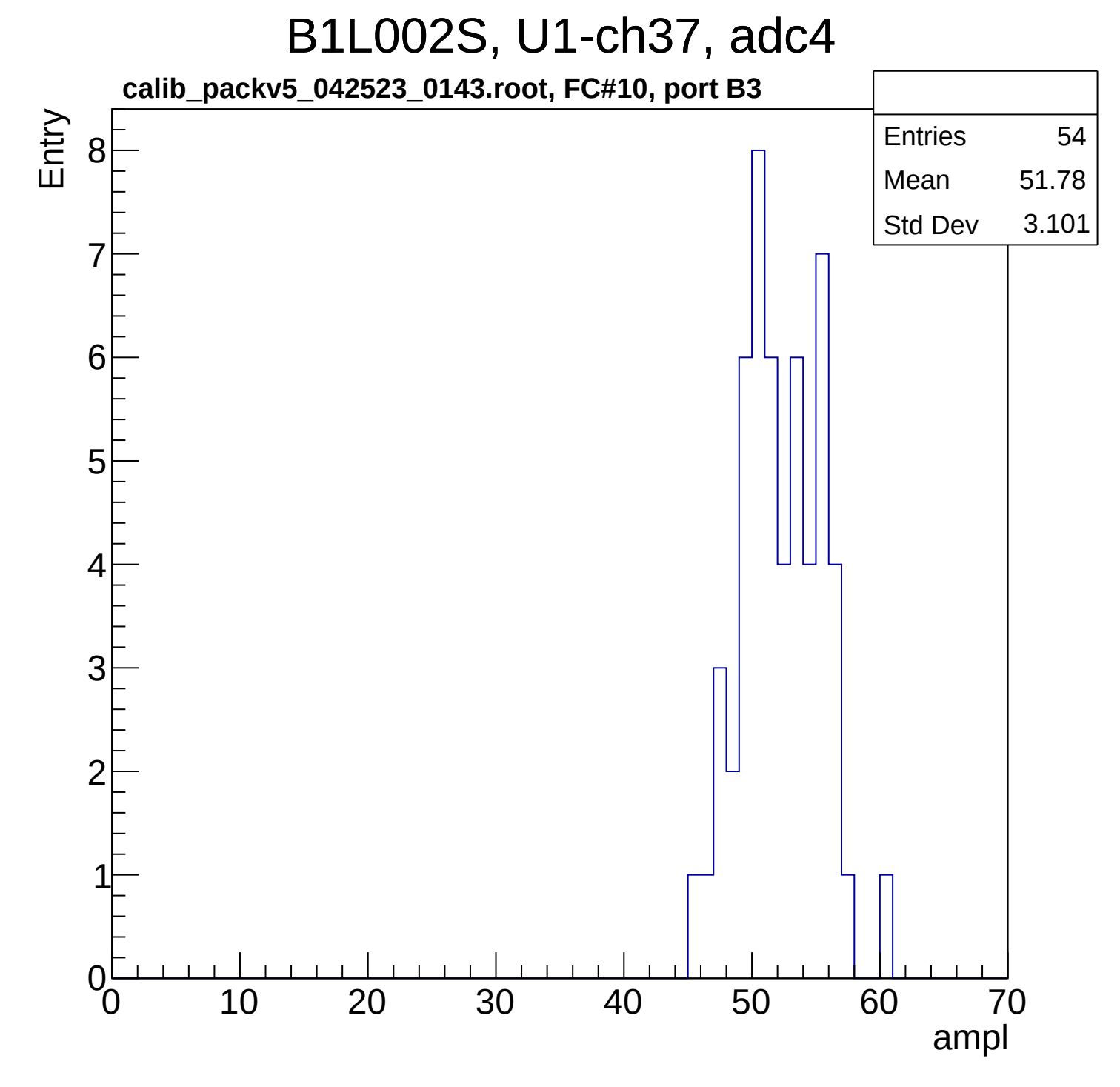
B1L002S, U1-ch37, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 51.78
Std Dev 3.101
Entry
ampl
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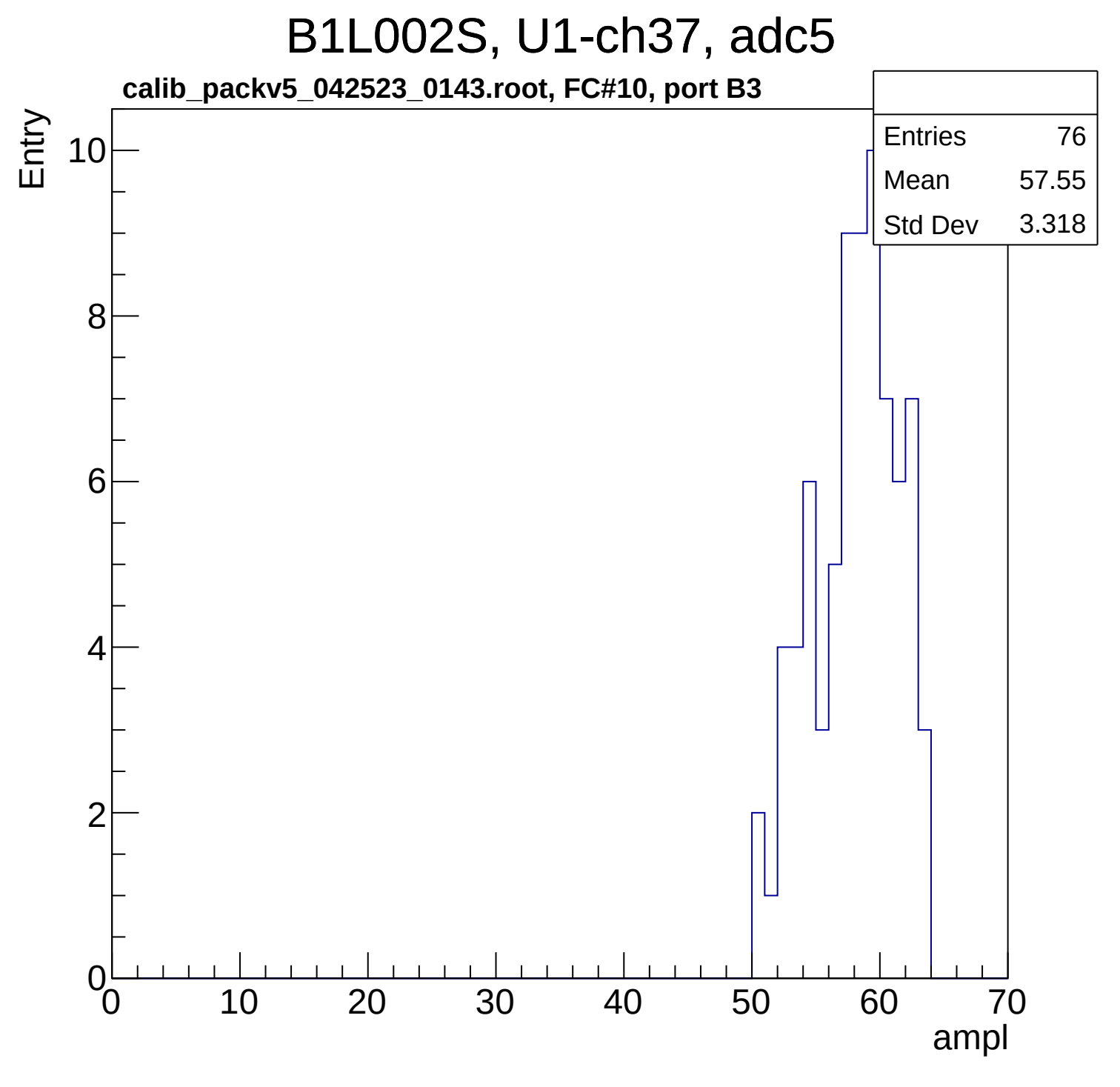
B1L002S, U1-ch37, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 57.55
Std Dev 3.318
Entry
ampl
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2
4
6
8
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0
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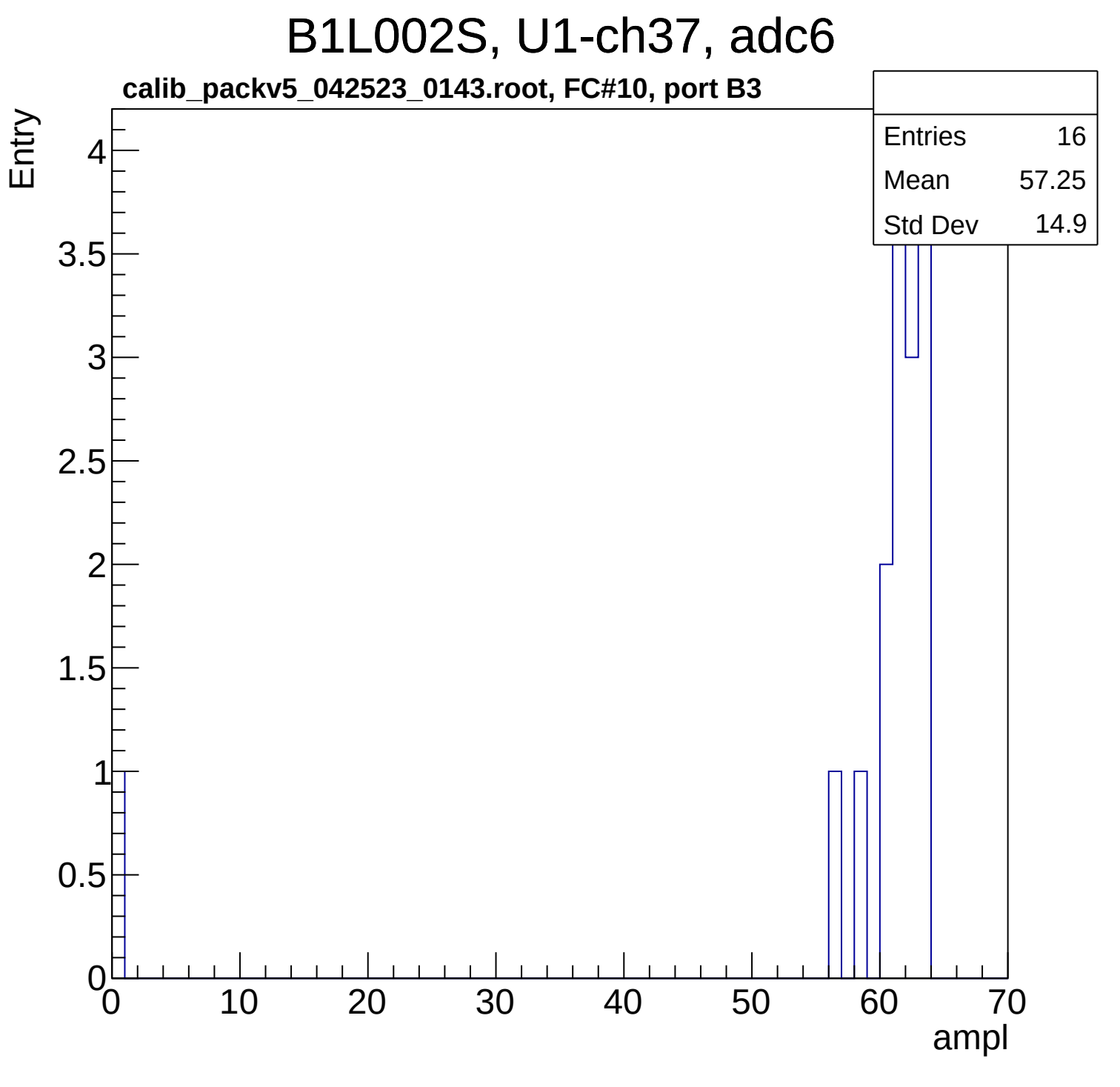
60
70


B1L002S, U1-ch37, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.25
Std Dev 14.9
Entry
ampl
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1.5
2
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3
3.5
4
0
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20
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40
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60
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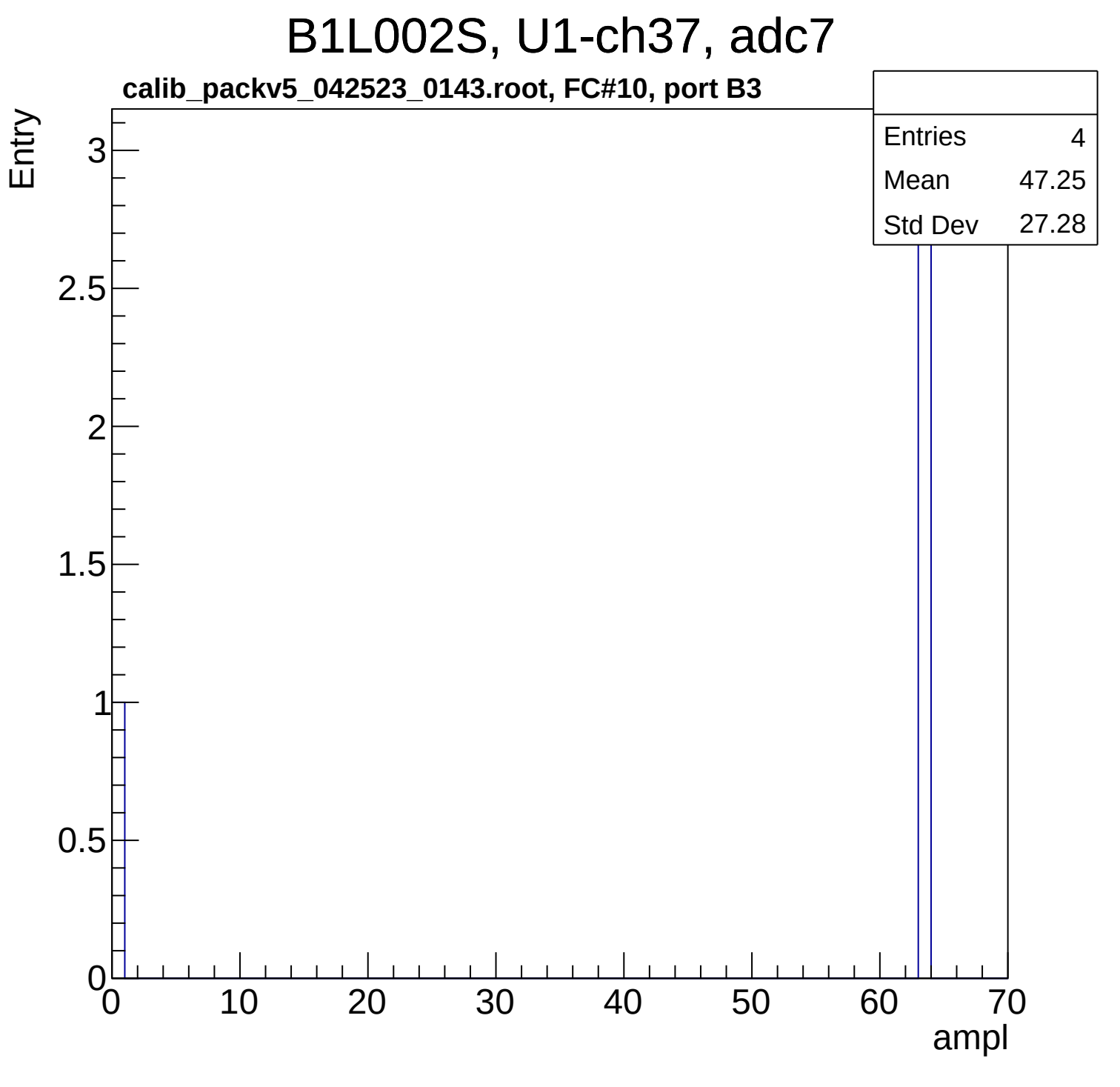
B1L002S, U1-ch37, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 47.25
Std Dev 27.28
Entry
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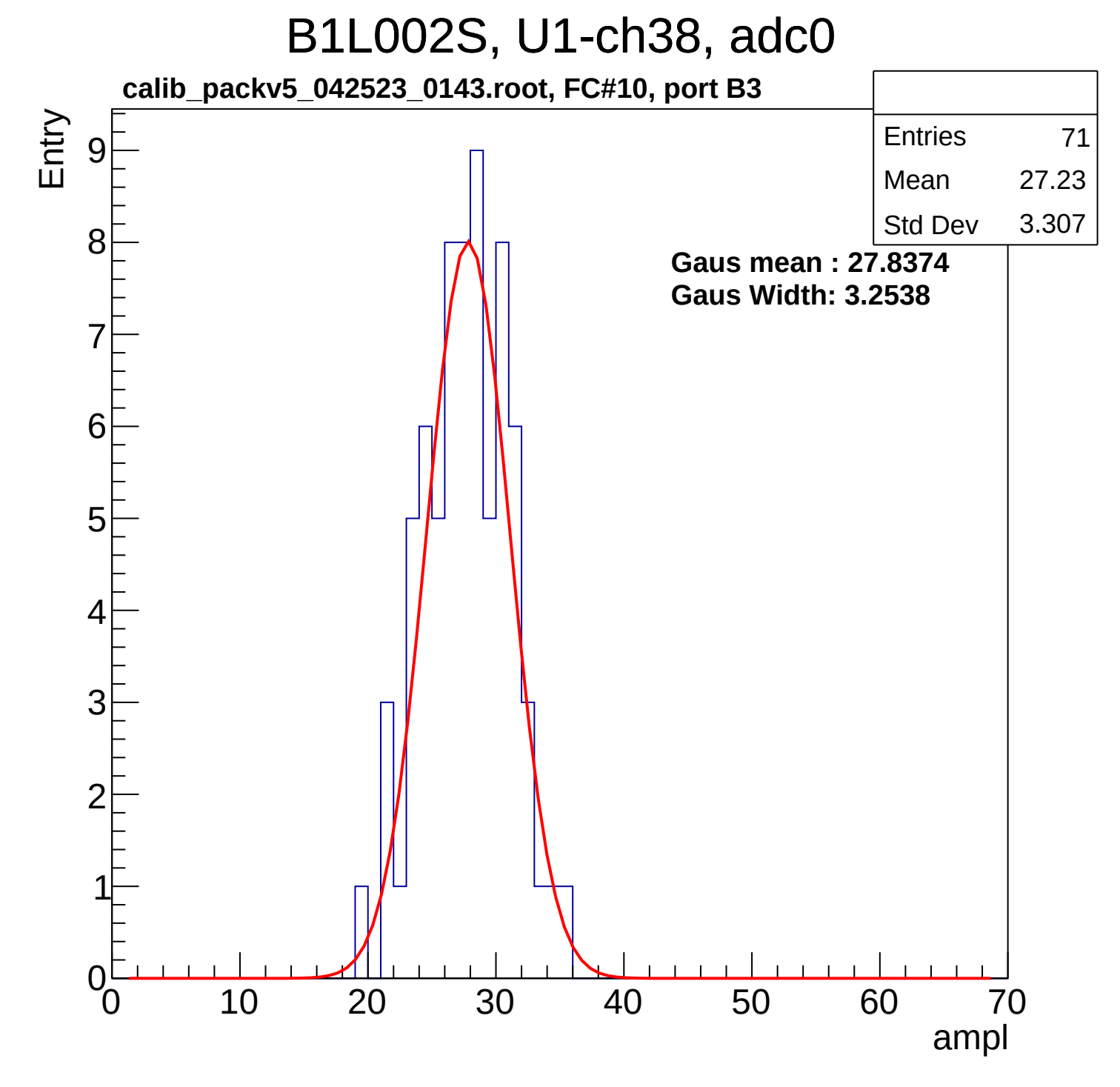
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch38, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.23
Std Dev 3.307
Gaus mean : 27.8374
Gaus Width: 3.2538
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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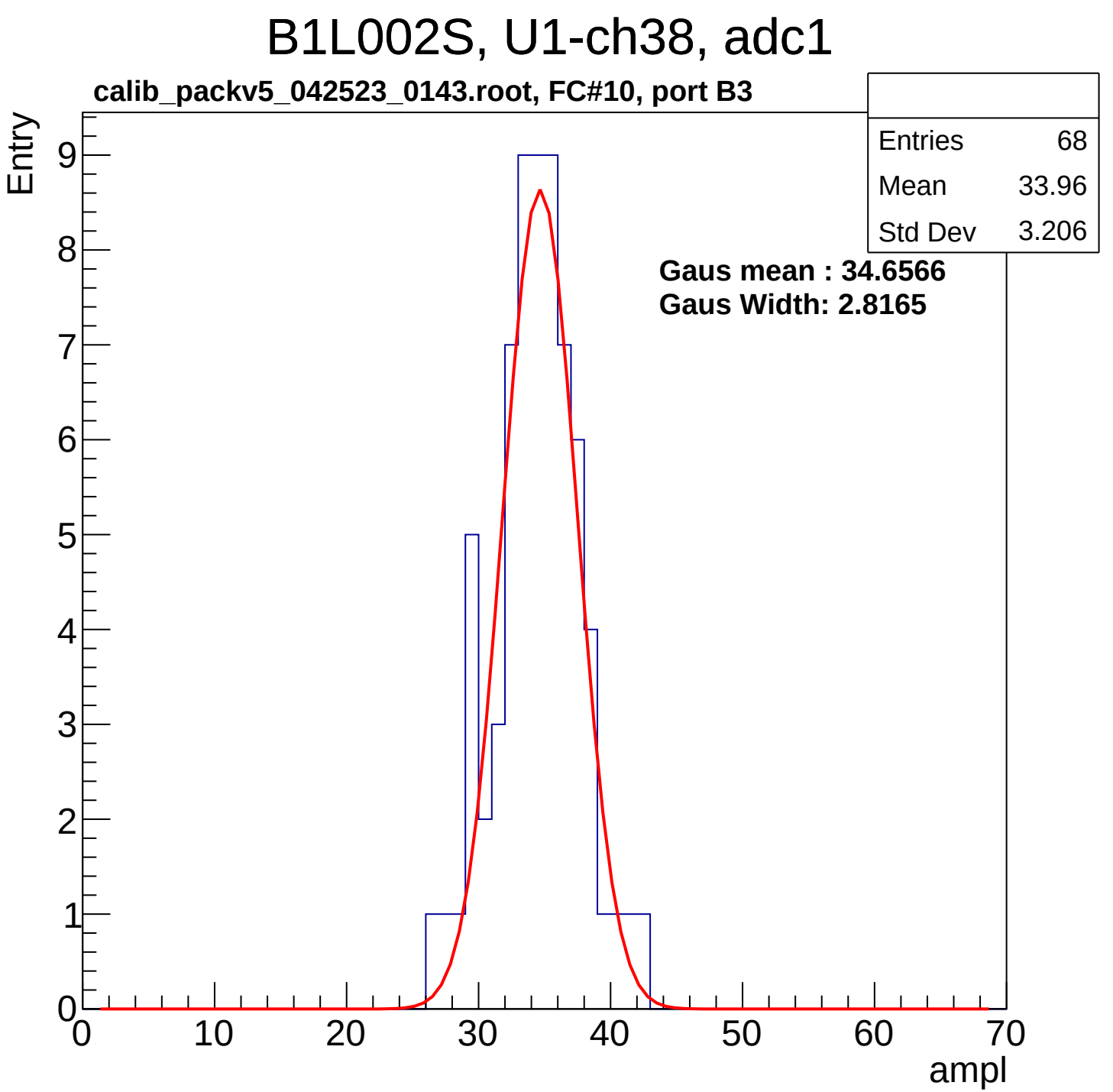
B1L002S, U1-ch38, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 33.96
Std Dev 3.206
Gaus mean : 34.6566
Gaus Width: 2.8165
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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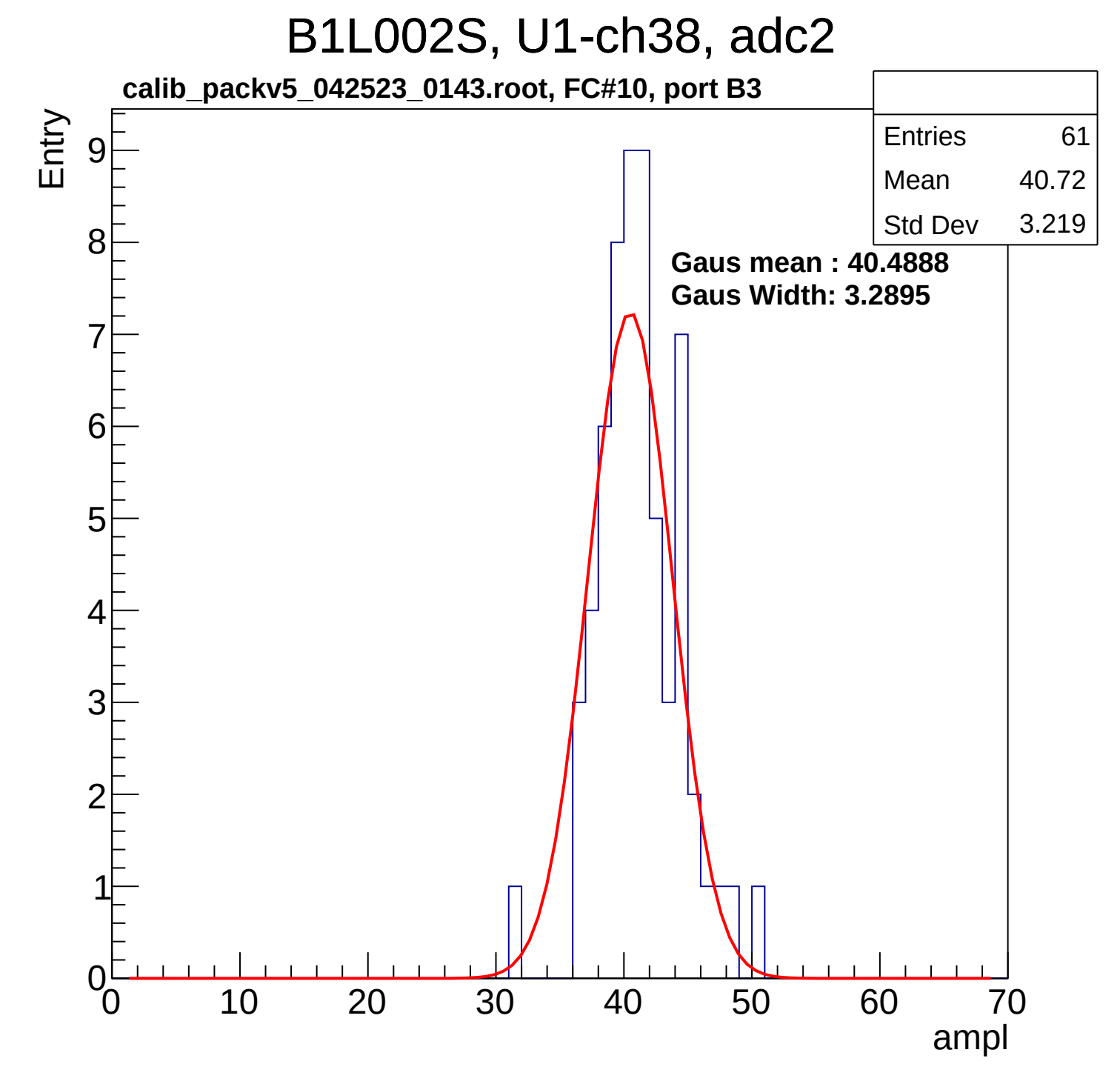
B1L002S, U1-ch38, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.72
Std Dev 3.219
Gaus mean : 40.4888
Gaus Width: 3.2895
Entry
ampl
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2
3
4
5
6
7
8
9
0
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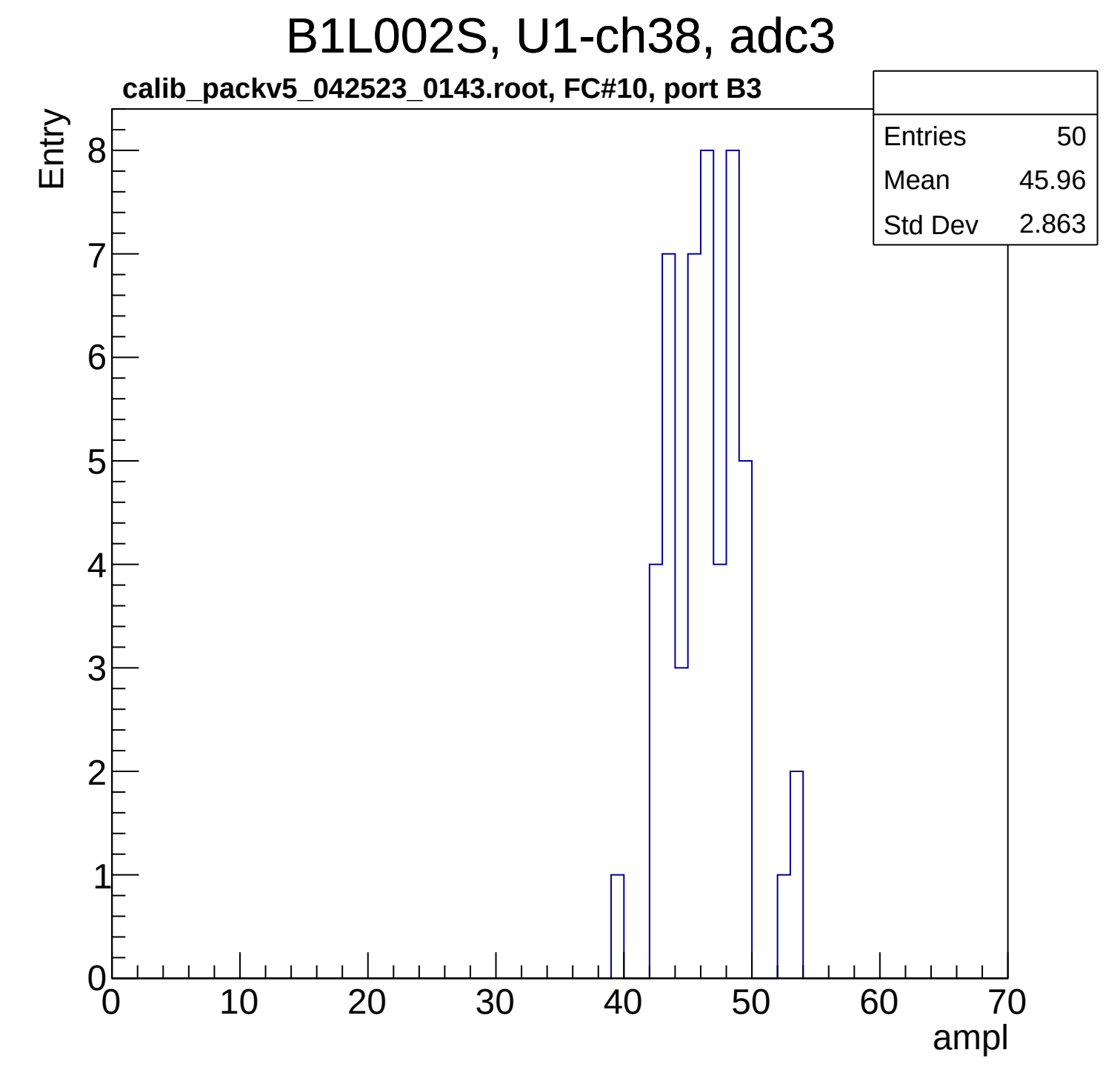
B1L002S, U1-ch38, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 45.96
Std Dev 2.863
Entry
ampl
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2
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4
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7
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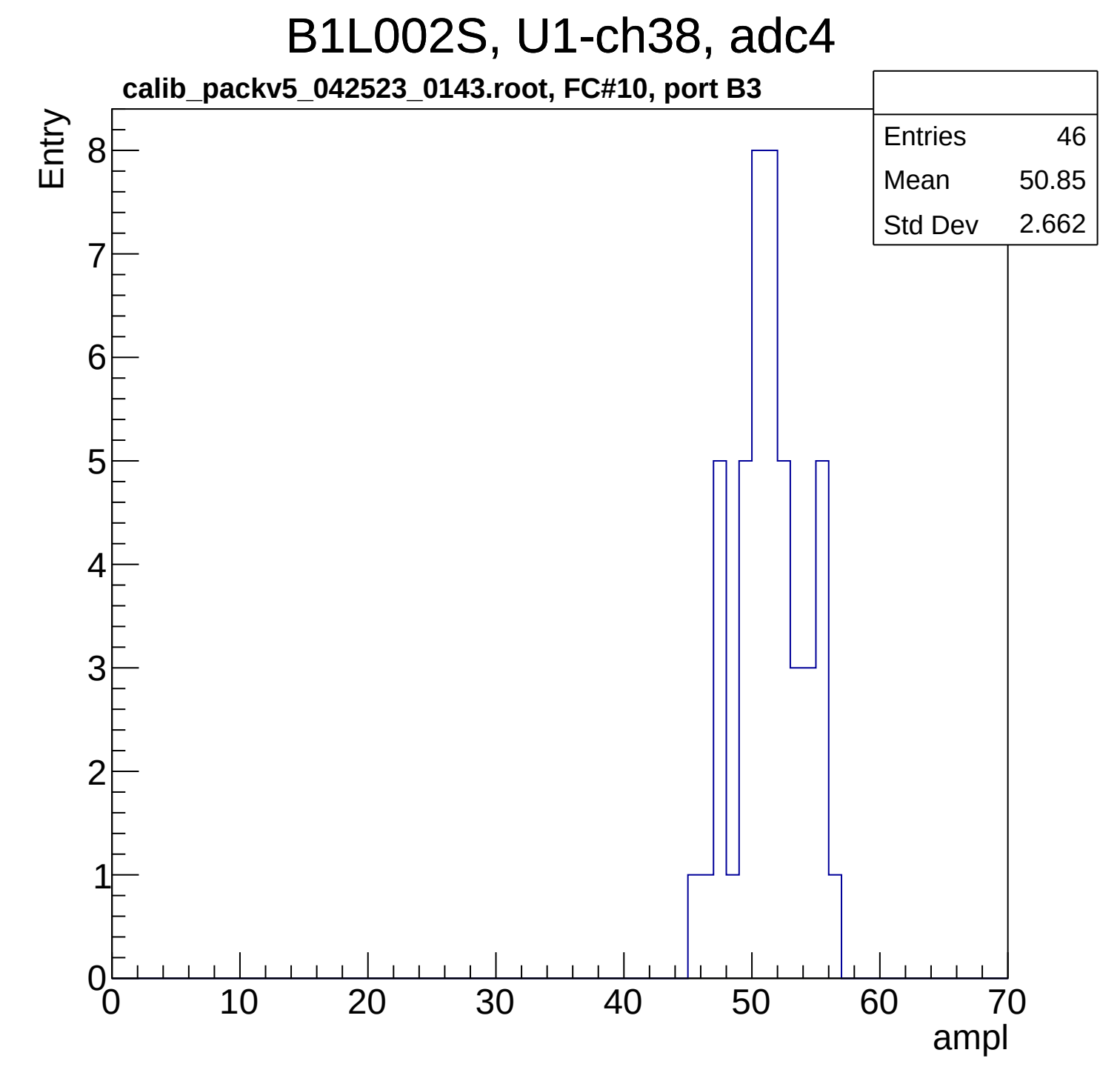
B1L002S, U1-ch38, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 50.85
Std Dev 2.662
Entry
ampl
0
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2
3
4
5
6
7
8
0
10
20
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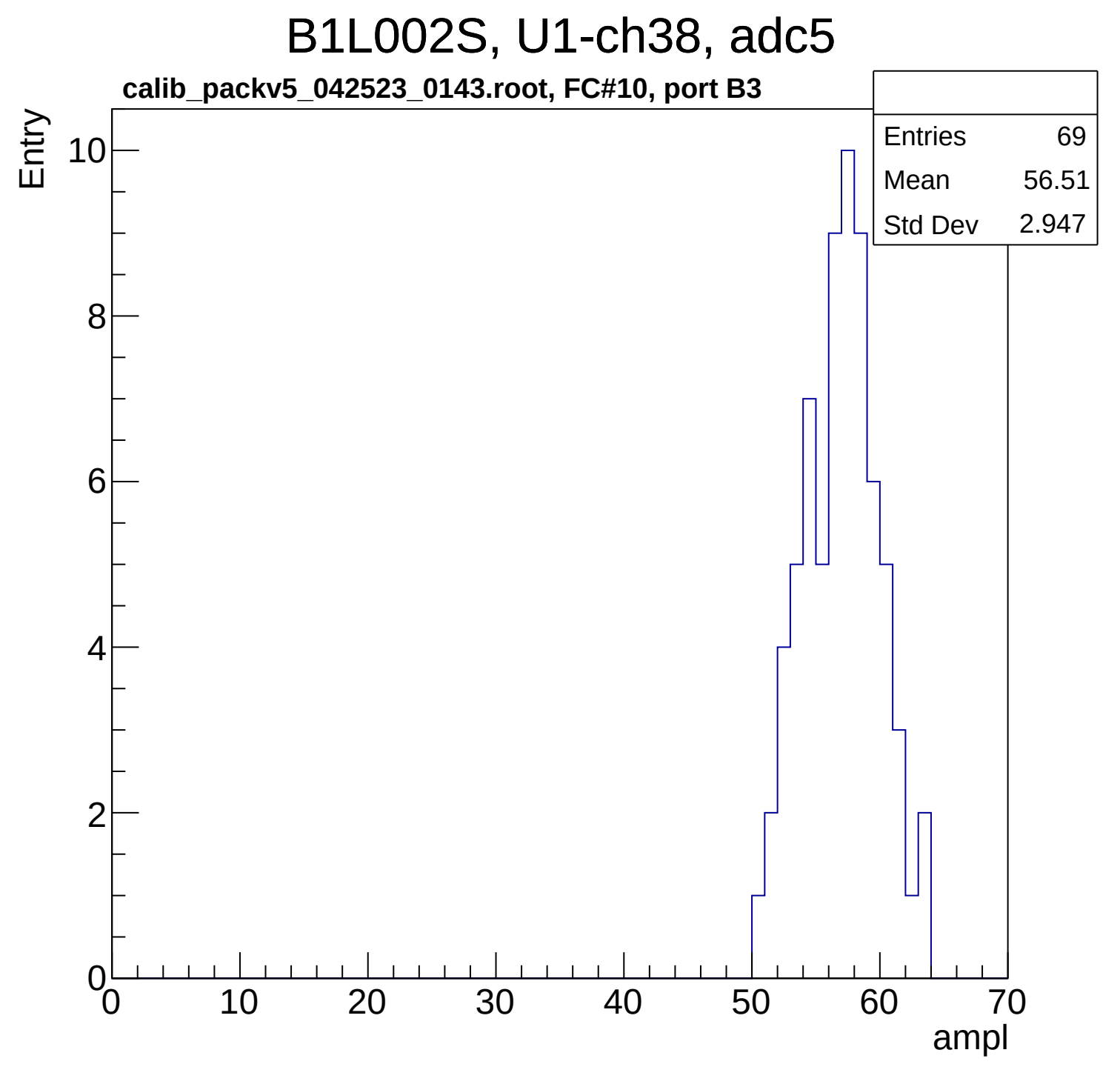
40
50
60
70


B1L002S, U1-ch38, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 56.51
Std Dev 2.947
Entry
ampl
0
2
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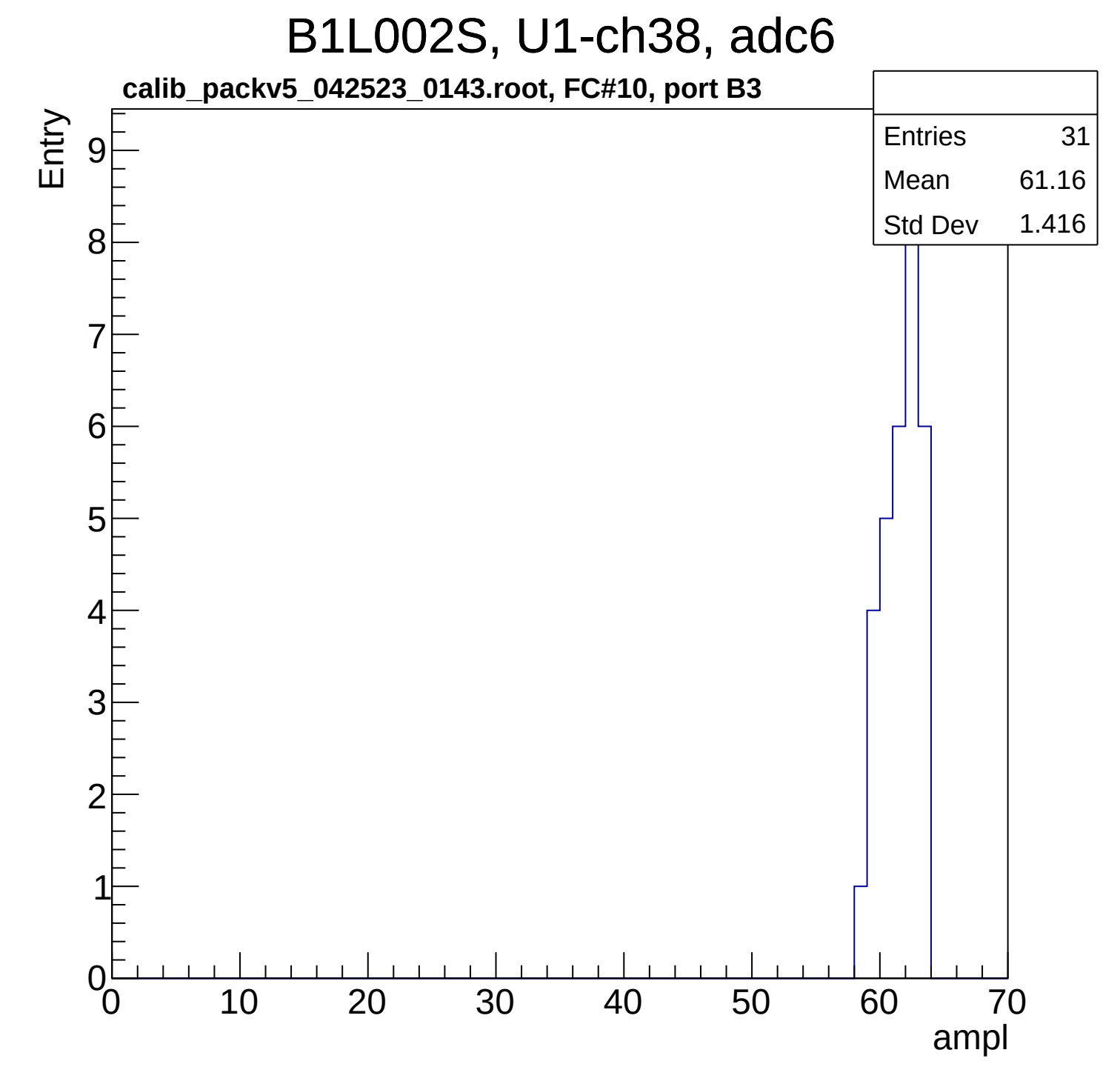
B1L002S, U1-ch38, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 61.16
Std Dev 1.416
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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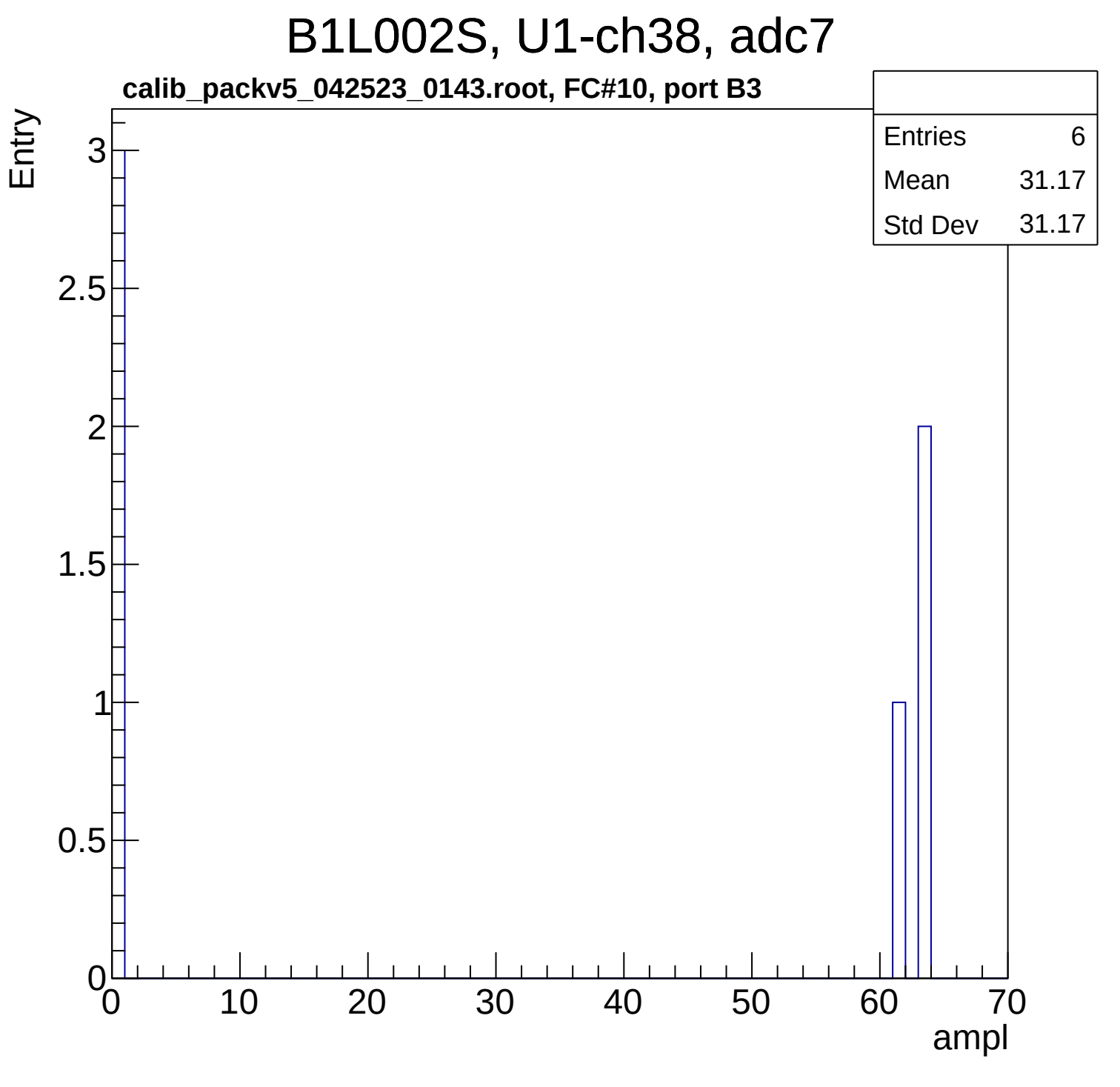
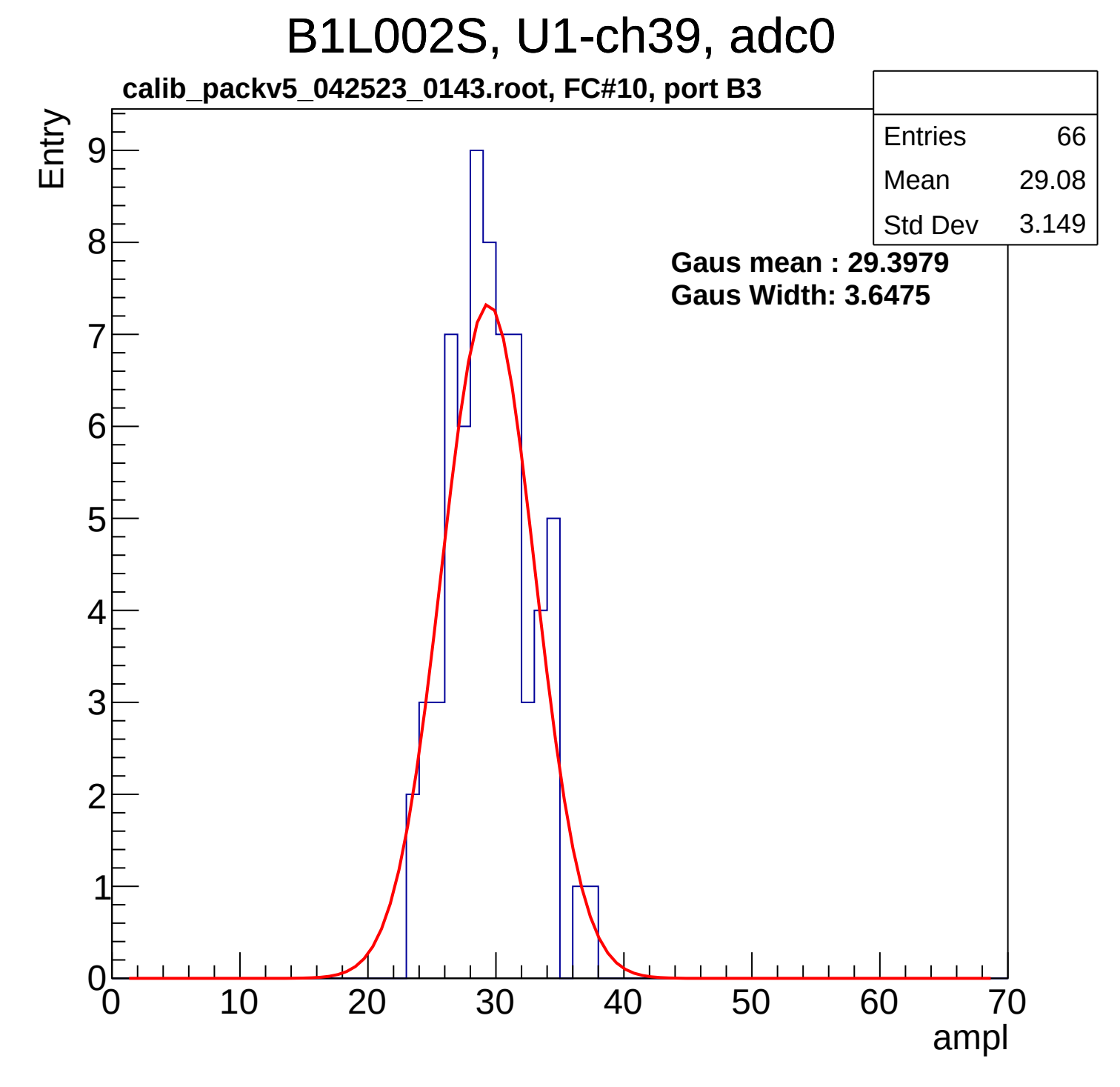

B1L002S, U1-ch38, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 31.17
Std Dev 31.17
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch39, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 29.08
Std Dev 3.149
Gaus mean : 29.3979
Gaus Width: 3.6475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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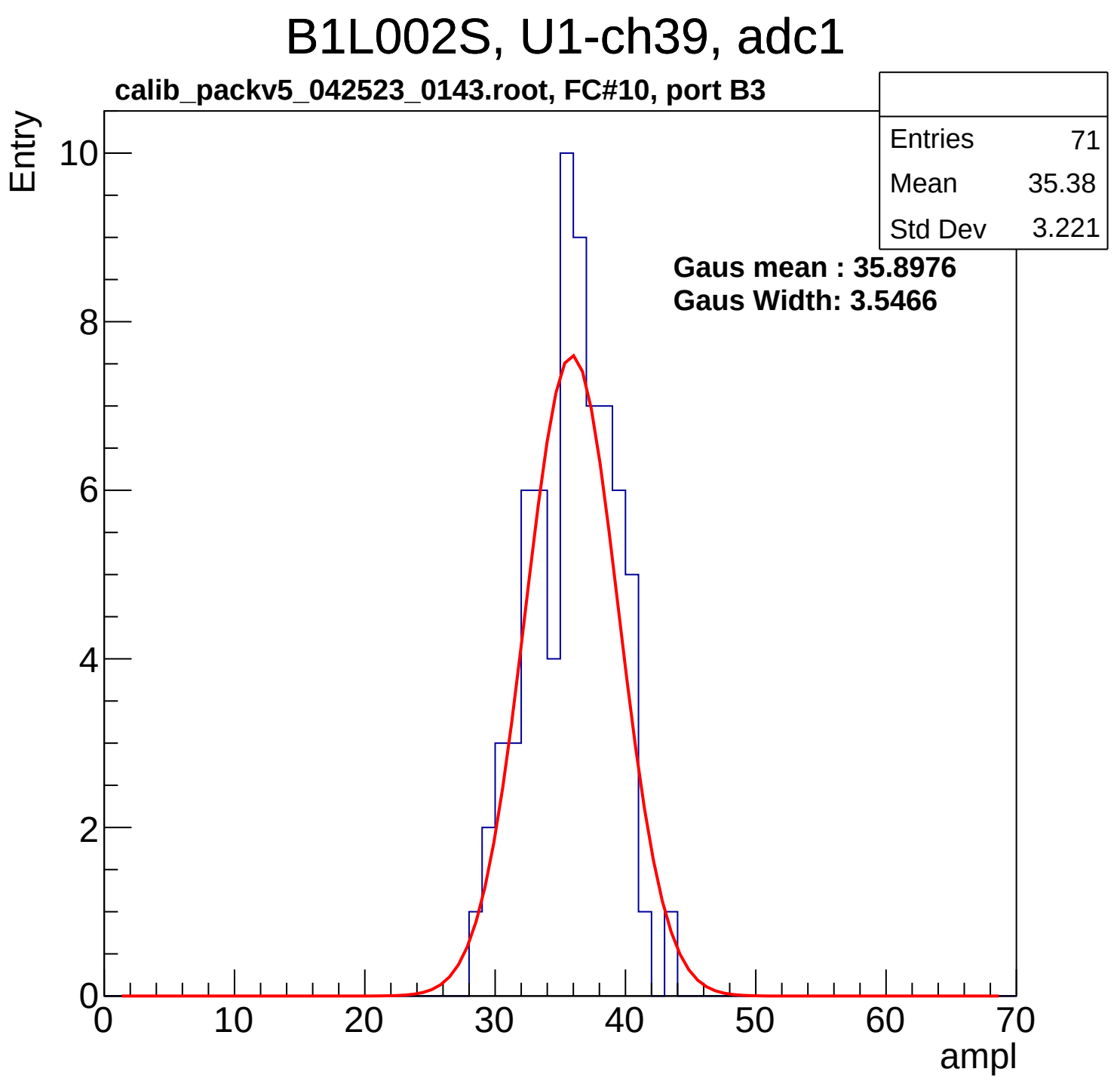
B1L002S, U1-ch39, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.38
Std Dev 3.221
Gaus mean : 35.8976
Gaus Width: 3.5466
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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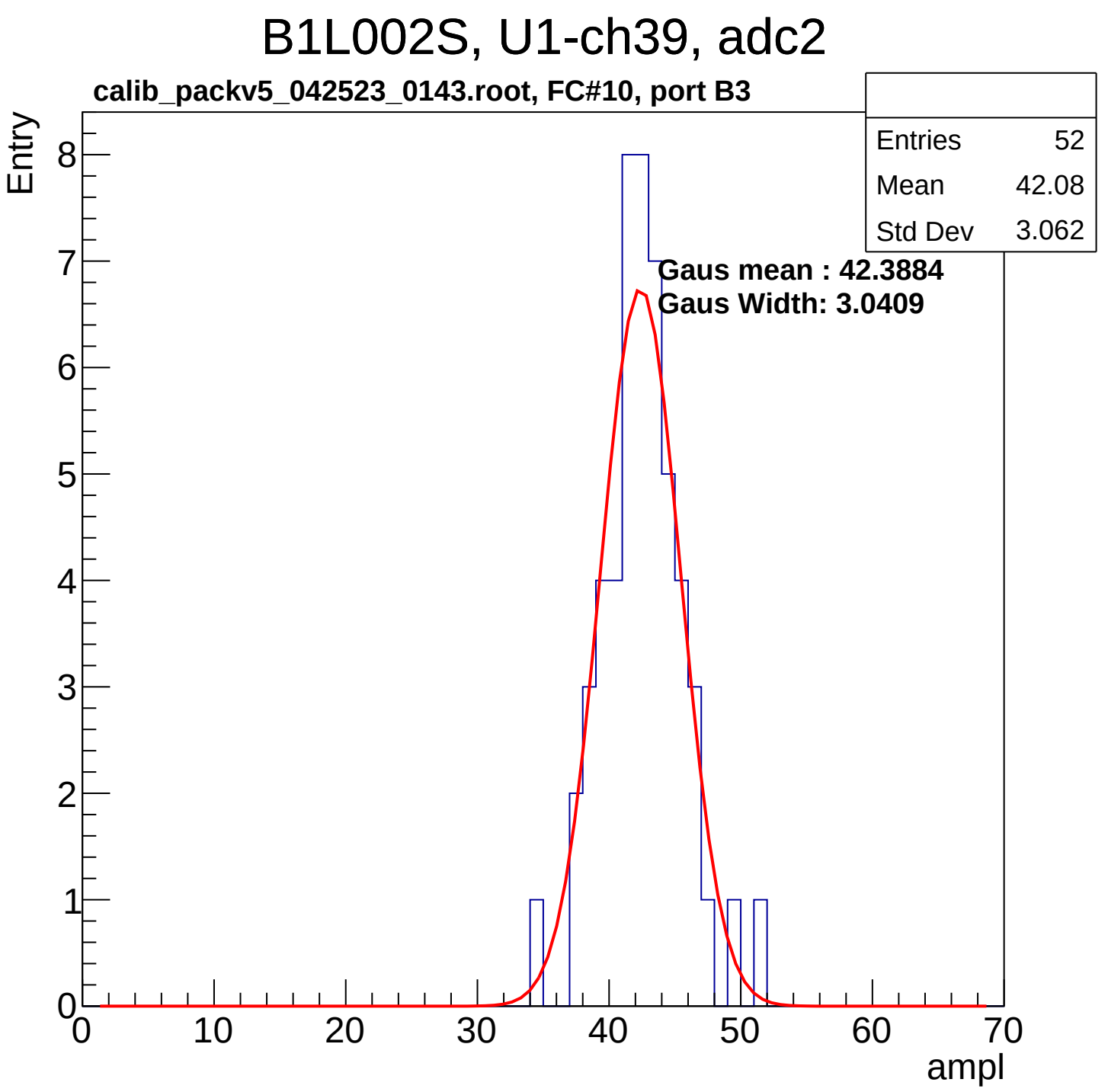
B1L002S, U1-ch39, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 42.08
Std Dev 3.062
Gaus mean : 42.3884
Gaus Width: 3.0409
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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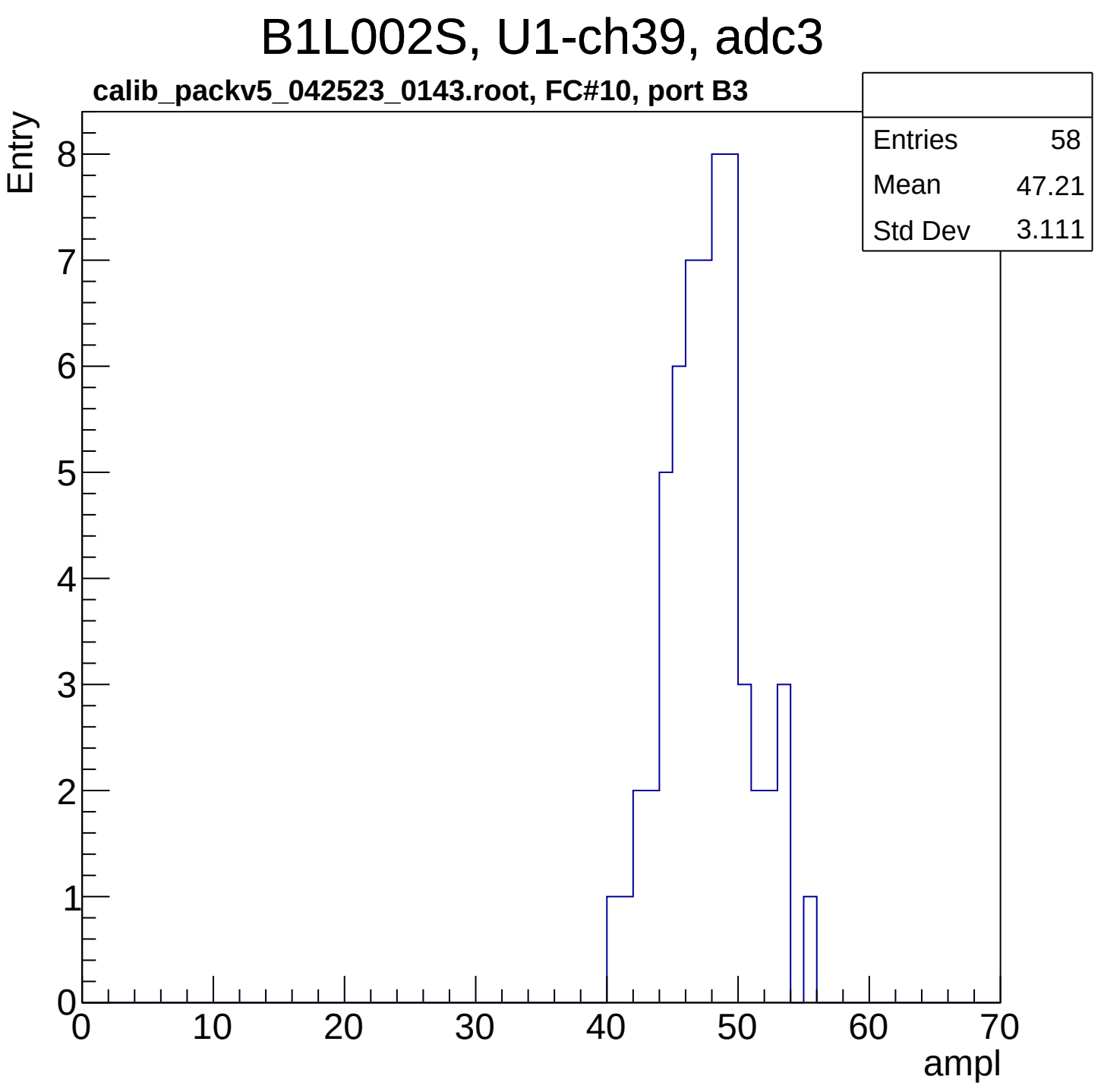
B1L002S, U1-ch39, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 47.21
Std Dev 3.111
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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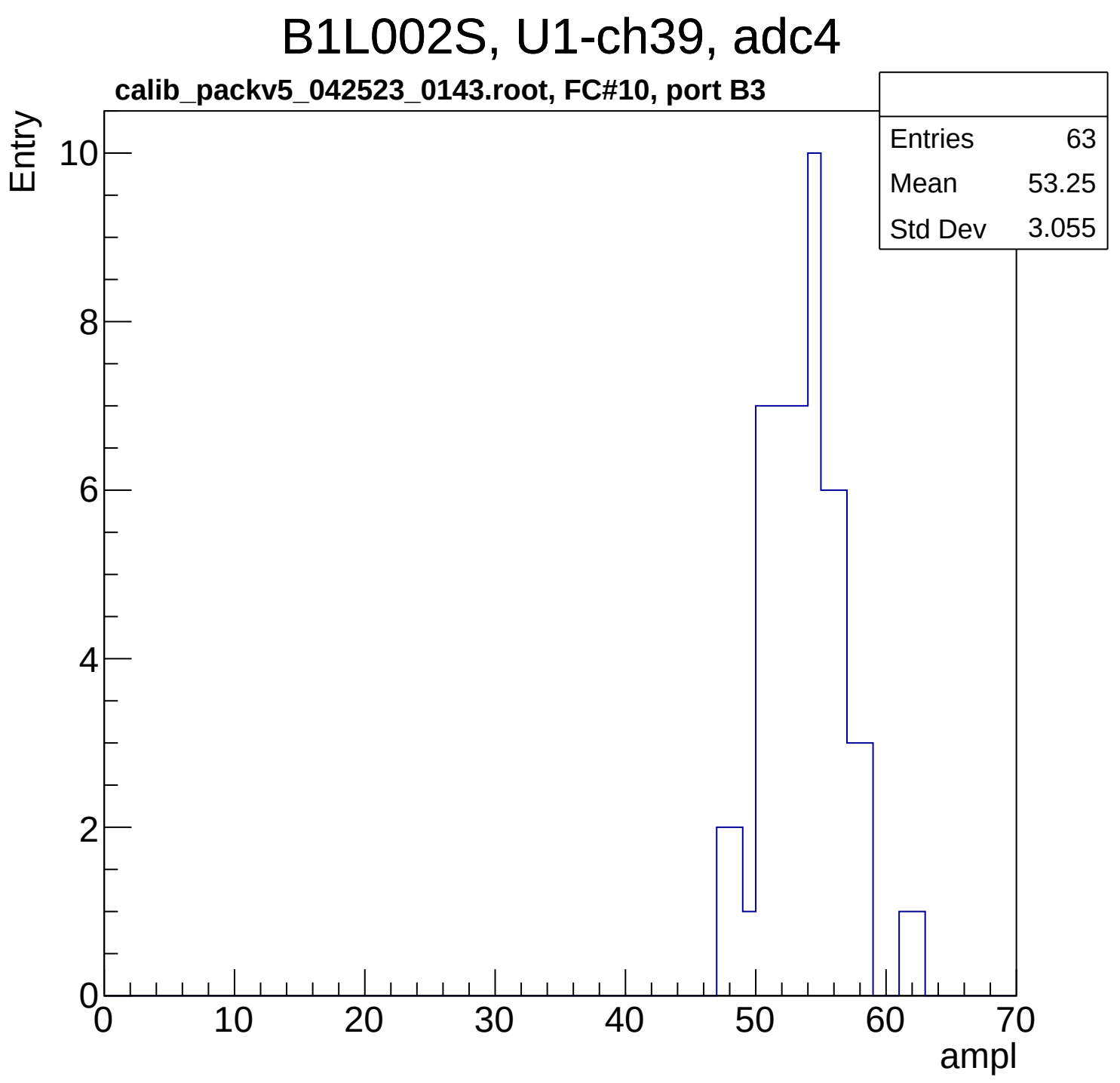
B1L002S, U1-ch39, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.25
Std Dev 3.055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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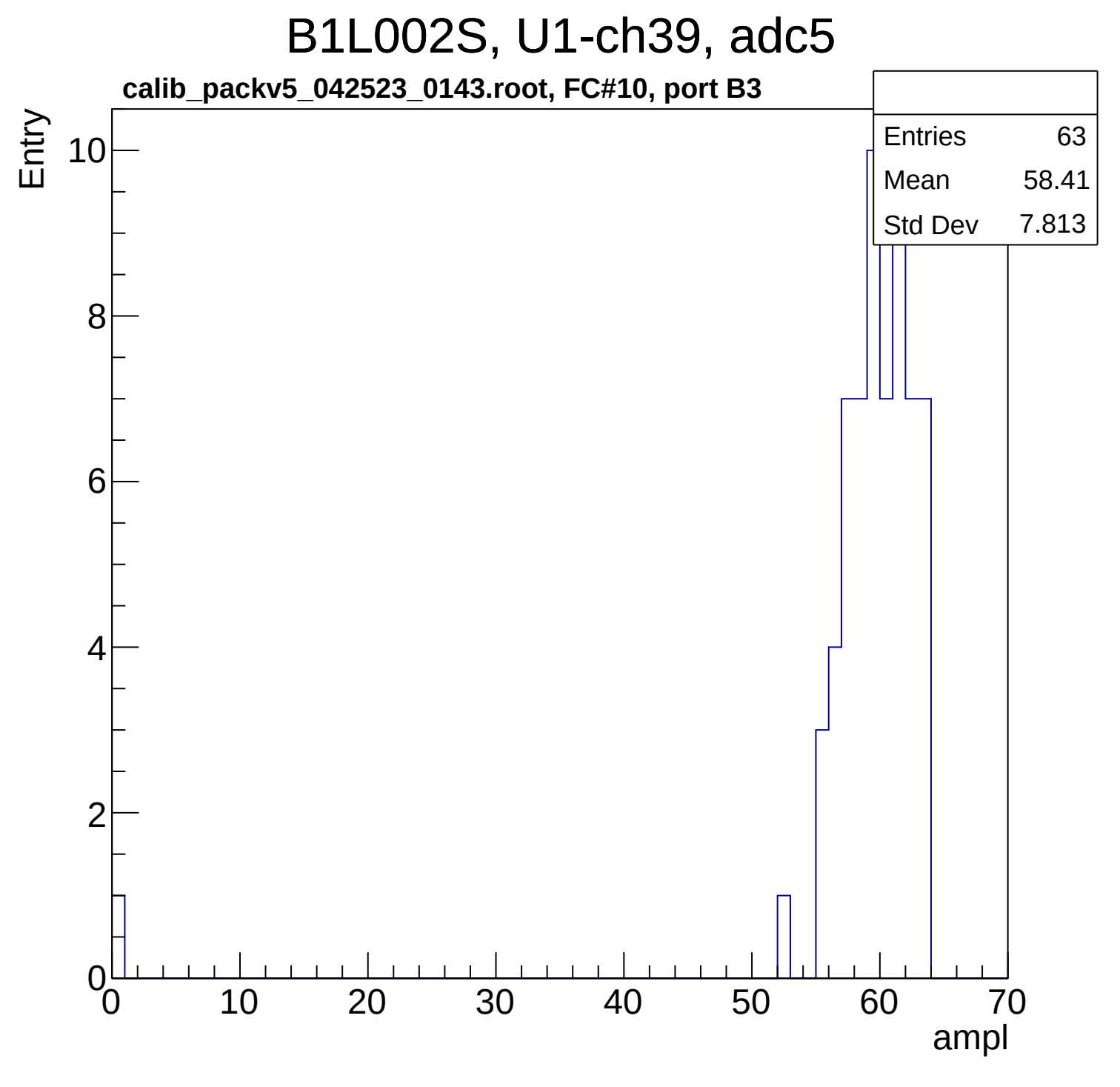
B1L002S, U1-ch39, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.41
Std Dev 7.813
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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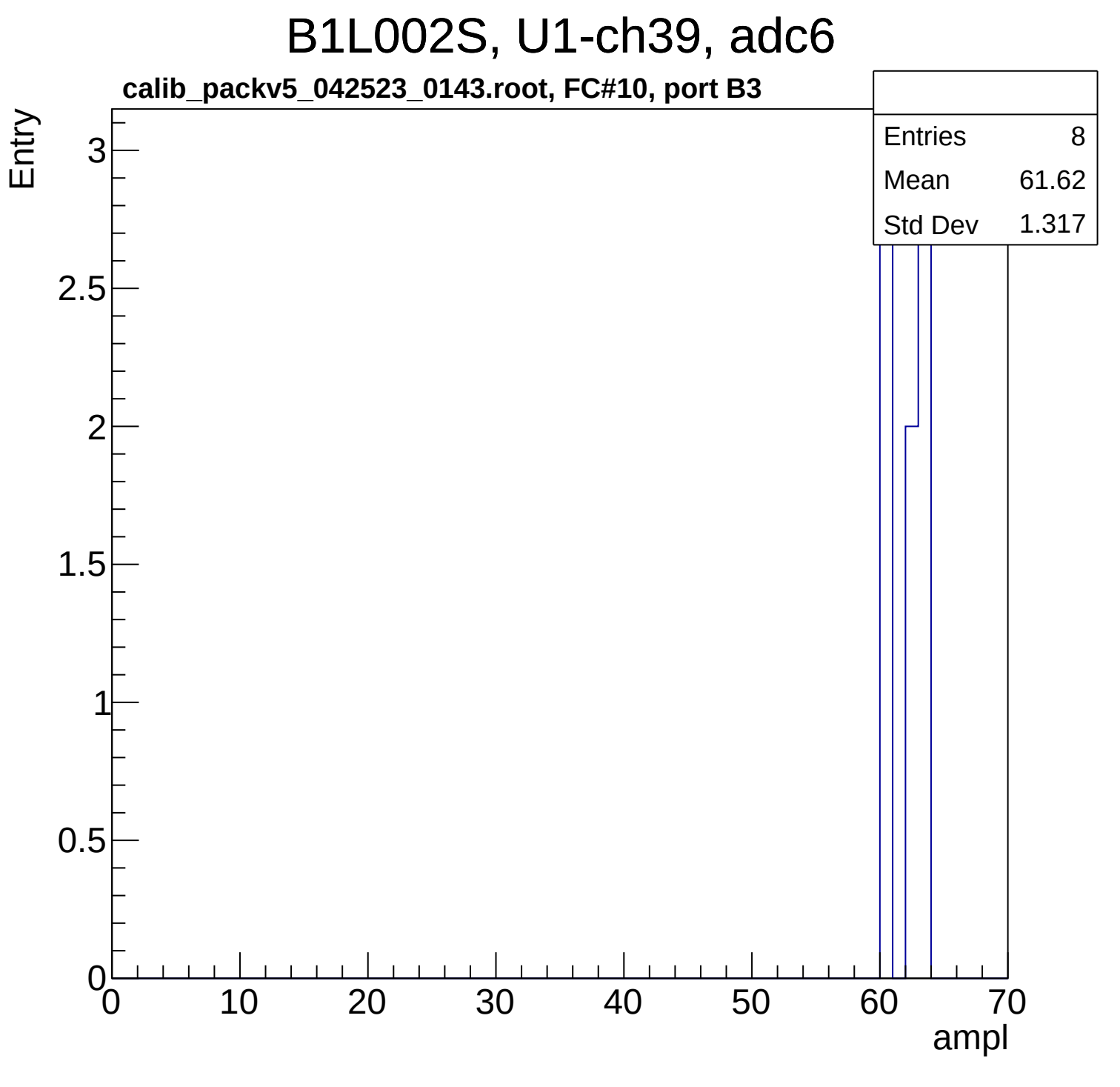
B1L002S, U1-ch39, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 61.62
Std Dev 1.317
Entry
ampl
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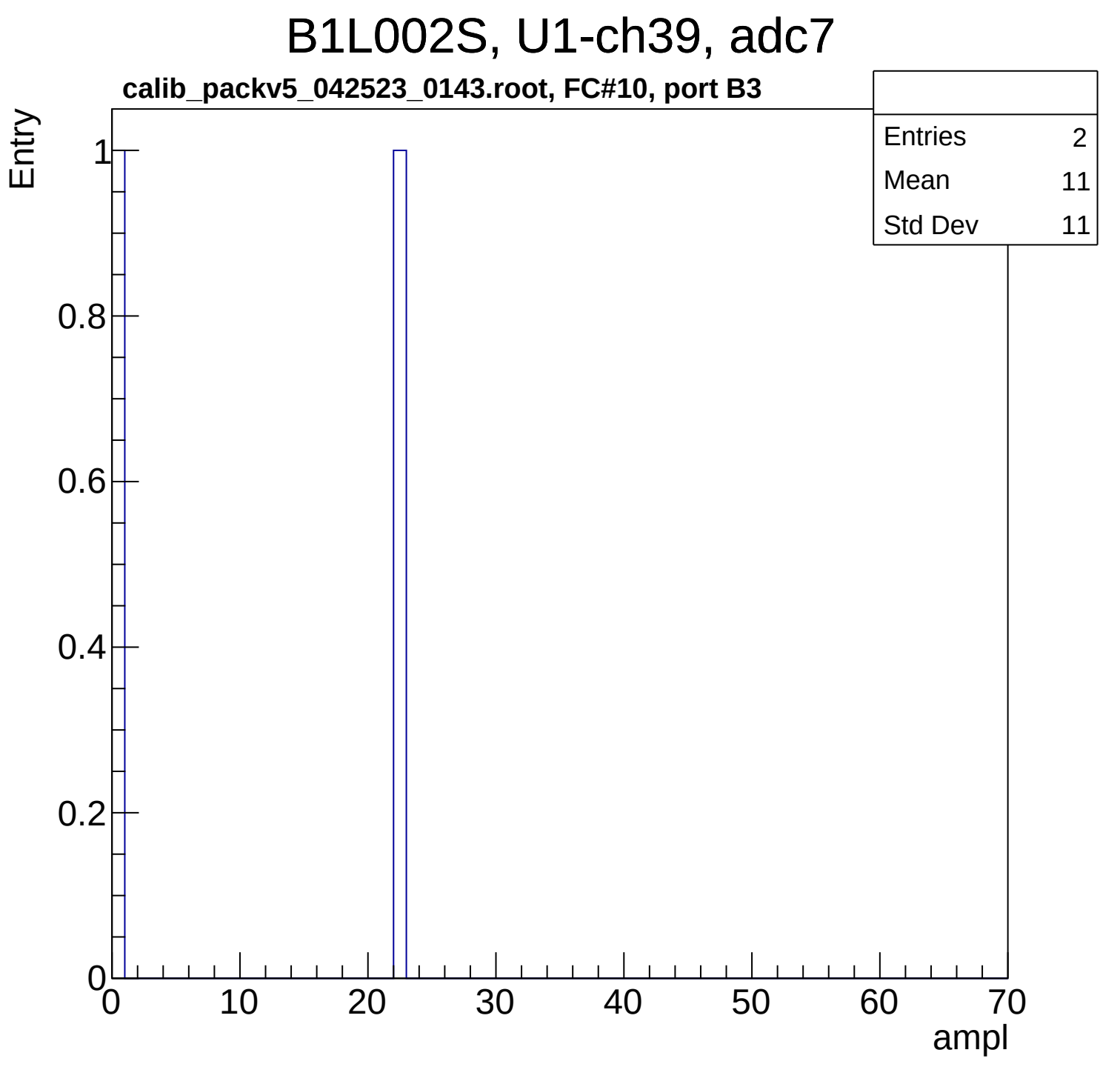
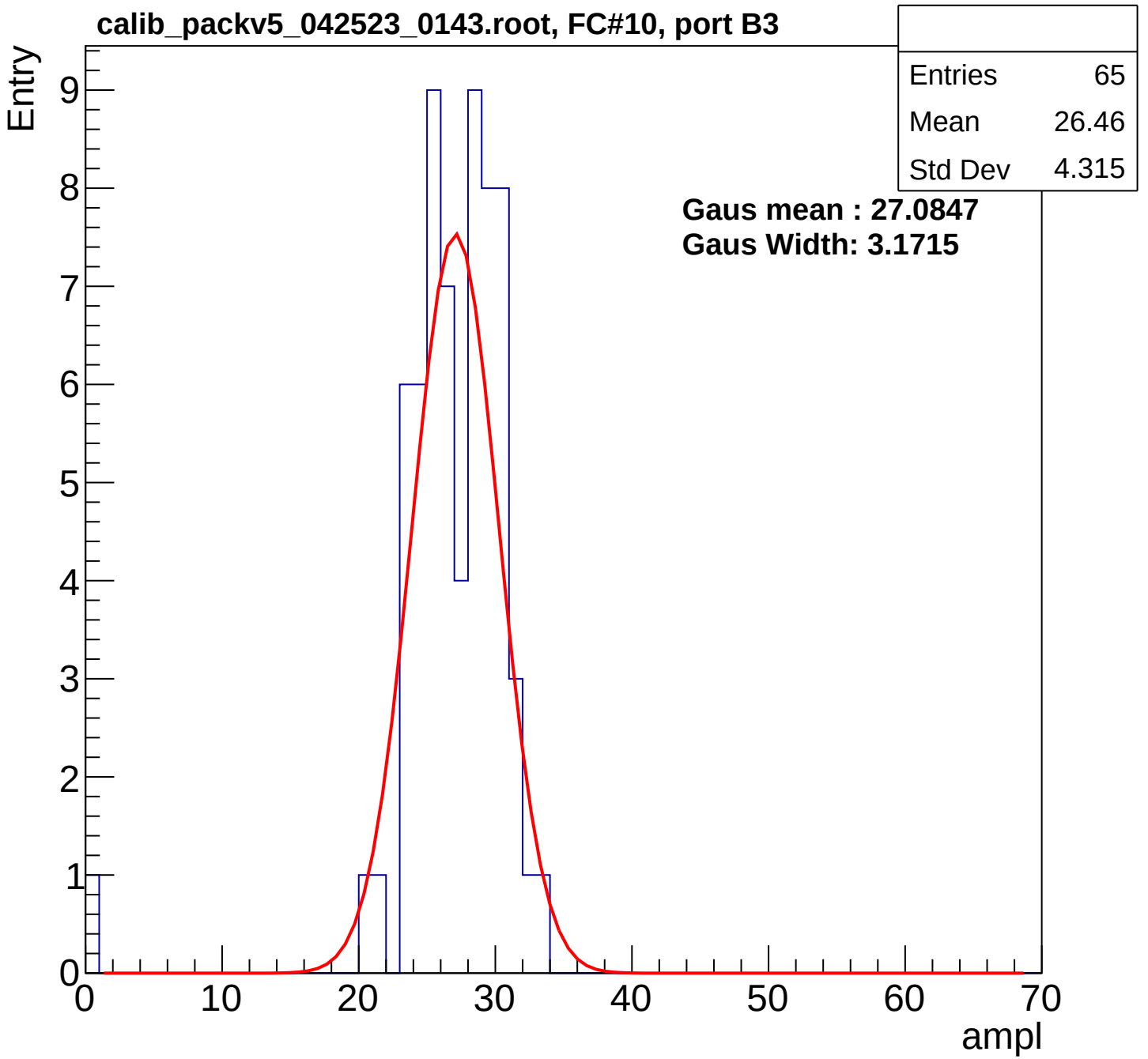
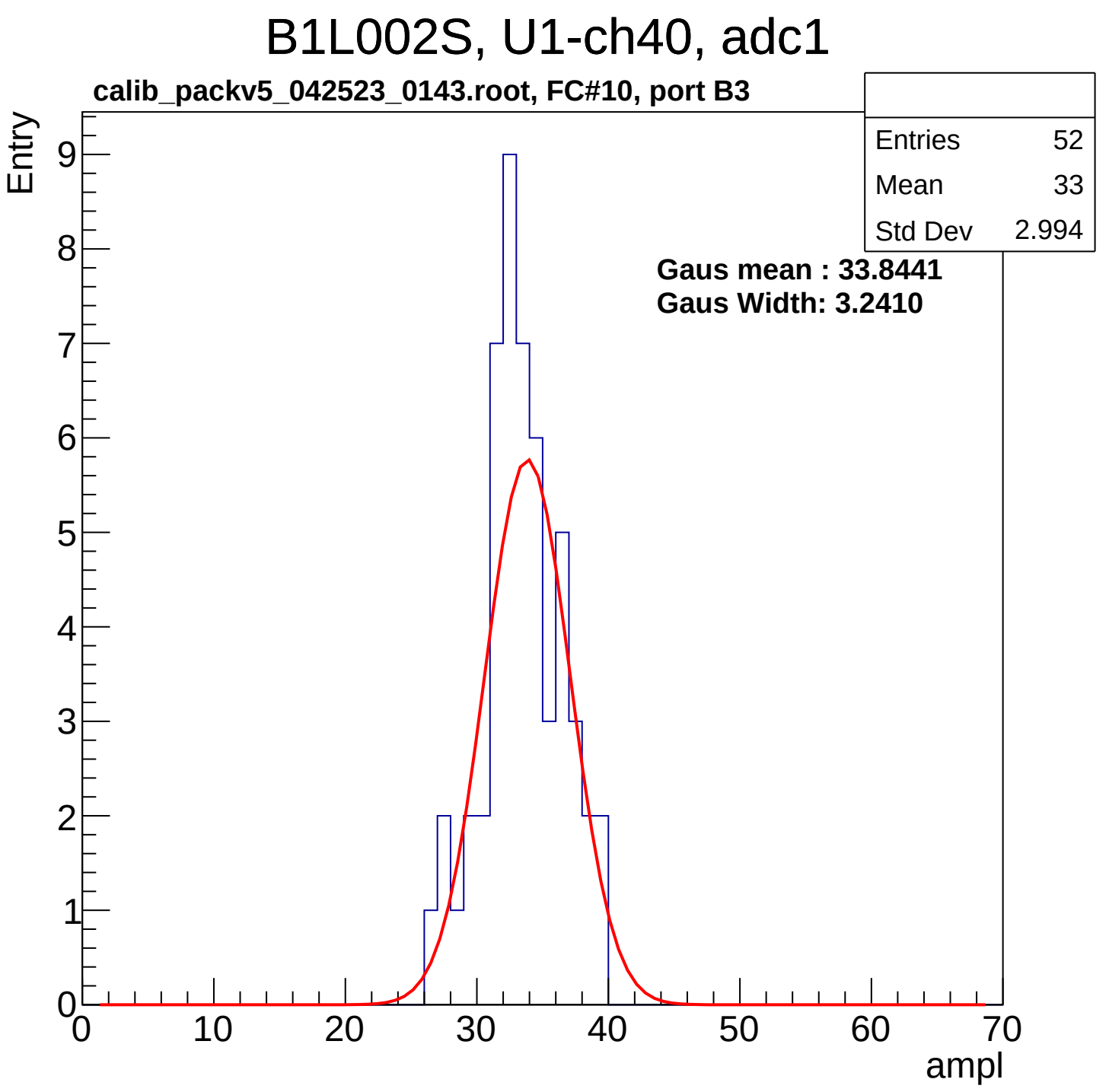
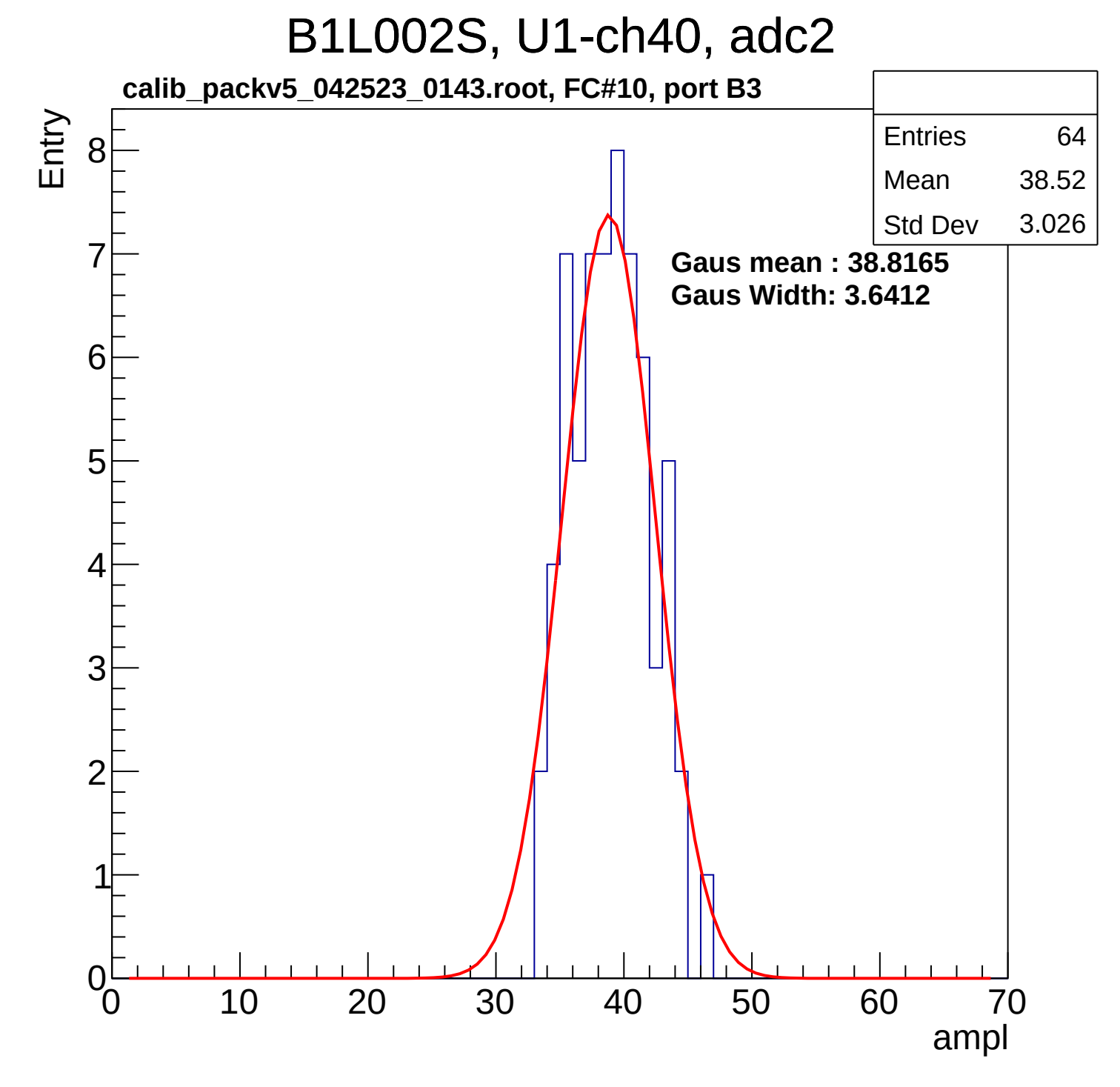
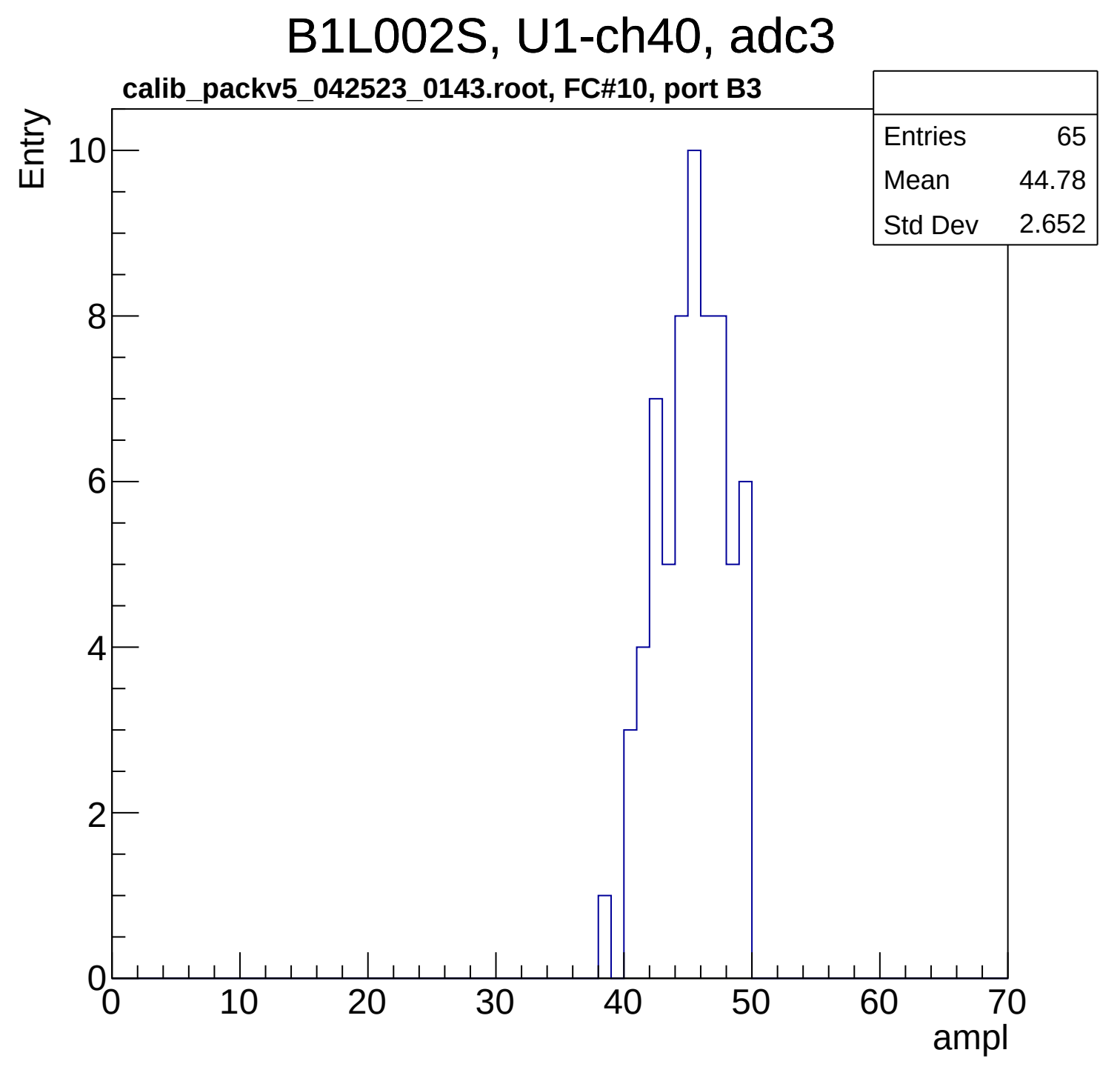
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

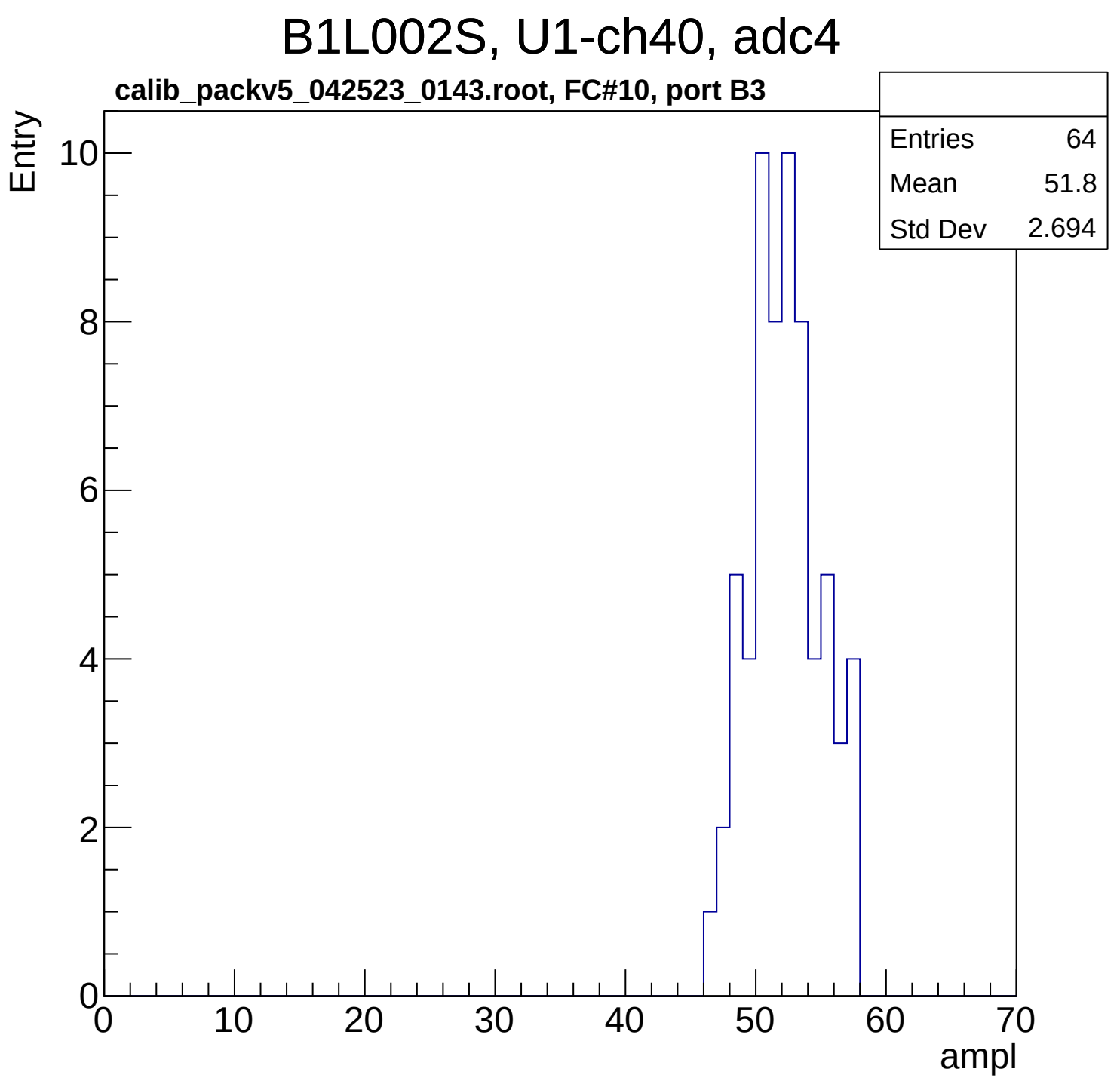

B1L002S, U1-ch39, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

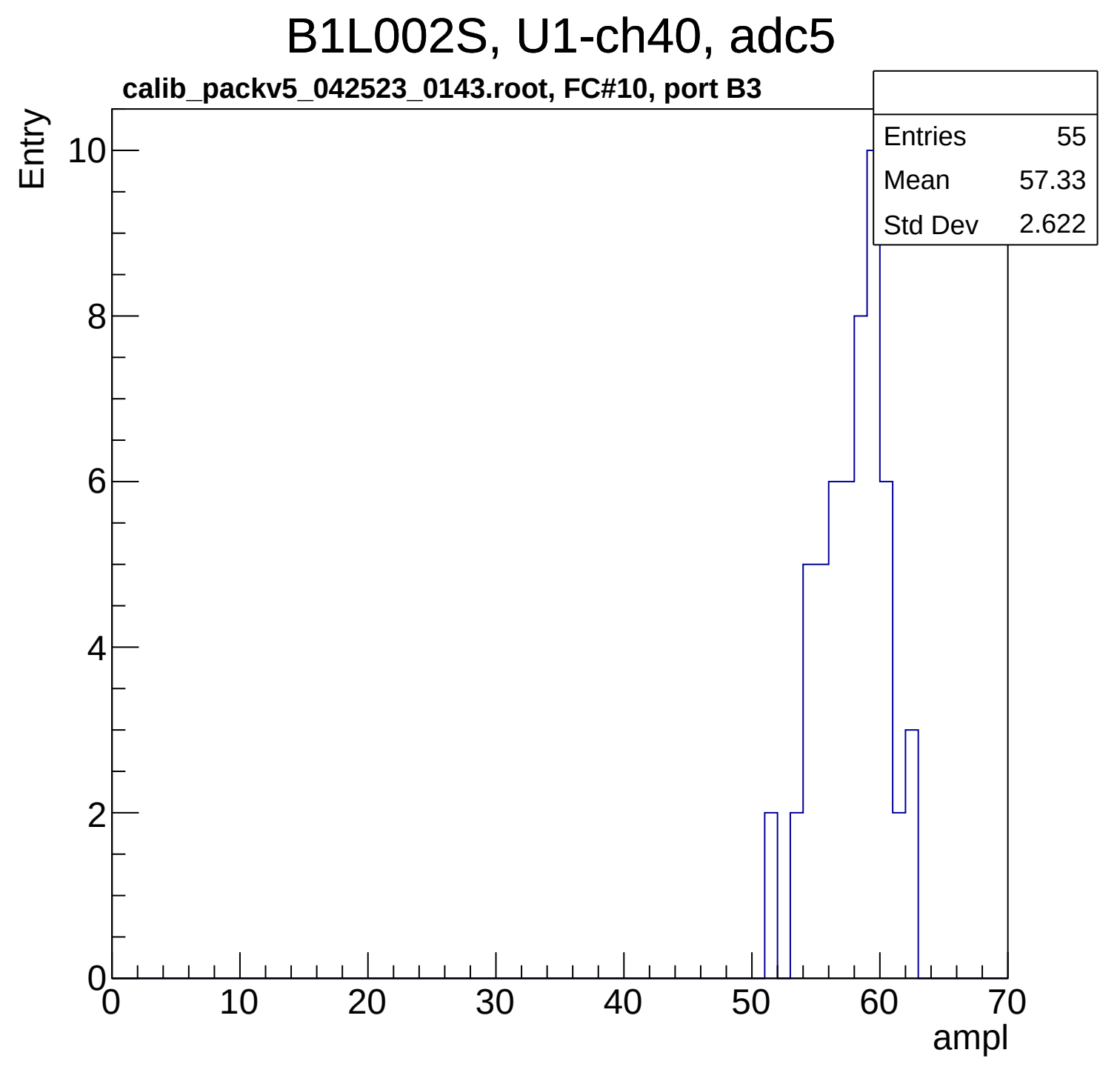

B1L002S, U1-ch40, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 26.46
Std Dev 4.315
Gaus mean : 27.0847
Gaus Width: 3.1715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

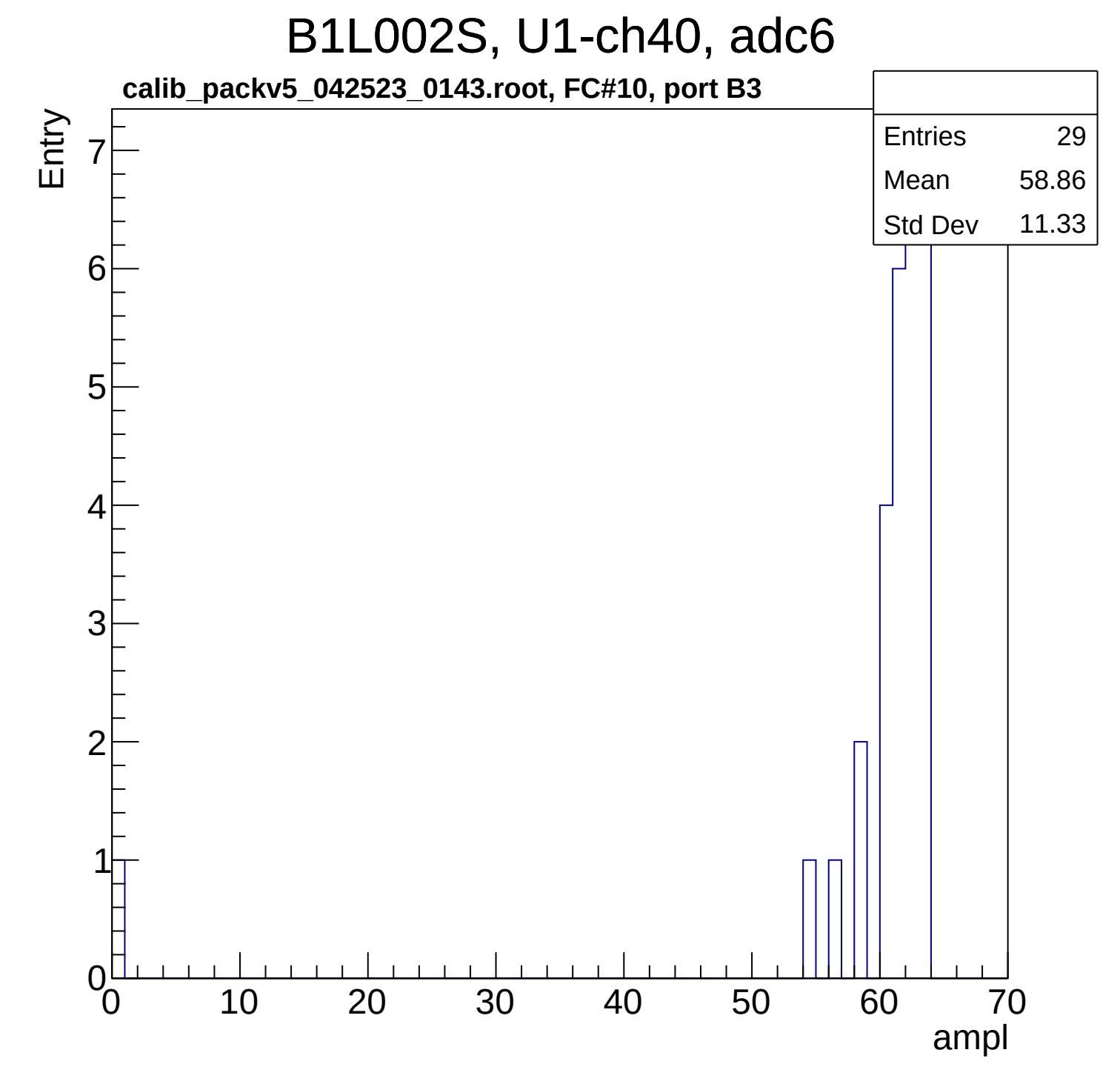

B1L002S, U1-ch40, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 33
Std Dev 2.994
Gaus mean : 33.8441
Gaus Width: 3.2410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch40, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 38.52
Std Dev 3.026
Gaus mean : 38.8165
Gaus Width: 3.6412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch40, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 44.78
Std Dev 2.652
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch40, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 51.8
Std Dev 2.694
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch40, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.33
Std Dev 2.622
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch40, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 58.86
Std Dev 11.33
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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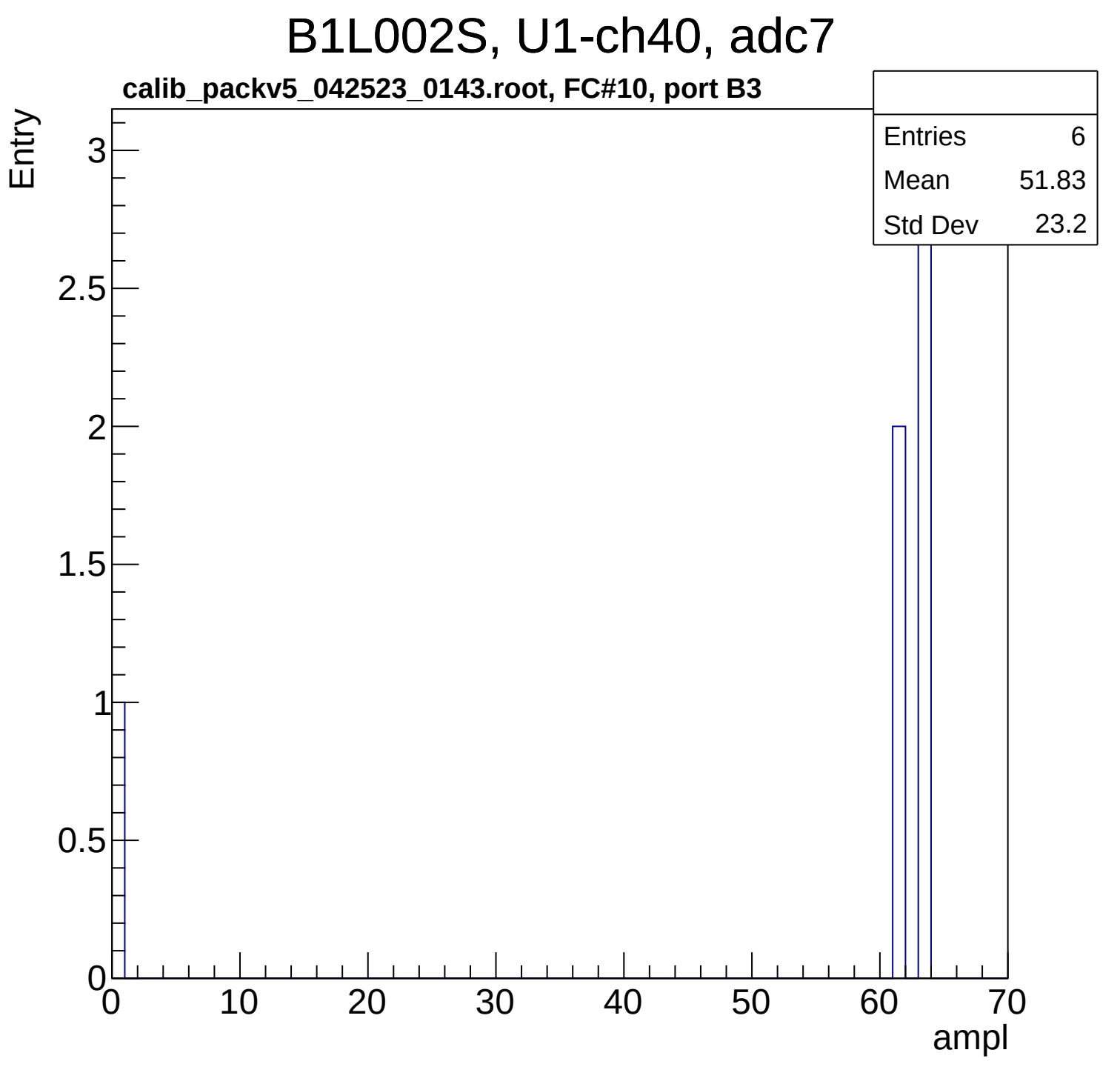
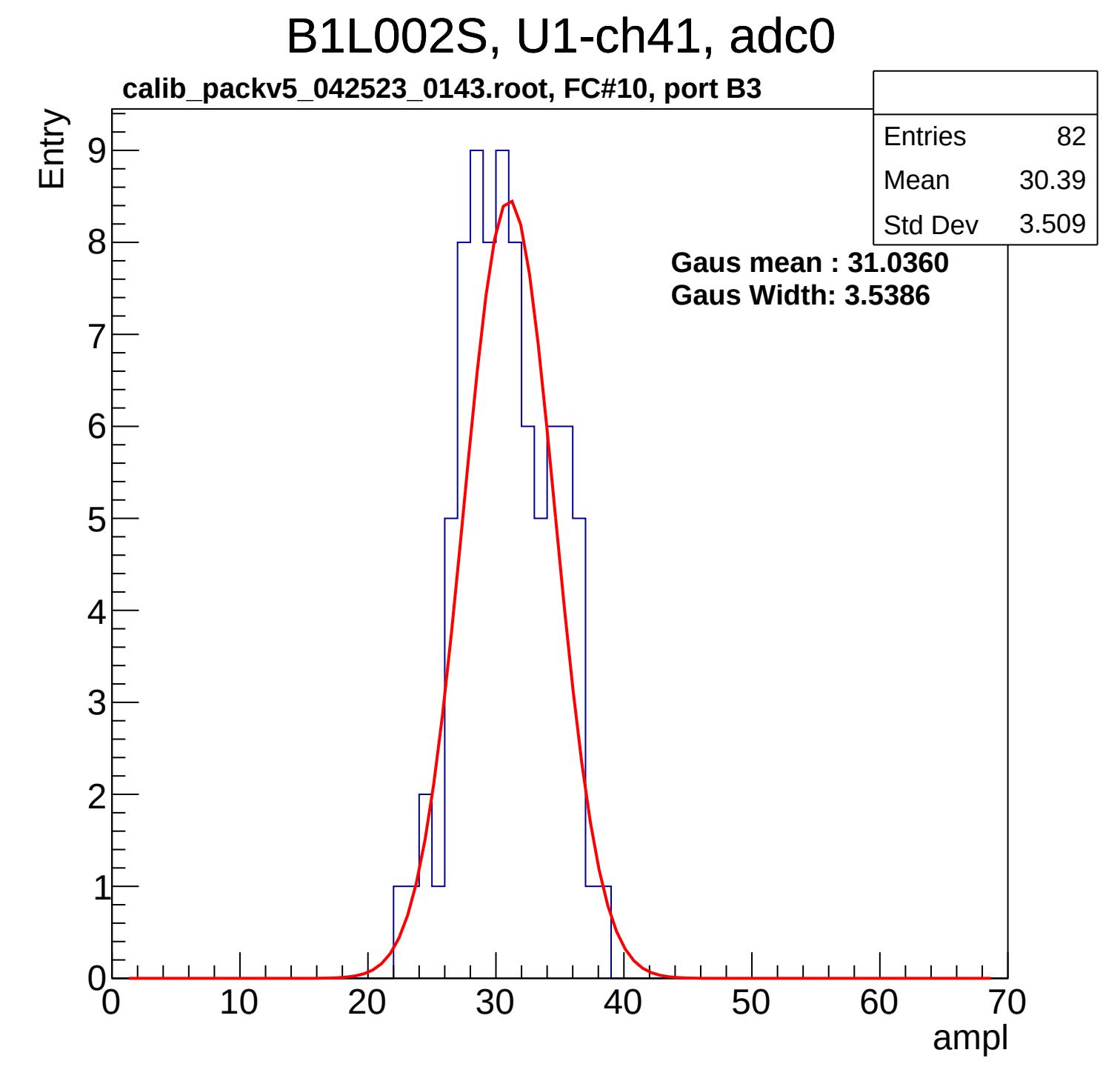

B1L002S, U1-ch40, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 51.83
Std Dev 23.2
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch41, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 30.39
Std Dev 3.509
Gaus mean : 31.0360
Gaus Width: 3.5386
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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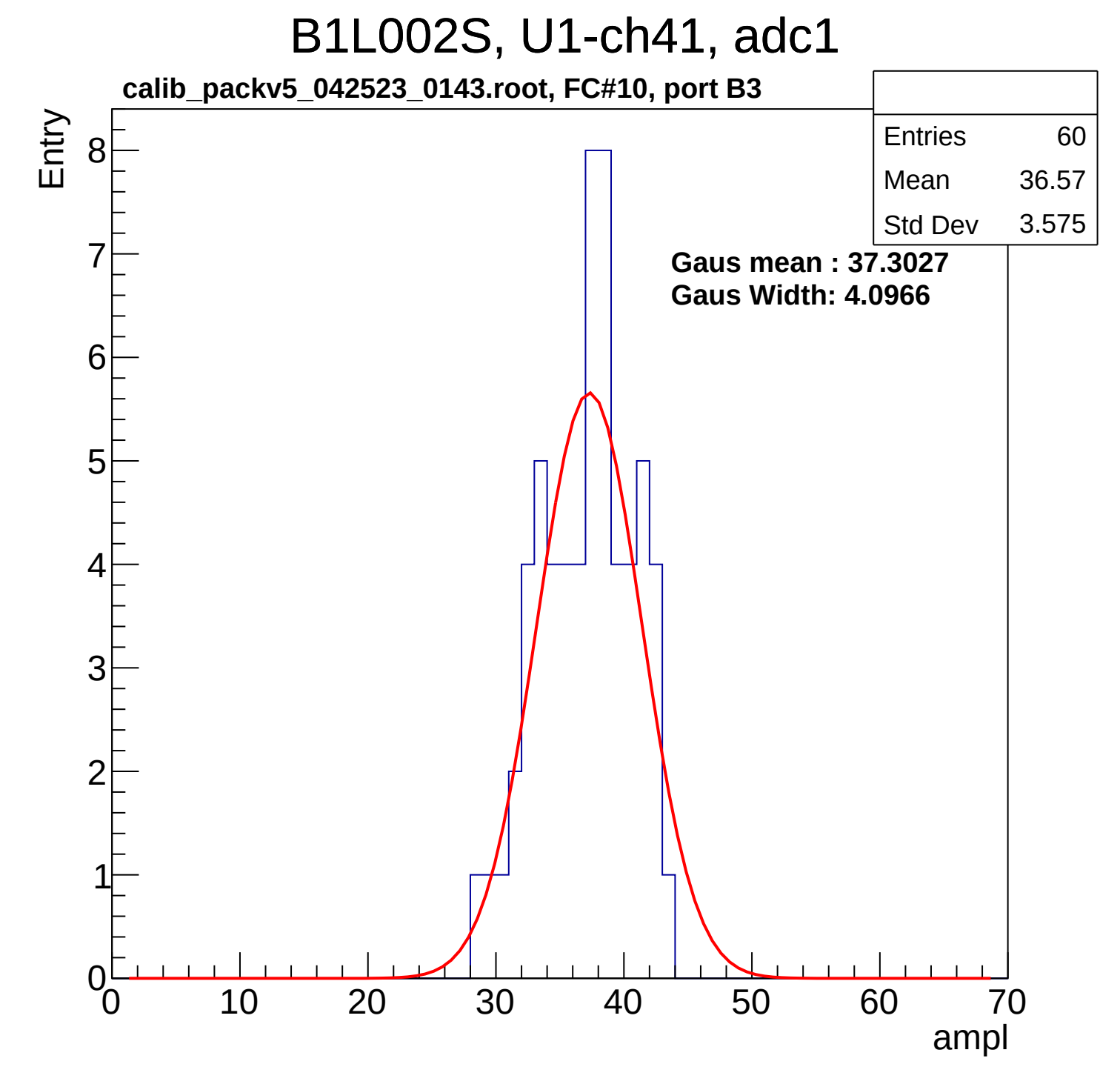
60
70


B1L002S, U1-ch41, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 36.57
Std Dev 3.575
Gaus mean : 37.3027
Gaus Width: 4.0966
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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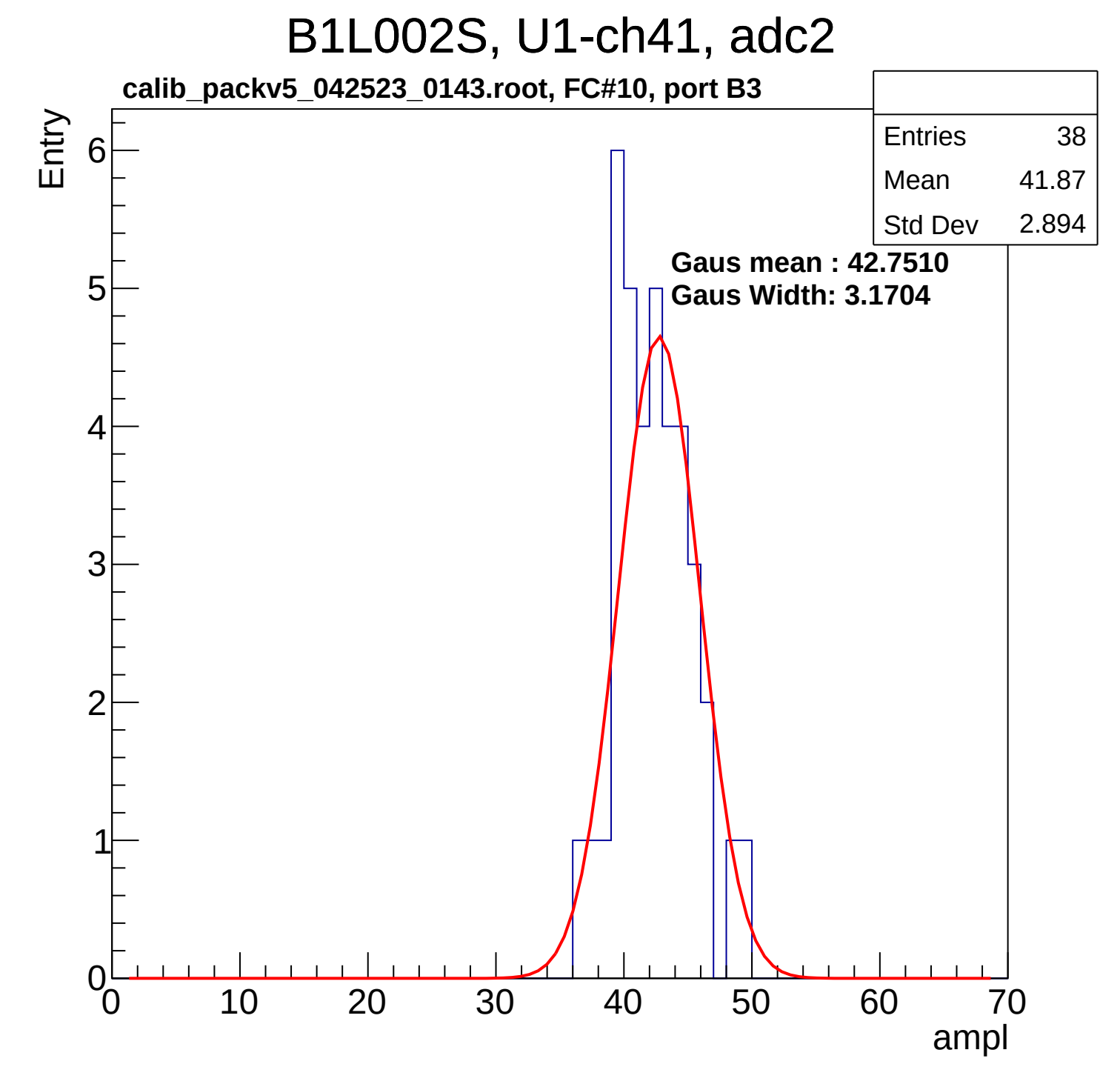
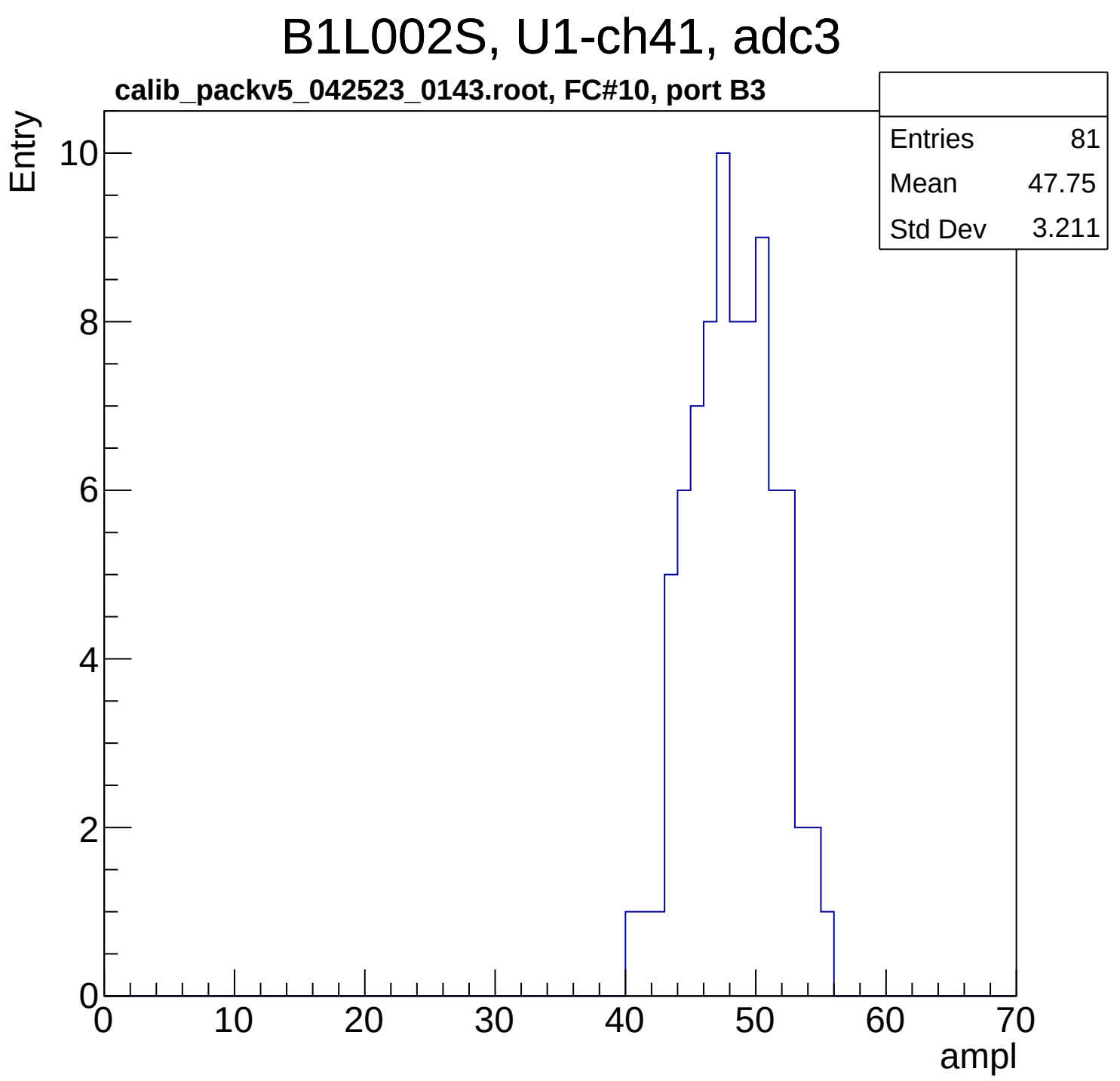

B1L002S, U1-ch41, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 41.87
Std Dev 2.894
Gaus mean : 42.7510
Gaus Width: 3.1704
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U1-ch41, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 47.75
Std Dev 3.211
Entry
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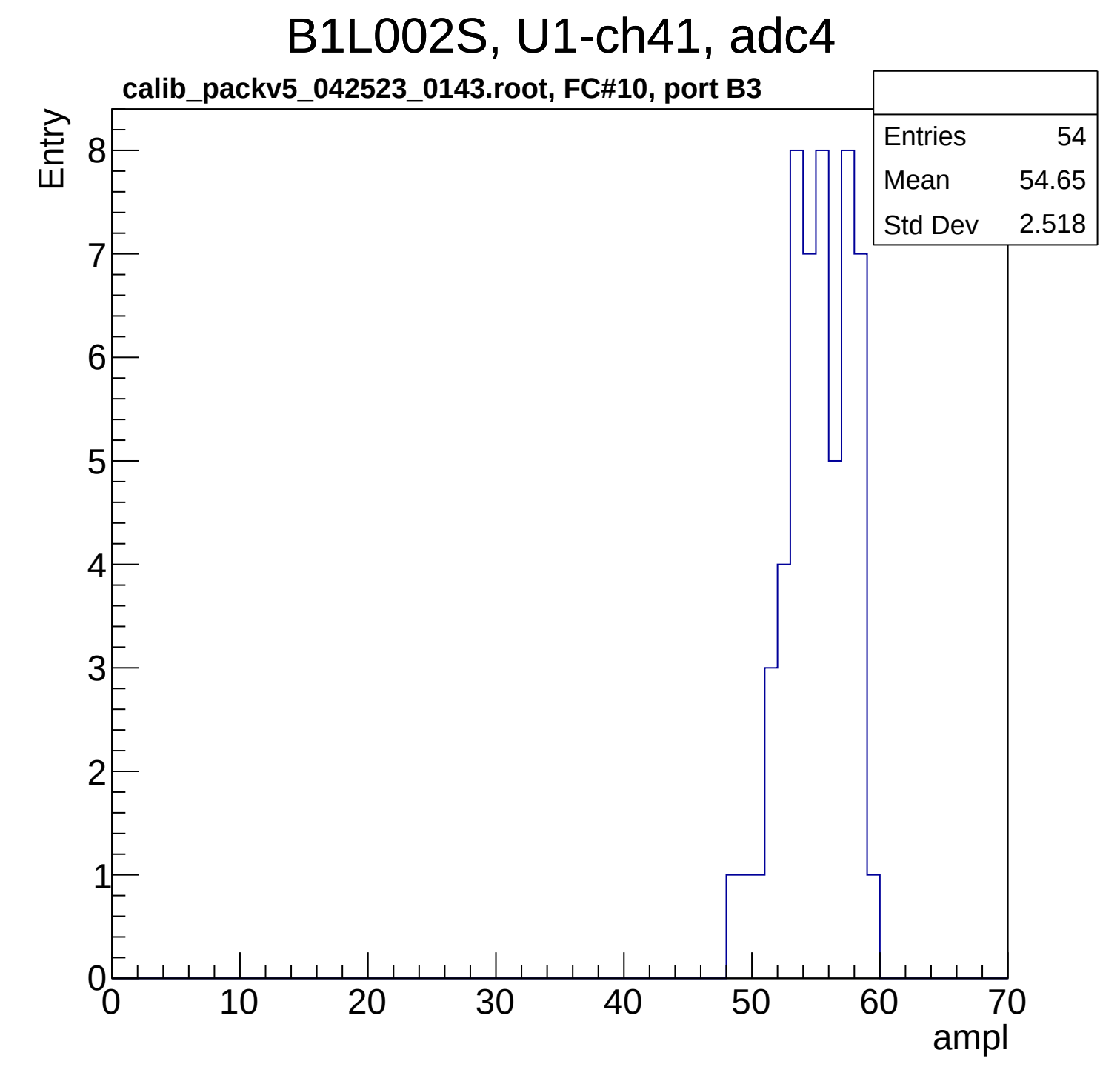
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch41, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 54.65
Std Dev 2.518
Entry
ampl
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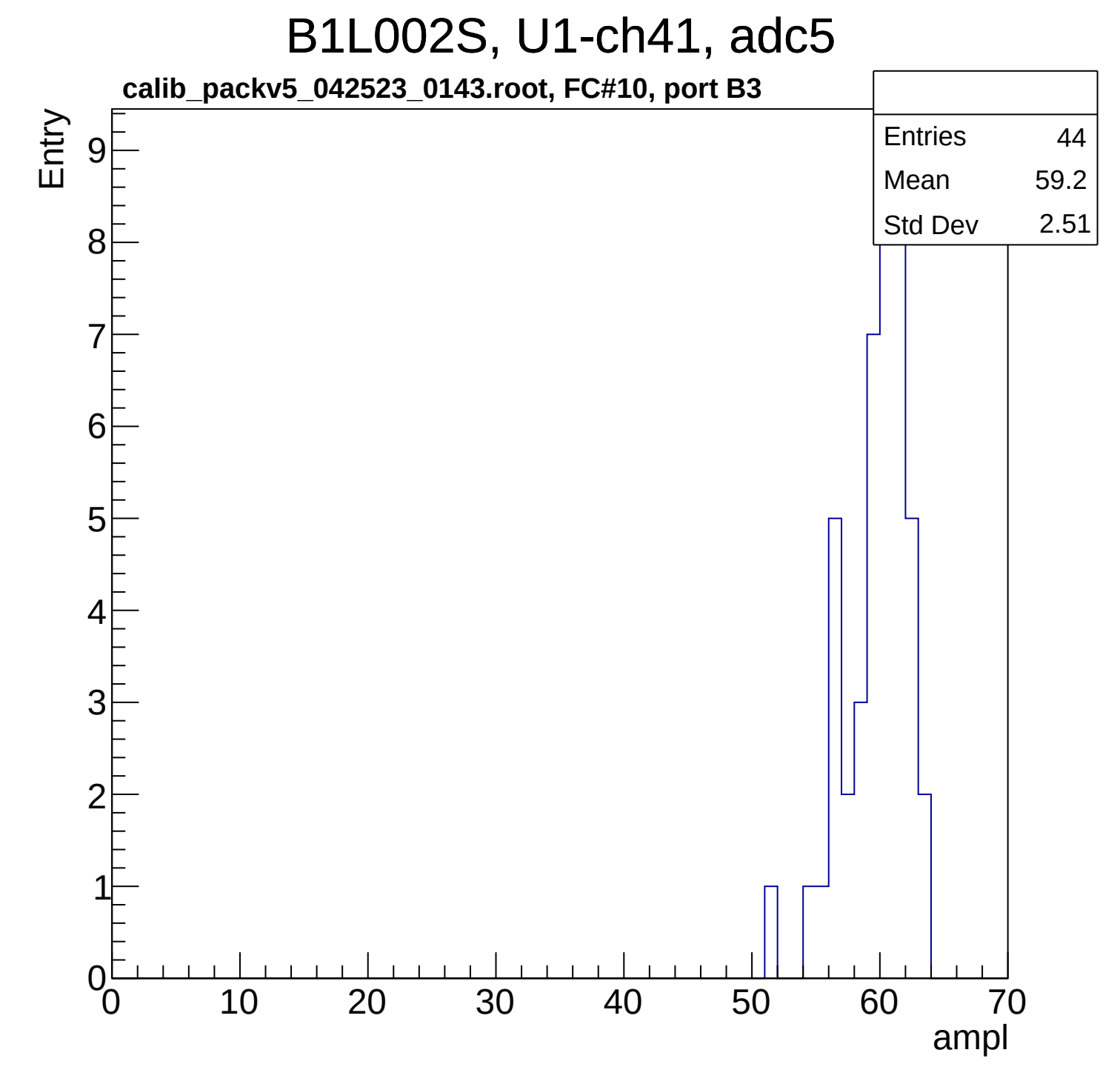
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch41, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 59.2
Std Dev 2.51
Entry
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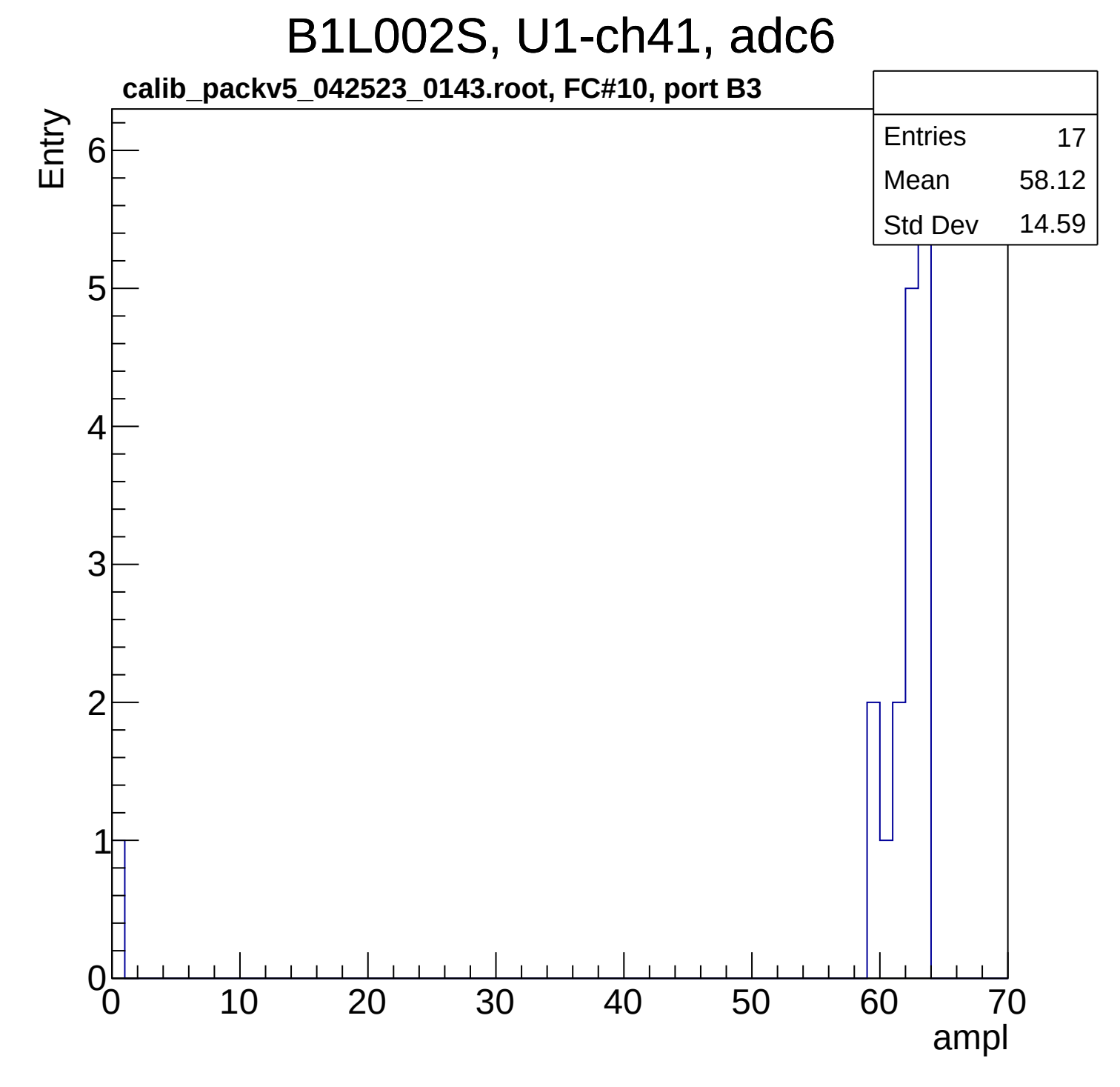
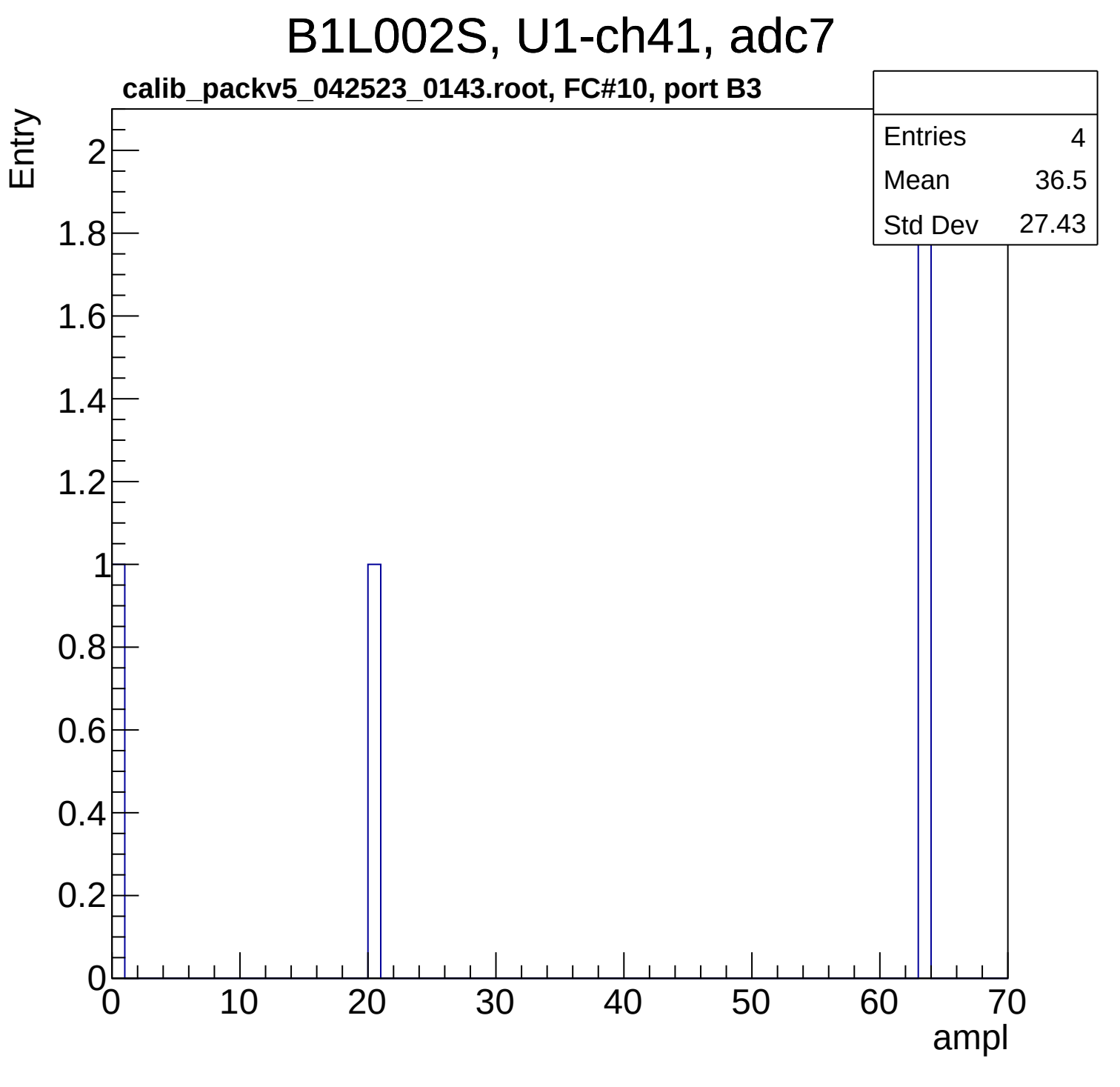
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch41, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58.12
Std Dev 14.59
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch41, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.5
Std Dev 27.43
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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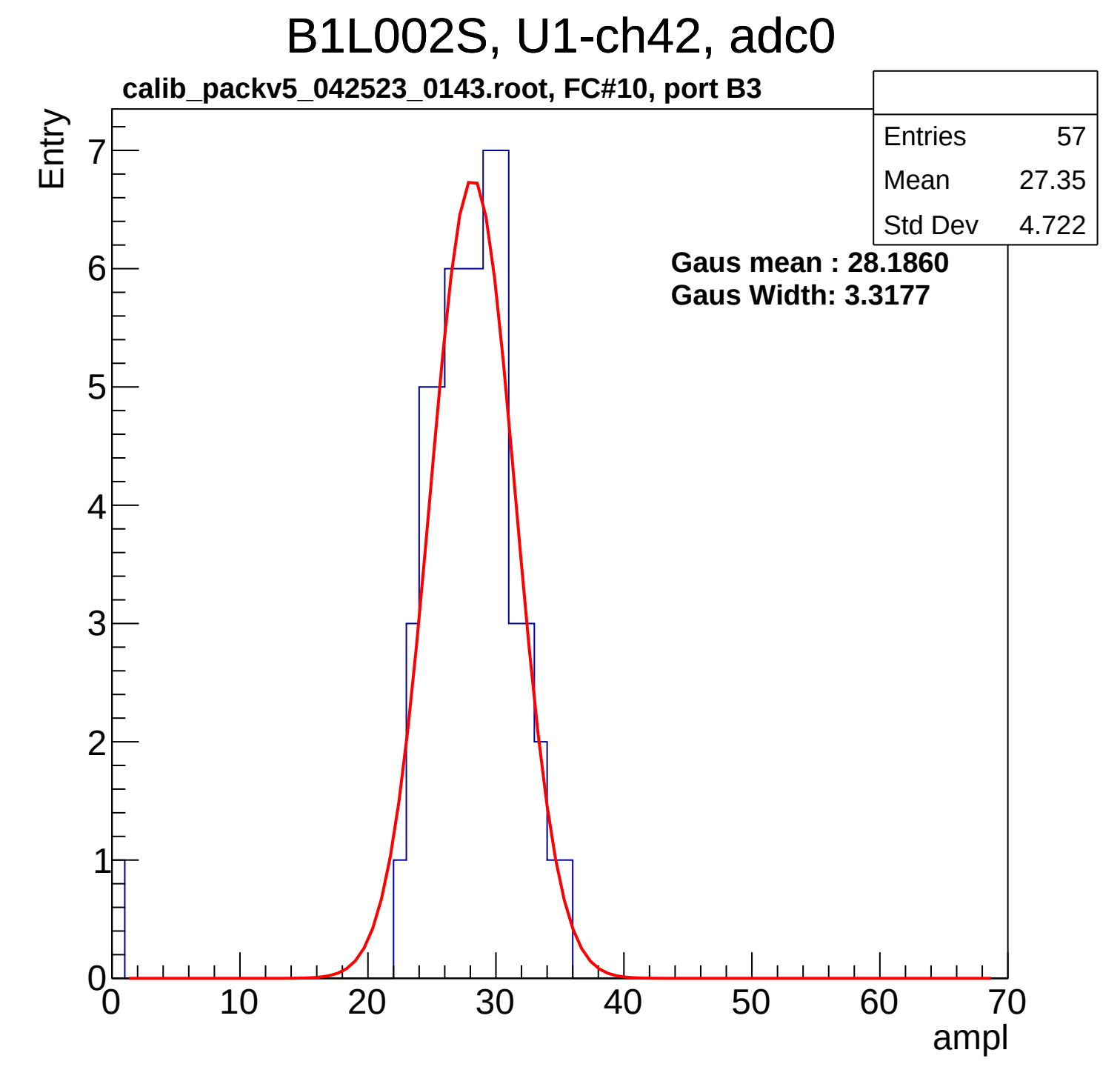
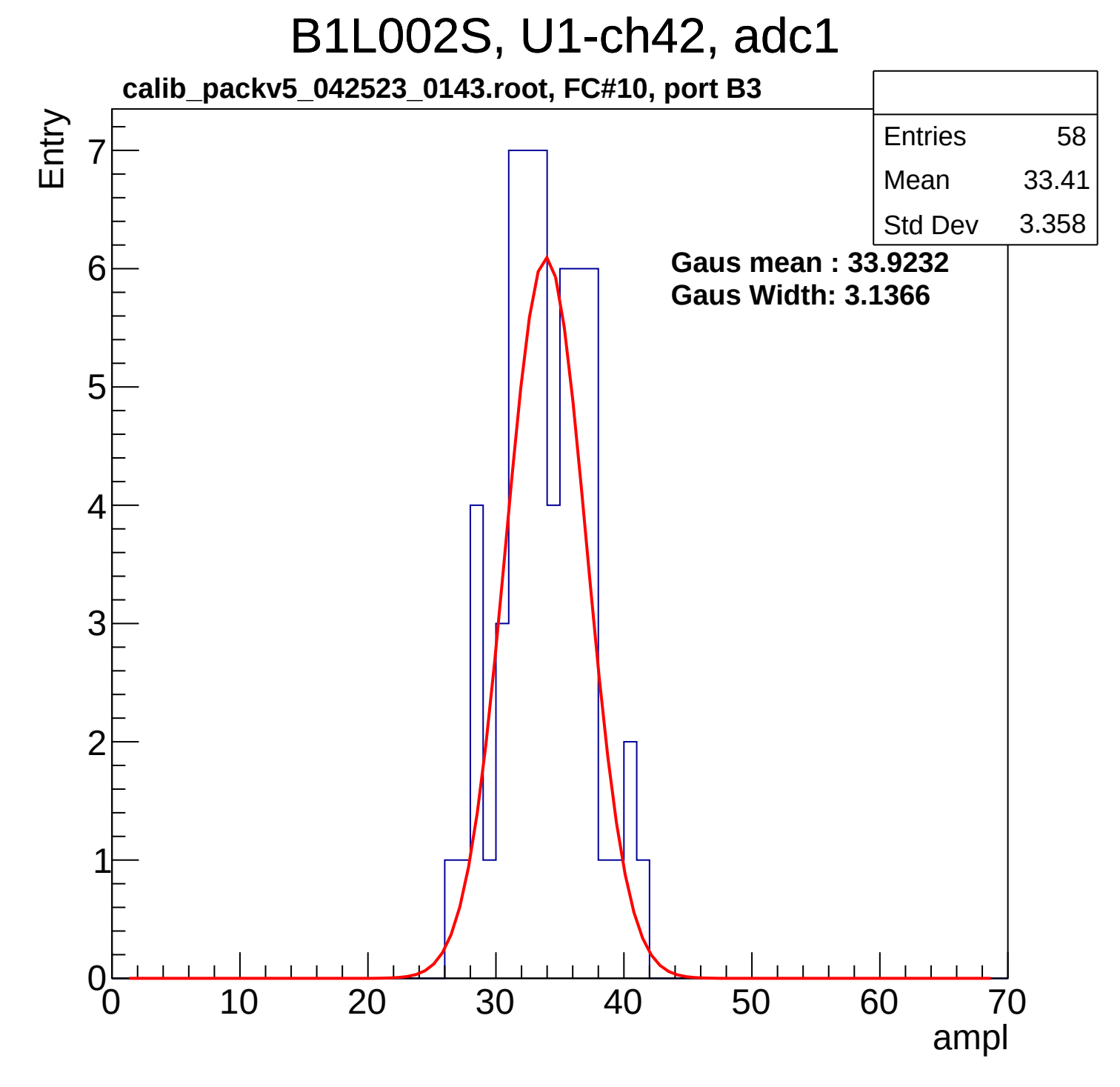
40
50
60
70


B1L002S, U1-ch42, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.35
Std Dev 4.722
Gaus mean : 28.1860
Gaus Width: 3.3177
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch42, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 33.41
Std Dev 3.358
Gaus mean : 33.9232
Gaus Width: 3.1366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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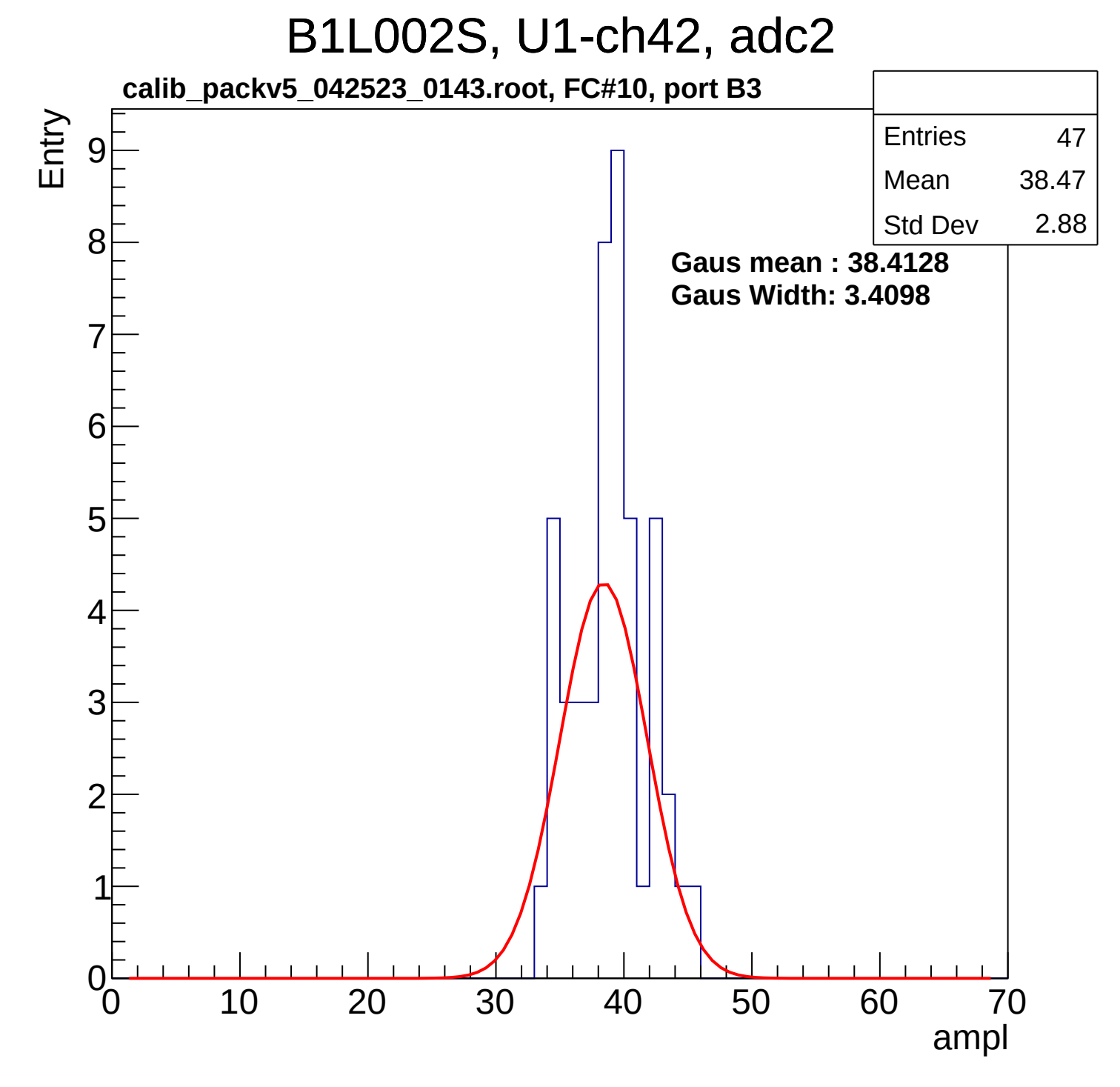
B1L002S, U1-ch42, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 38.47
Std Dev 2.88
Gaus mean : 38.4128
Gaus Width: 3.4098
Entry
ampl
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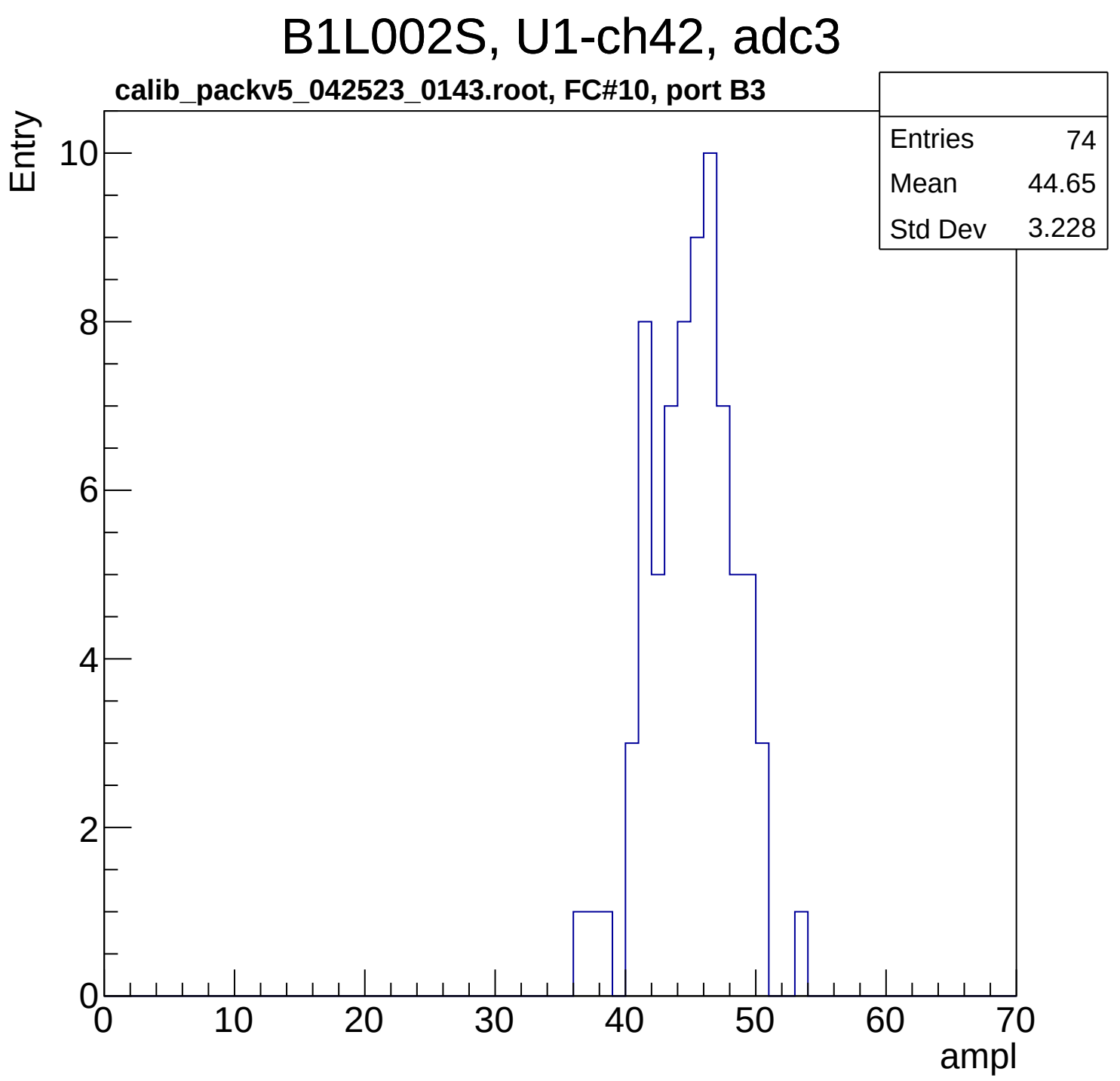
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch42, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 44.65
Std Dev 3.228
Entry
0
2
4
6
8
10
0
10
20
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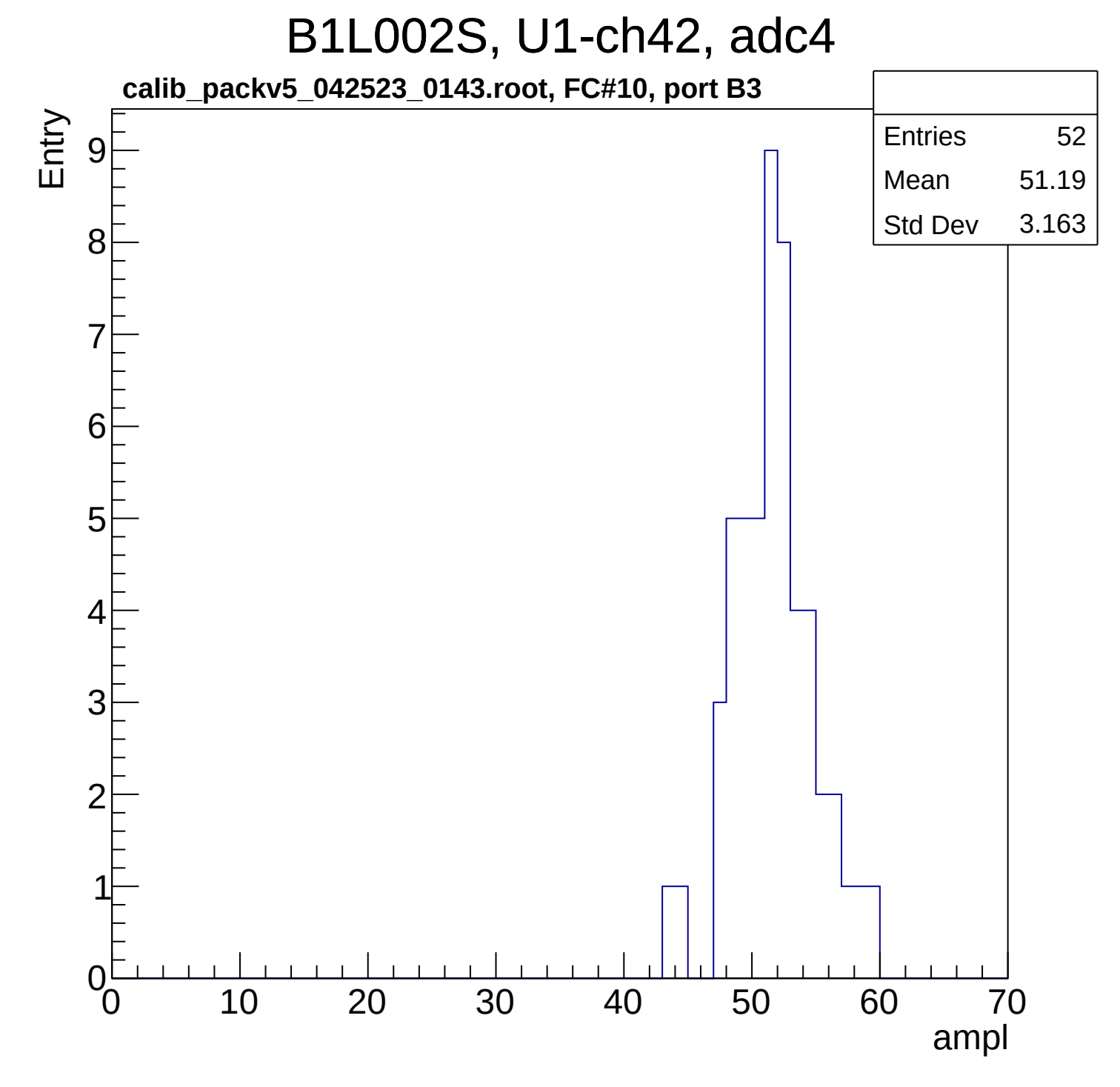
40
50
60
70
ampl


B1L002S, U1-ch42, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 51.19
Std Dev 3.163
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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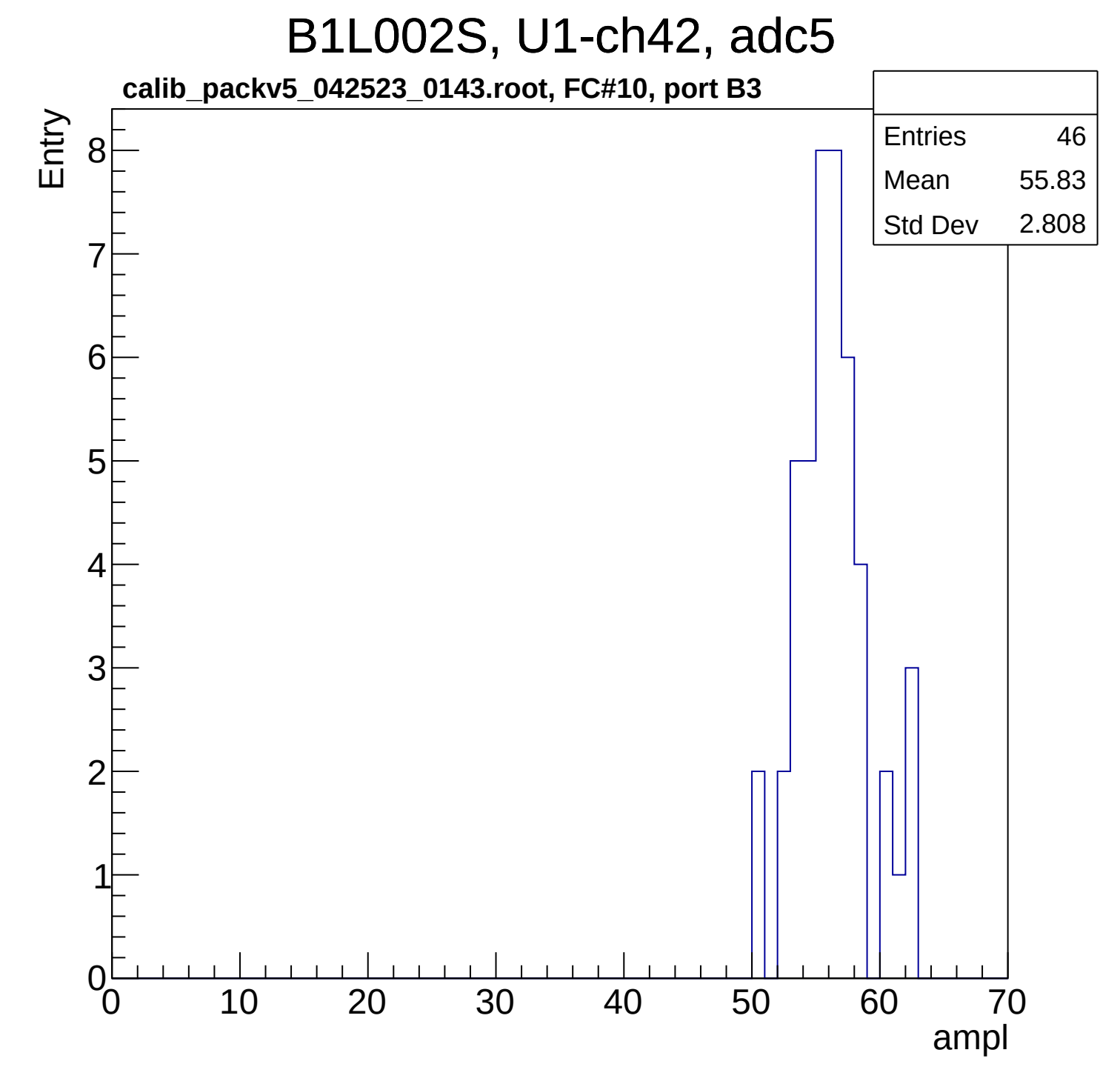
B1L002S, U1-ch42, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 55.83
Std Dev 2.808
Entry
ampl
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1
2
3
4
5
6
7
8
0
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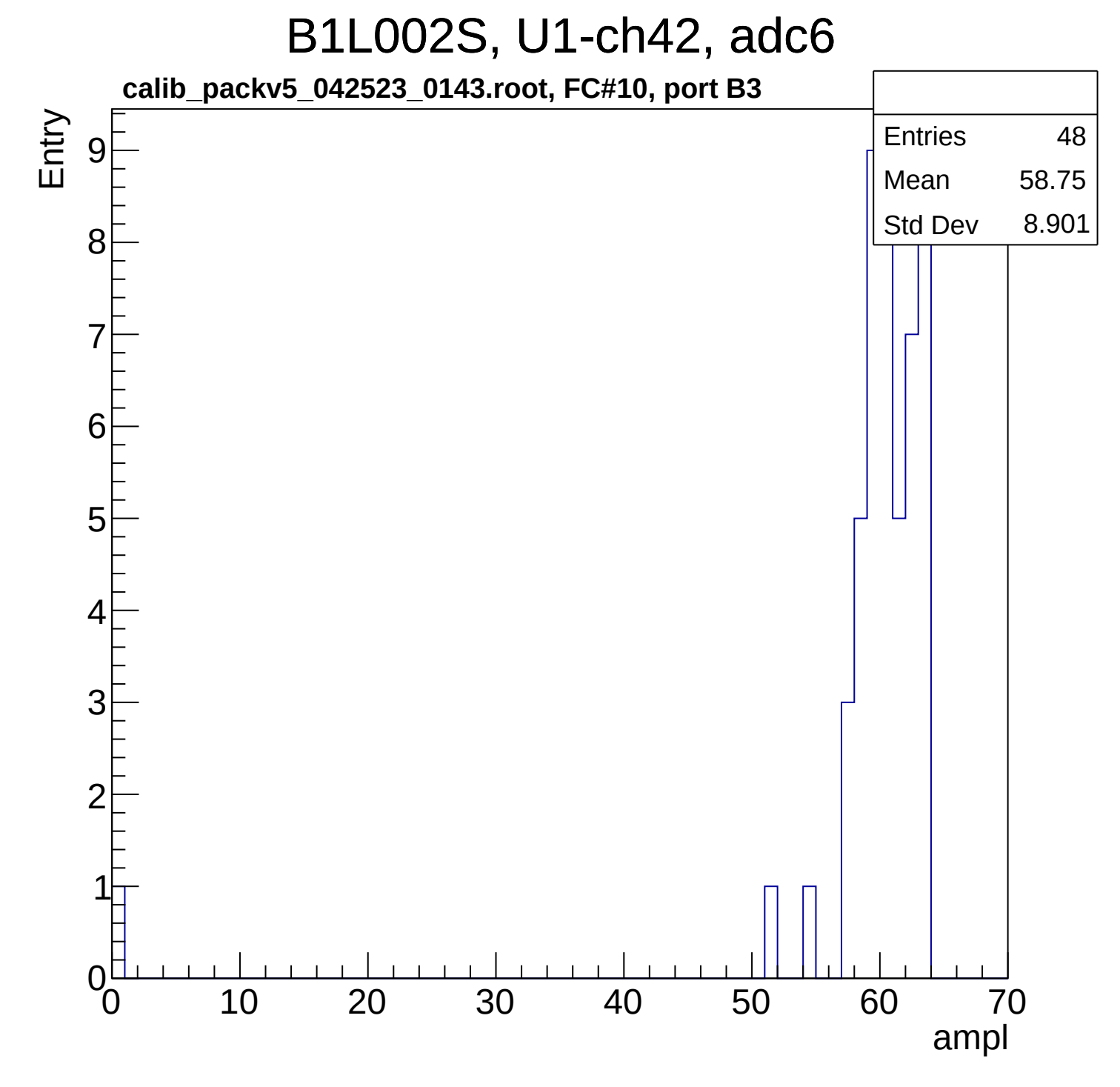
B1L002S, U1-ch42, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.75
Std Dev 8.901
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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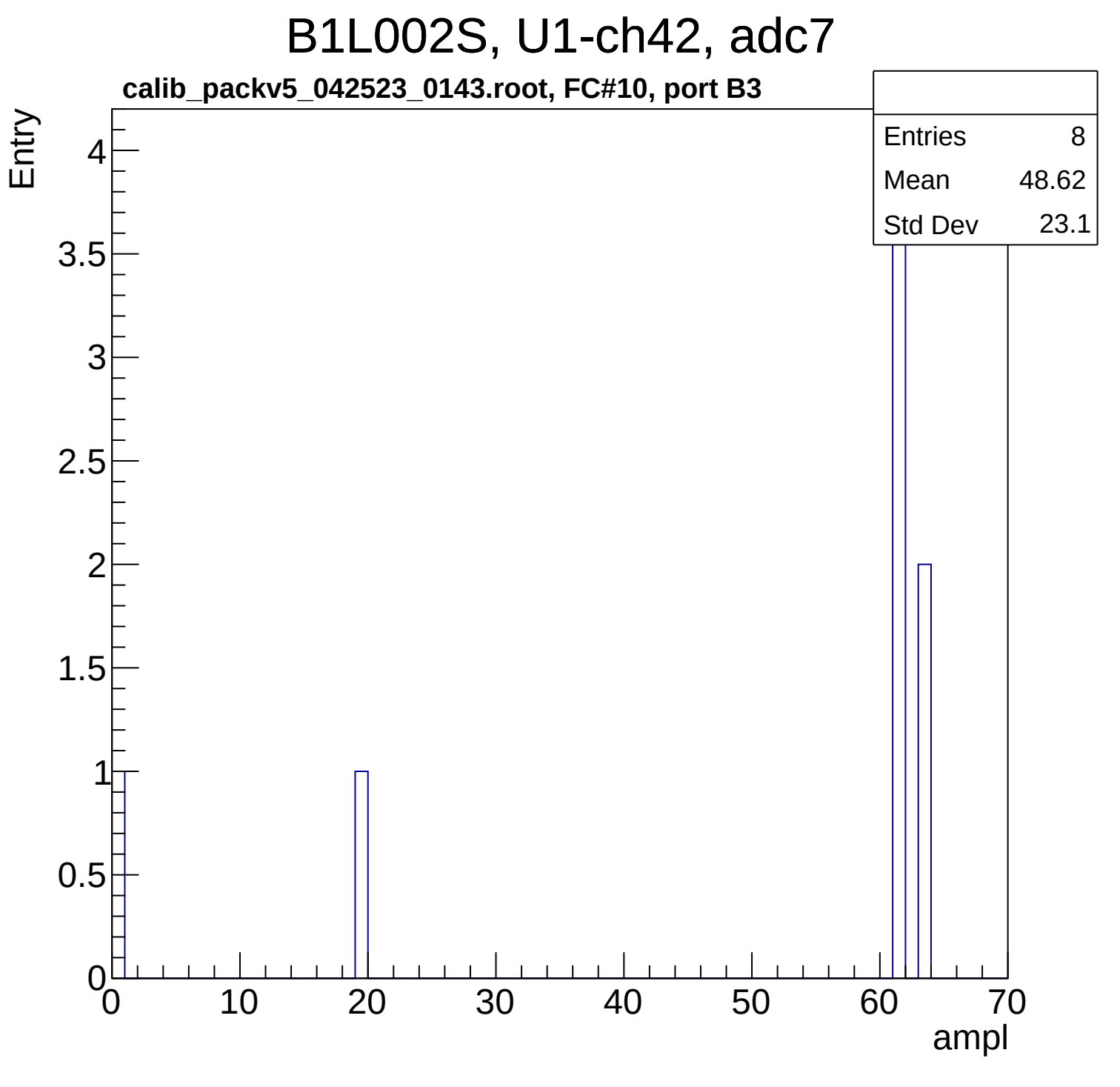
B1L002S, U1-ch42, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 48.62
Std Dev 23.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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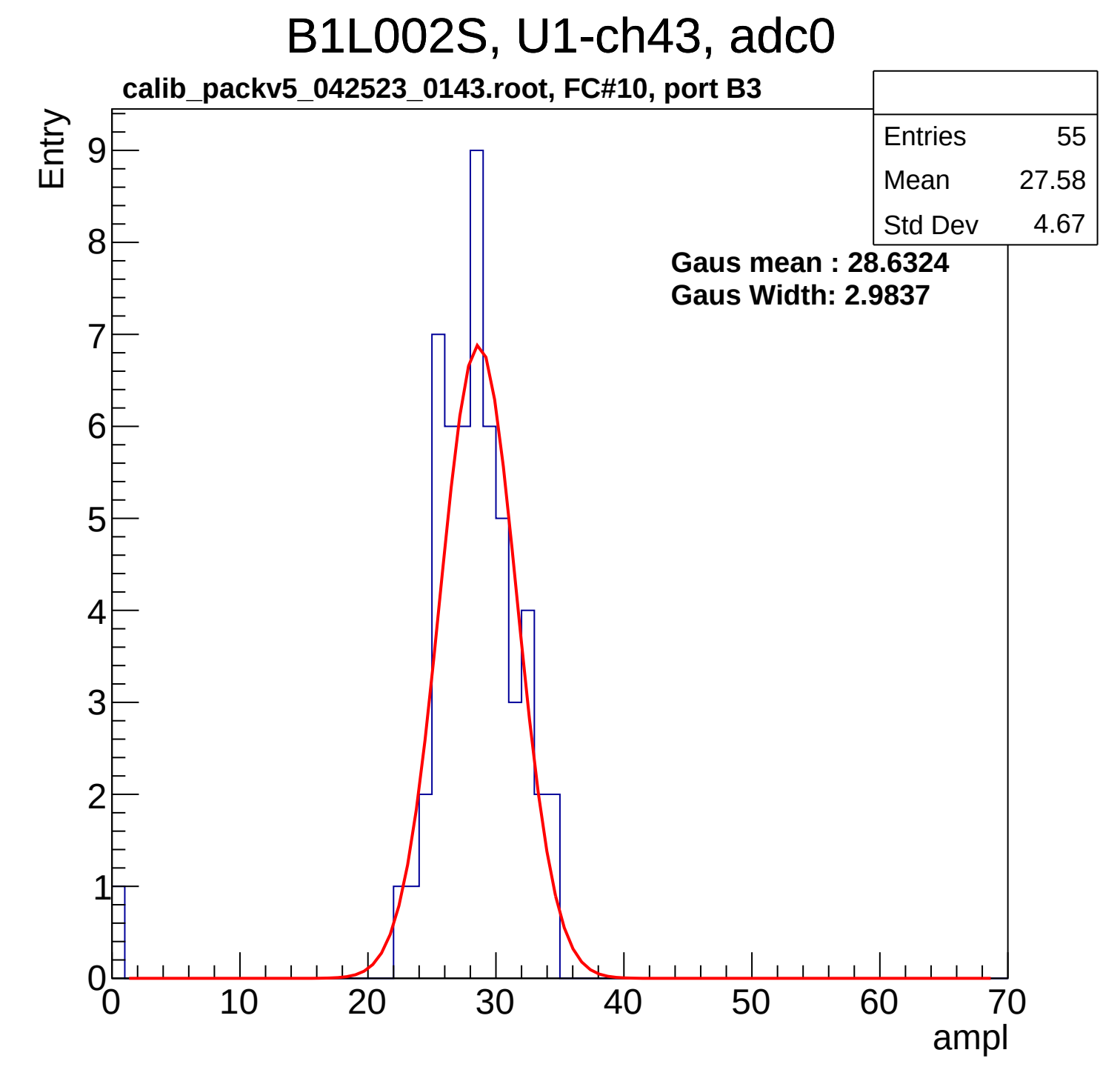
B1L002S, U1-ch43, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 27.58
Std Dev 4.67
Gaus mean : 28.6324
Gaus Width: 2.9837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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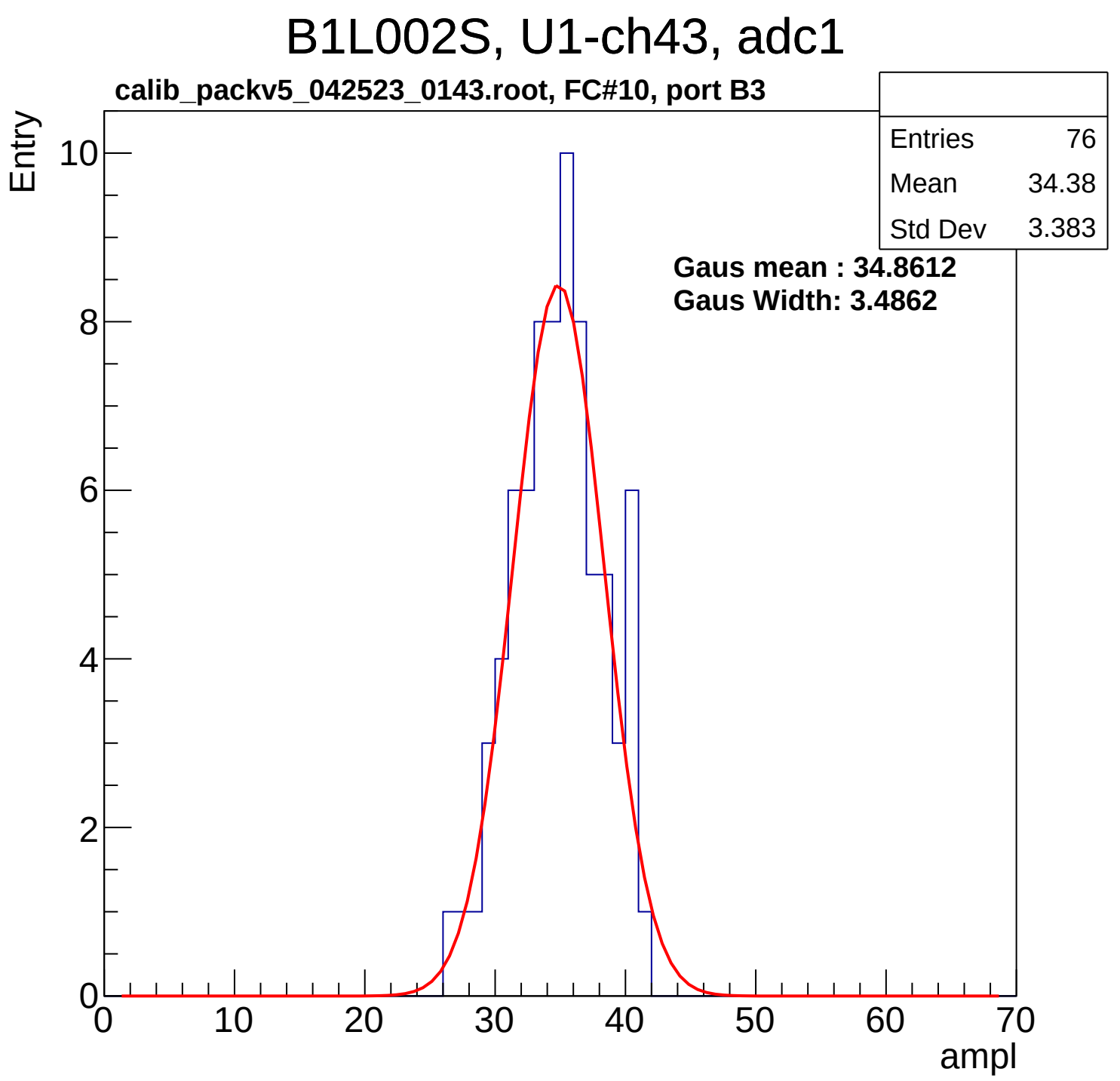
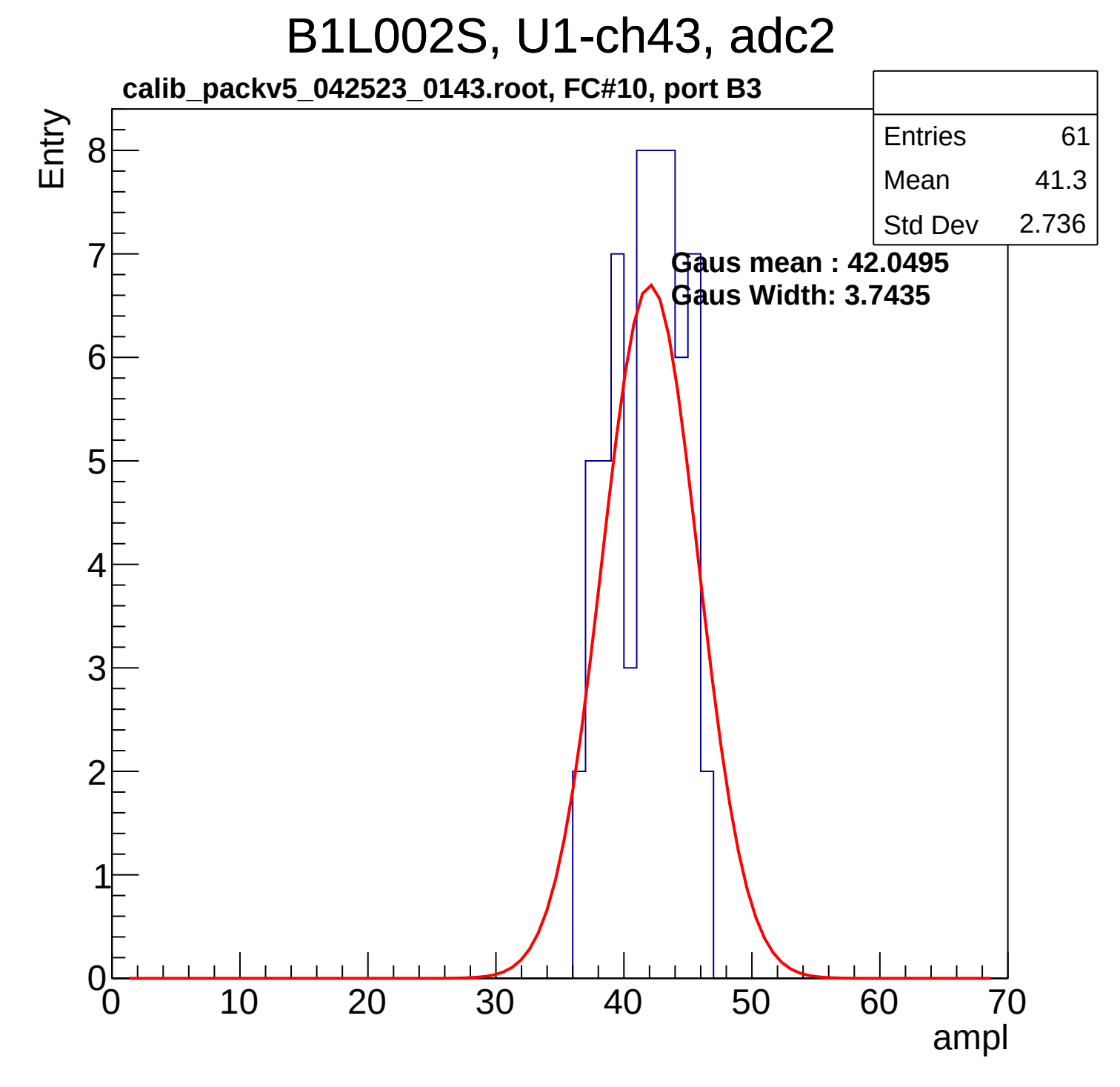

B1L002S, U1-ch43, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 34.38
Std Dev 3.383
Gaus mean : 34.8612
Gaus Width: 3.4862
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch43, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.3
Std Dev 2.736
Gaus mean : 42.0495
Gaus Width: 3.7435
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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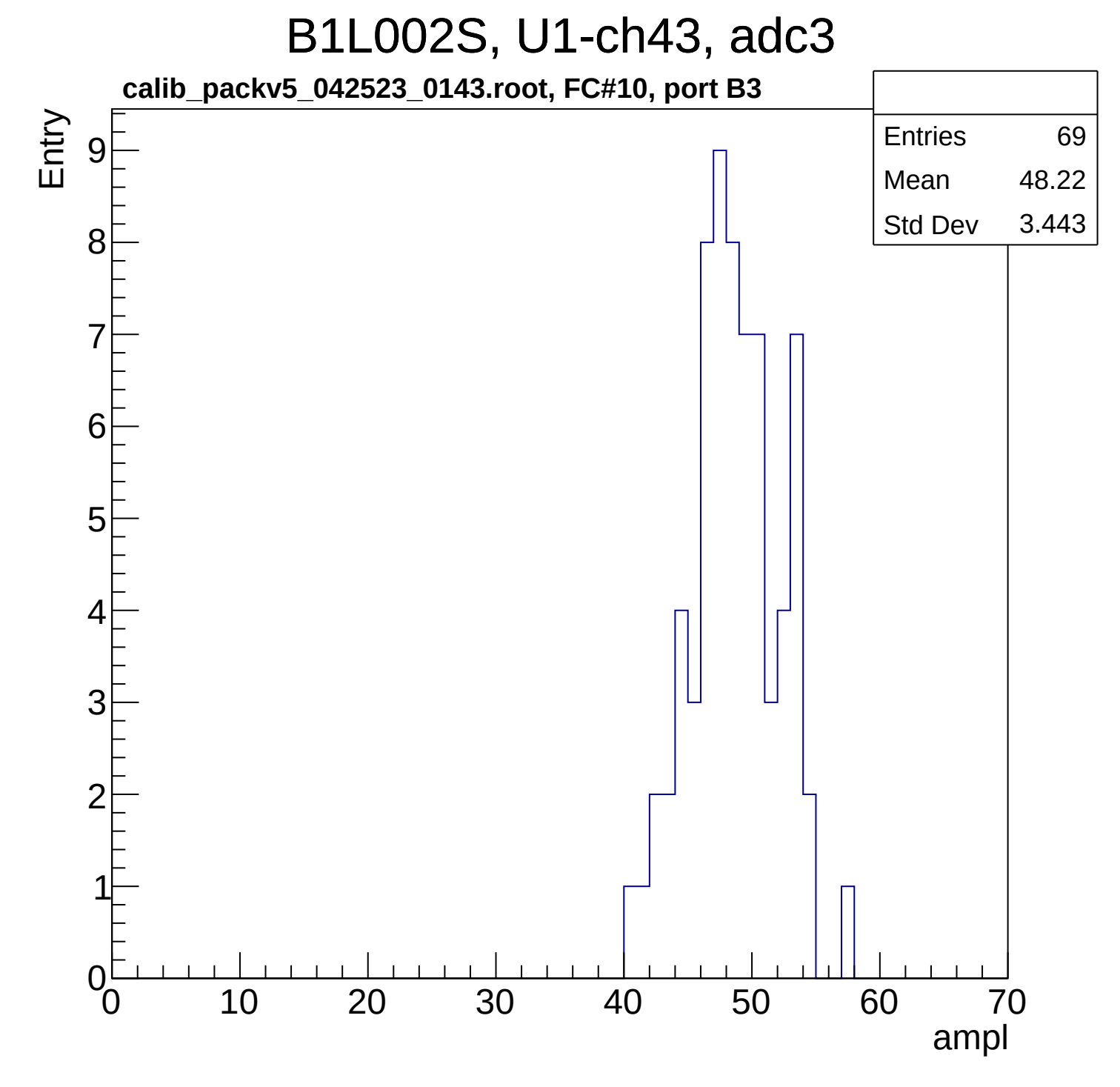
B1L002S, U1-ch43, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 48.22
Std Dev 3.443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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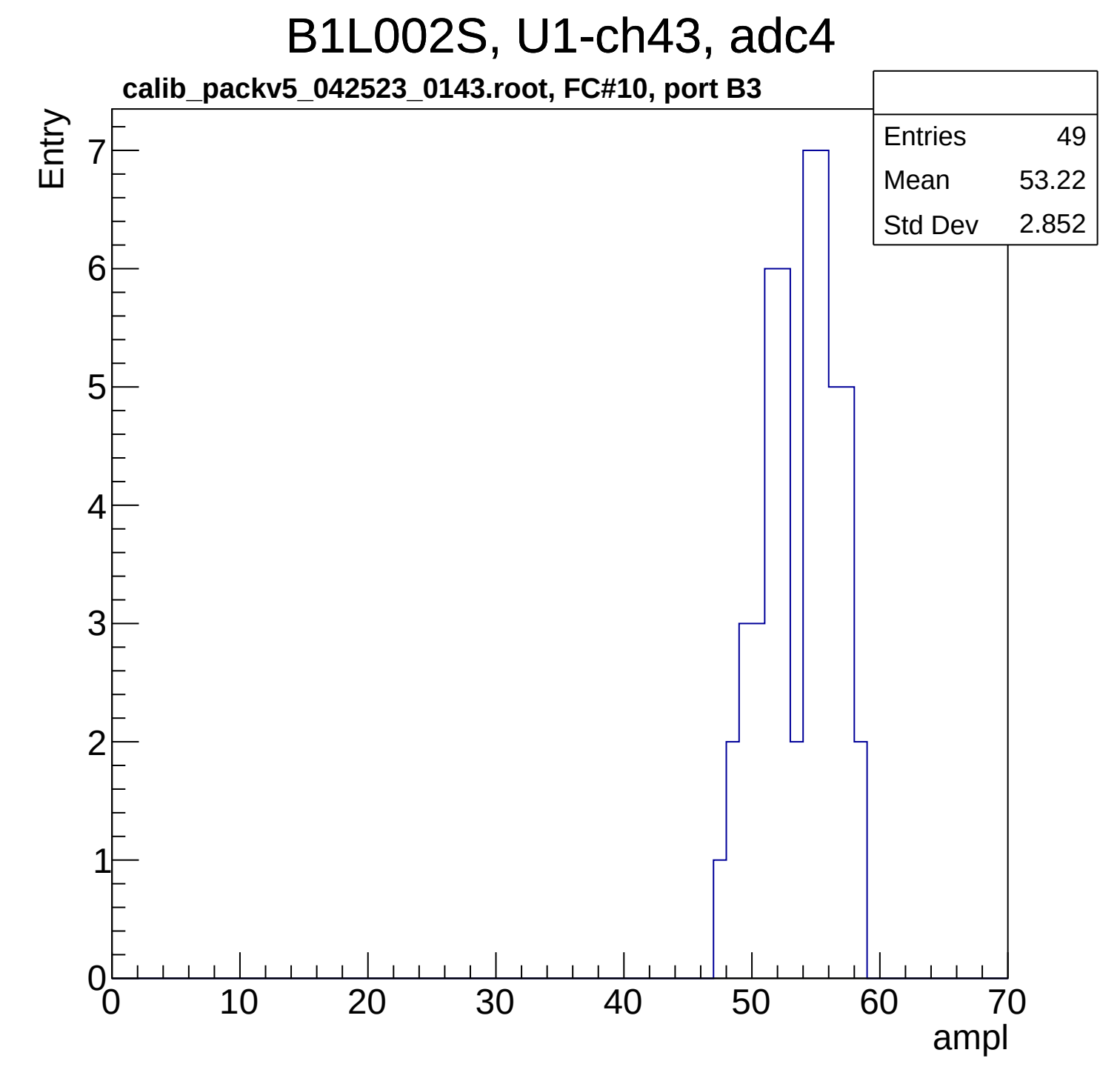
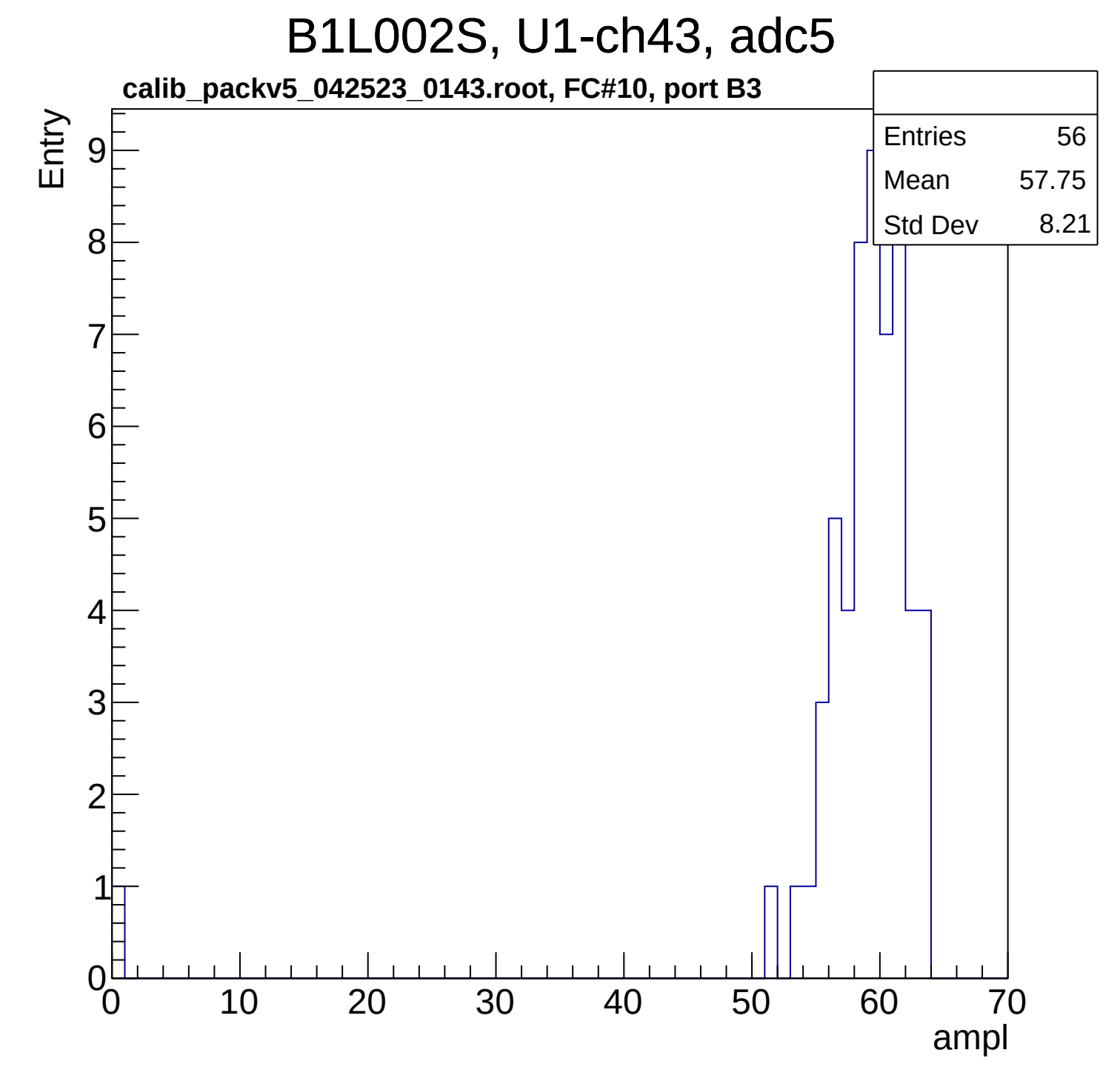
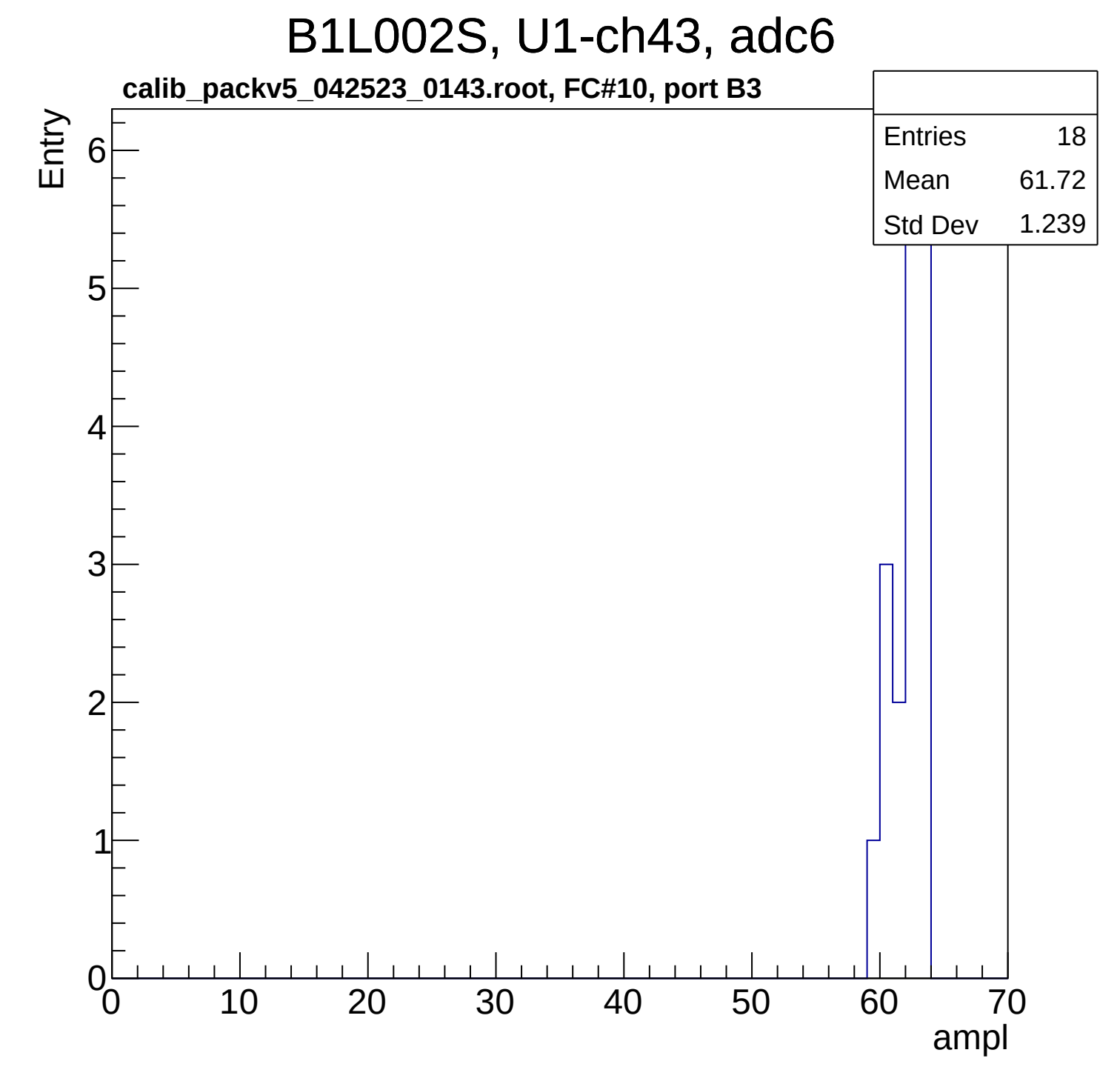
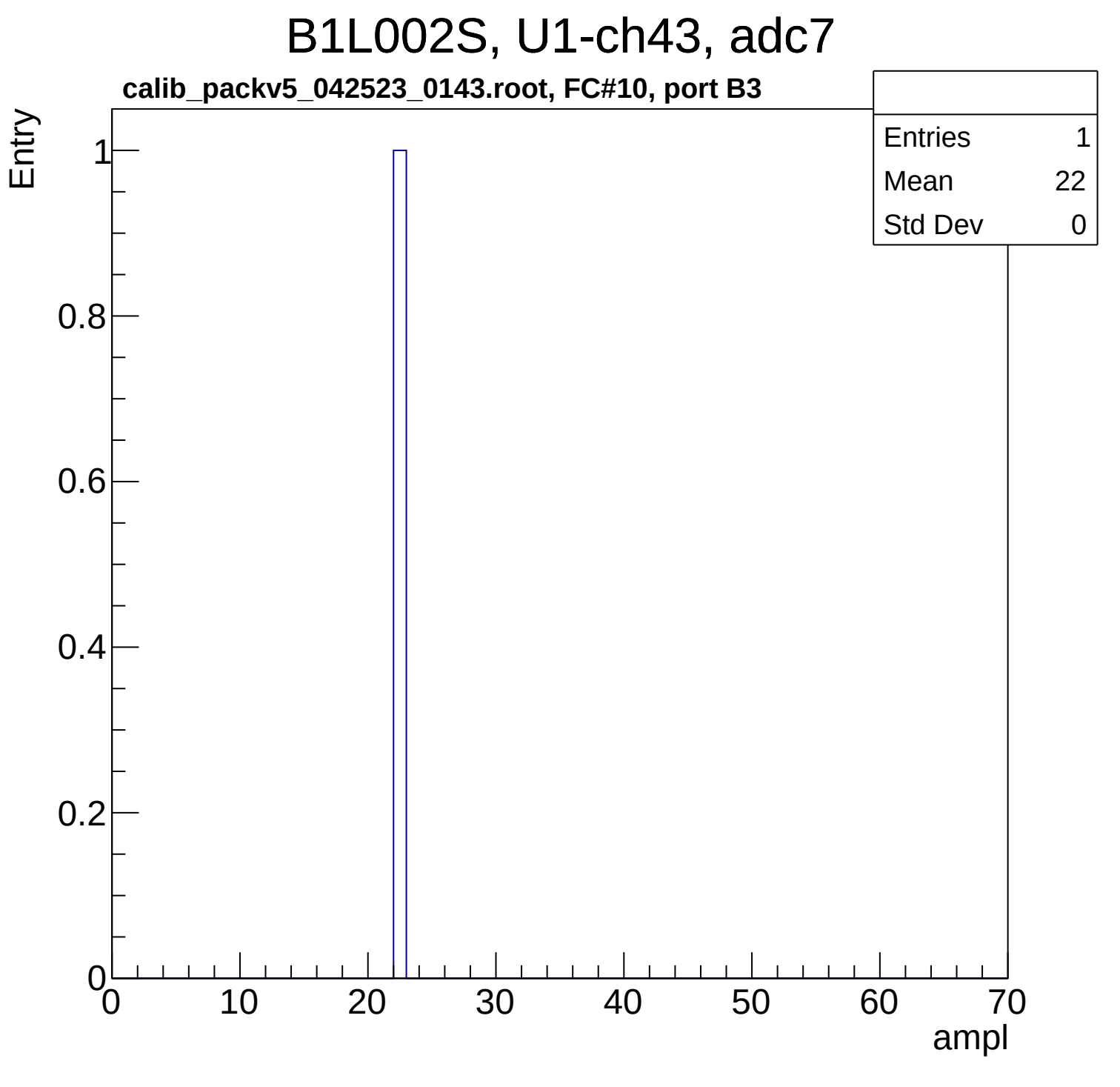

B1L002S, U1-ch43, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 53.22
Std Dev 2.852
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch43, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.75
Std Dev 8.21
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch43, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.72
Std Dev 1.239
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch43, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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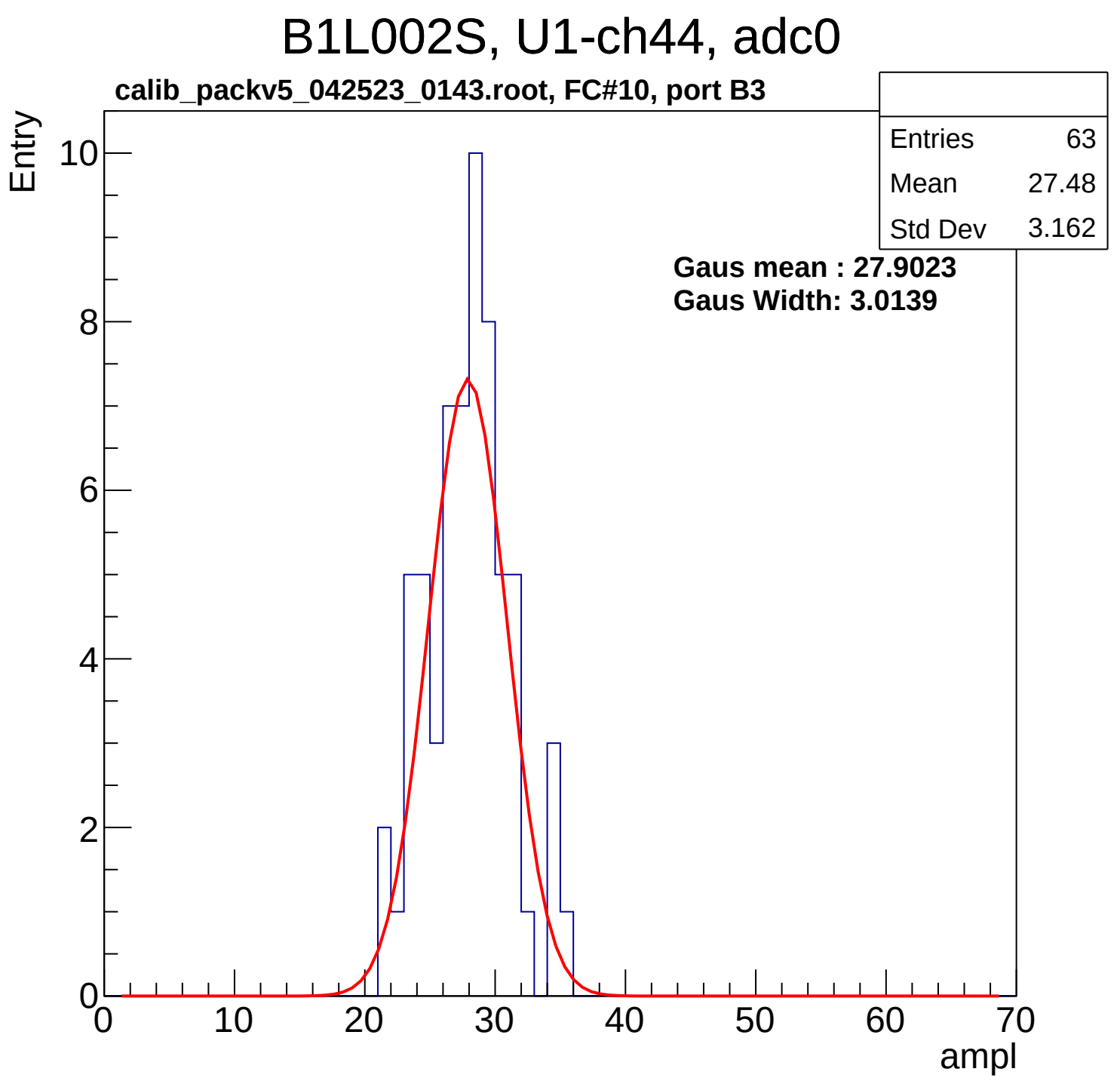
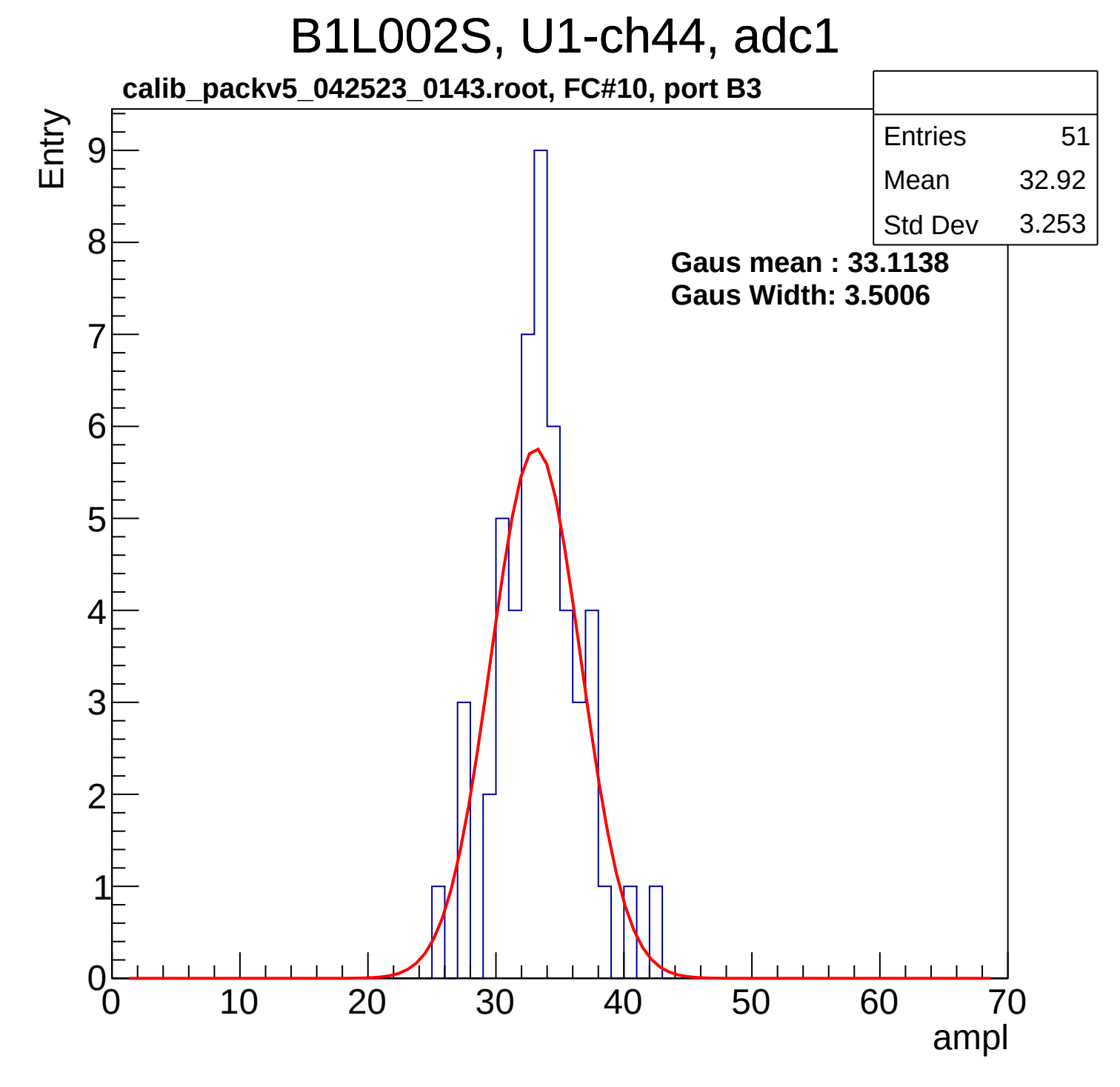
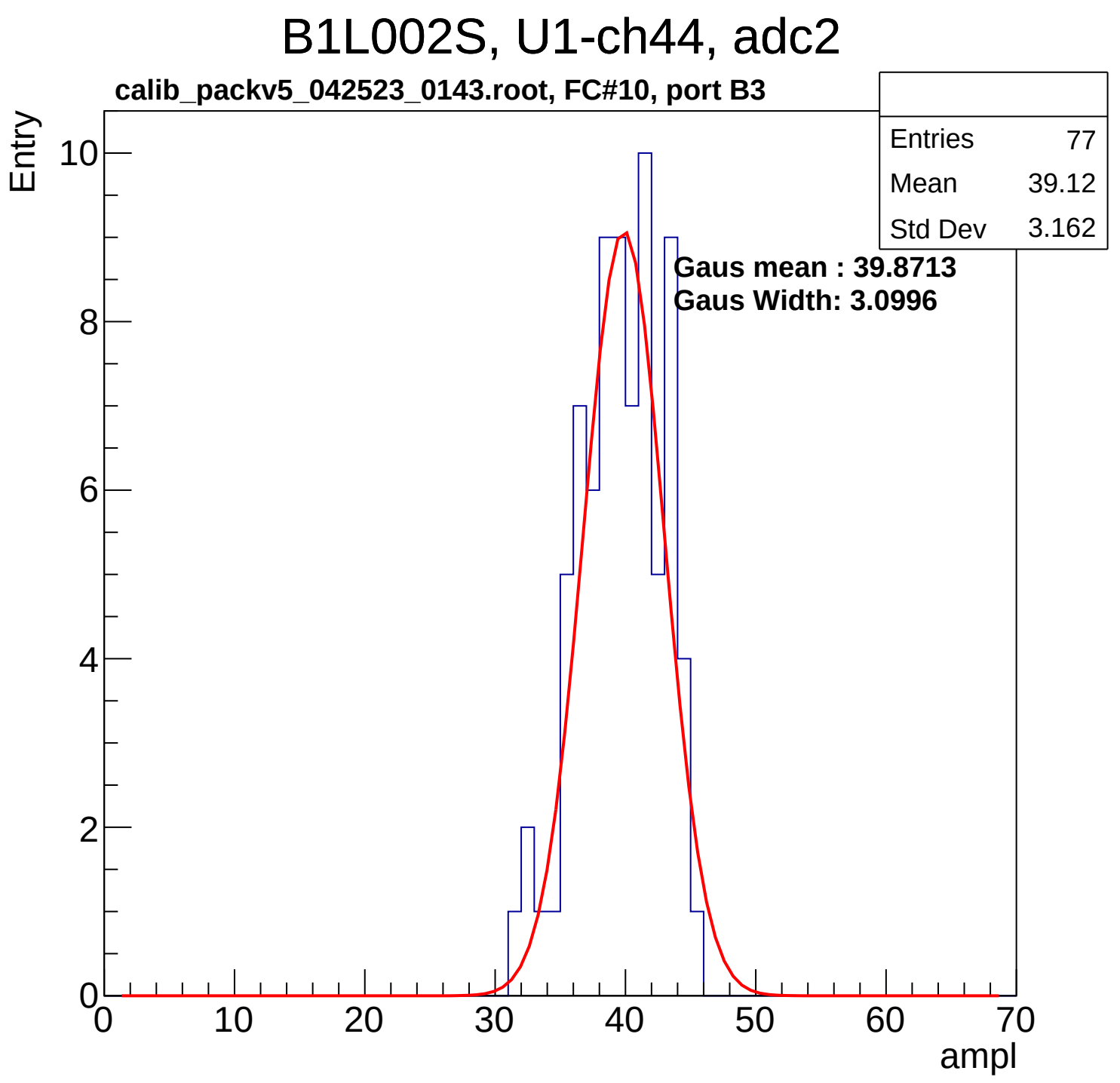
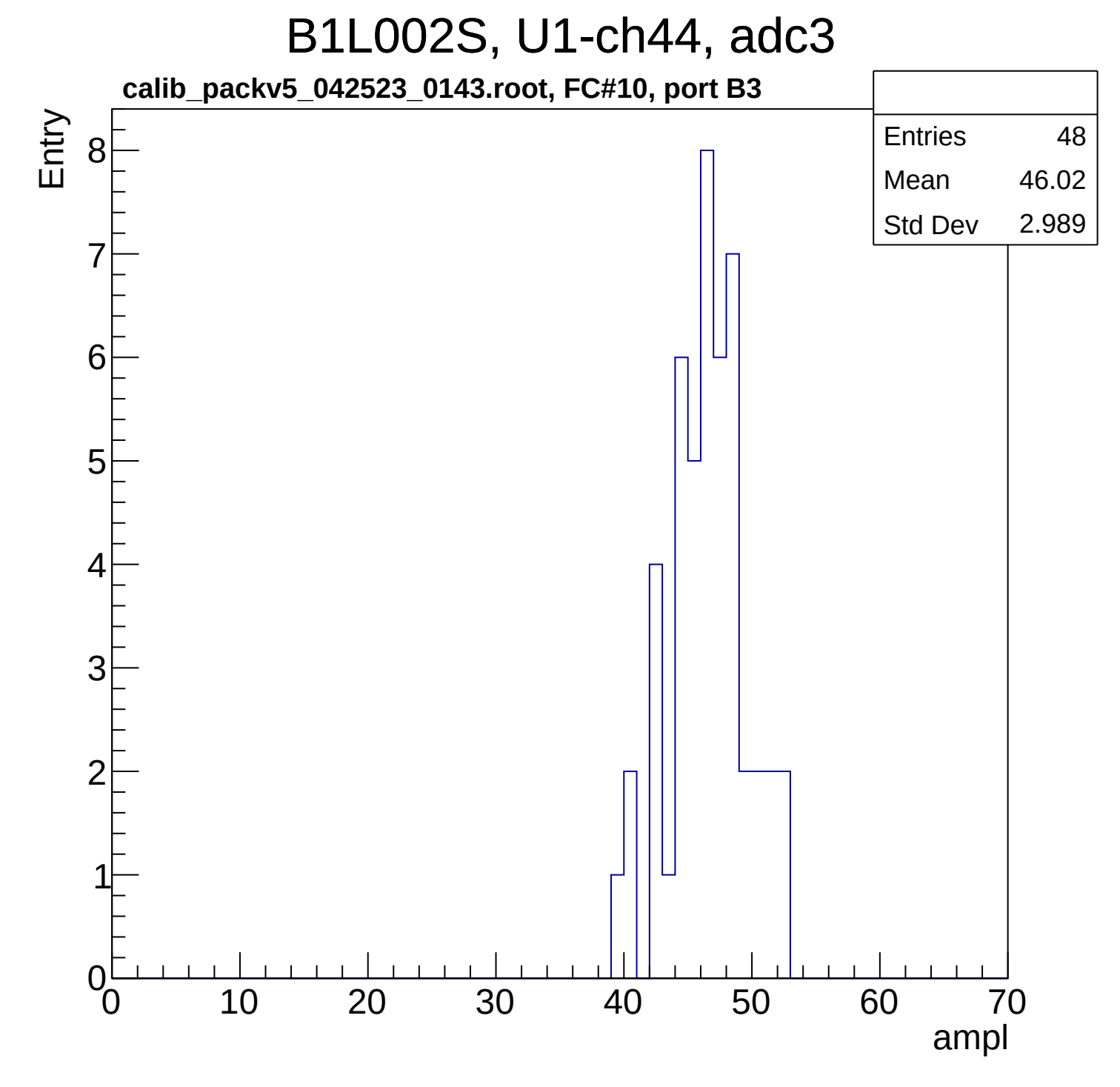
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch44, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.48
Std Dev 3.162
Gaus mean : 27.9023
Gaus Width: 3.0139
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch44, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 32.92
Std Dev 3.253
Gaus mean : 33.1138
Gaus Width: 3.5006
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch44, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 39.12
Std Dev 3.162
Gaus mean : 39.8713
Gaus Width: 3.0996
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch44, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 46.02
Std Dev 2.989
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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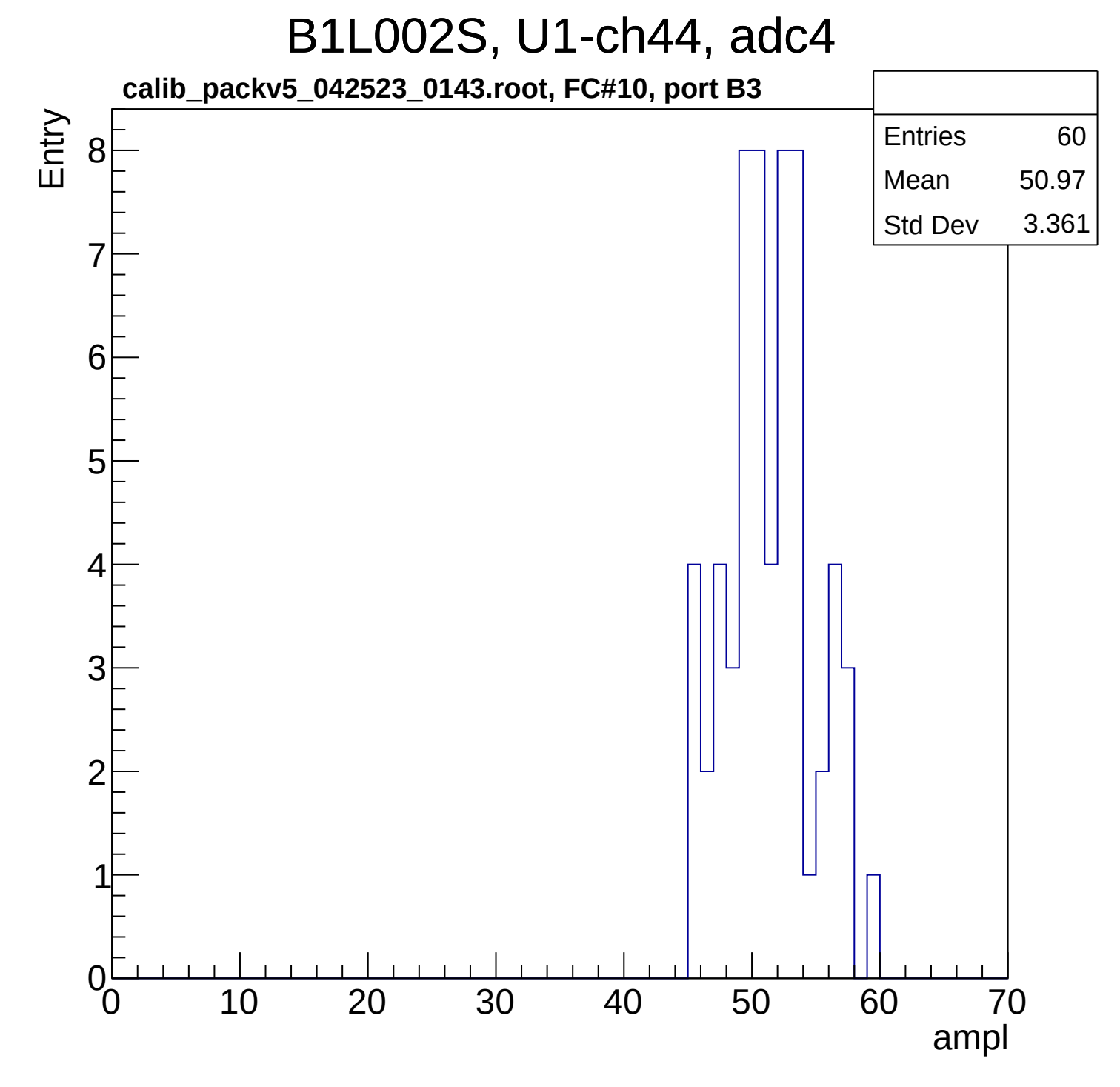
B1L002S, U1-ch44, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 50.97
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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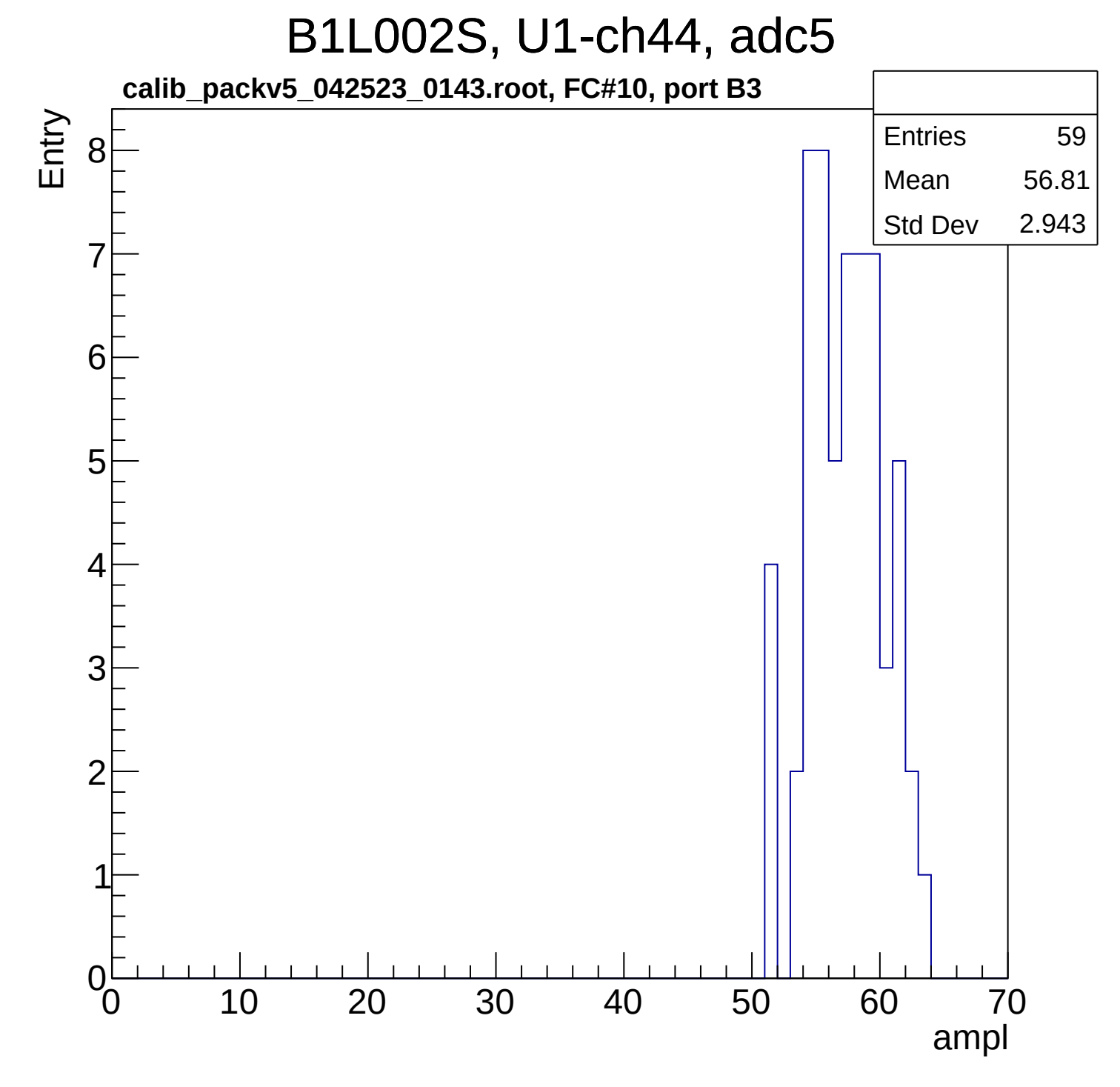
B1L002S, U1-ch44, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 56.81
Std Dev 2.943
Entry
ampl
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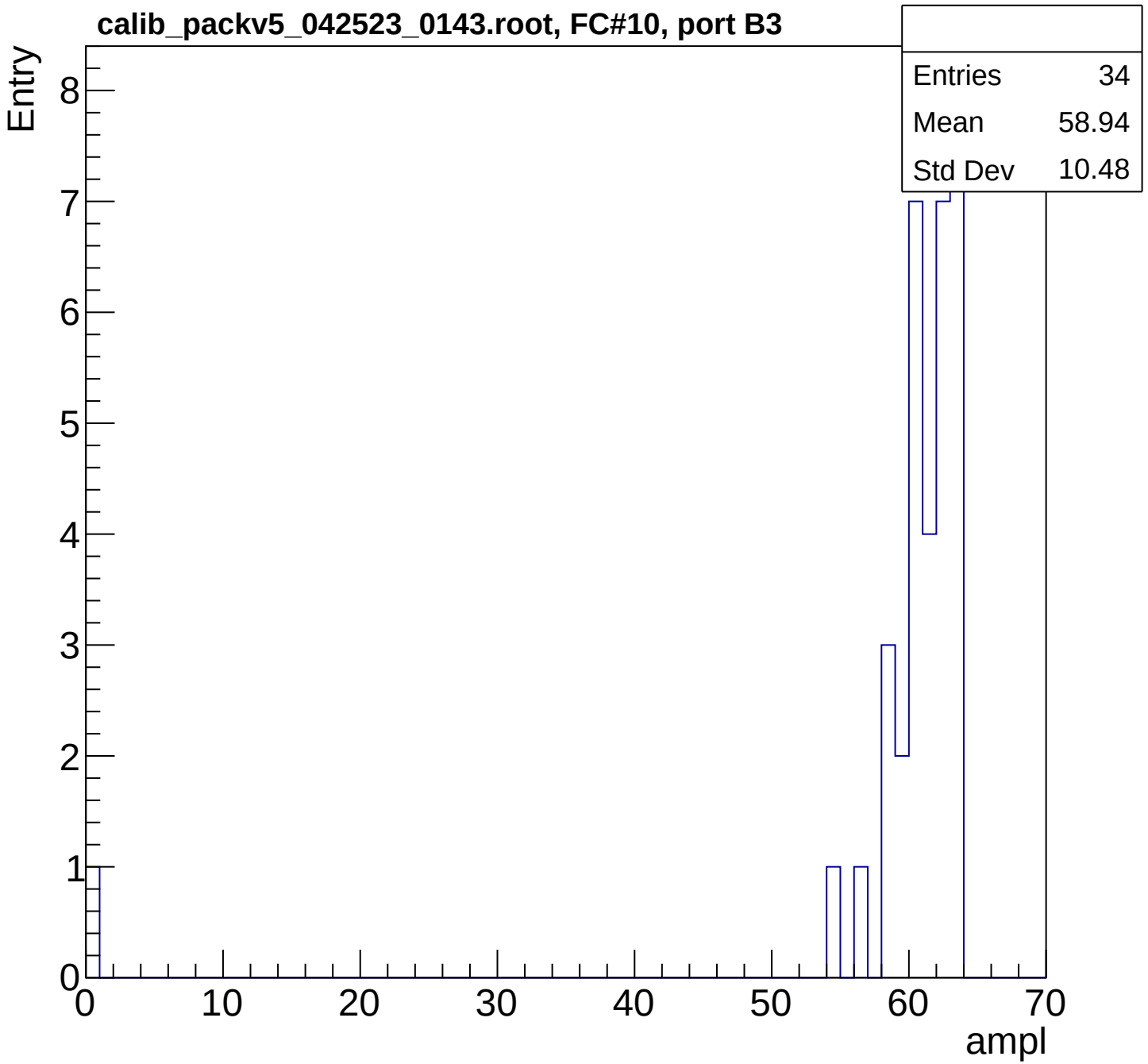
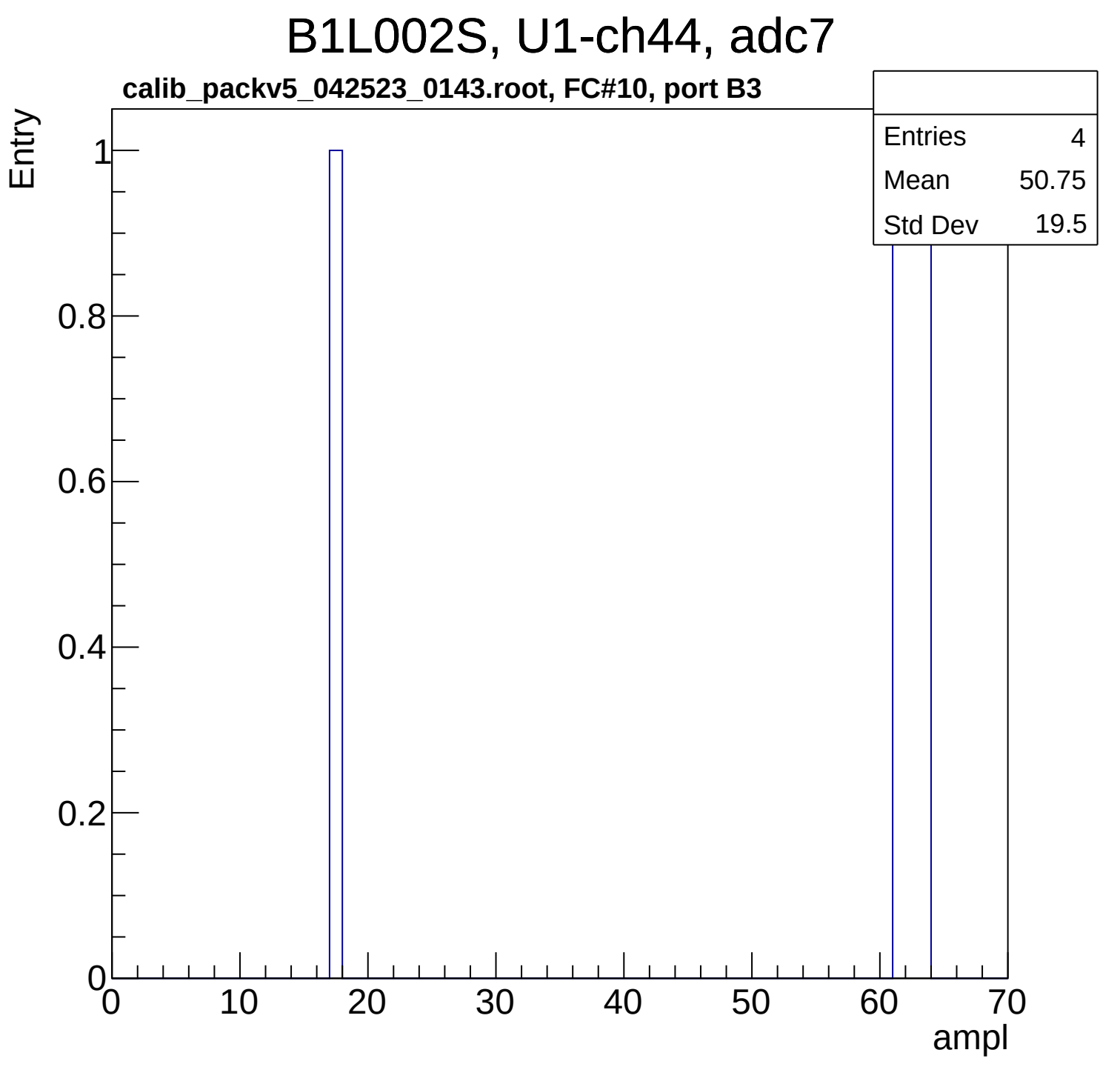
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch44, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.94
Std Dev 10.48
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch44, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 50.75
Std Dev 19.5
Entry
ampl
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0.6
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0
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60
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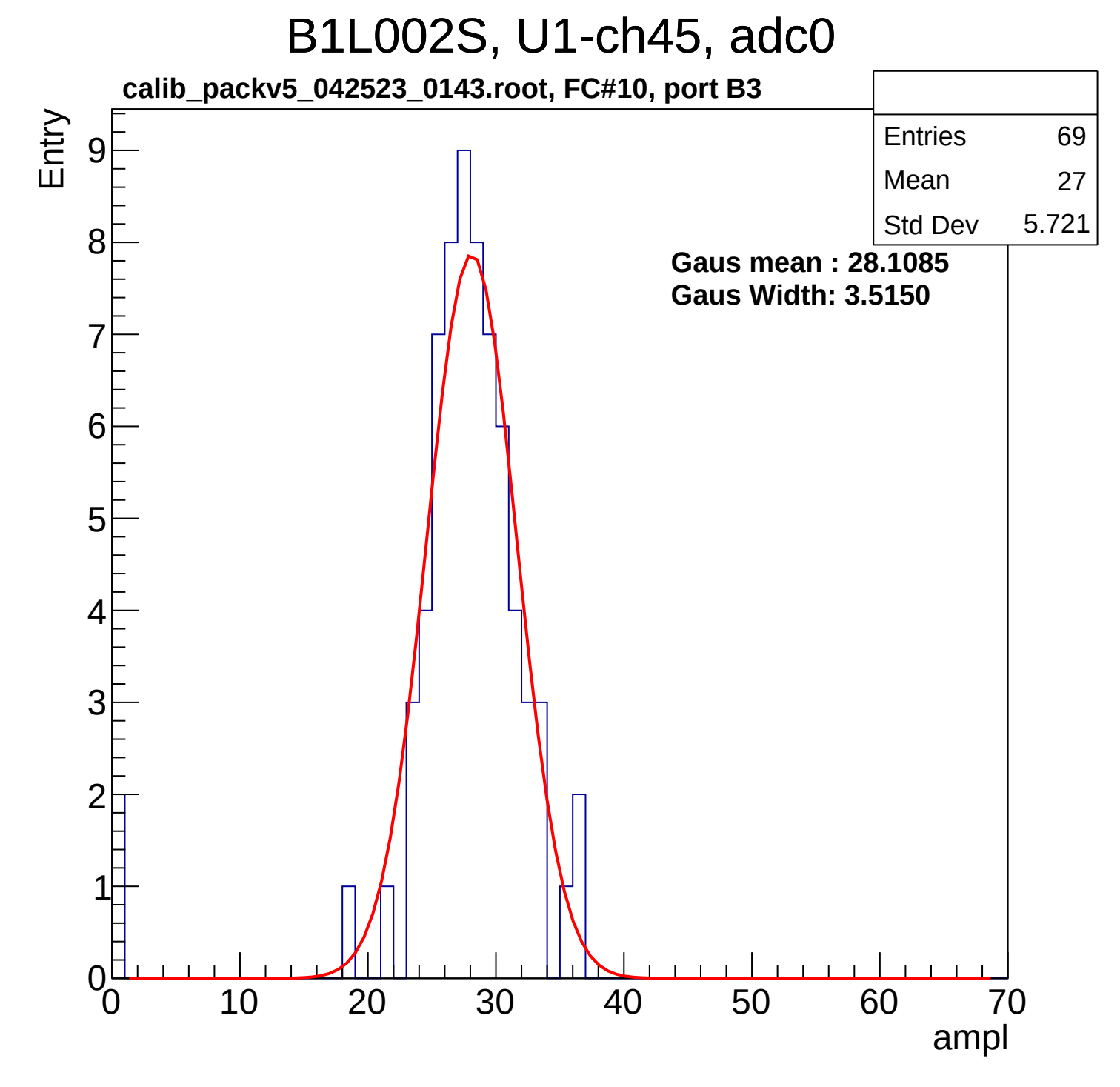
B1L002S, U1-ch45, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27
Std Dev 5.721
Gaus mean : 28.1085
Gaus Width: 3.5150
Entry
ampl
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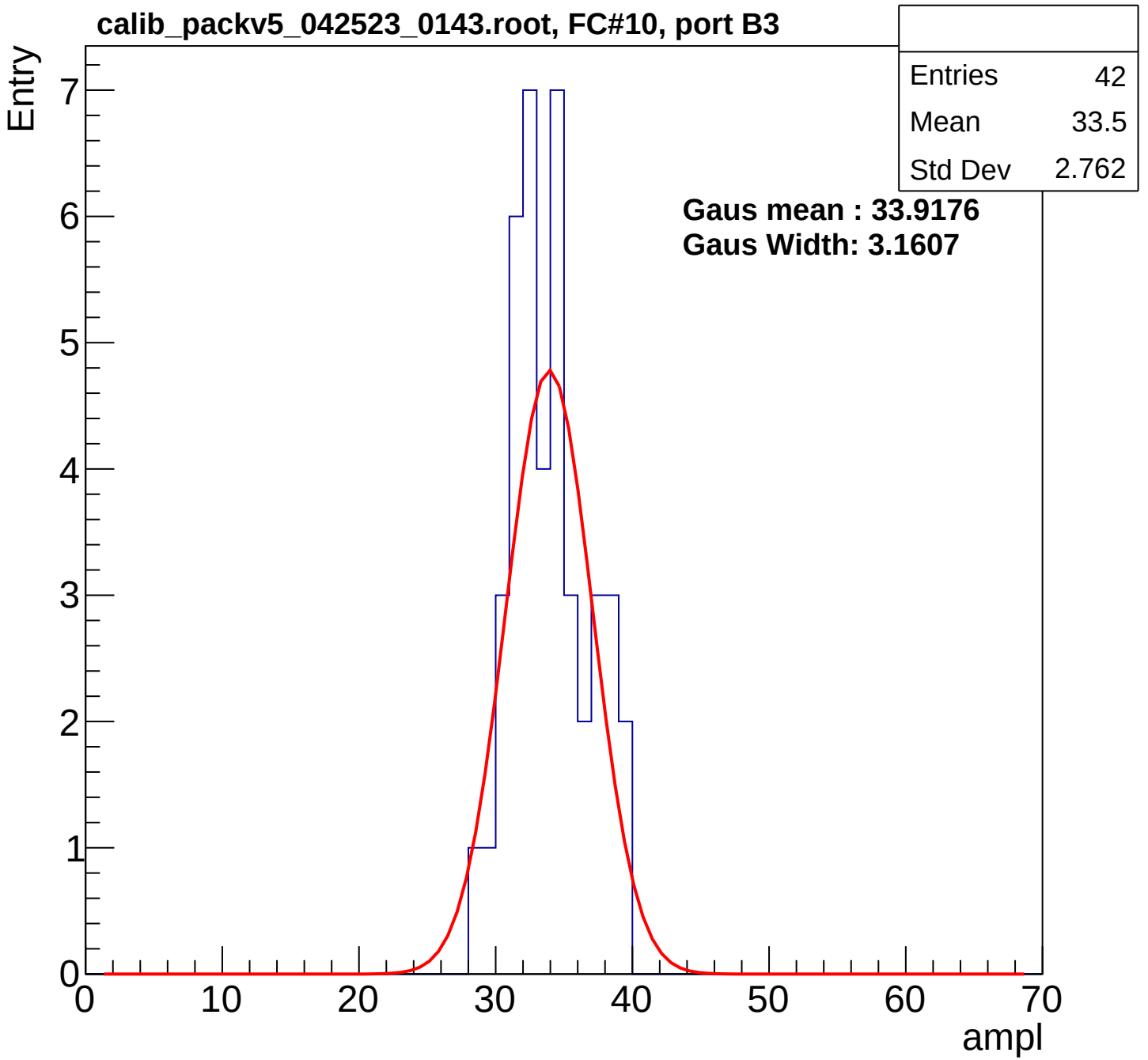
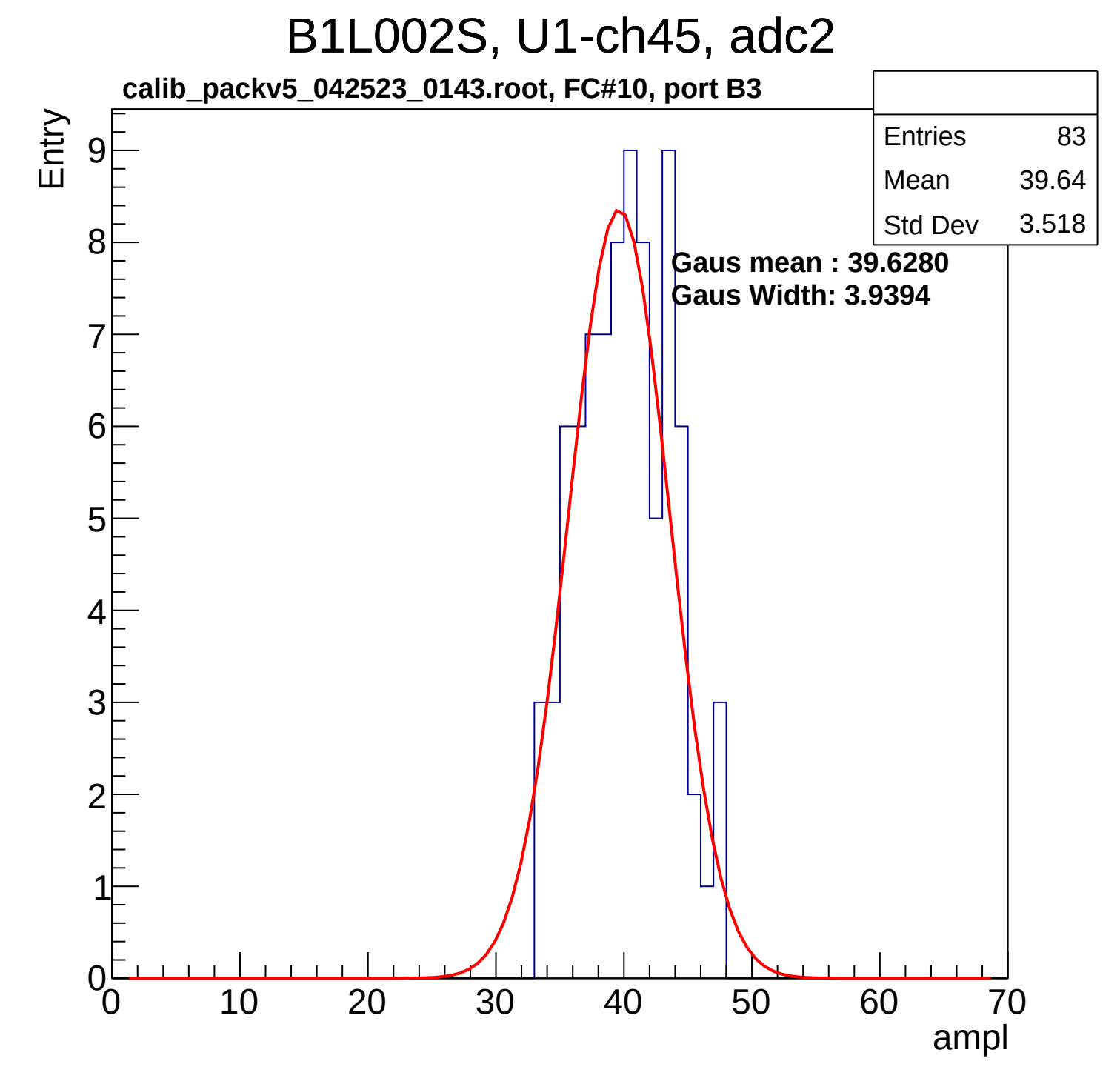
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch45, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 33.5
Std Dev 2.762
Gaus mean : 33.9176
Gaus Width: 3.1607
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch45, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 39.64
Std Dev 3.518
Gaus mean : 39.6280
Gaus Width: 3.9394
Entry
ampl
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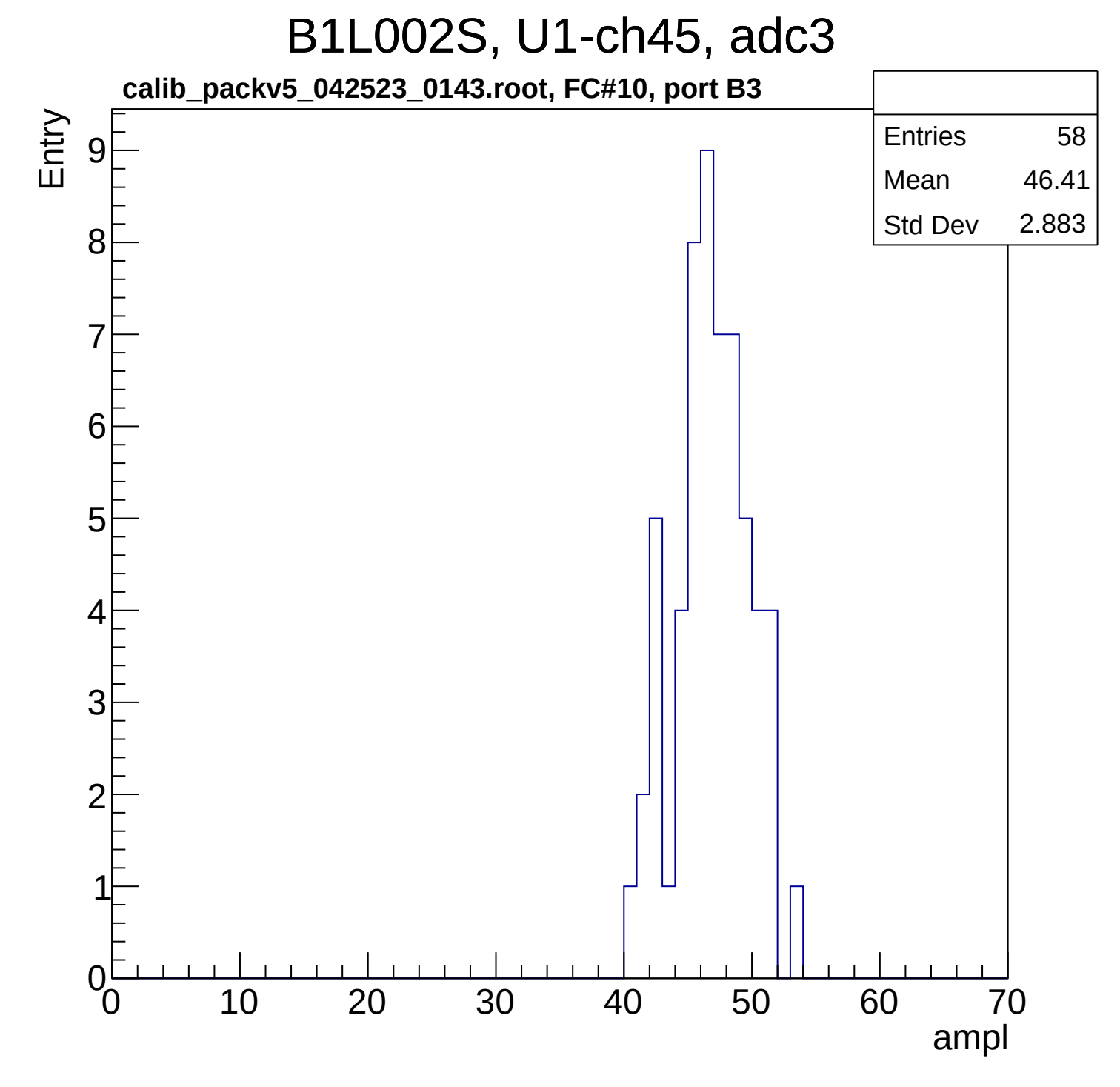
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch45, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.41
Std Dev 2.883
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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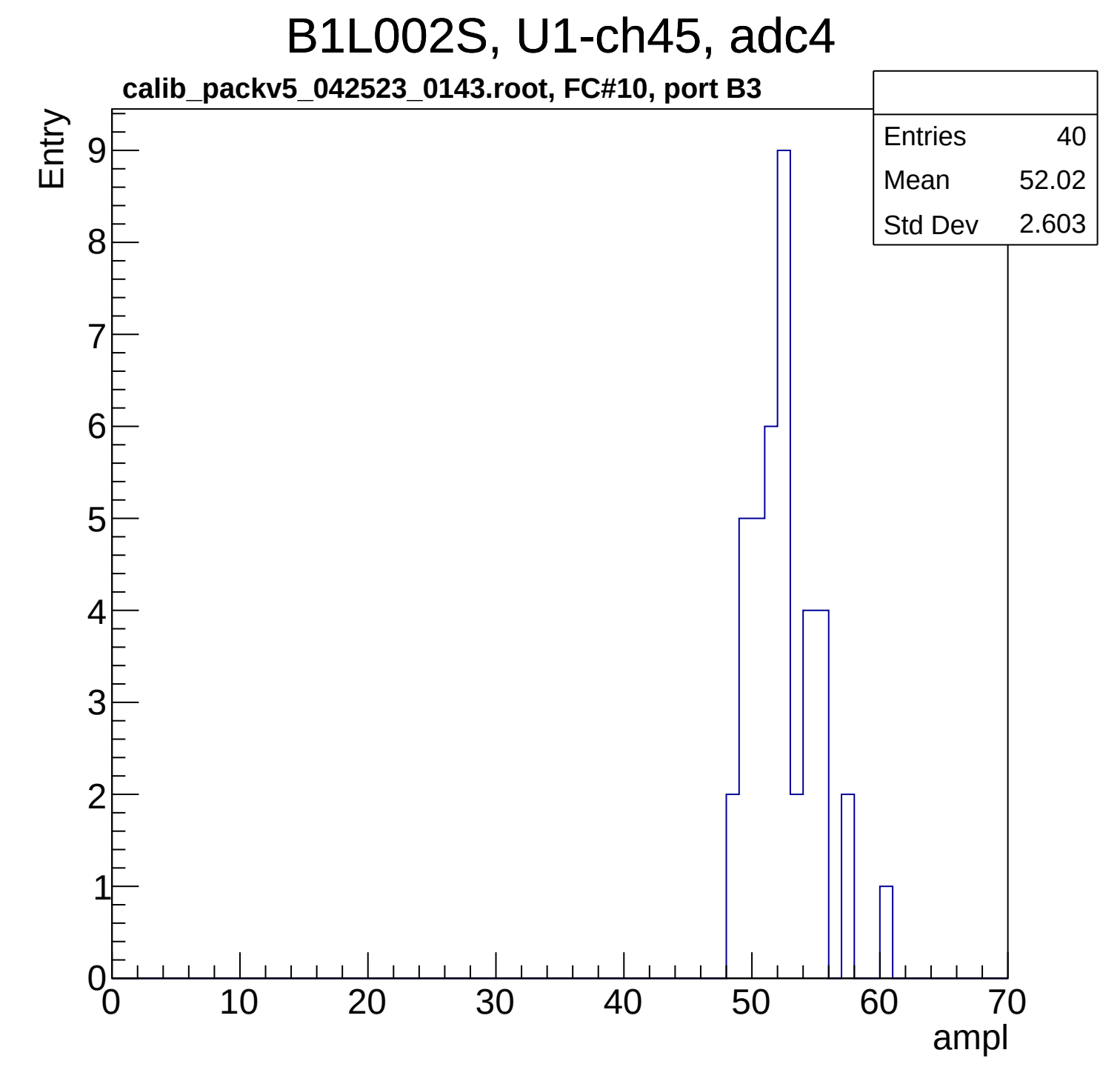
B1L002S, U1-ch45, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 52.02
Std Dev 2.603
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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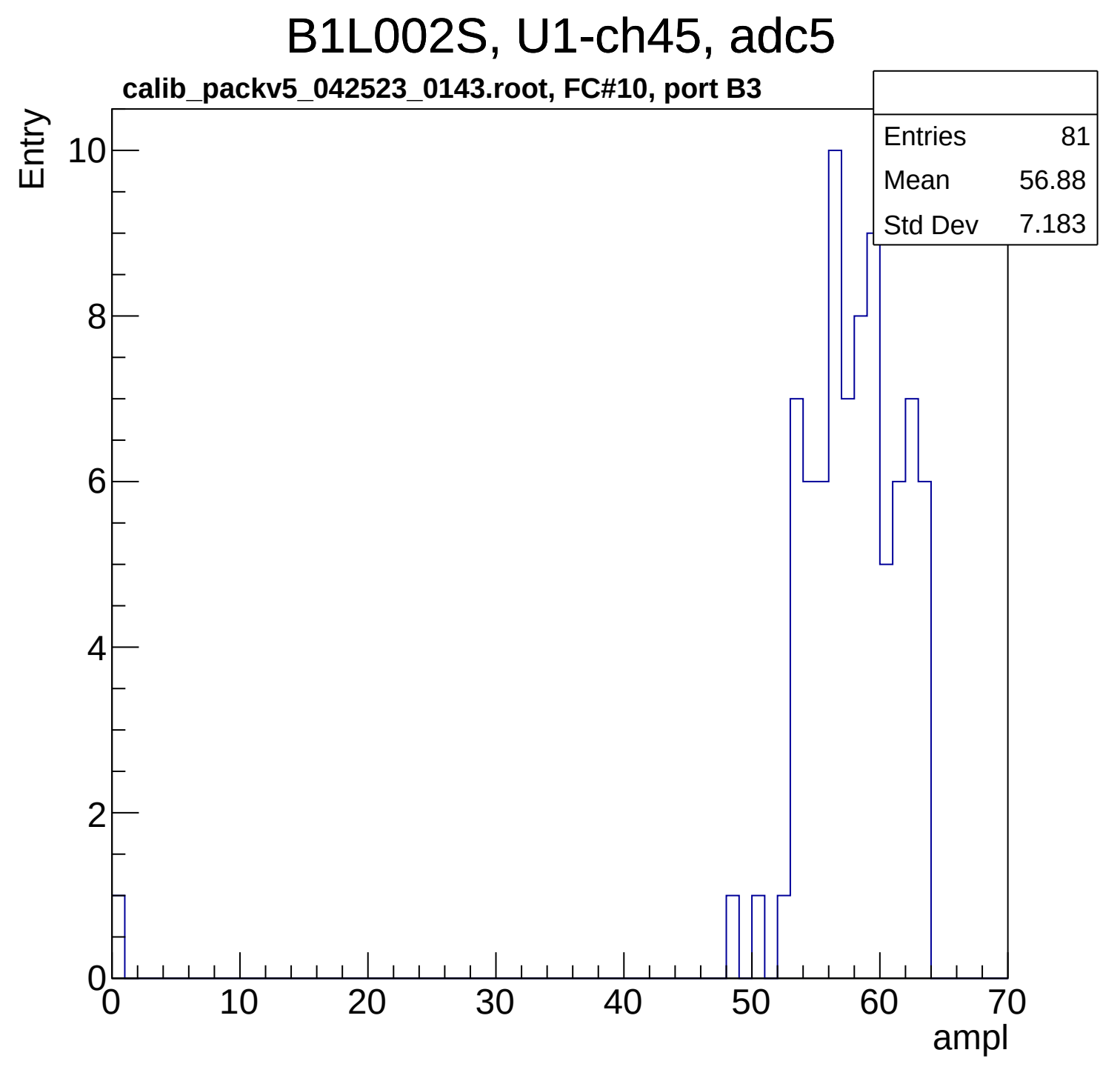
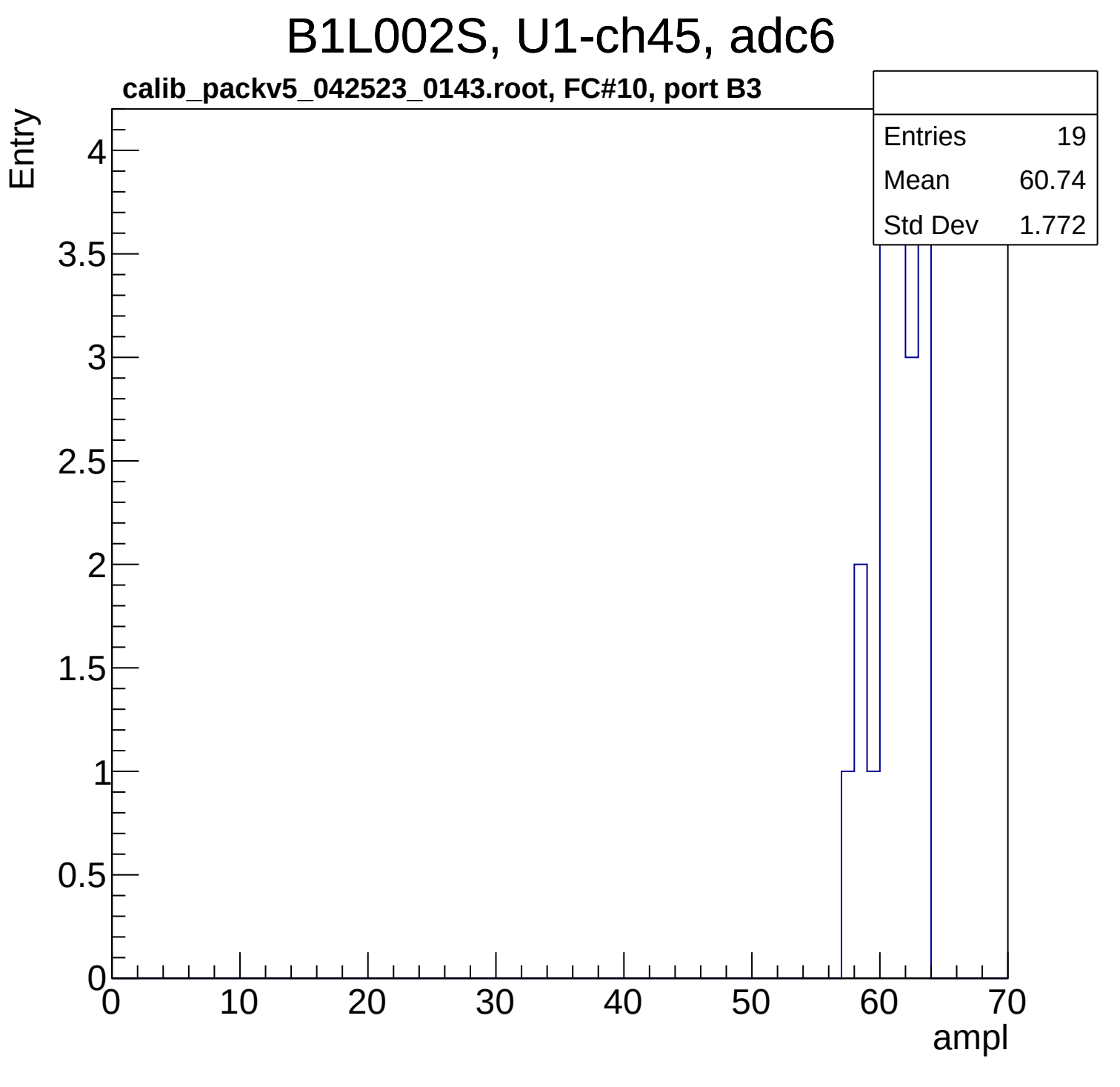

B1L002S, U1-ch45, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 56.88
Std Dev 7.183
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch45, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 60.74
Std Dev 1.772
Entry
ampl
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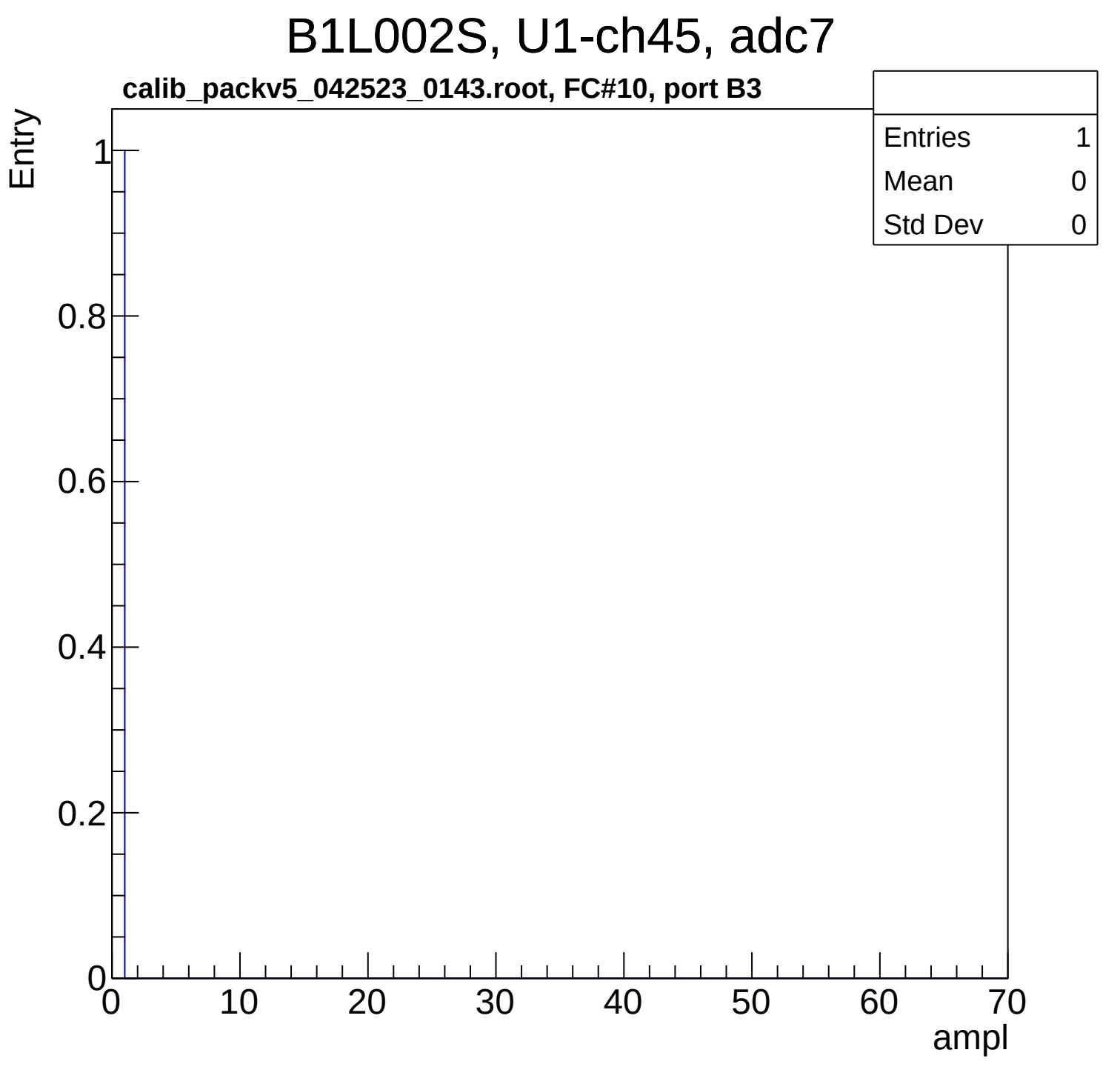
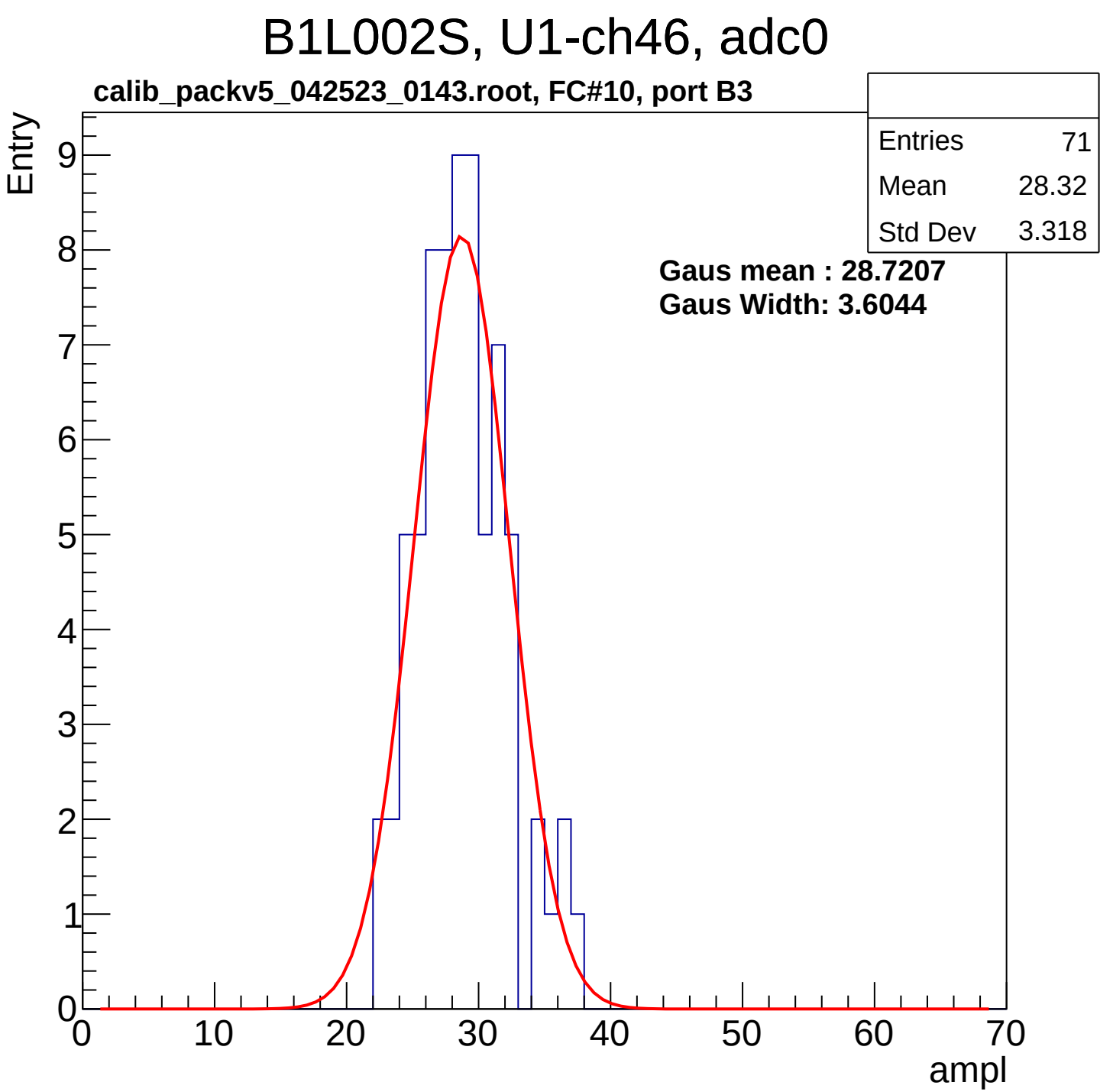
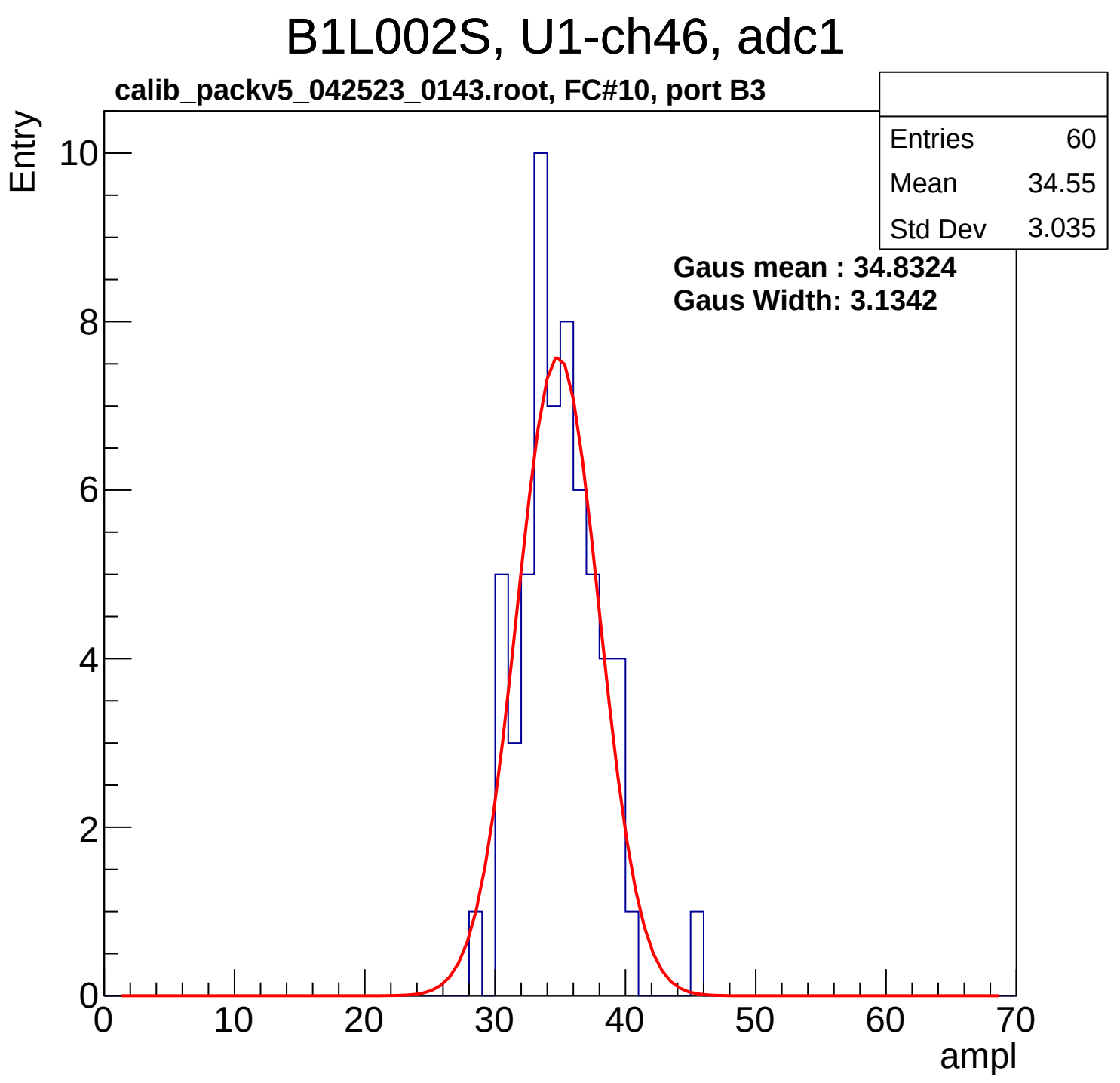
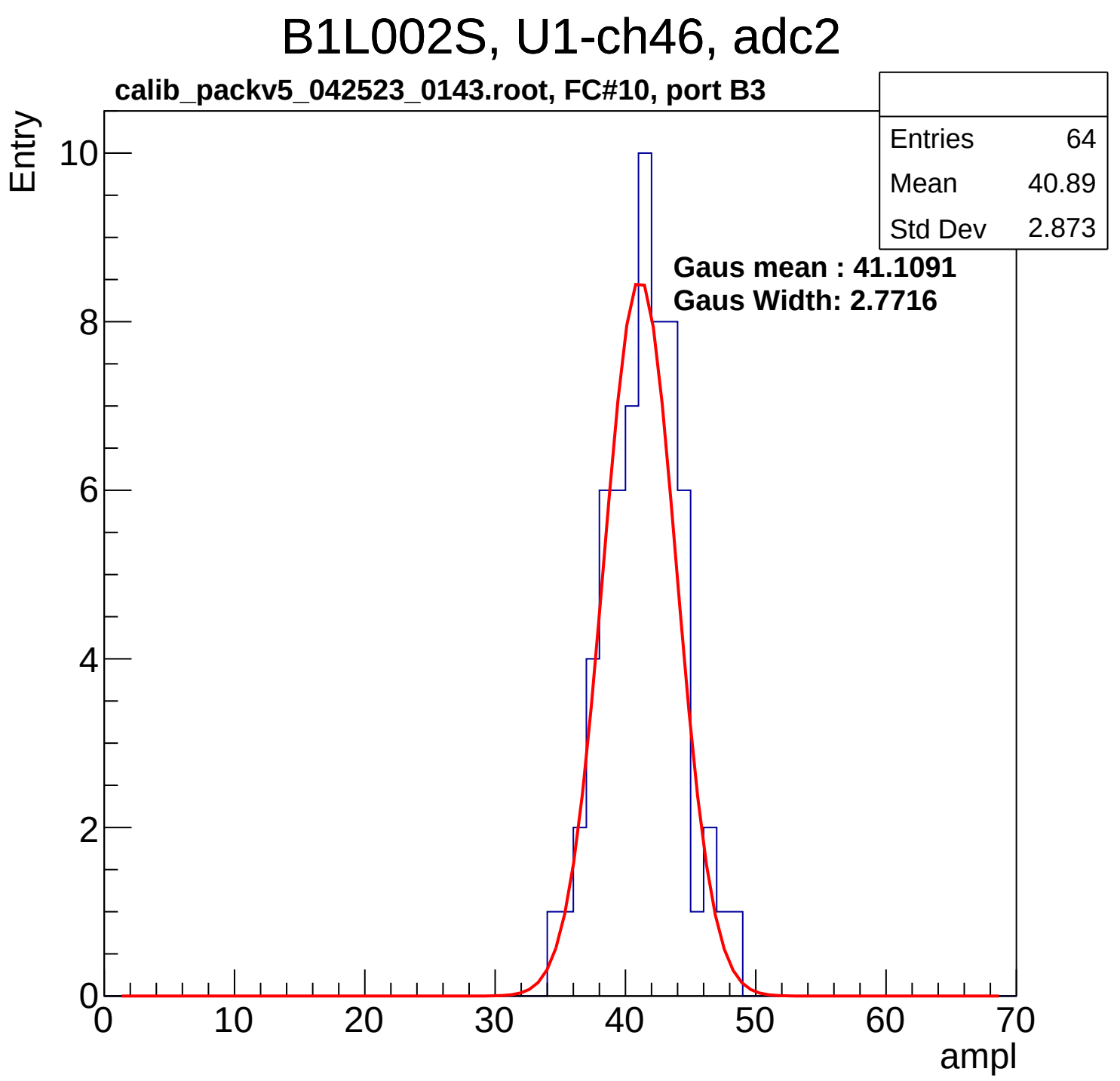
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U1-ch45, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch46, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.32
Std Dev 3.318
Gaus mean : 28.7207
Gaus Width: 3.6044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch46, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 34.55
Std Dev 3.035
Gaus mean : 34.8324
Gaus Width: 3.1342
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch46, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.89
Std Dev 2.873
Gaus mean : 41.1091
Gaus Width: 2.7716
Entry
ampl
0
2
4
6
8
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0
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20
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60
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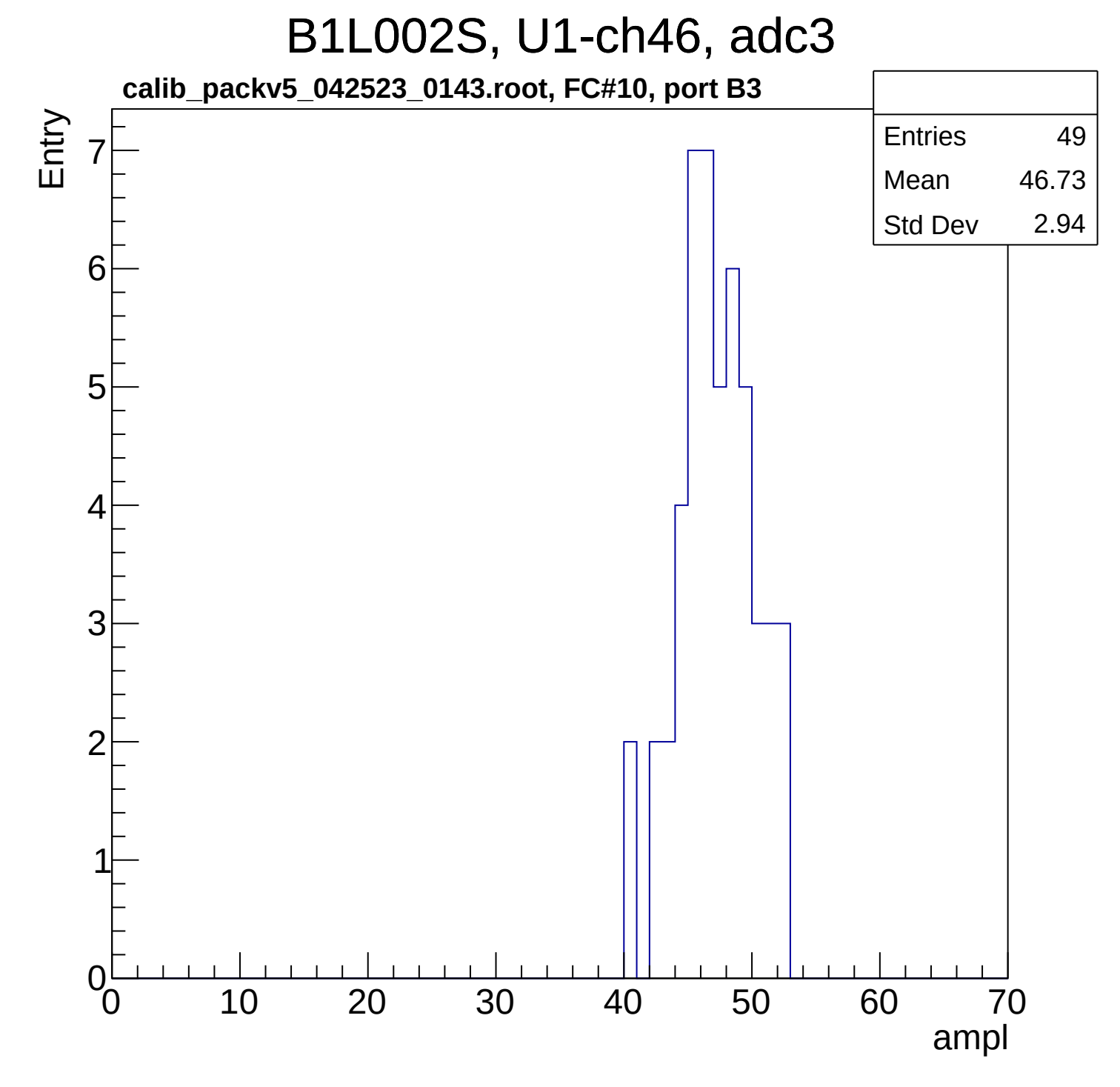
B1L002S, U1-ch46, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 46.73
Std Dev 2.94
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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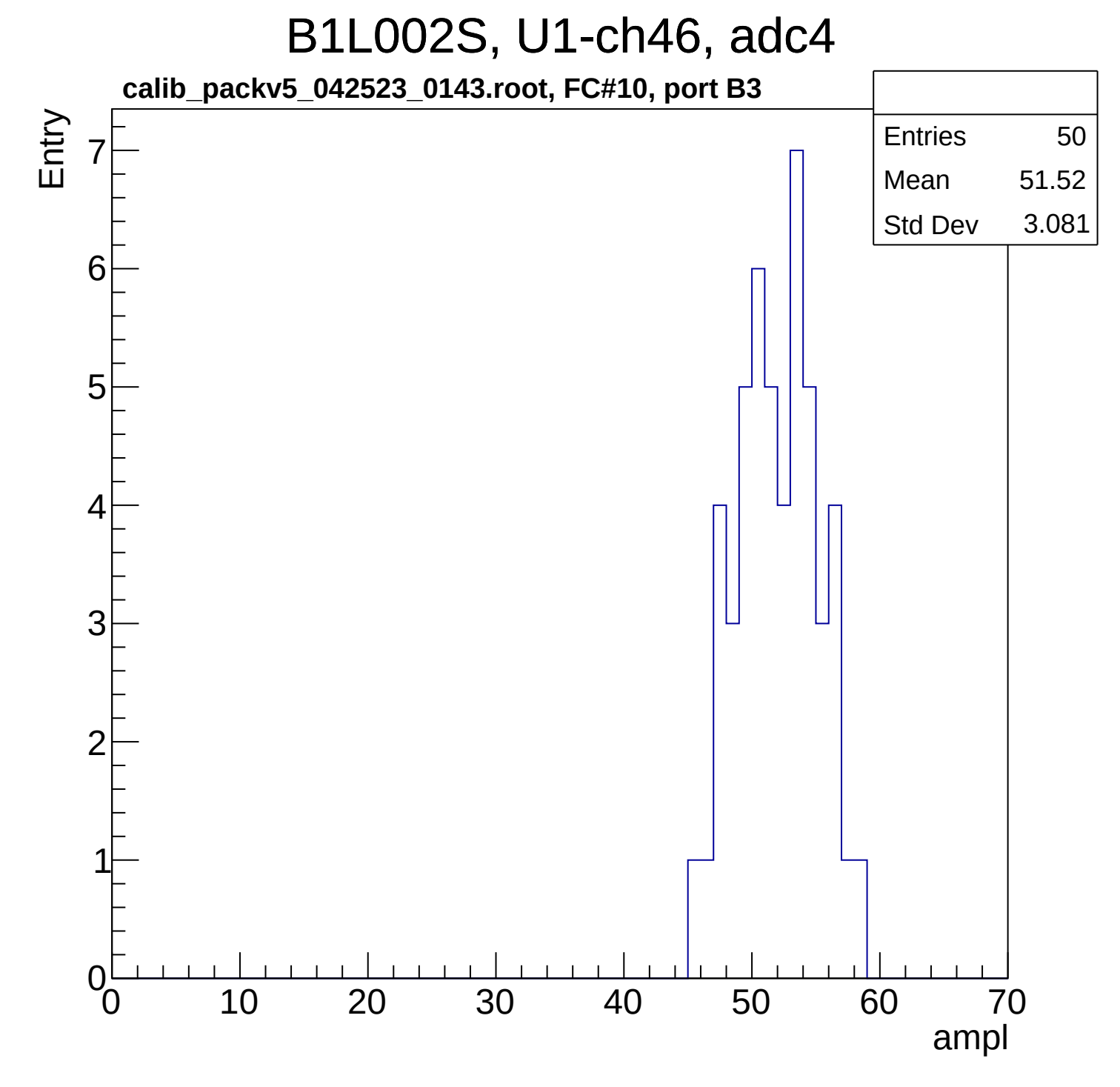
B1L002S, U1-ch46, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.52
Std Dev 3.081
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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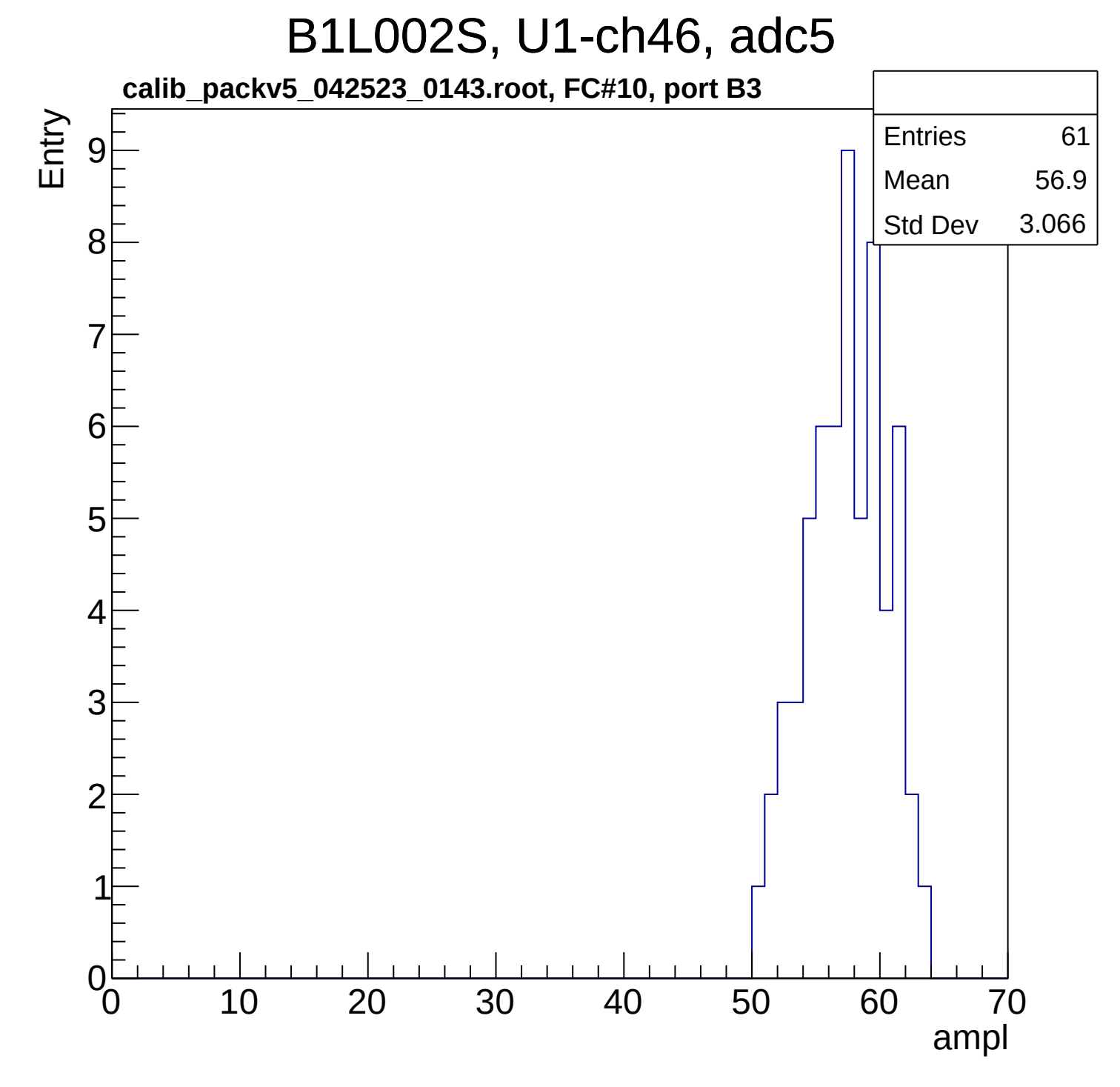
B1L002S, U1-ch46, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 56.9
Std Dev 3.066
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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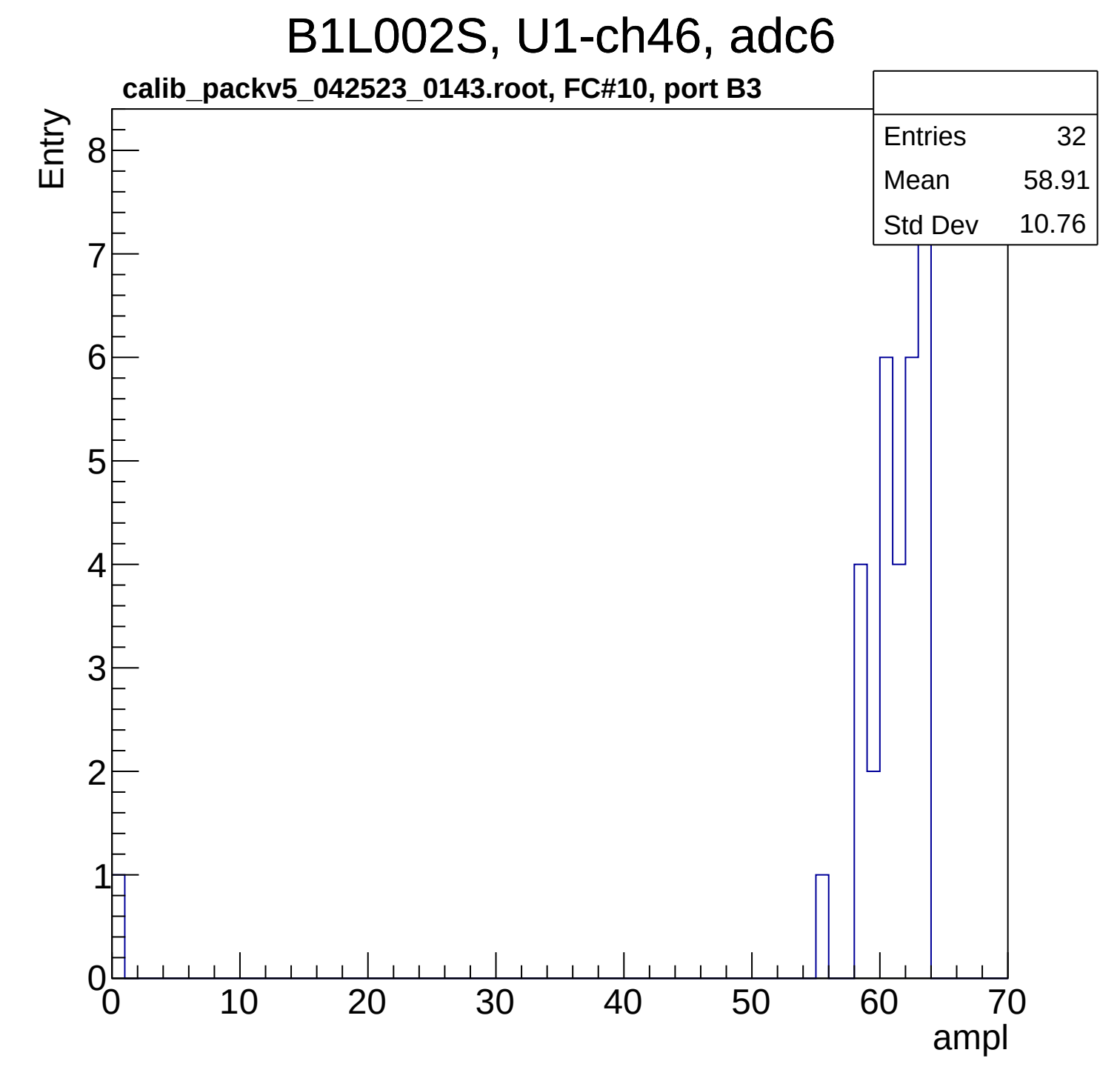
B1L002S, U1-ch46, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 58.91
Std Dev 10.76
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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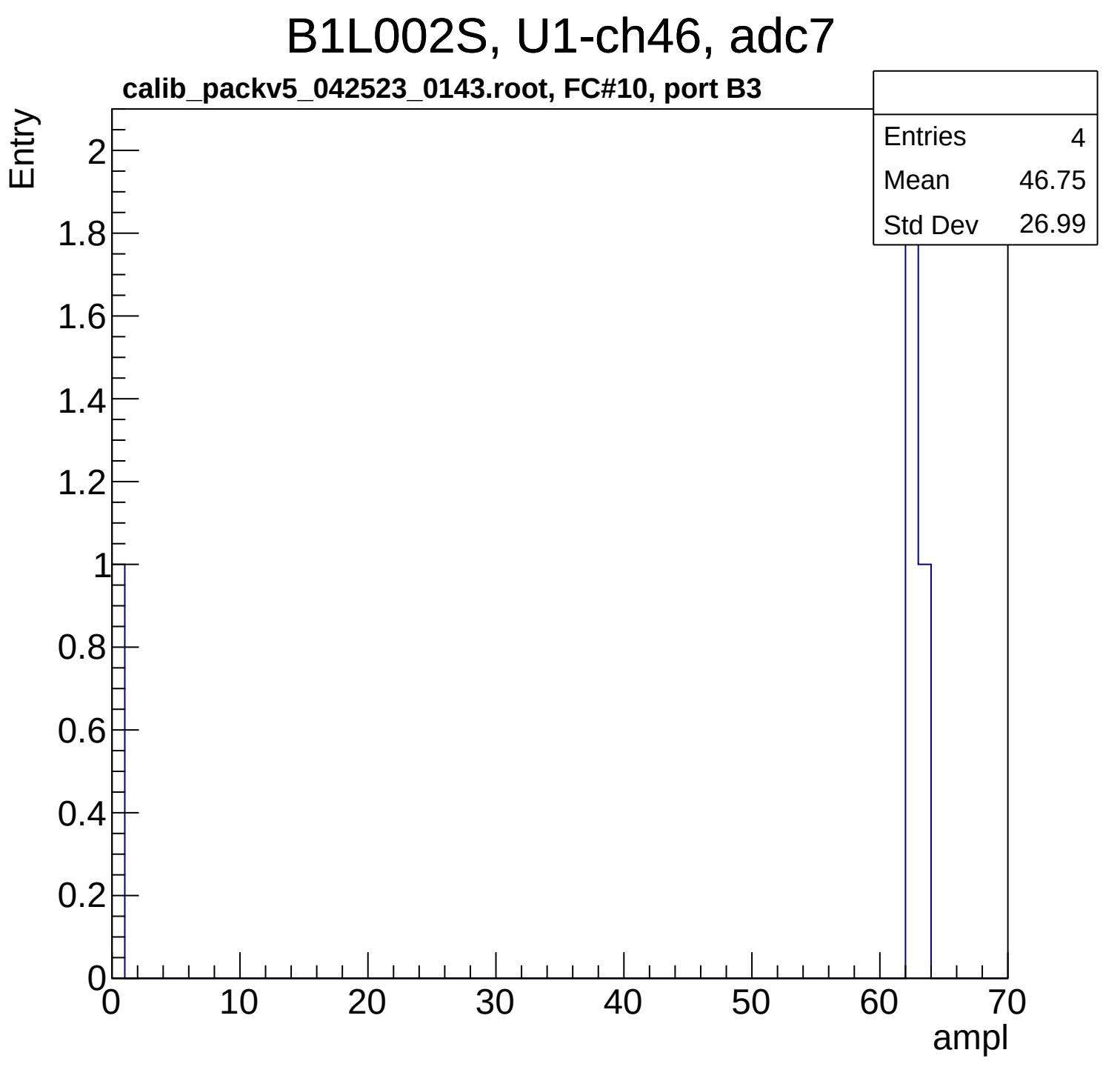
B1L002S, U1-ch46, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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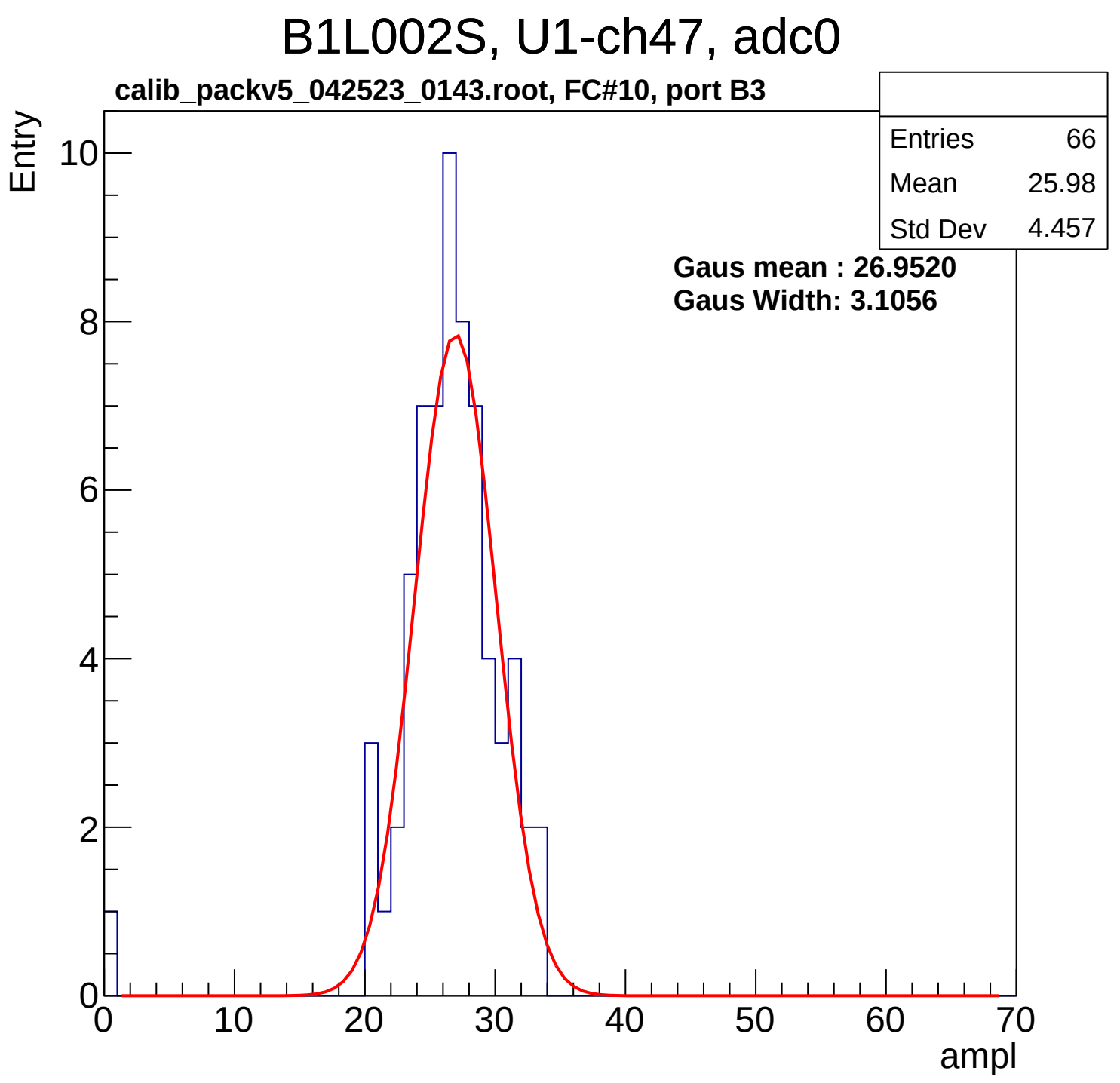
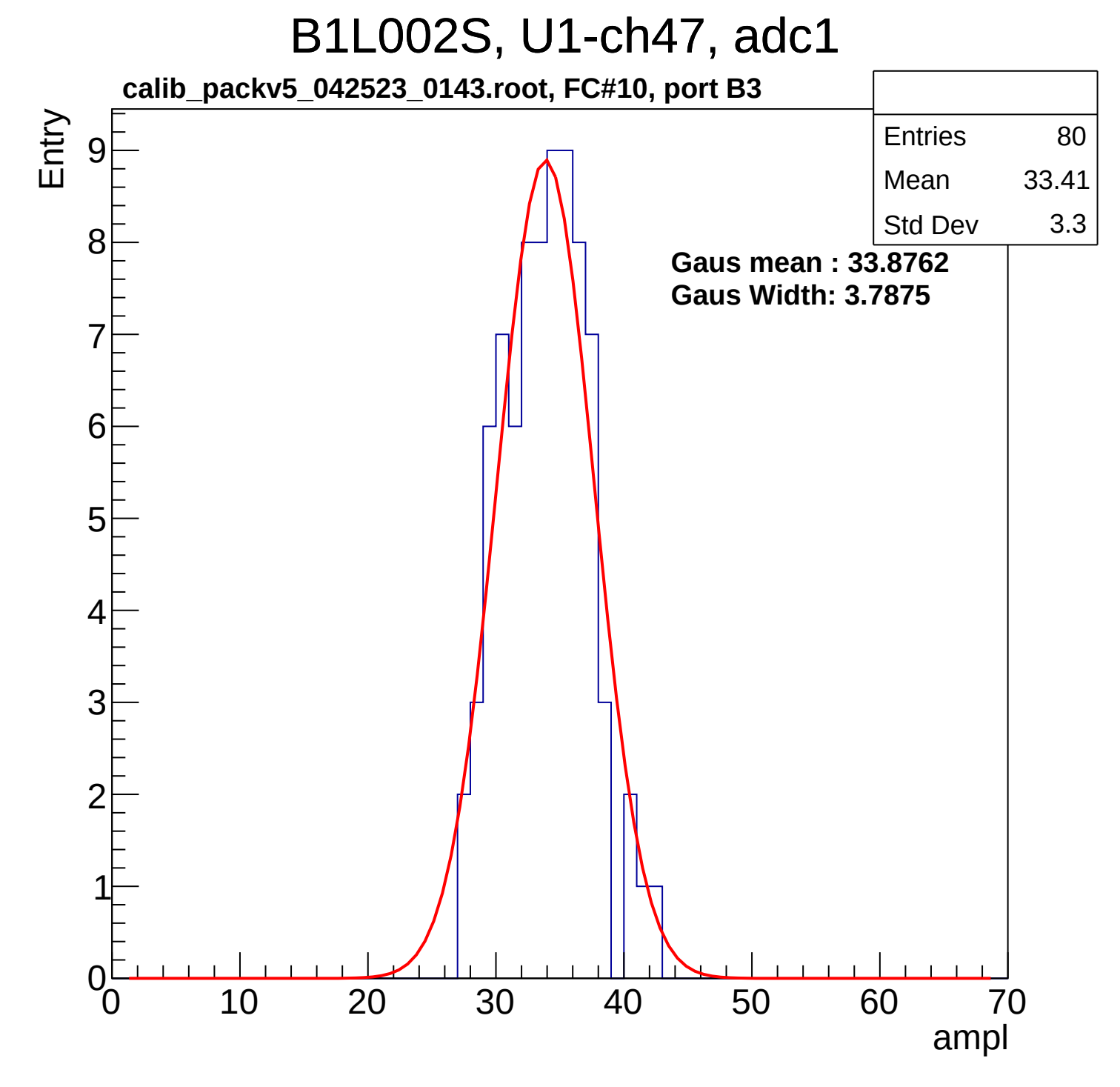
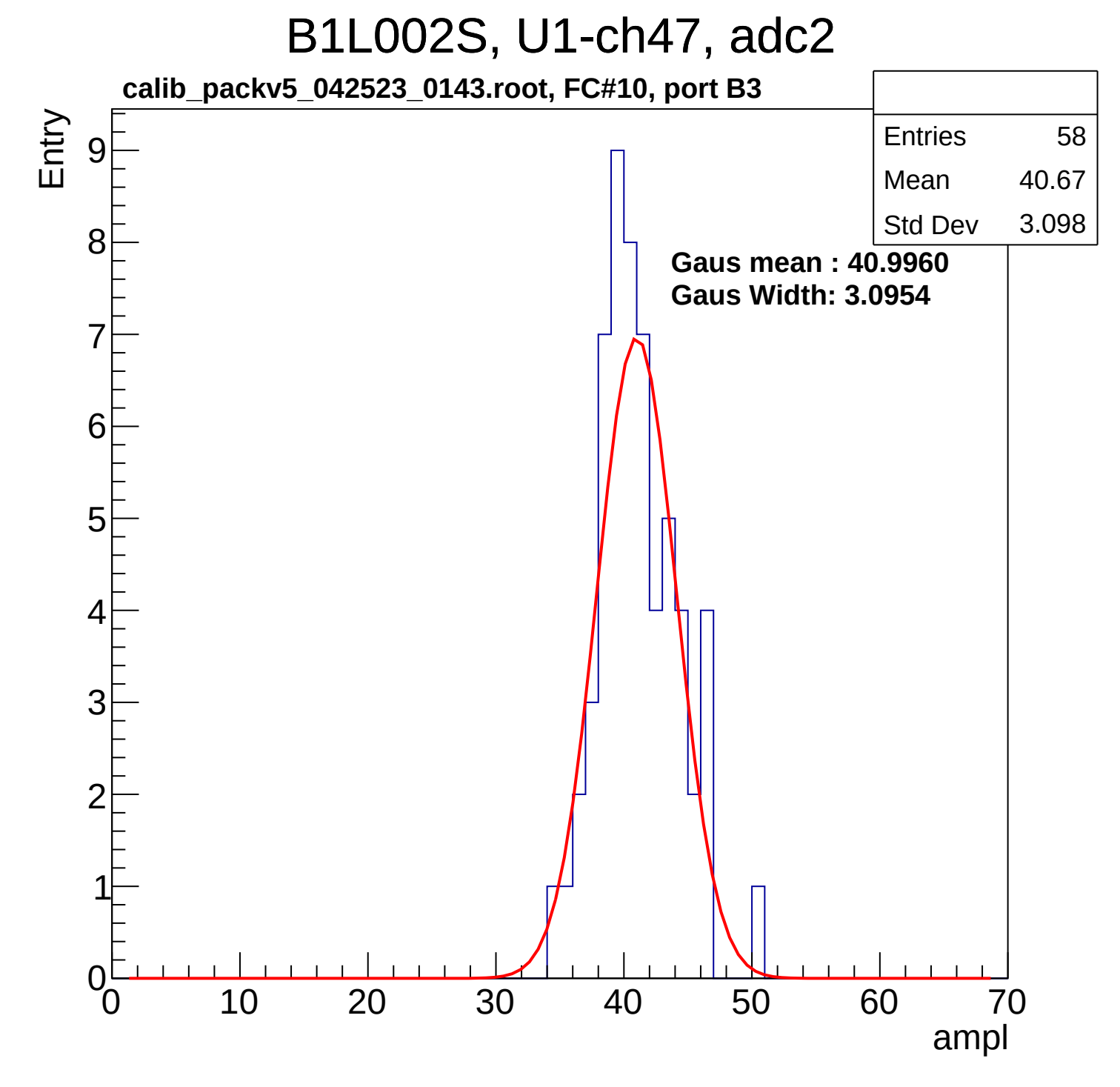

B1L002S, U1-ch47, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 25.98
Std Dev 4.457
Gaus mean : 26.9520
Gaus Width: 3.1056
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch47, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 33.41
Std Dev 3.3
Gaus mean : 33.8762
Gaus Width: 3.7875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch47, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.67
Std Dev 3.098
Gaus mean : 40.9960
Gaus Width: 3.0954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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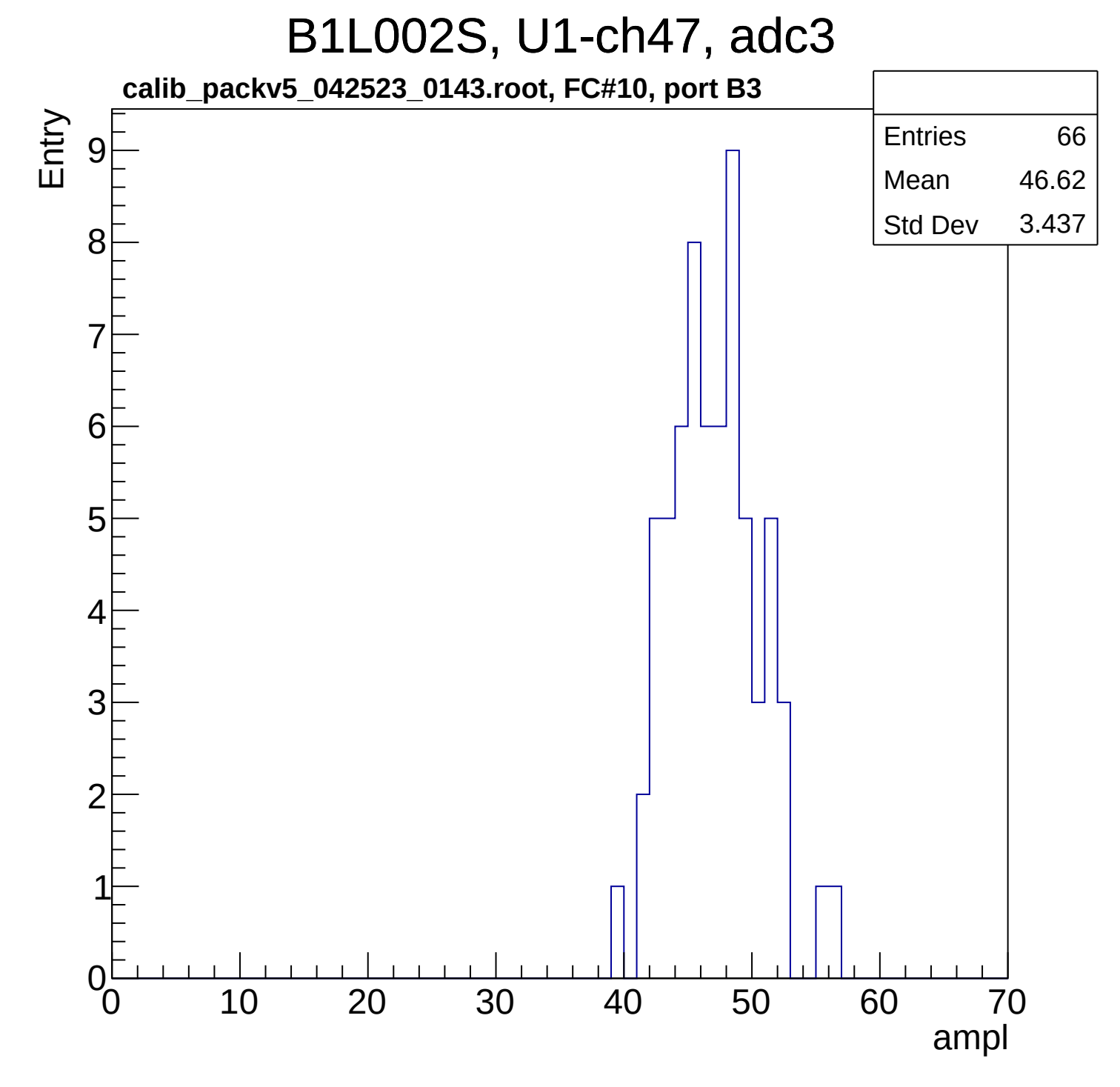
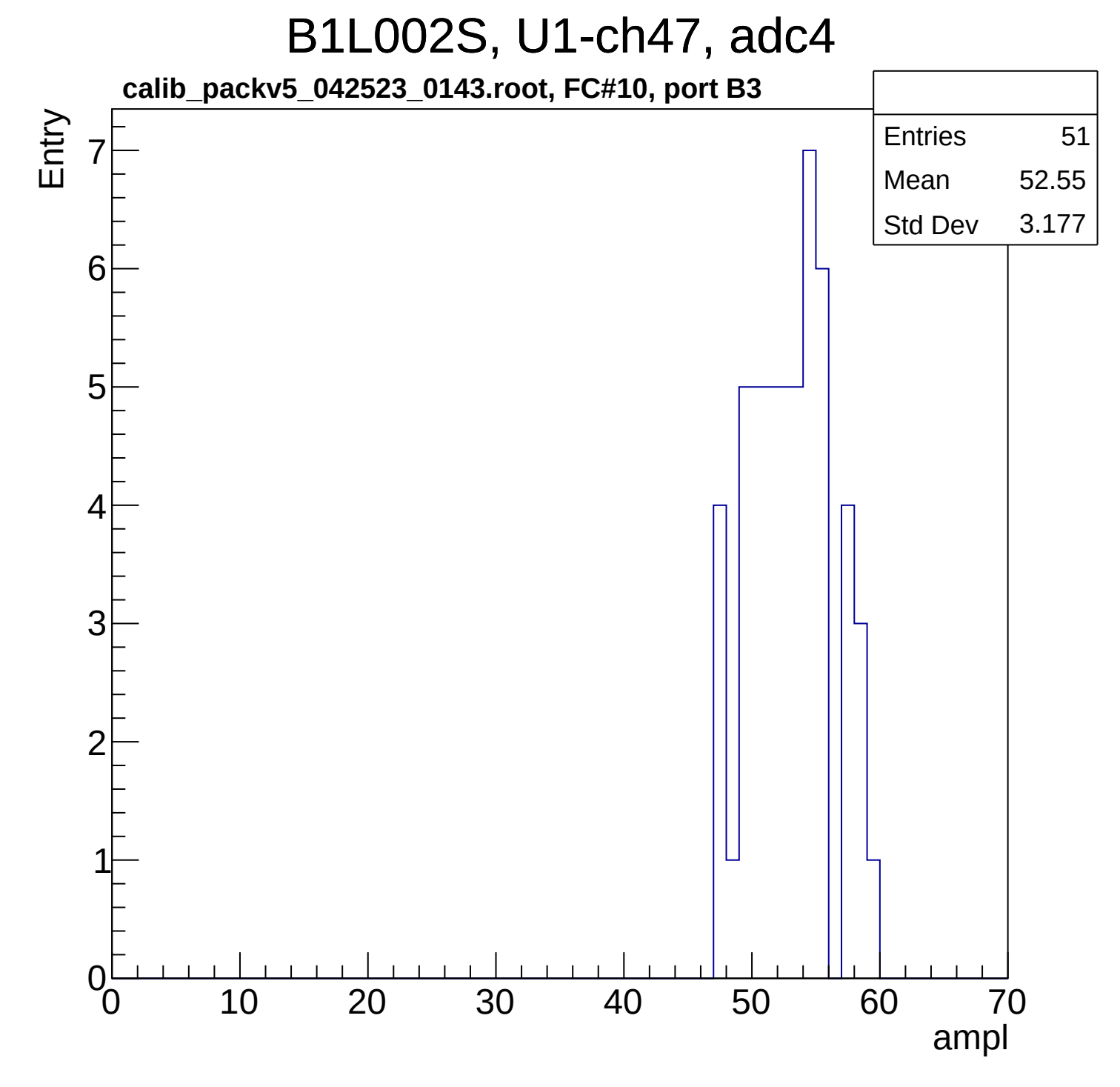
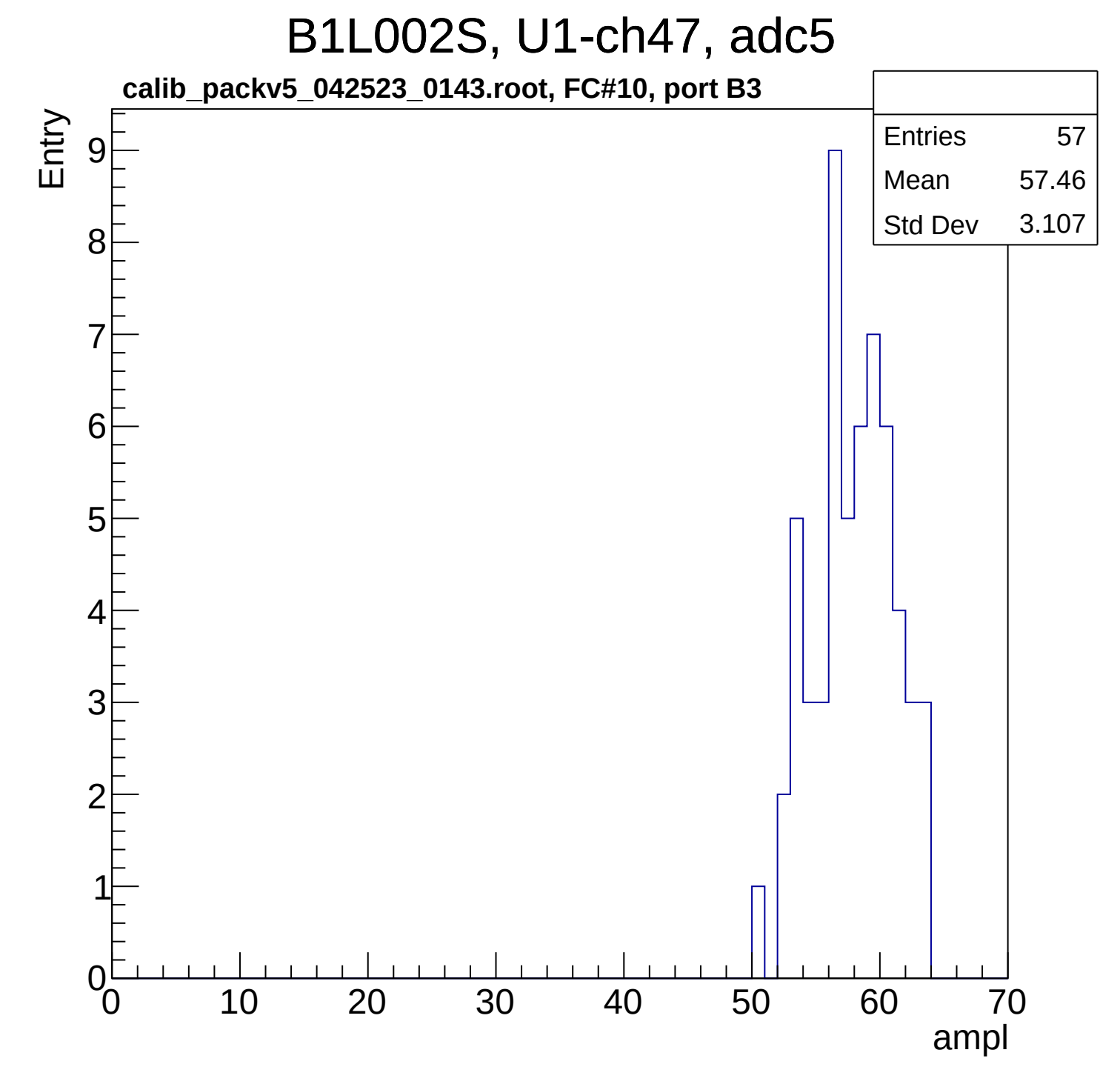

B1L002S, U1-ch47, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 46.62
Std Dev 3.437
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch47, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 52.55
Std Dev 3.177
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch47, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.46
Std Dev 3.107
Entry
ampl
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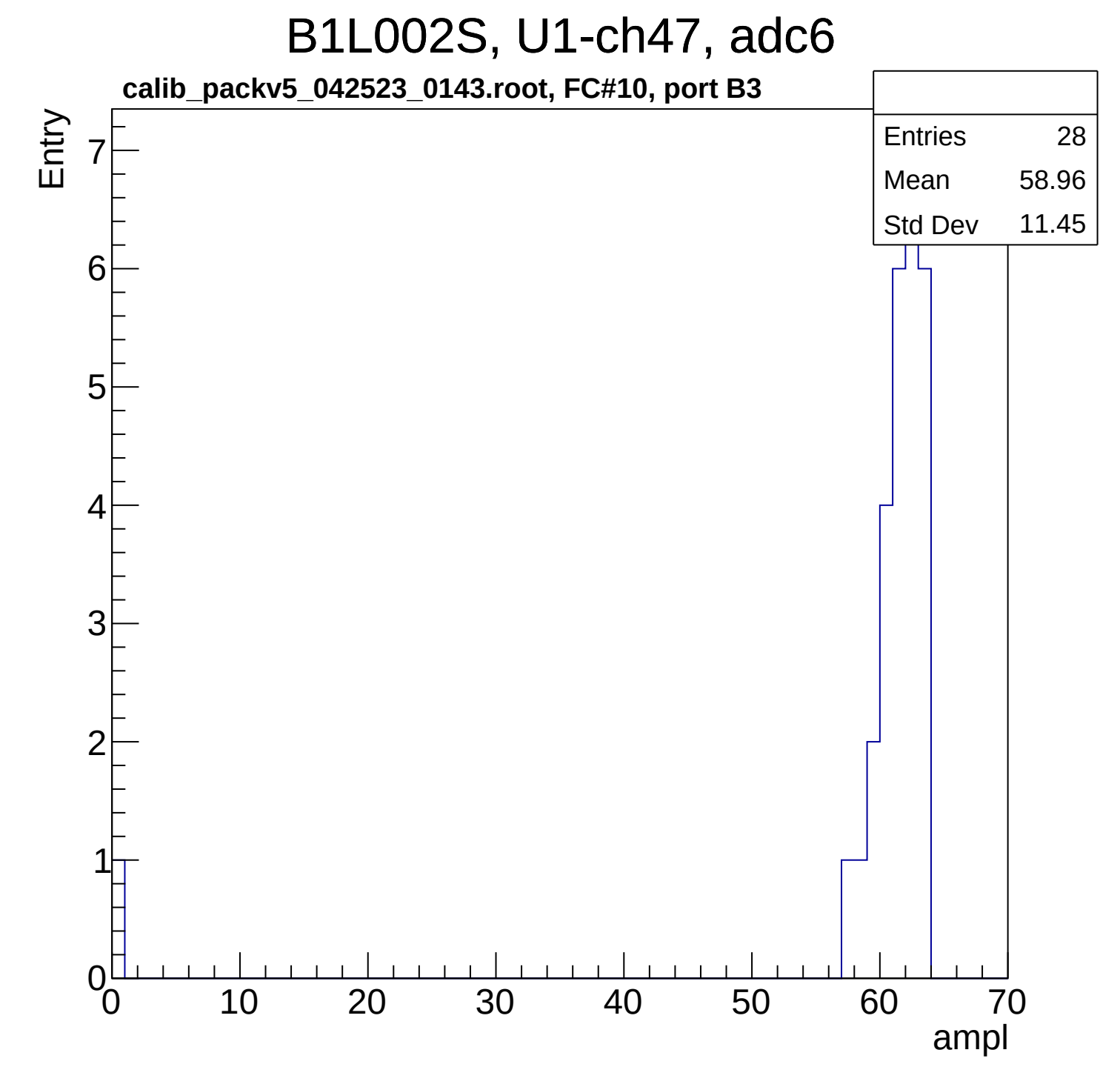
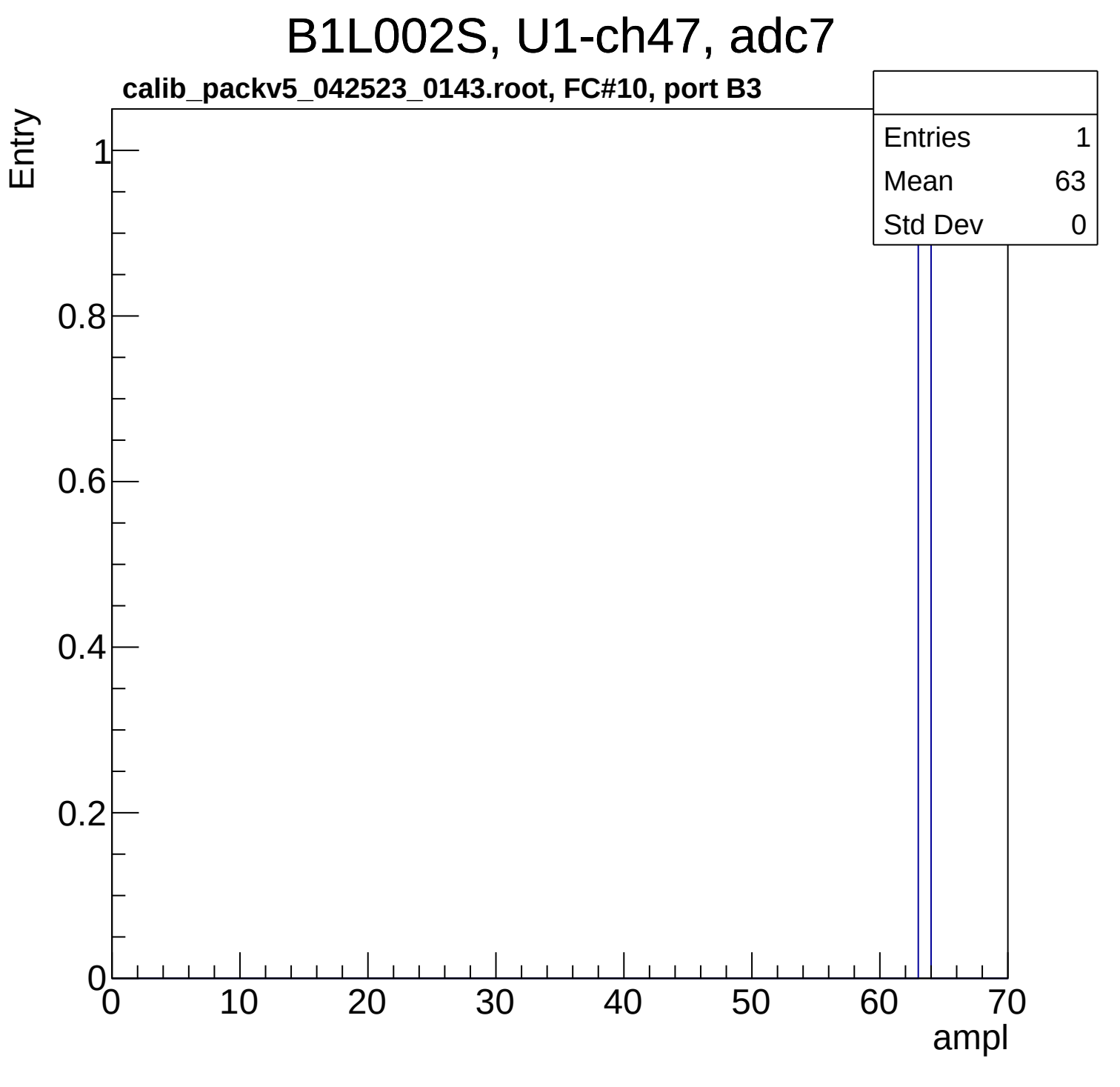
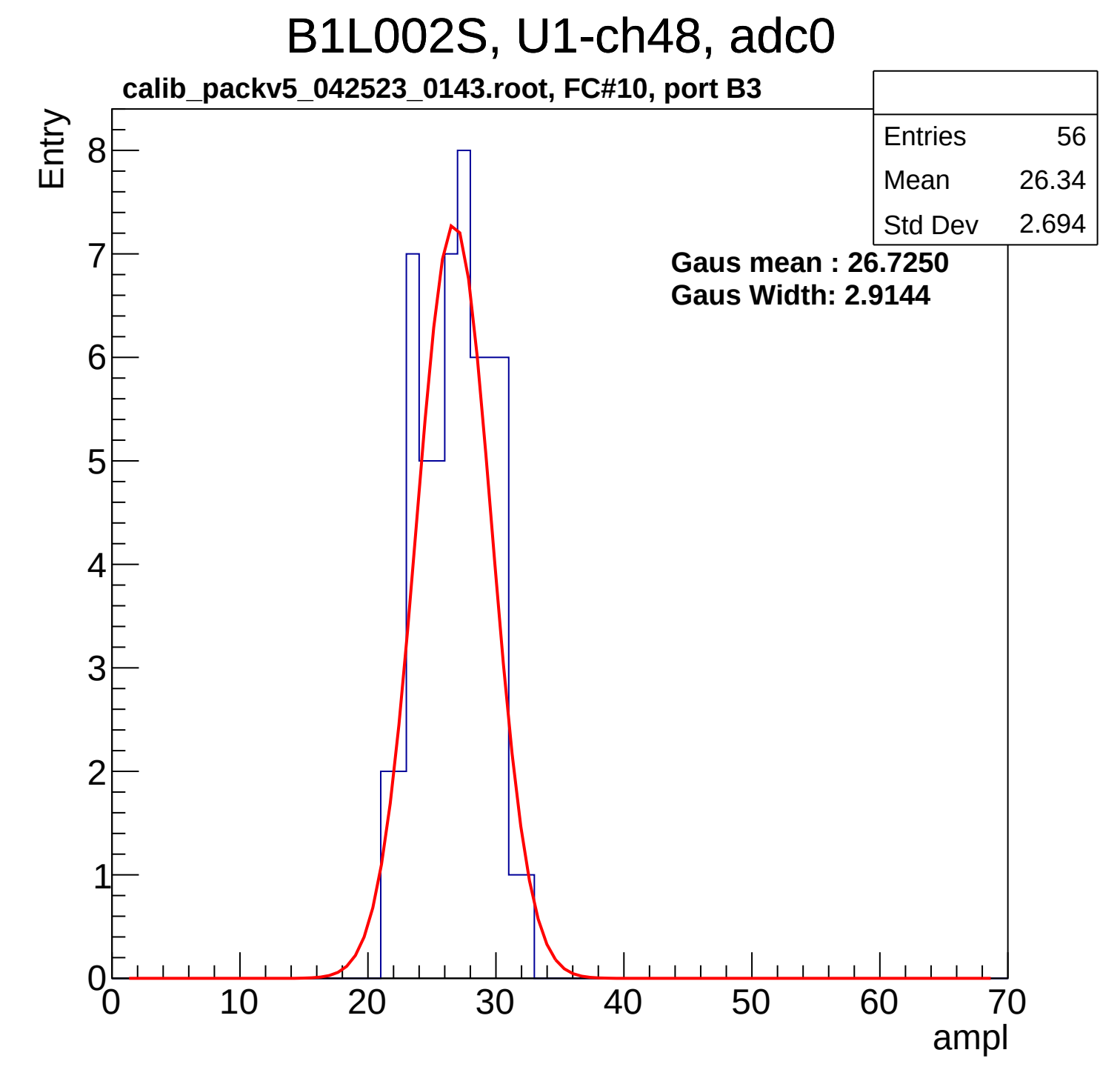
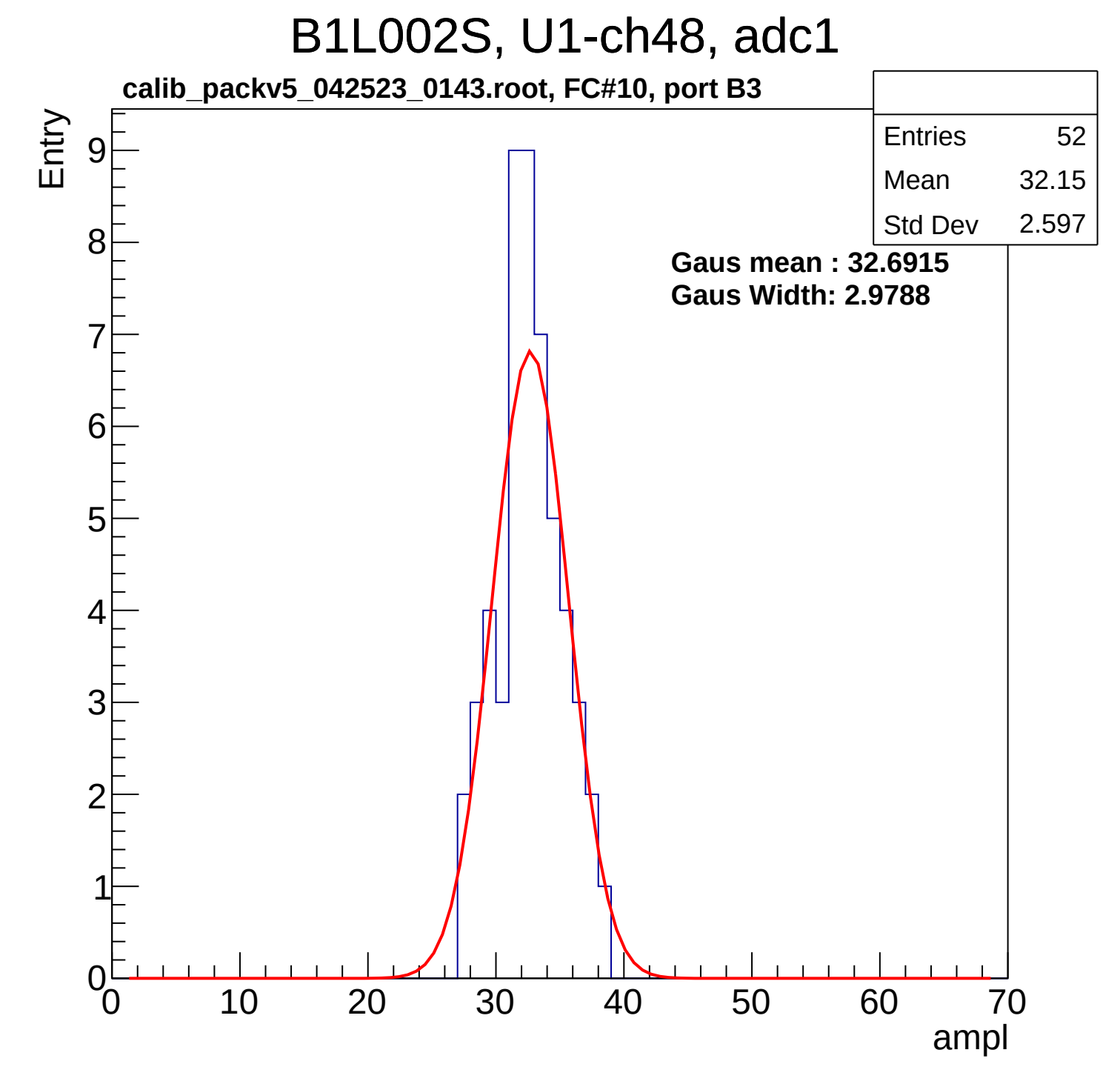
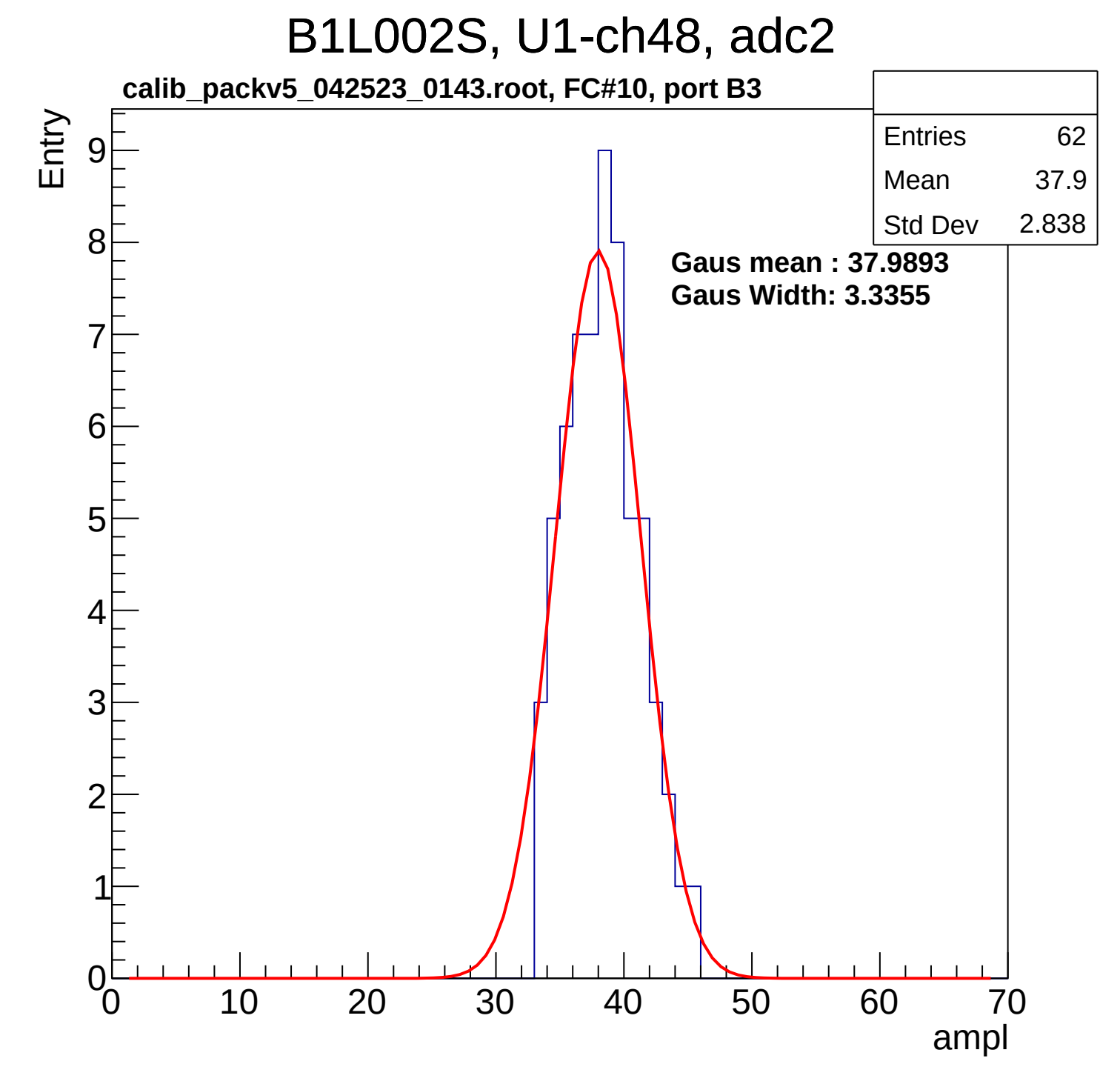
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch47, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 58.96
Std Dev 11.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch47, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch48, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 26.34
Std Dev 2.694
Gaus mean : 26.7250
Gaus Width: 2.9144
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch48, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 32.15
Std Dev 2.597
Gaus mean : 32.6915
Gaus Width: 2.9788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch48, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 37.9
Std Dev 2.838
Gaus mean : 37.9893
Gaus Width: 3.3355
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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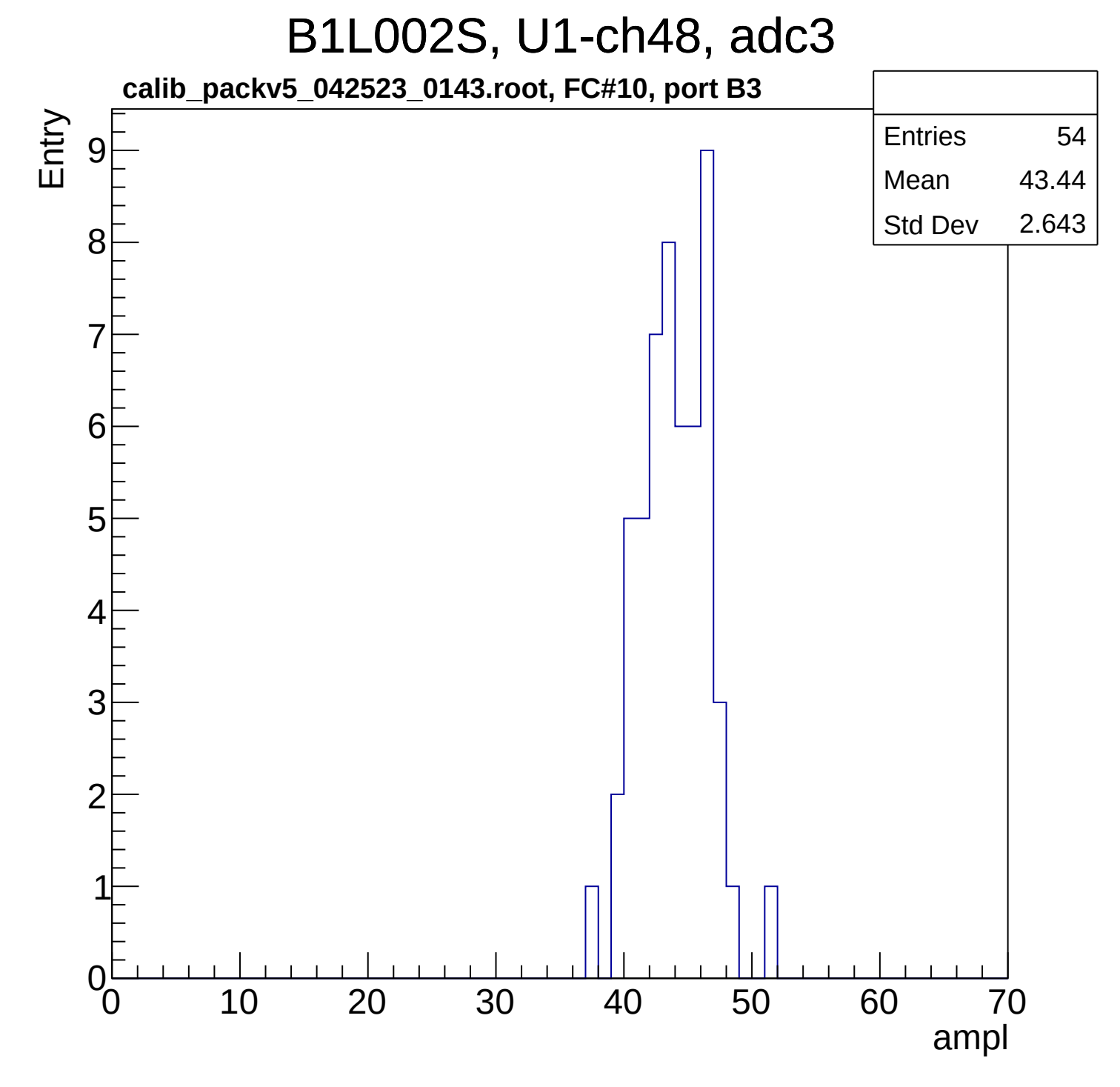
B1L002S, U1-ch48, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 43.44
Std Dev 2.643
Entry
ampl
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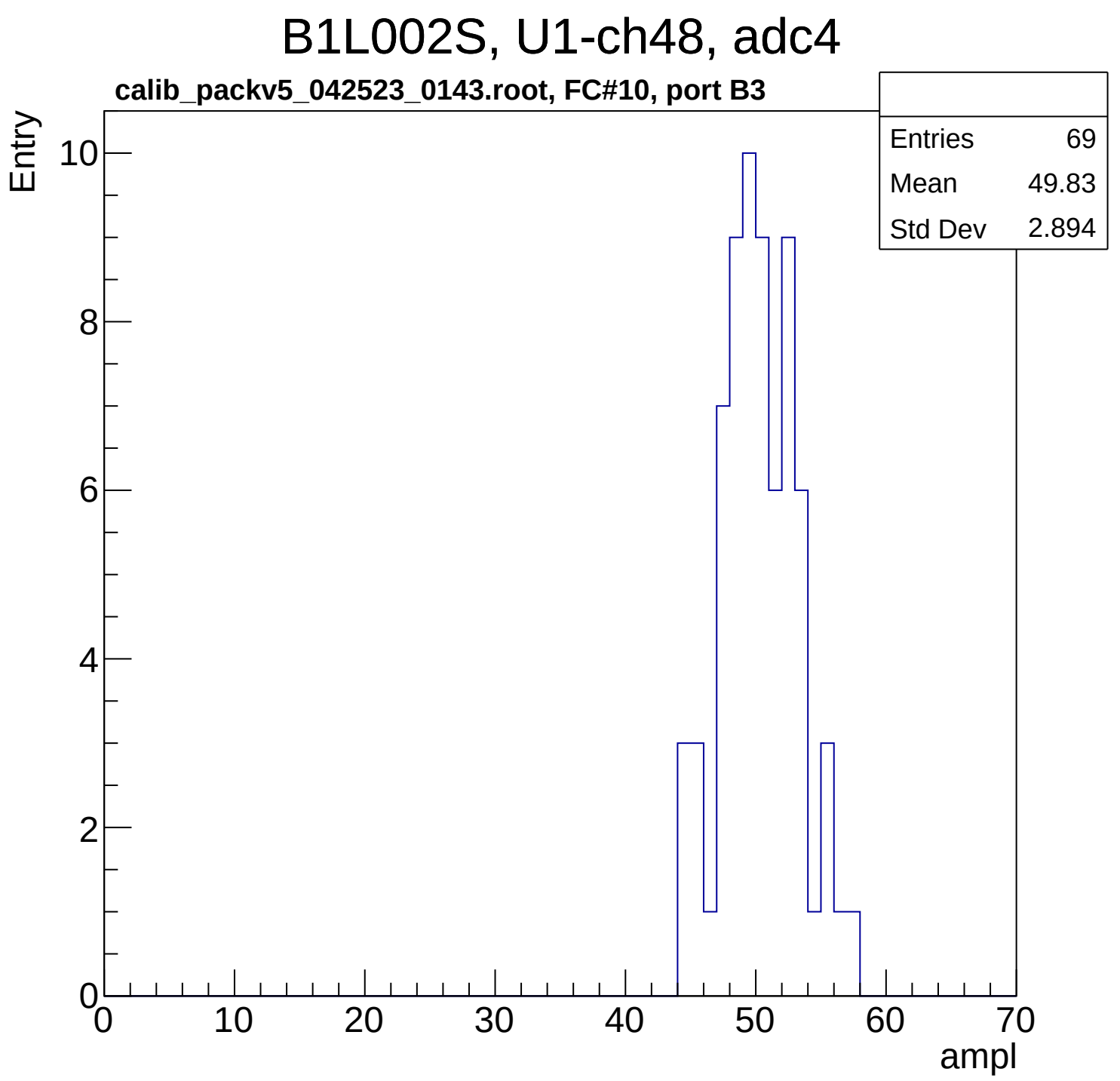
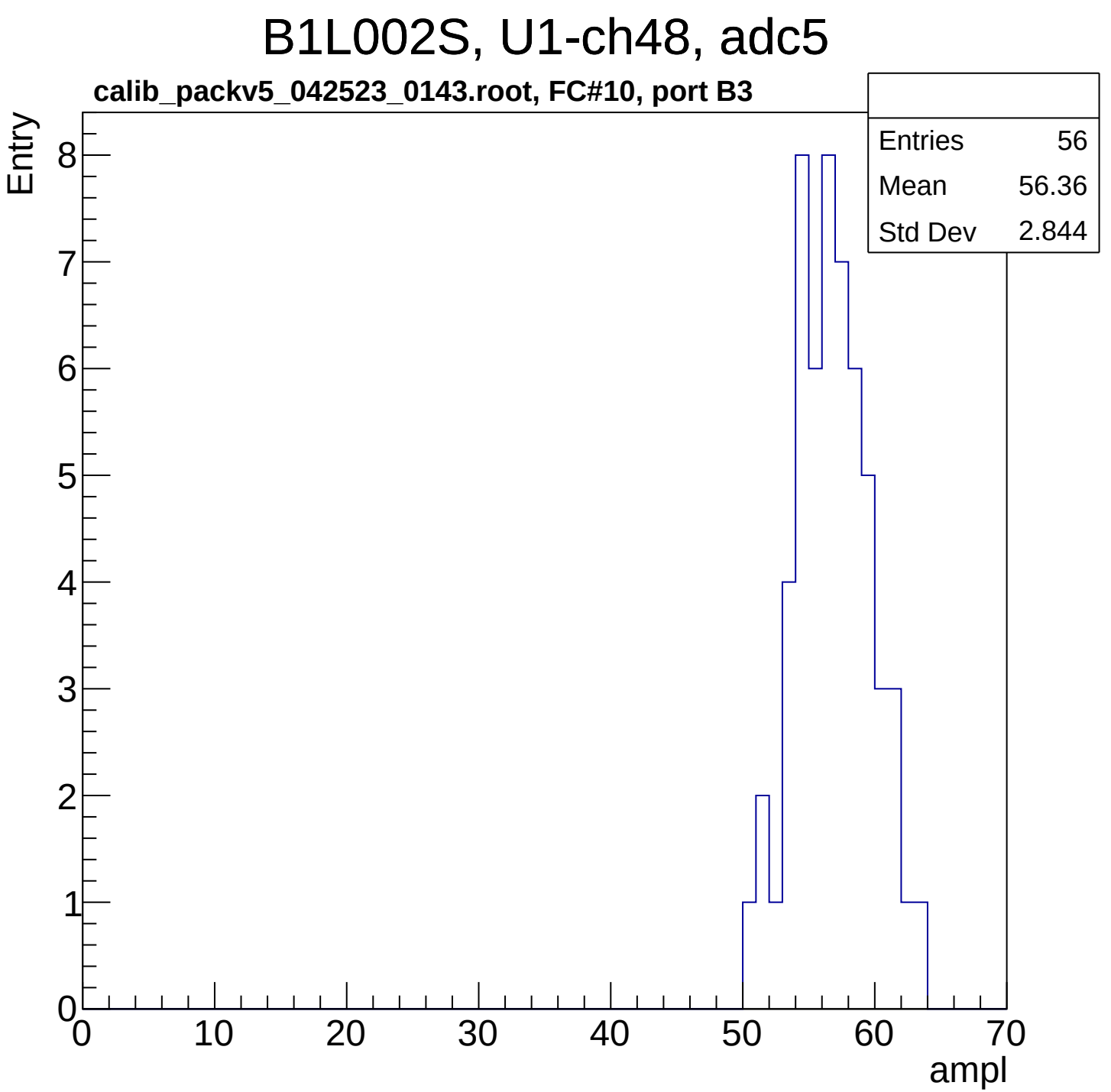
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch48, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 49.83
Std Dev 2.894
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch48, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 56.36
Std Dev 2.844
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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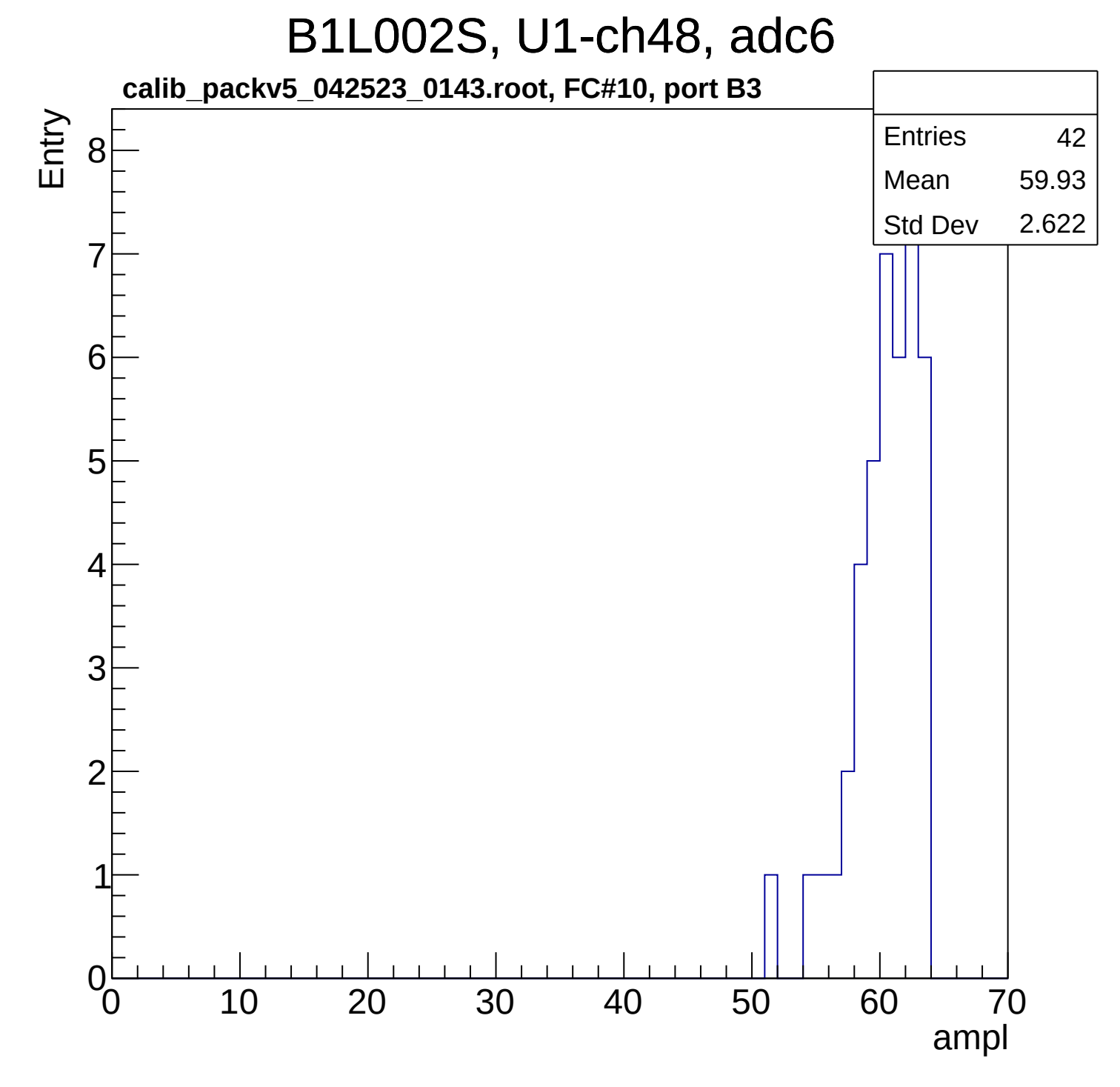
60
70


B1L002S, U1-ch48, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.93
Std Dev 2.622
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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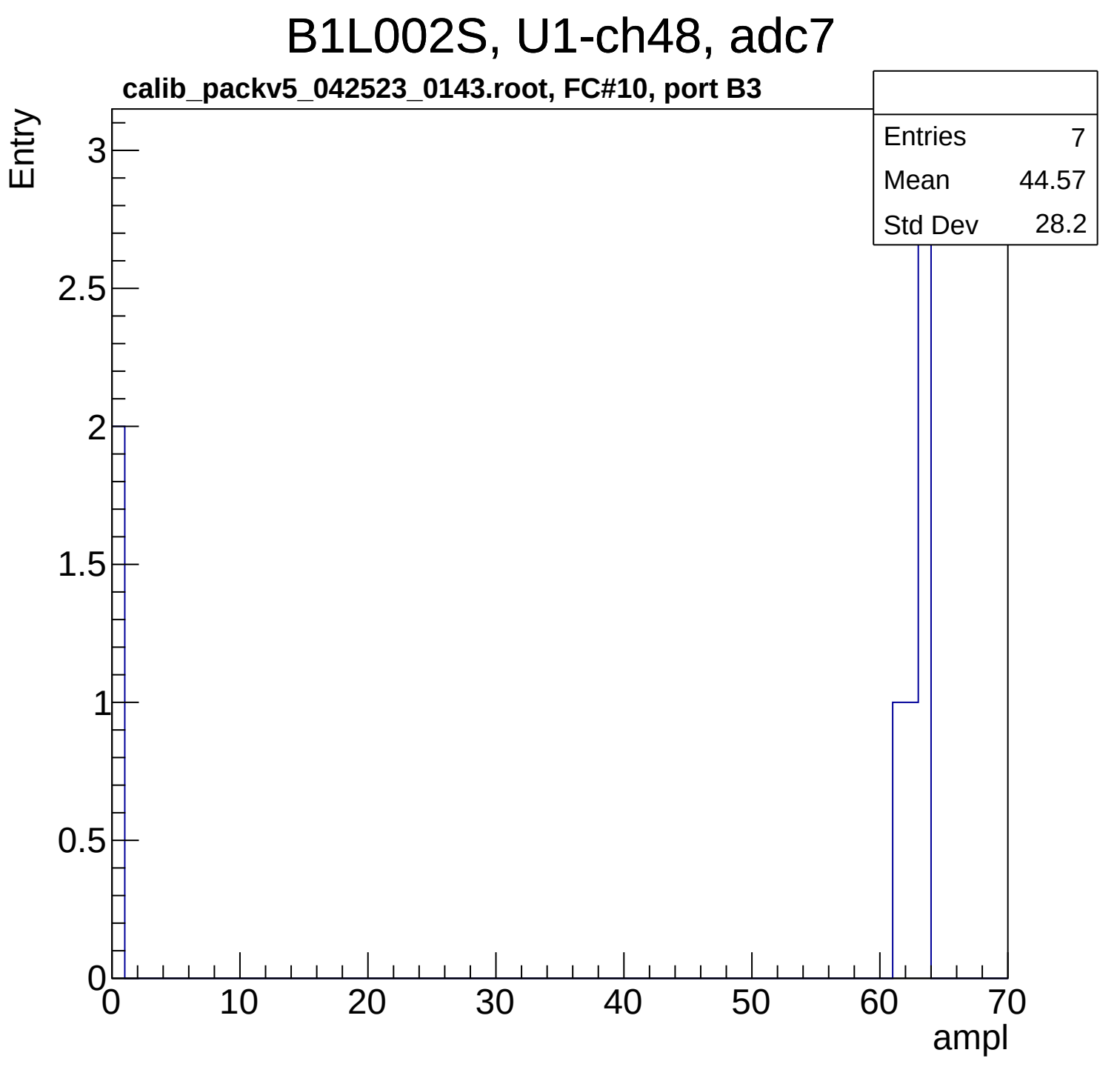
40
50
60
70


B1L002S, U1-ch48, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 44.57
Std Dev 28.2
Entry
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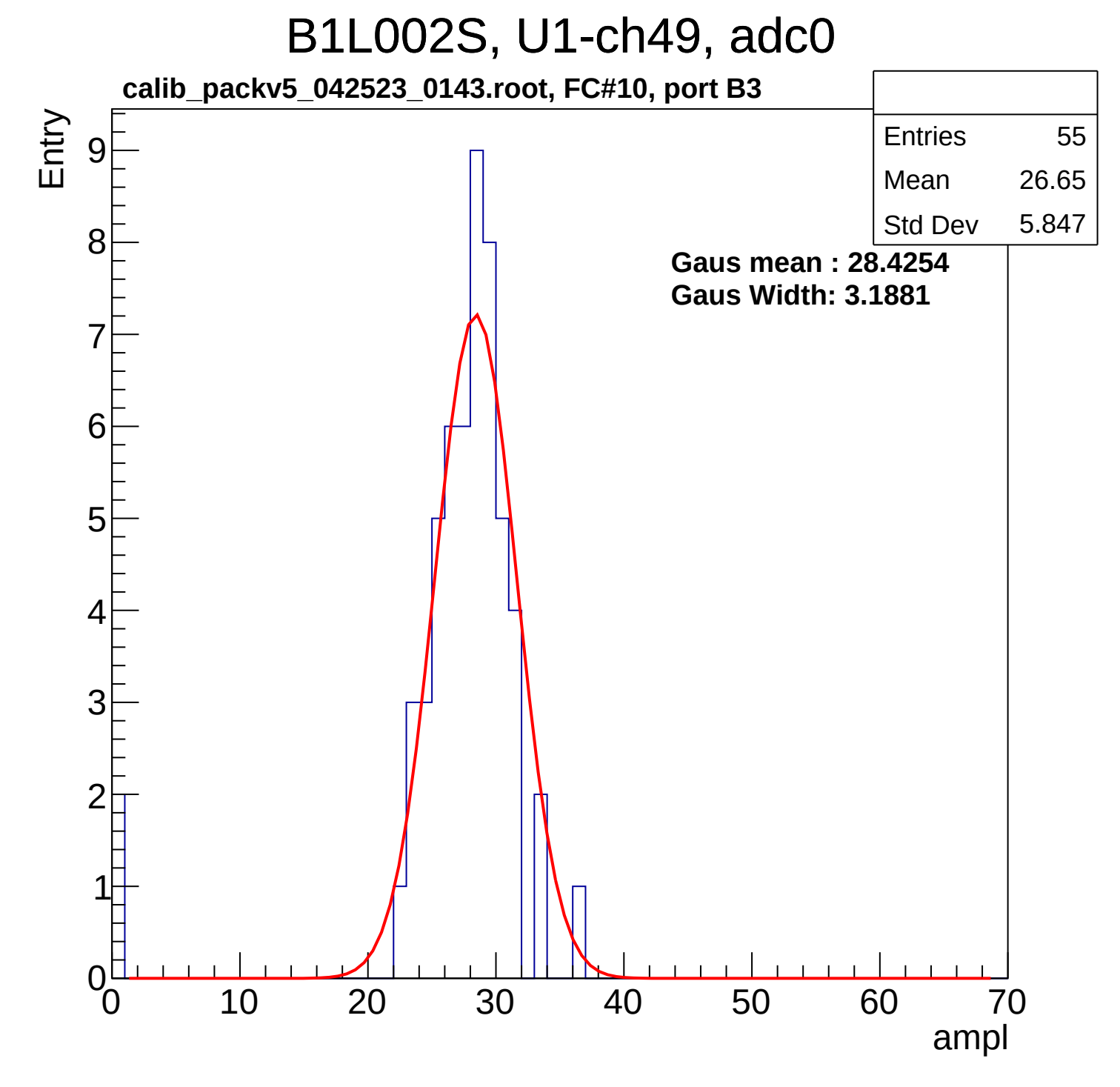
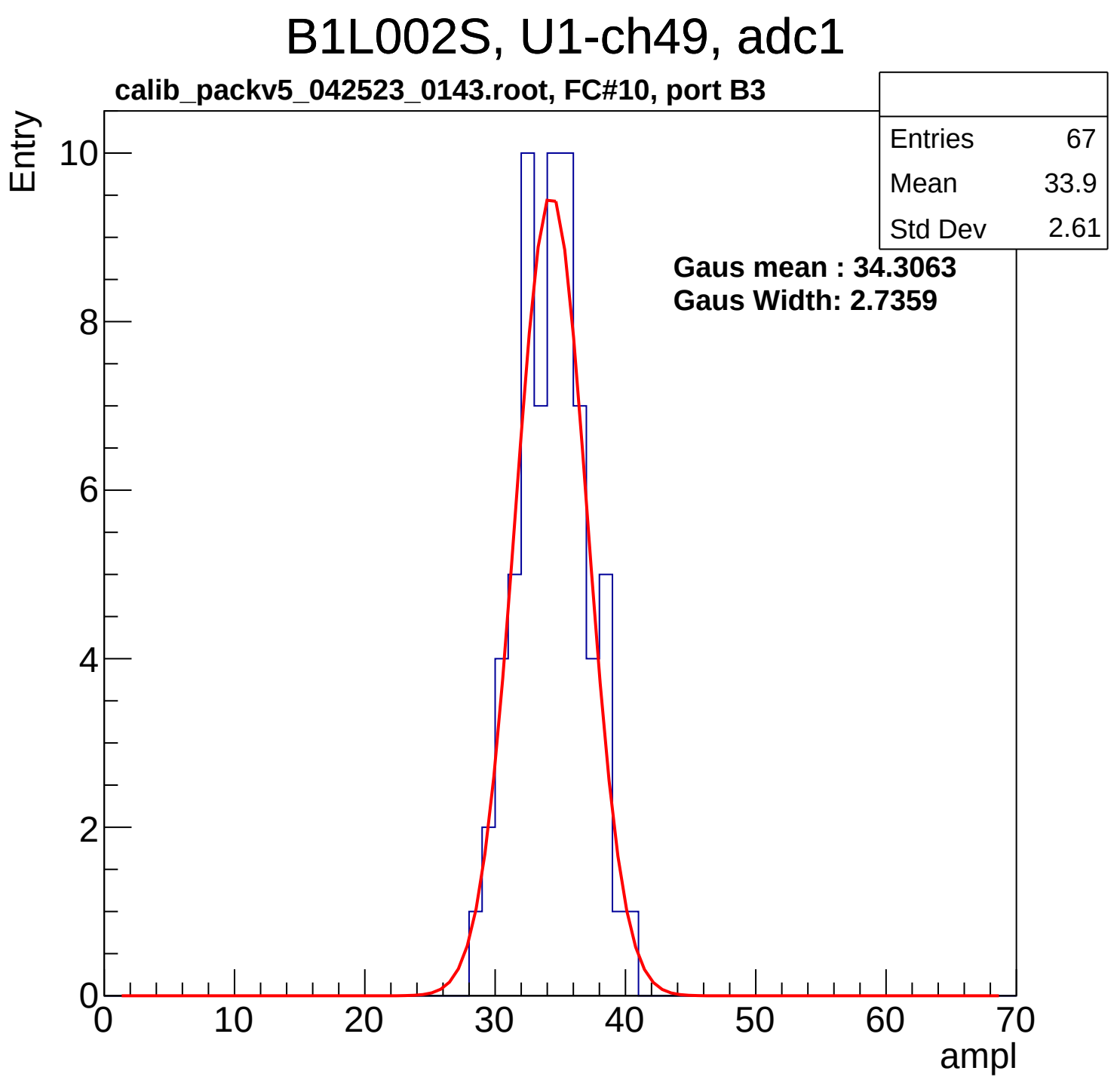
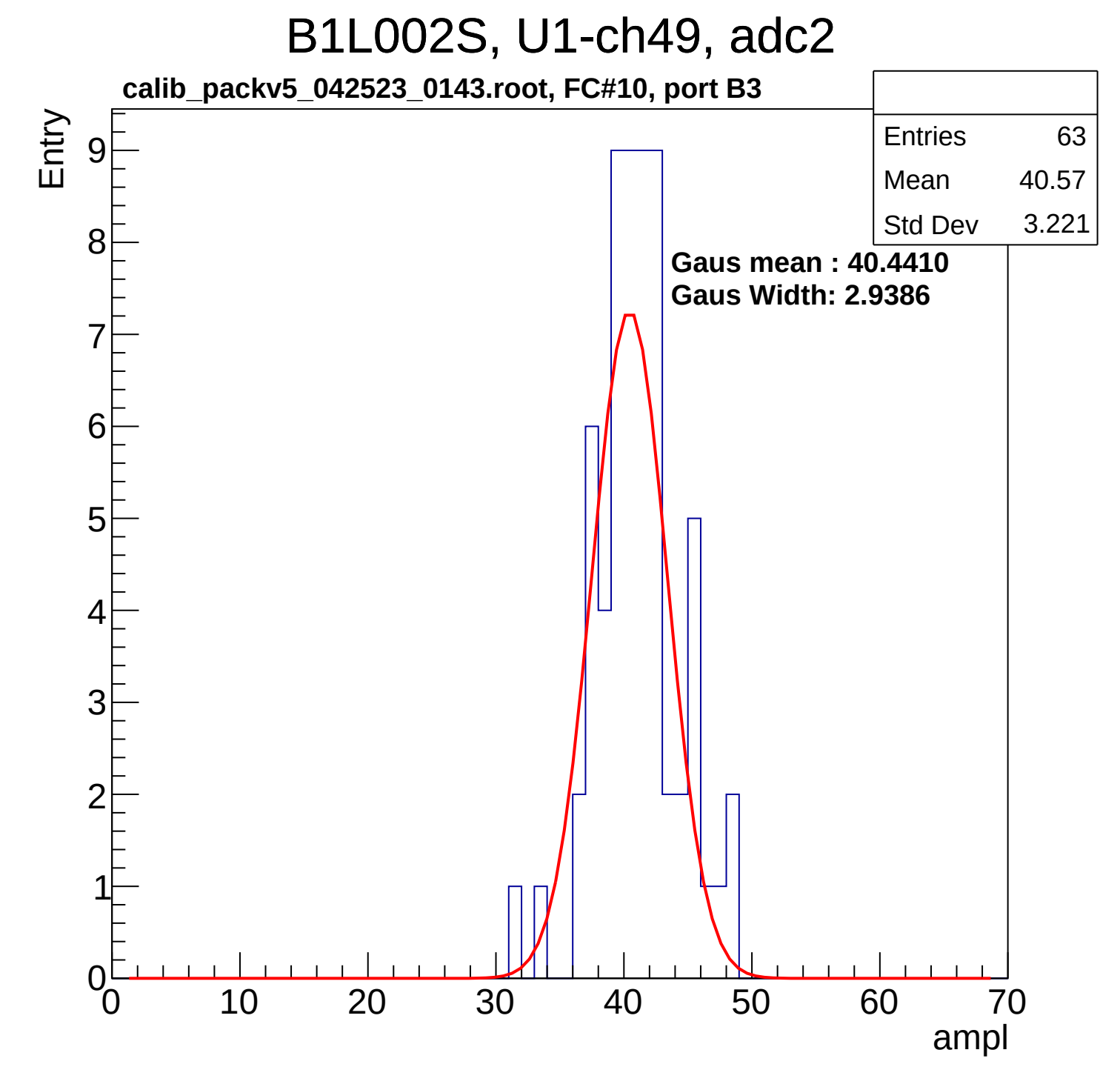
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch49, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 26.65
Std Dev 5.847
Gaus mean : 28.4254
Gaus Width: 3.1881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch49, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.9
Std Dev 2.61
Gaus mean : 34.3063
Gaus Width: 2.7359
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch49, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.57
Std Dev 3.221
Gaus mean : 40.4410
Gaus Width: 2.9386
Entry
ampl
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2
3
4
5
6
7
8
9
0
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30
40
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60
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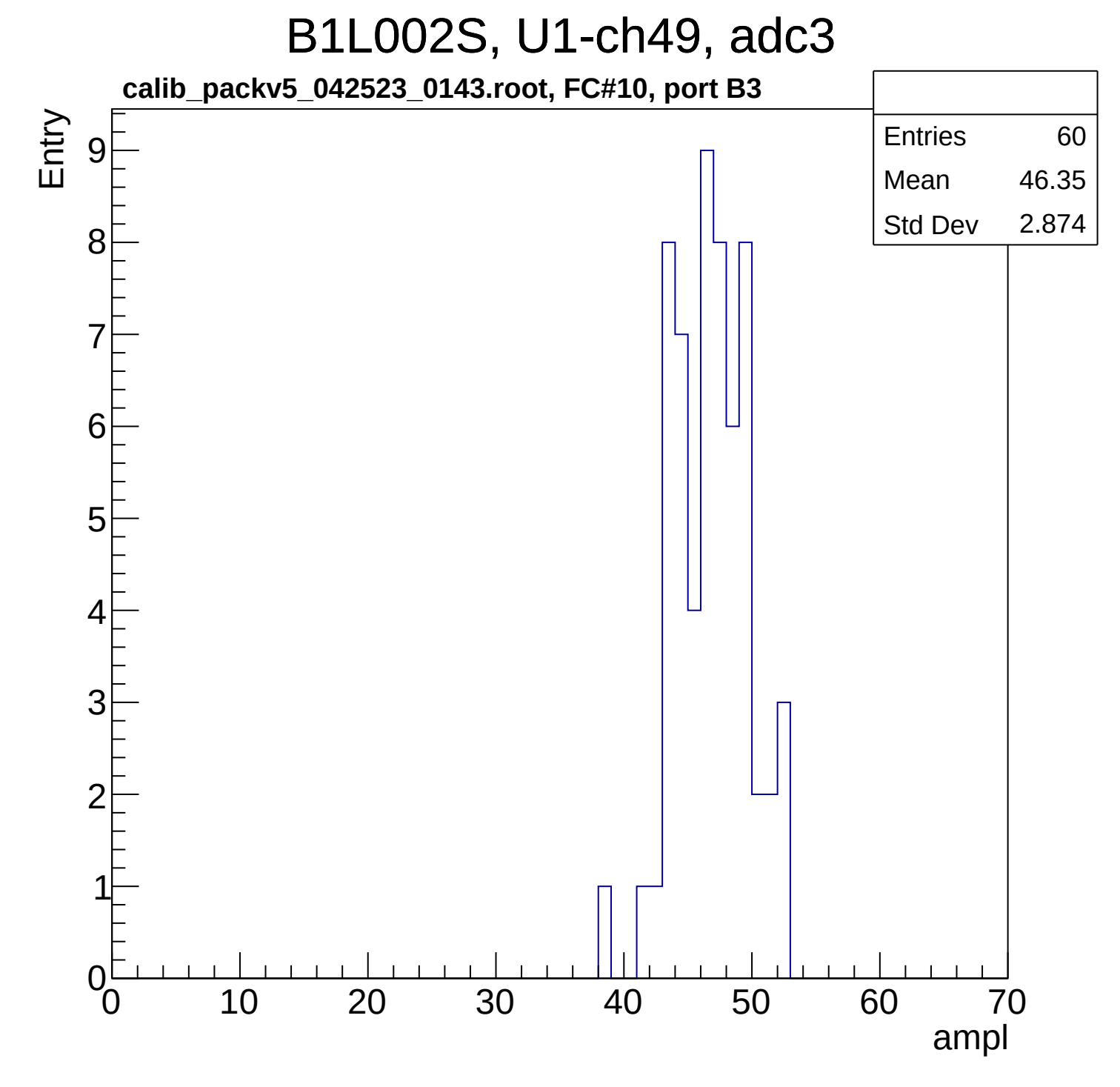
B1L002S, U1-ch49, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.35
Std Dev 2.874
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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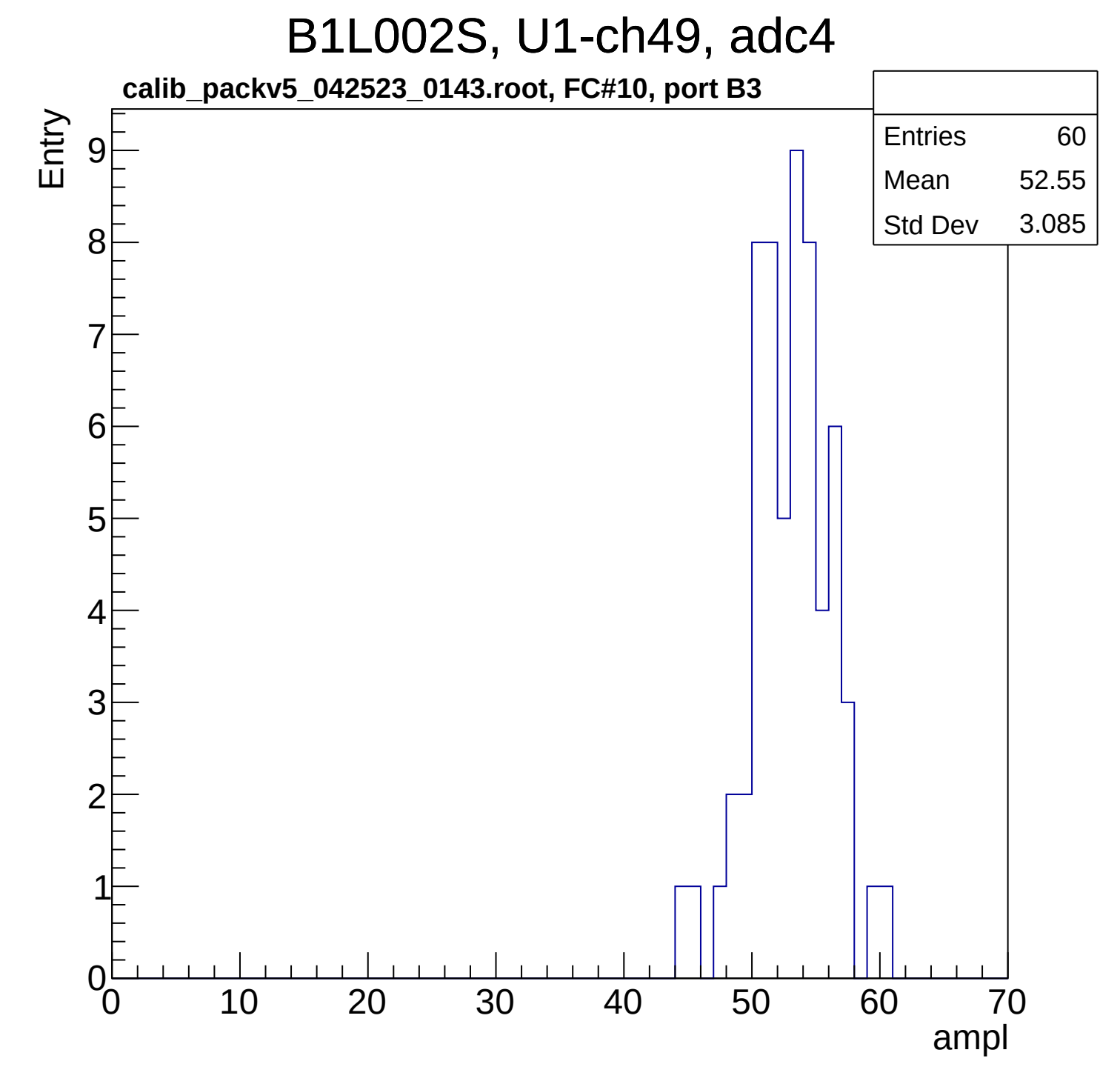
B1L002S, U1-ch49, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.55
Std Dev 3.085
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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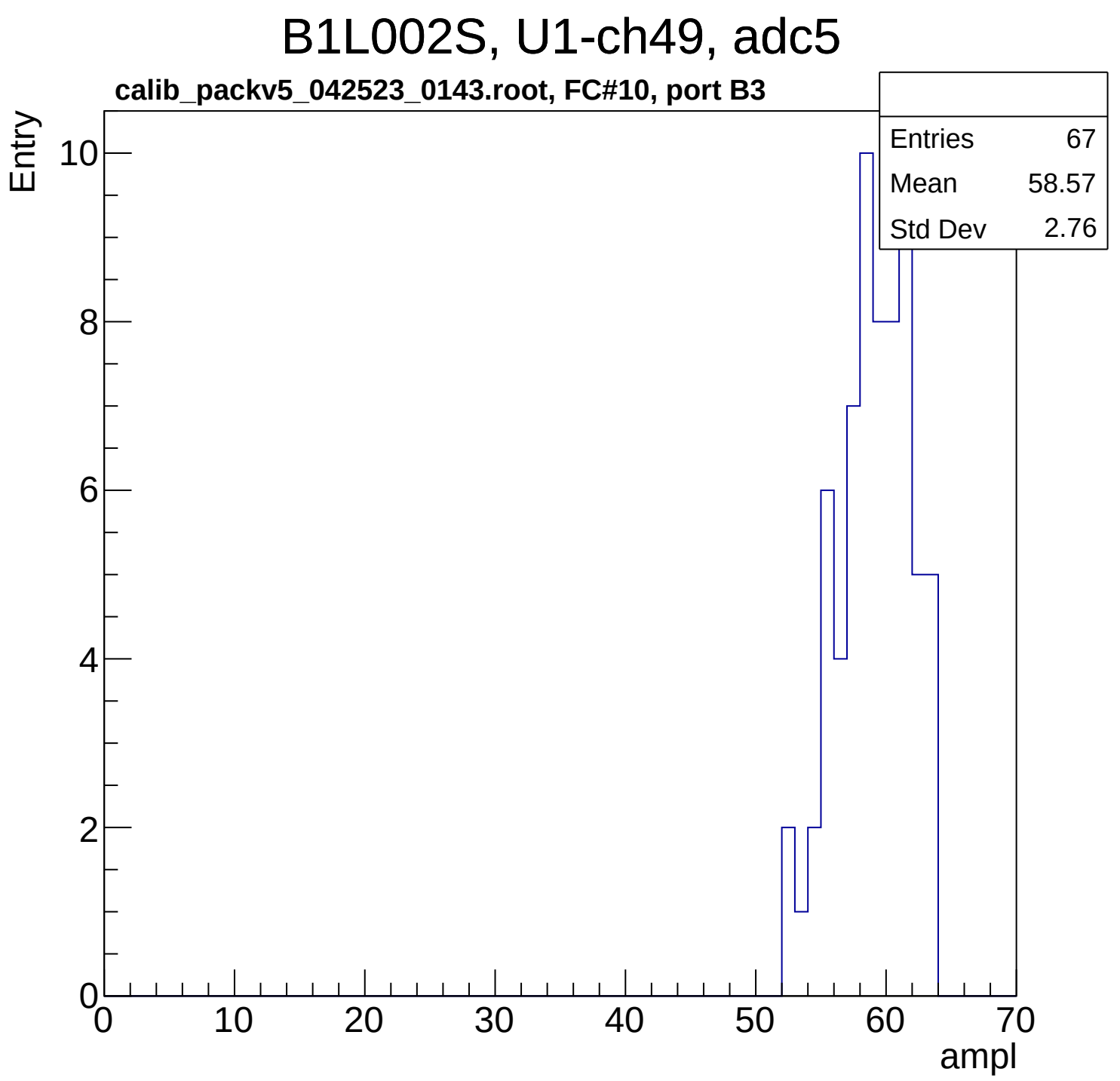
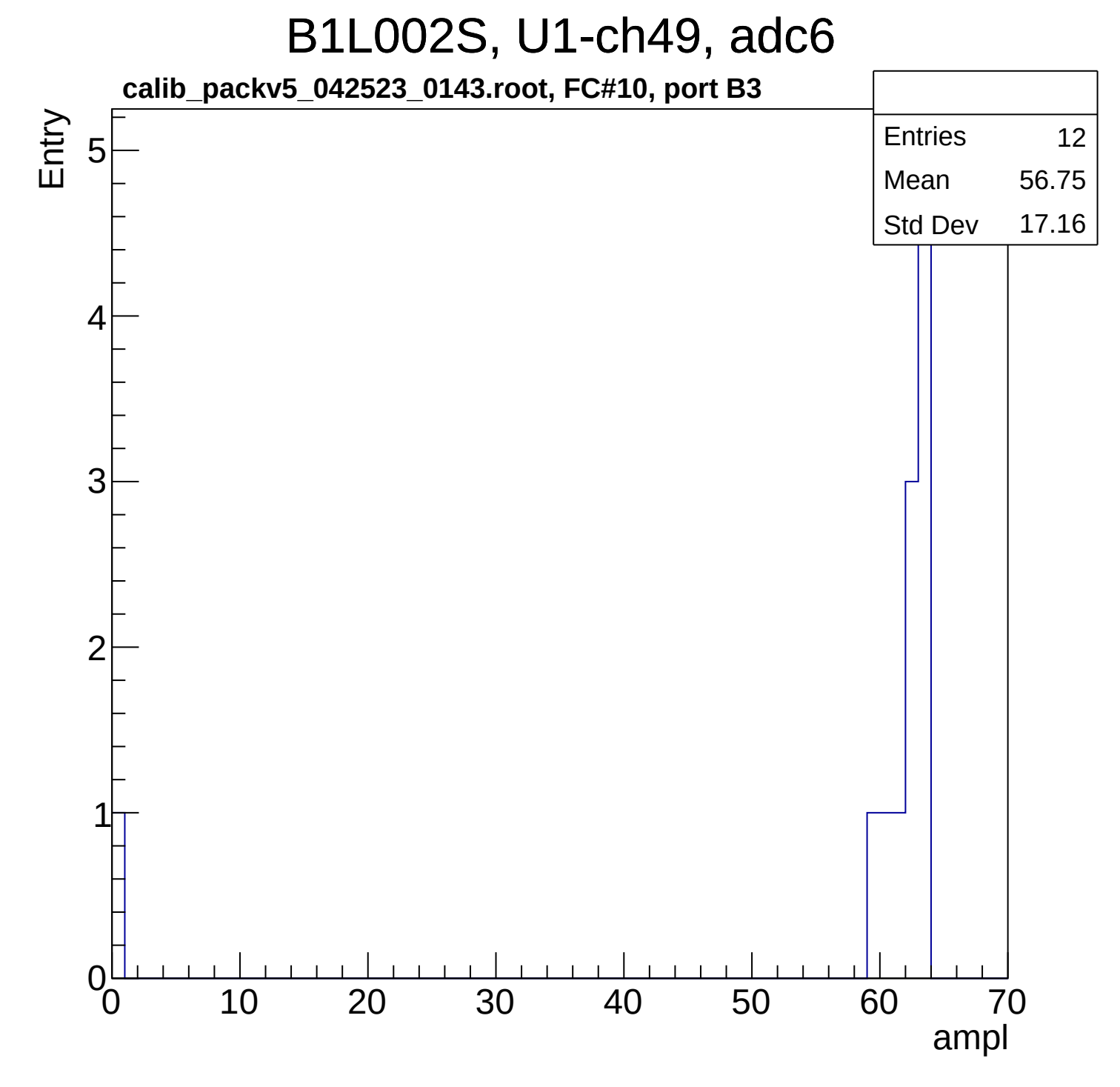
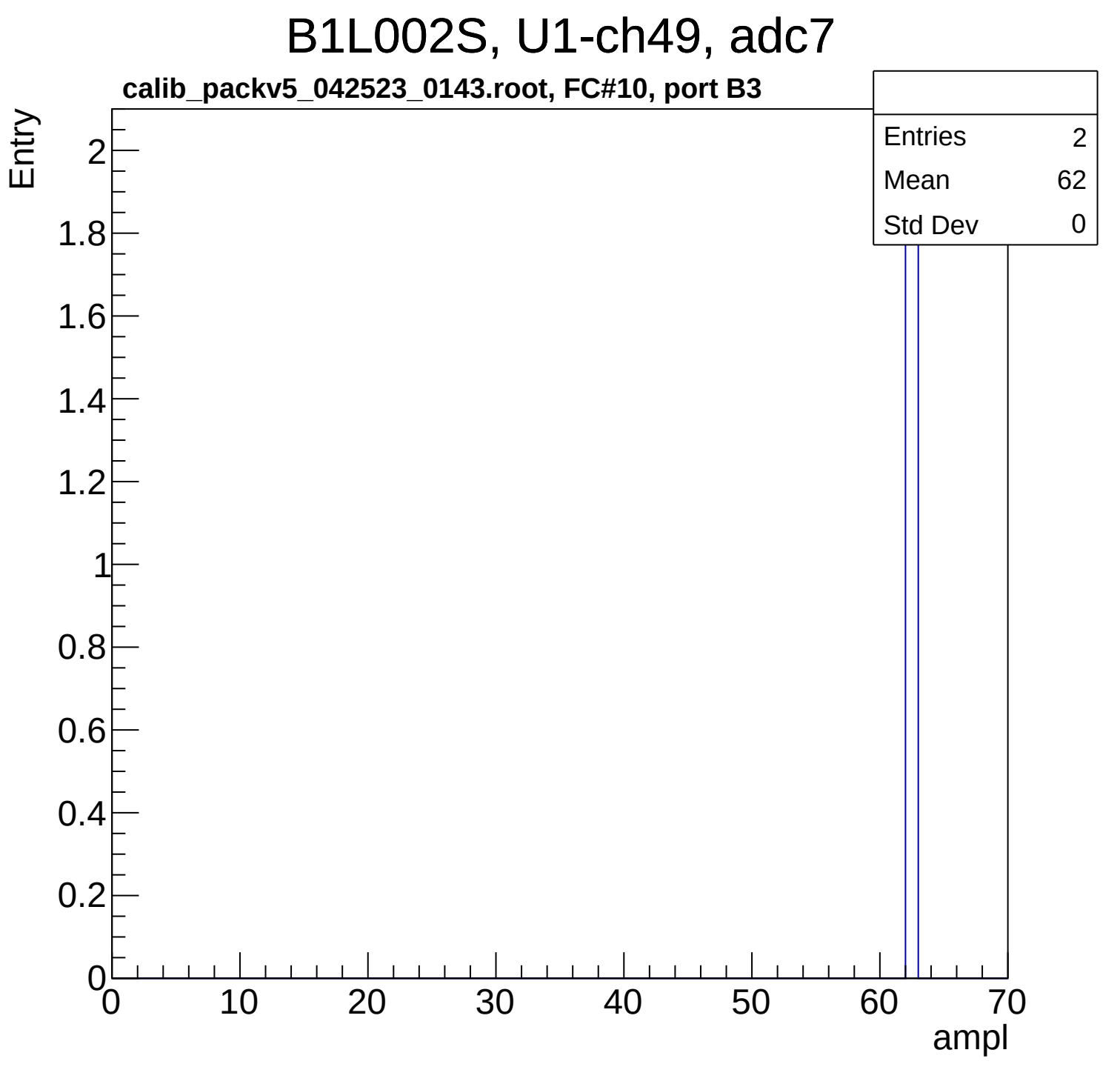

B1L002S, U1-ch49, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 58.57
Std Dev 2.76
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch49, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.75
Std Dev 17.16
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch49, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
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1
1.2
1.4
1.6
1.8
2
0
10
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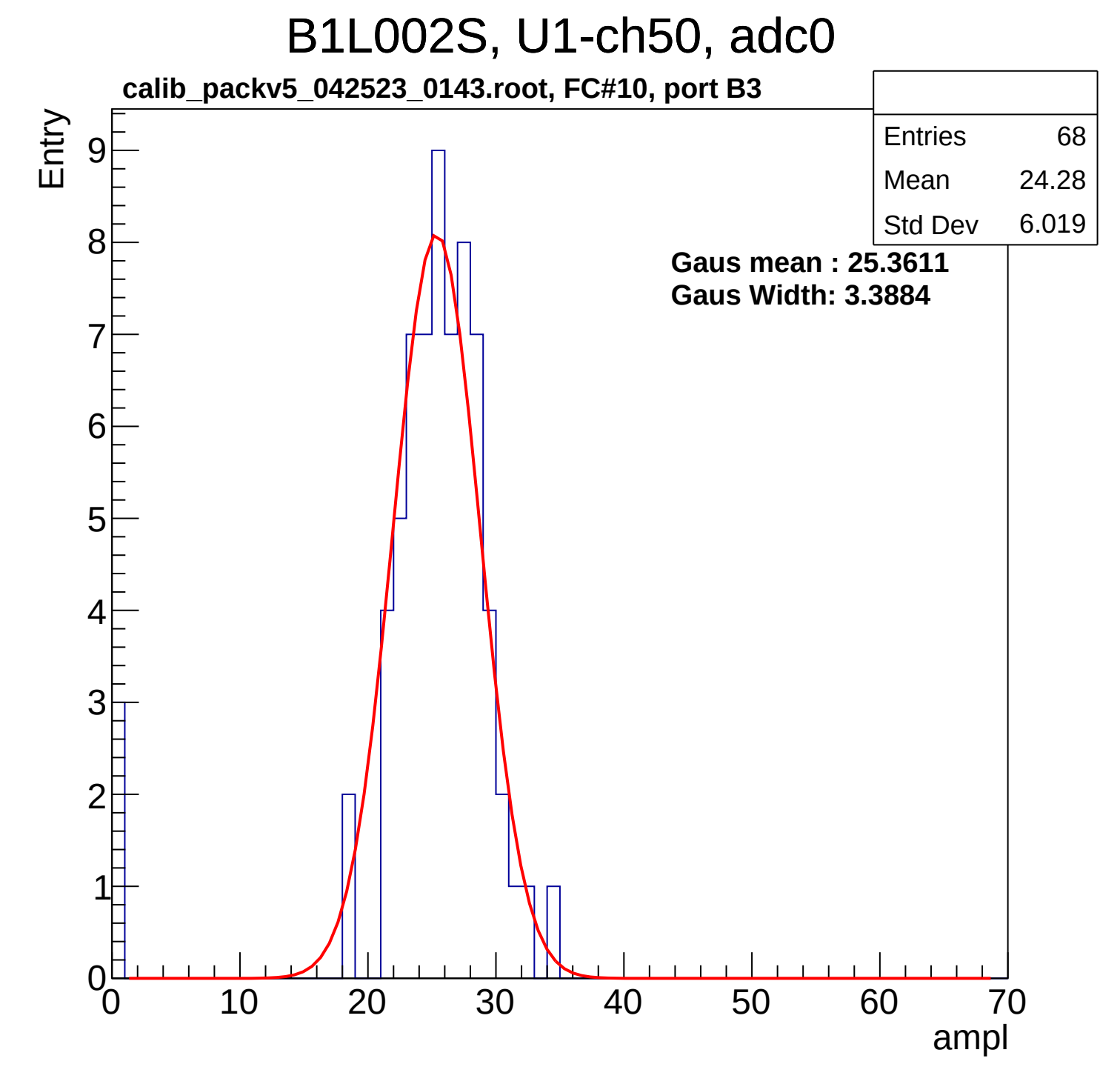
B1L002S, U1-ch50, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 24.28
Std Dev 6.019
Gaus mean : 25.3611
Gaus Width: 3.3884
Entry
ampl
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10
20
30
40
50
60
70
1
2
3
4
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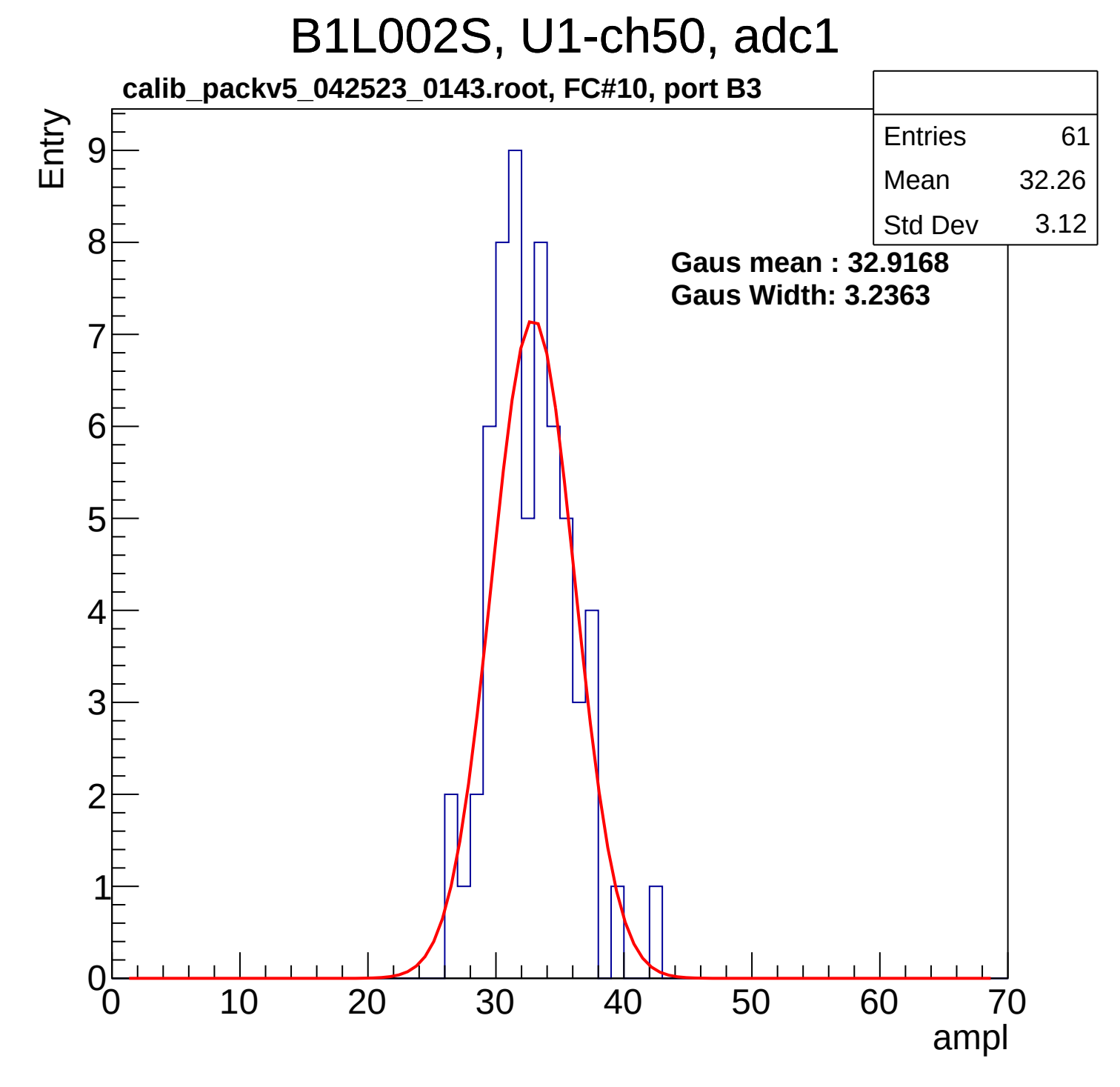
6
7
8
9


B1L002S, U1-ch50, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 32.26
Std Dev 3.12
Gaus mean : 32.9168
Gaus Width: 3.2363
Entry
ampl
0
1
2
3
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6
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60
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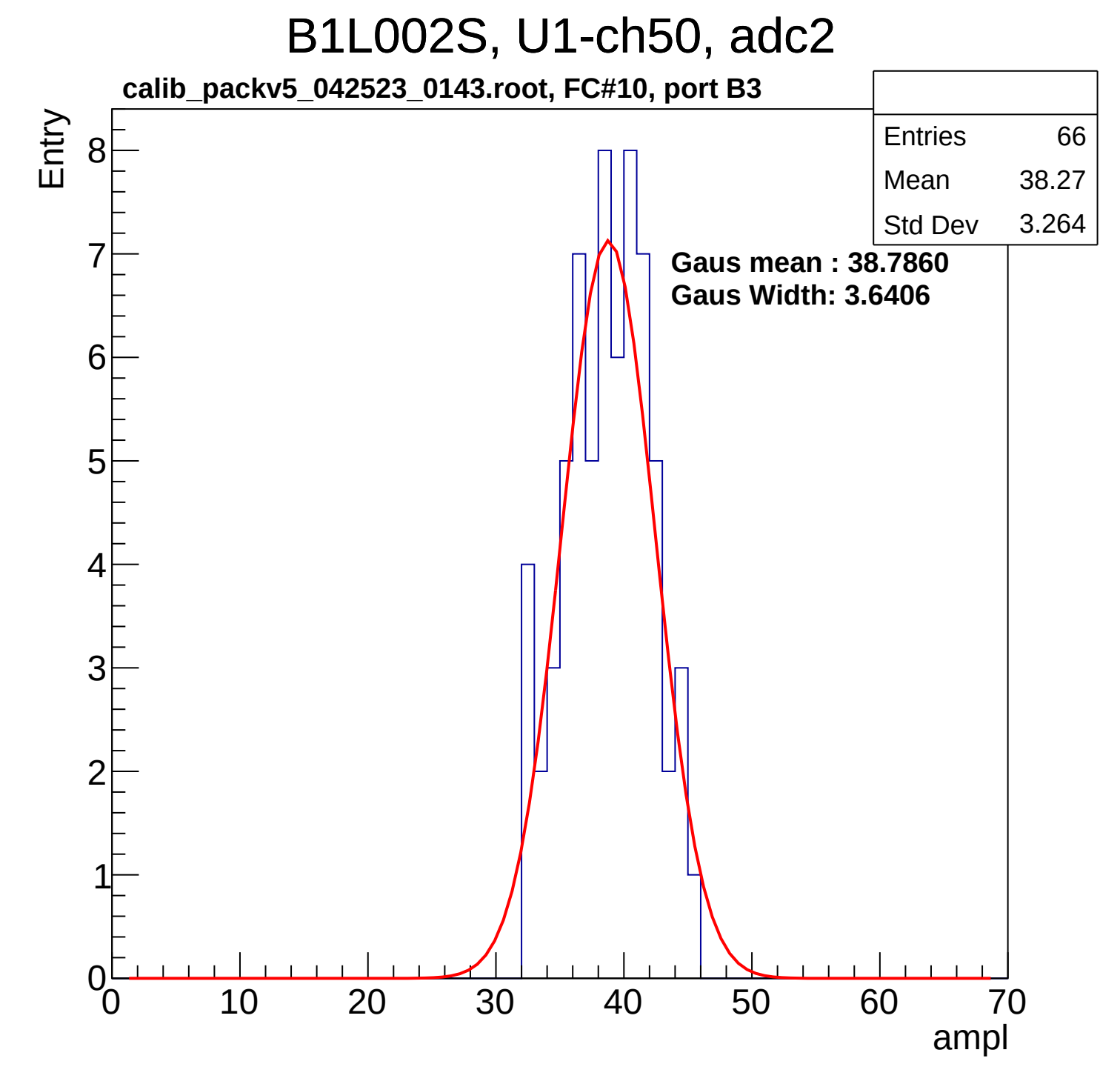
B1L002S, U1-ch50, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 38.27
Std Dev 3.264
Gaus mean : 38.7860
Gaus Width: 3.6406
Entry
ampl
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2
3
4
5
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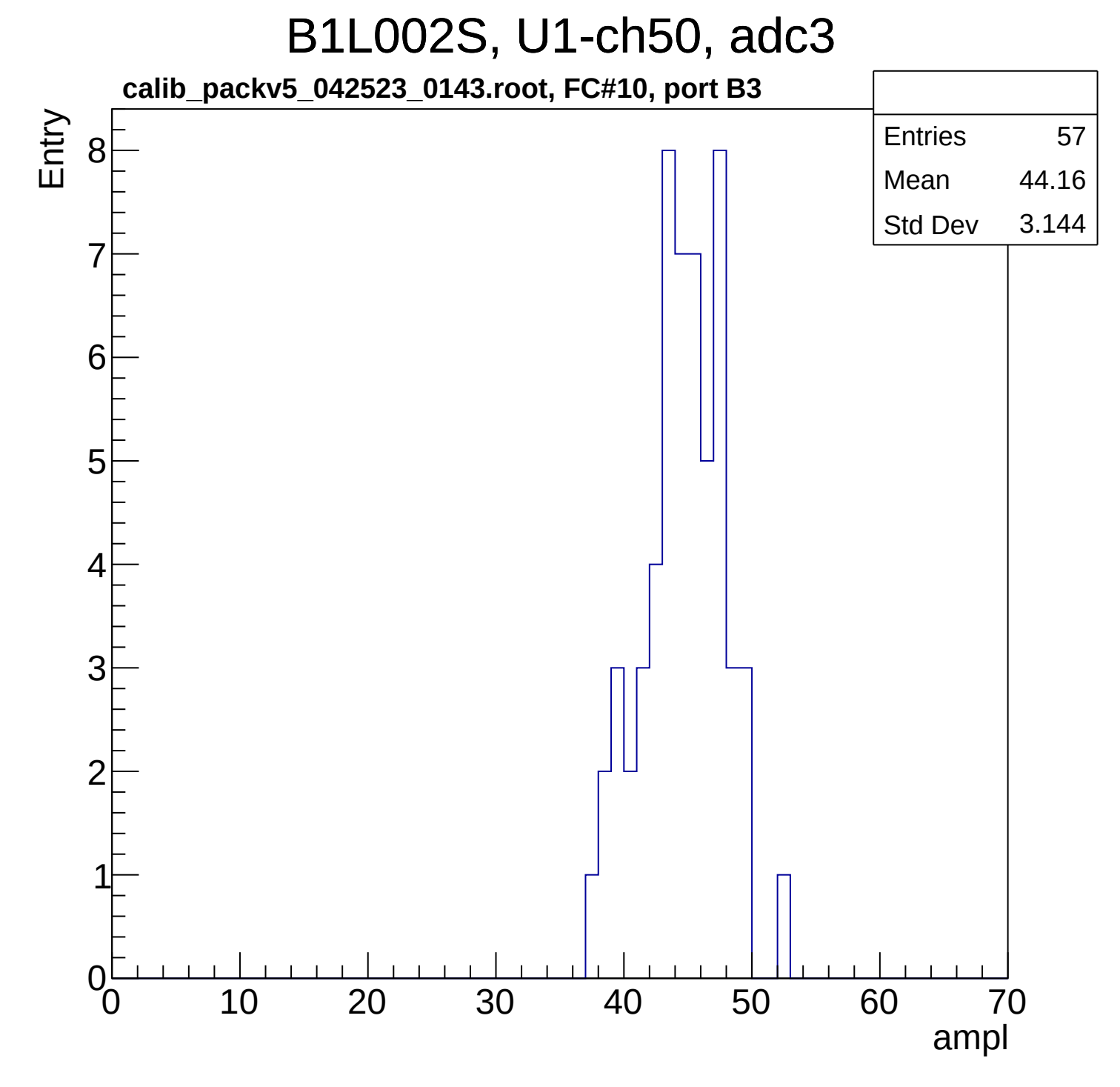
B1L002S, U1-ch50, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 44.16
Std Dev 3.144
Entry
ampl
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1
2
3
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5
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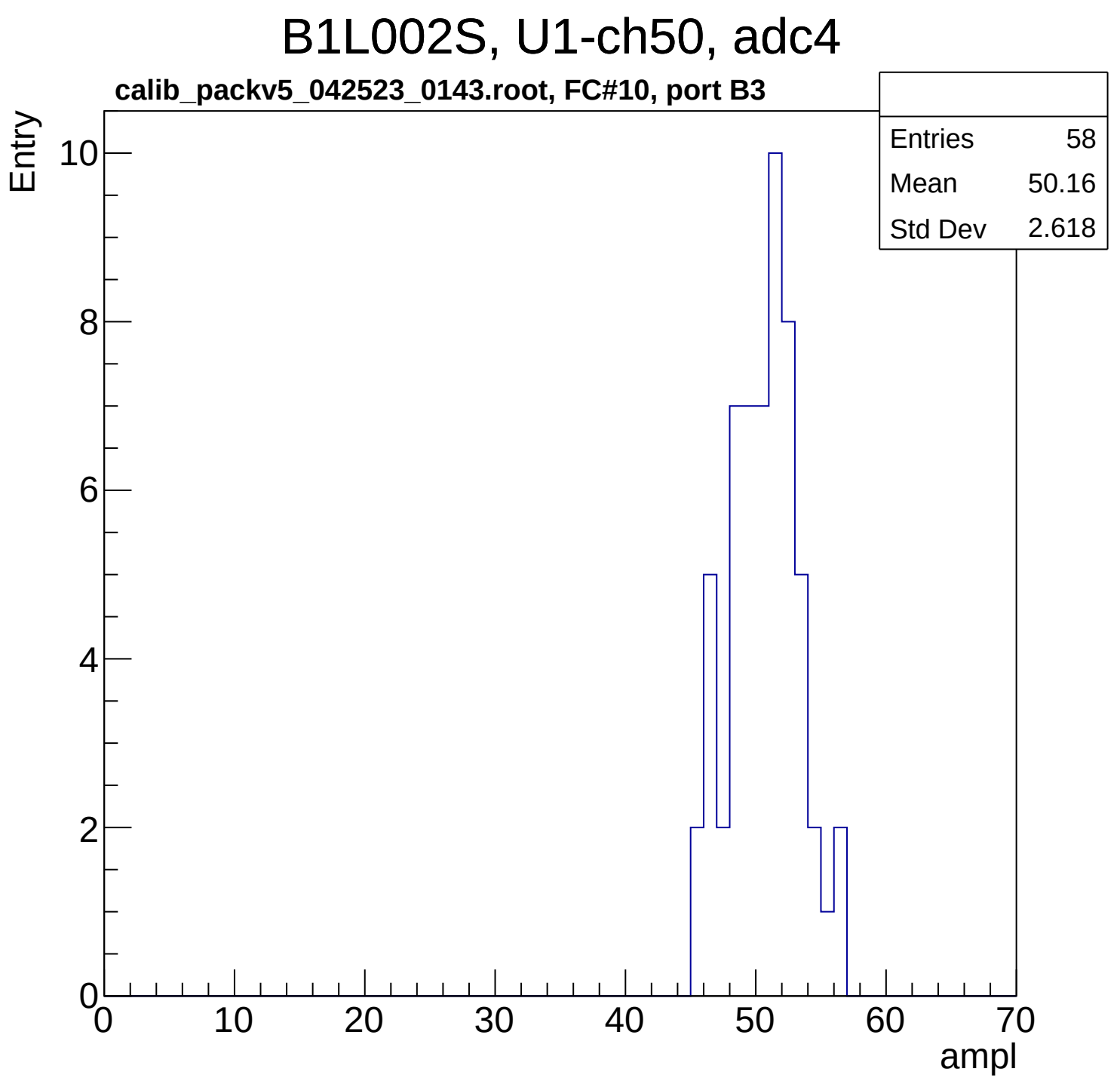
B1L002S, U1-ch50, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 50.16
Std Dev 2.618
Entry
0
2
4
6
8
10
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10
20
30
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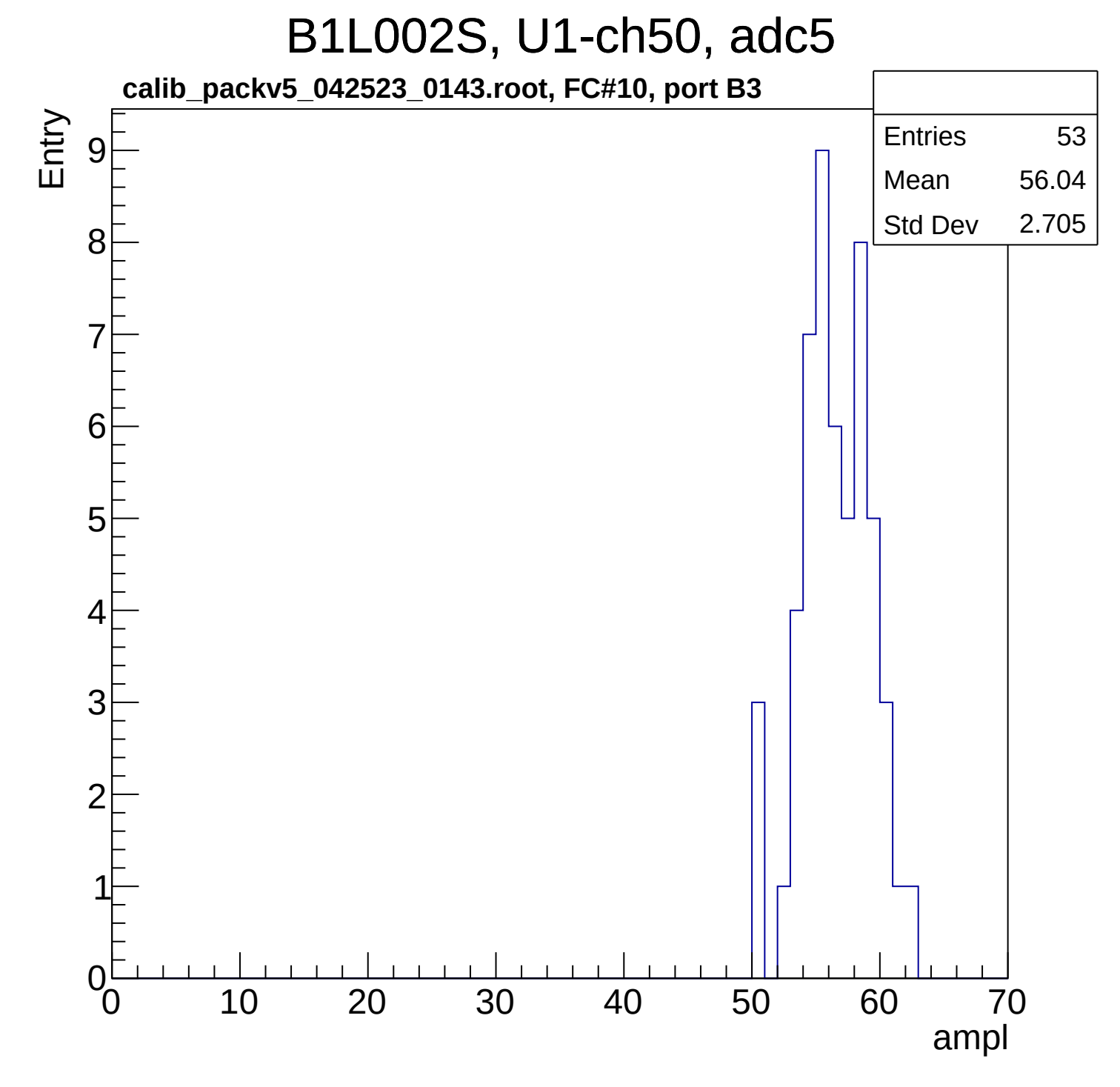
50
60
70
ampl


B1L002S, U1-ch50, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 56.04
Std Dev 2.705
Entry
ampl
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2
3
4
5
6
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0
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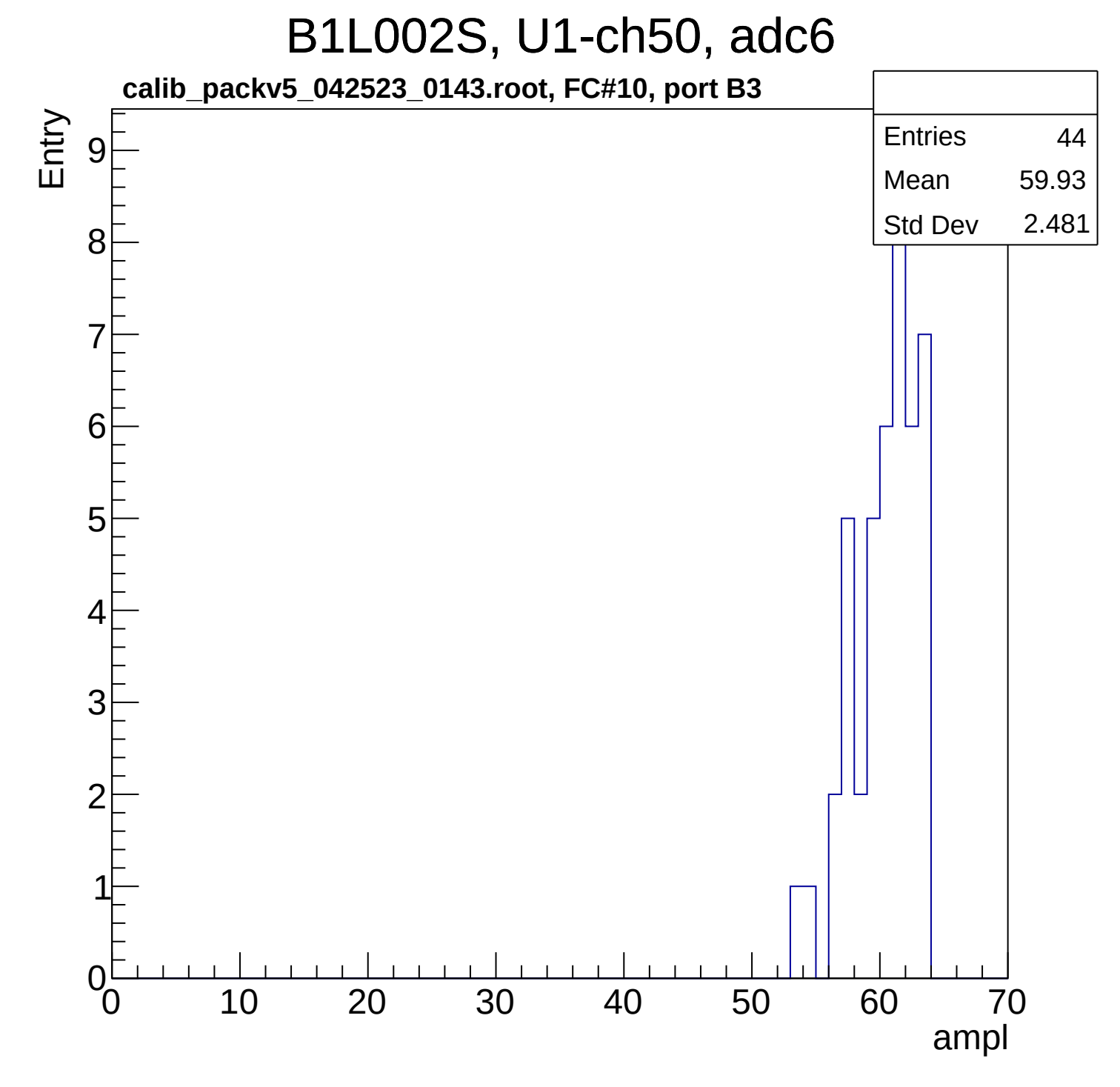
B1L002S, U1-ch50, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 59.93
Std Dev 2.481
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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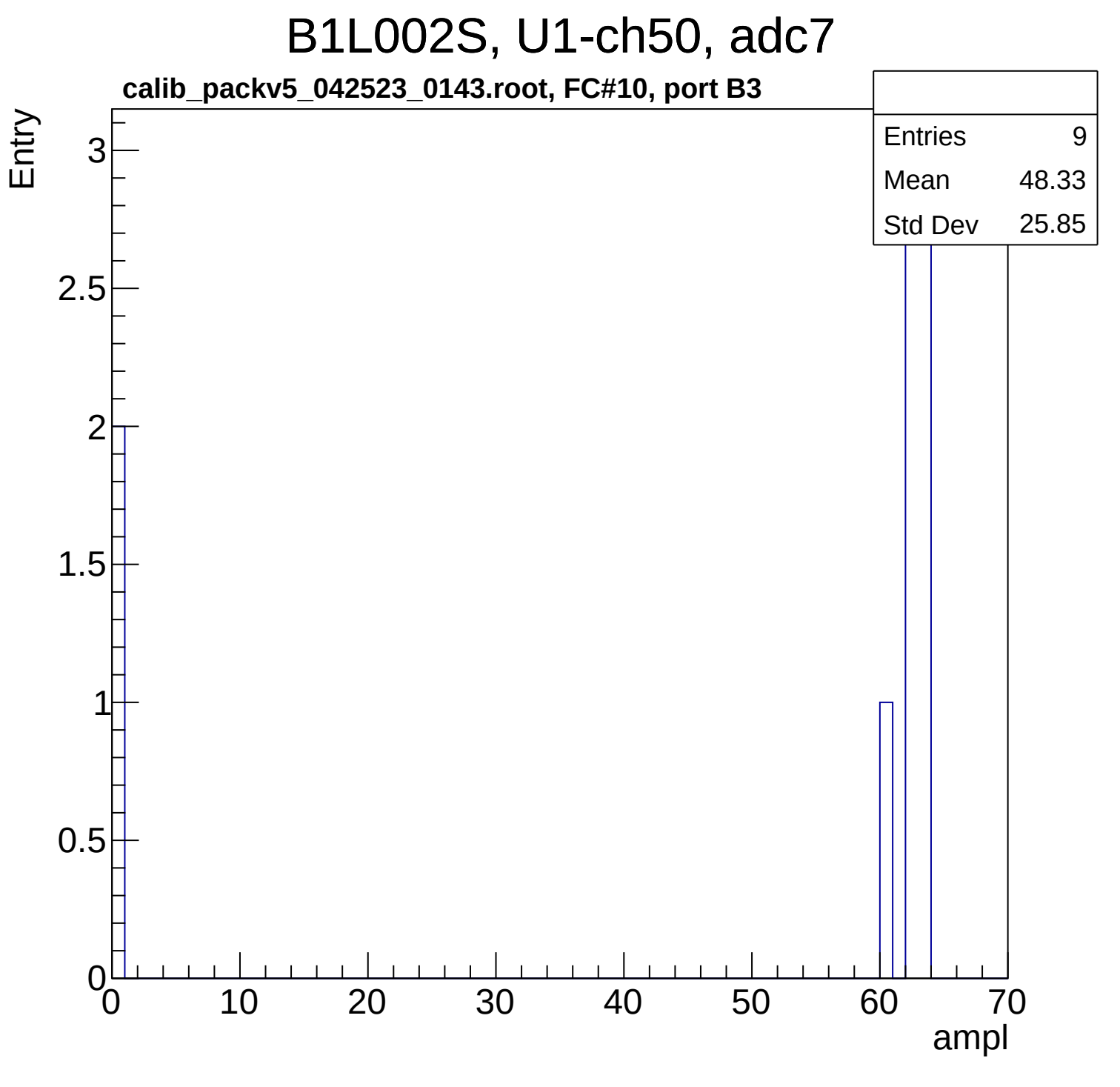
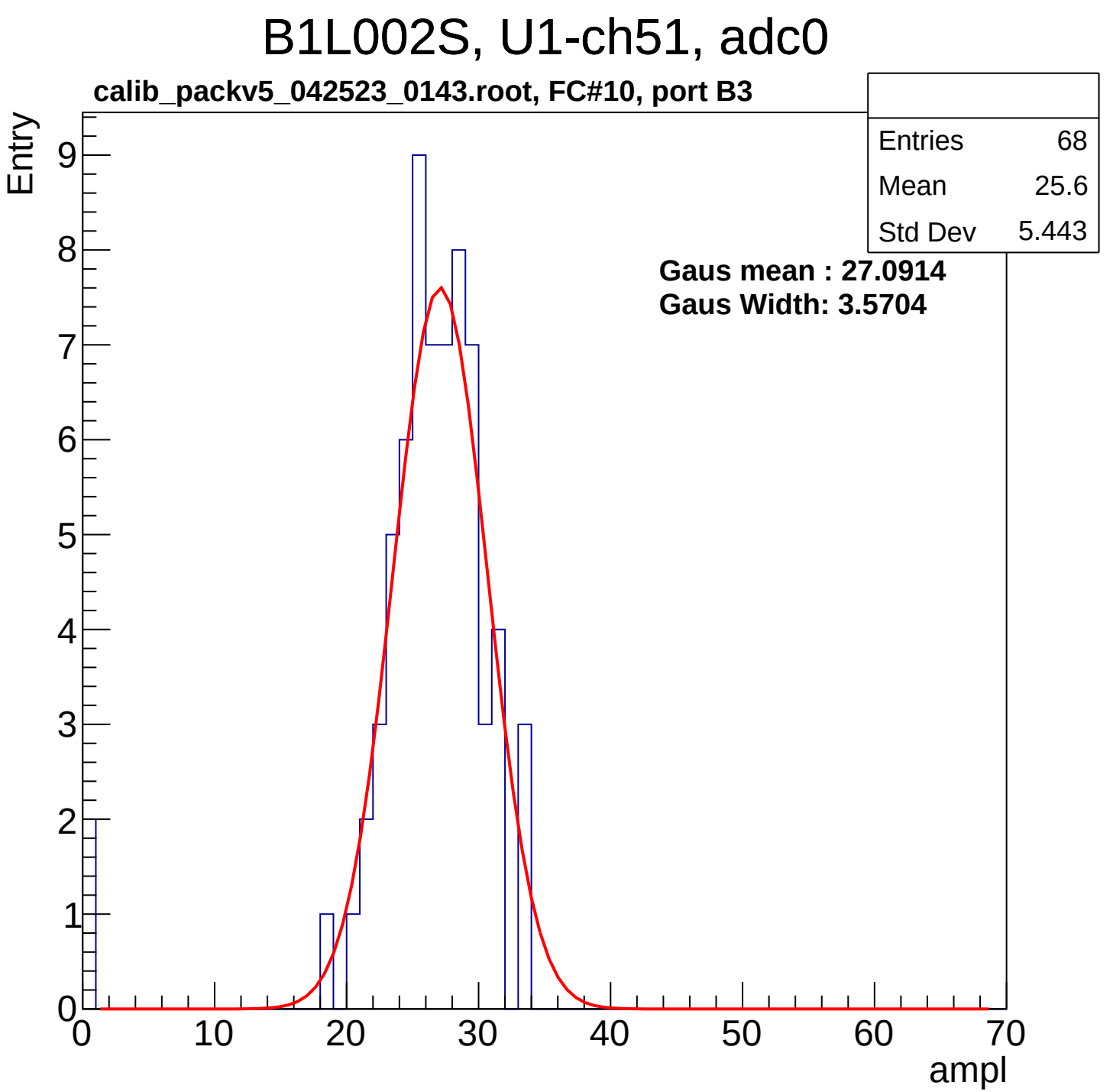

B1L002S, U1-ch50, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 48.33
Std Dev 25.85
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch51, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 25.6
Std Dev 5.443
Gaus mean : 27.0914
Gaus Width: 3.5704
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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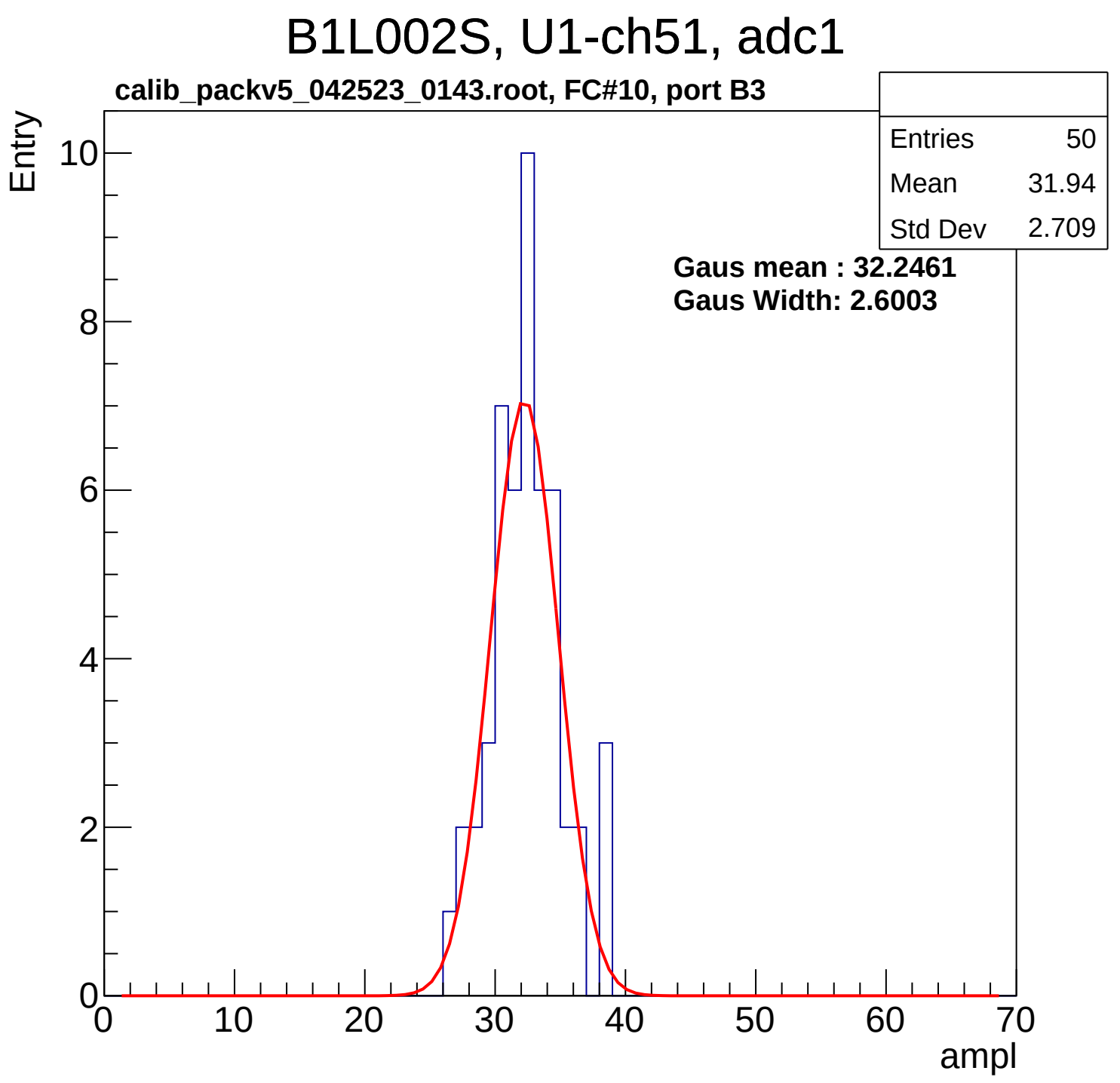
B1L002S, U1-ch51, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 31.94
Std Dev 2.709
Gaus mean : 32.2461
Gaus Width: 2.6003
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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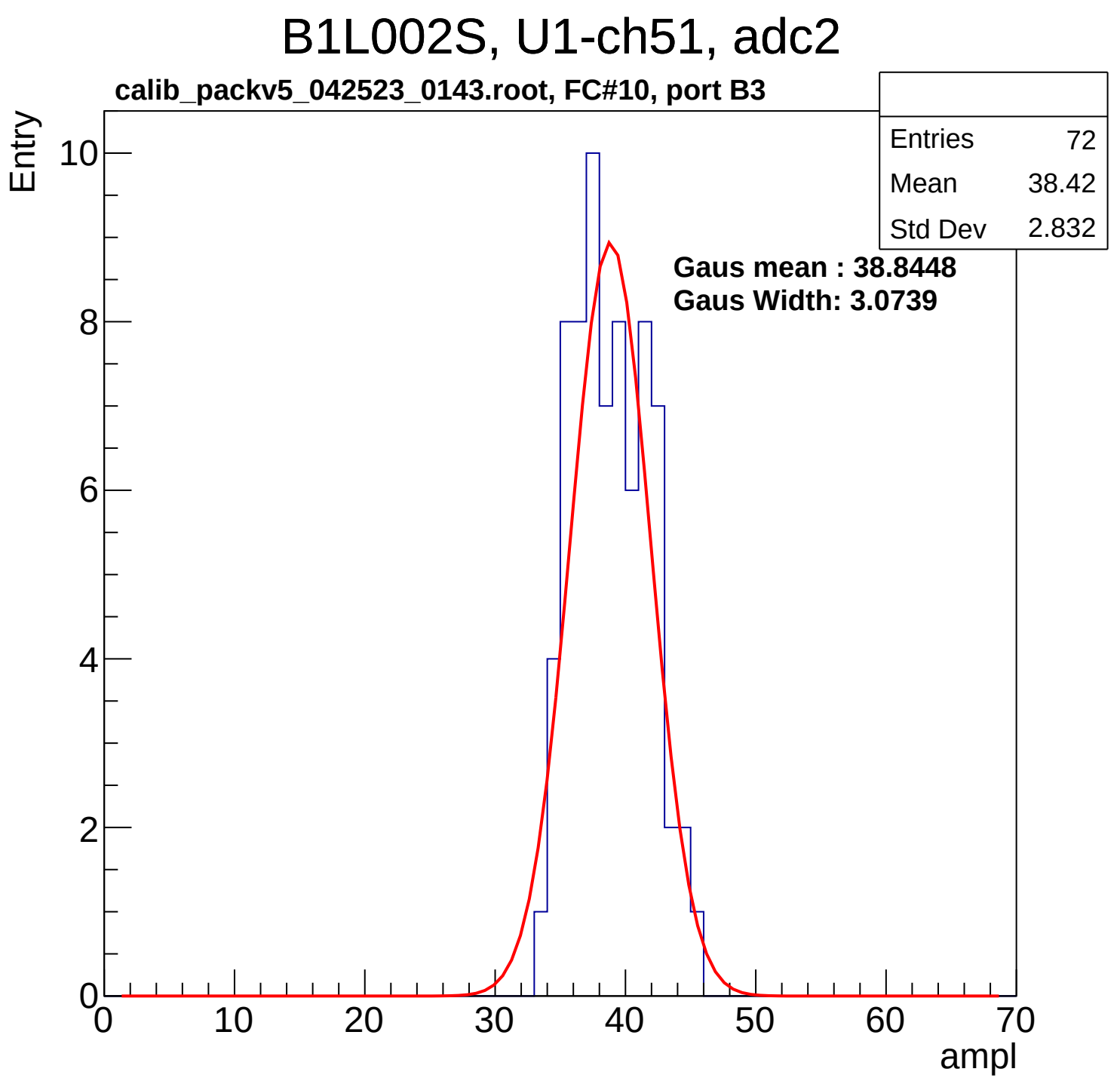
B1L002S, U1-ch51, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 38.42
Std Dev 2.832
Gaus mean : 38.8448
Gaus Width: 3.0739
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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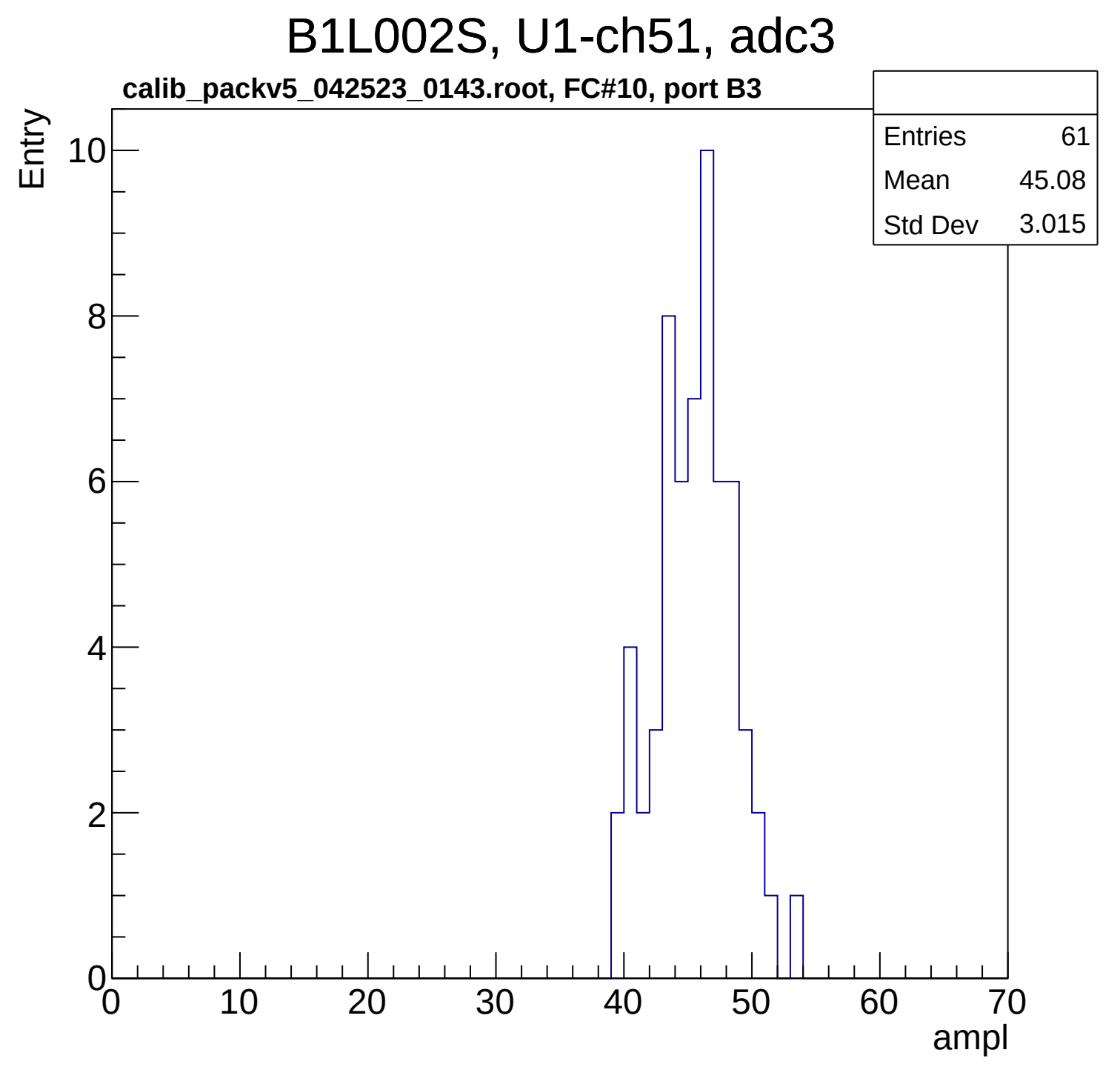
B1L002S, U1-ch51, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 45.08
Std Dev 3.015
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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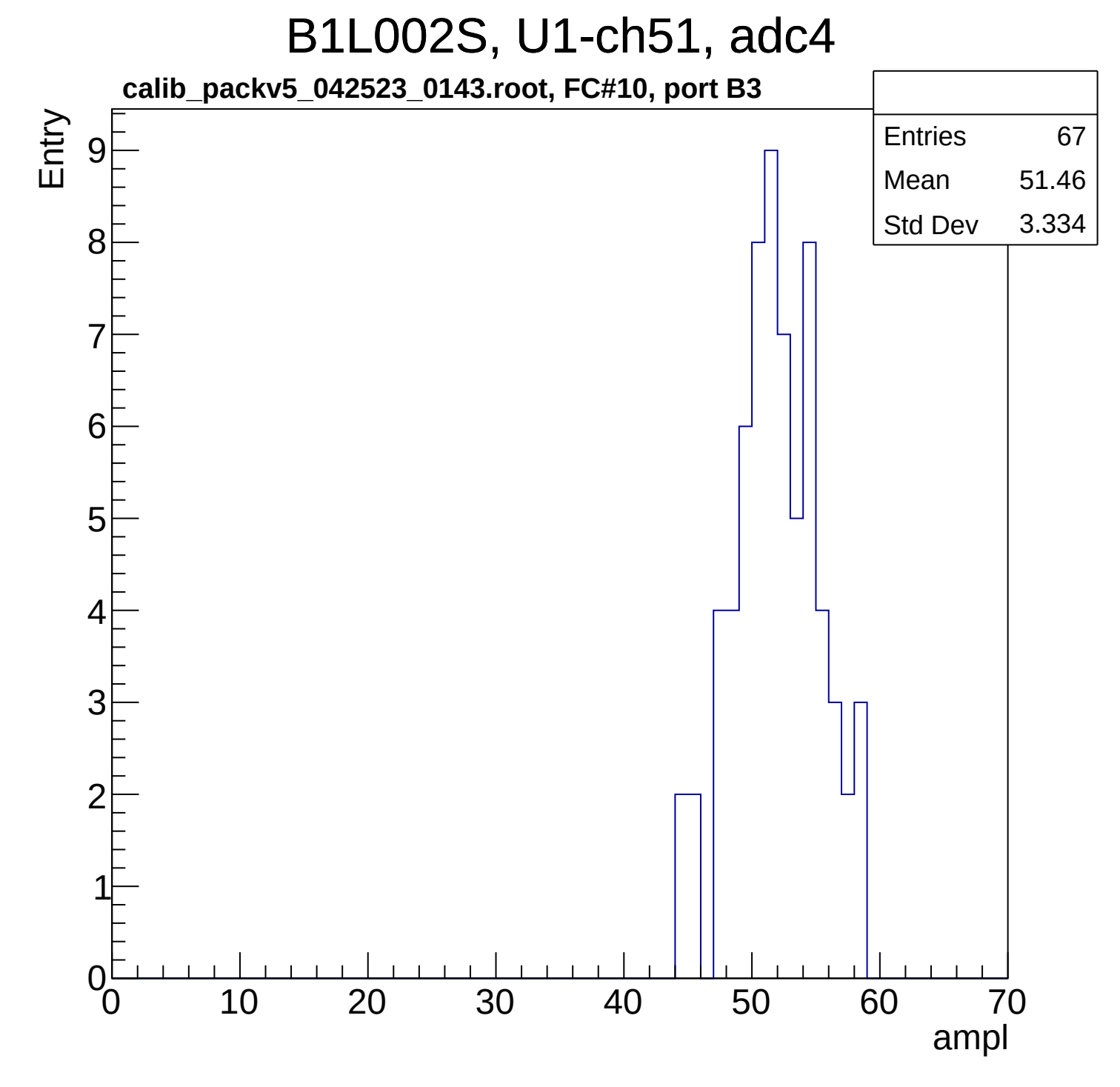
B1L002S, U1-ch51, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 51.46
Std Dev 3.334
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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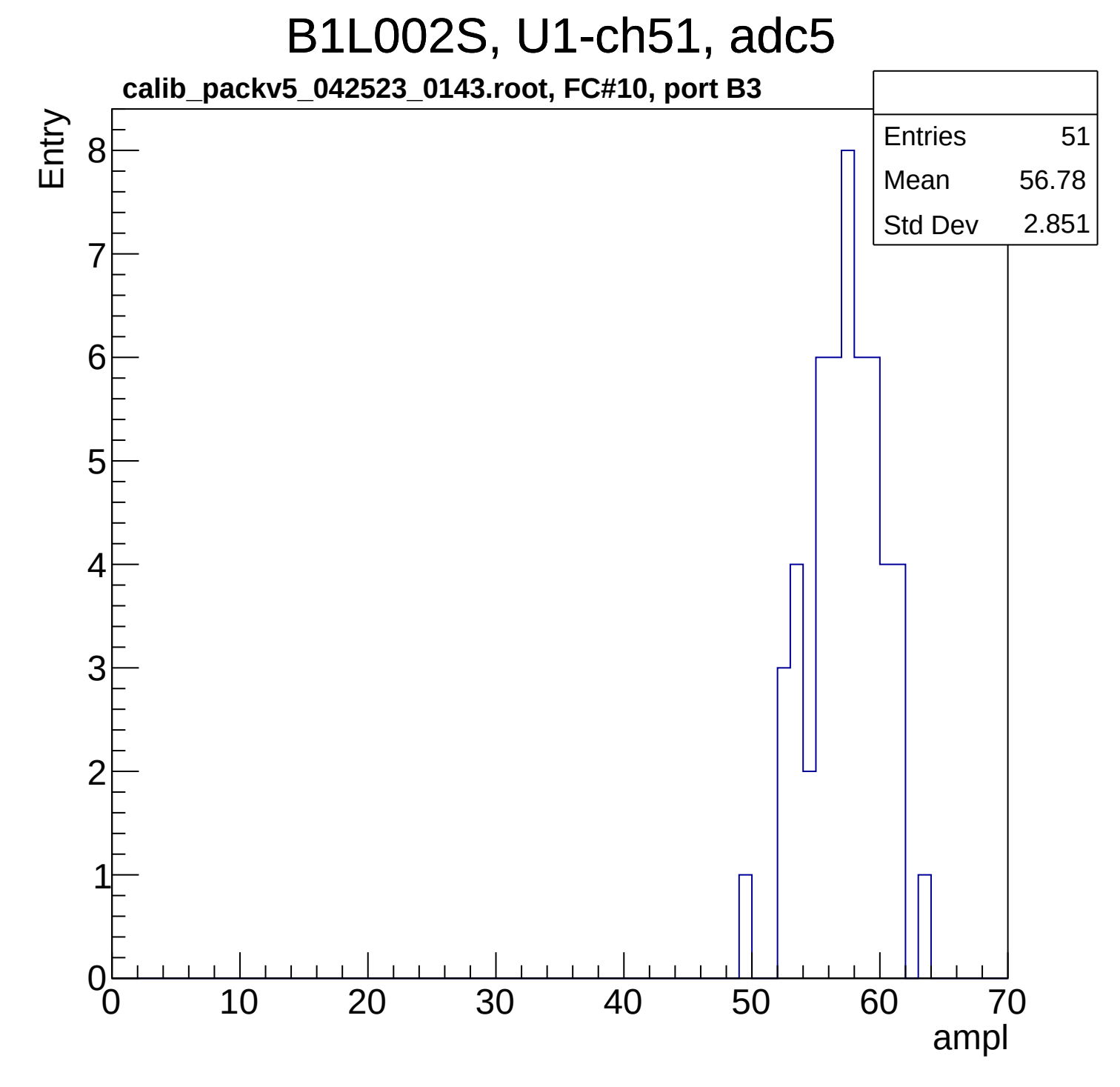
B1L002S, U1-ch51, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 56.78
Std Dev 2.851
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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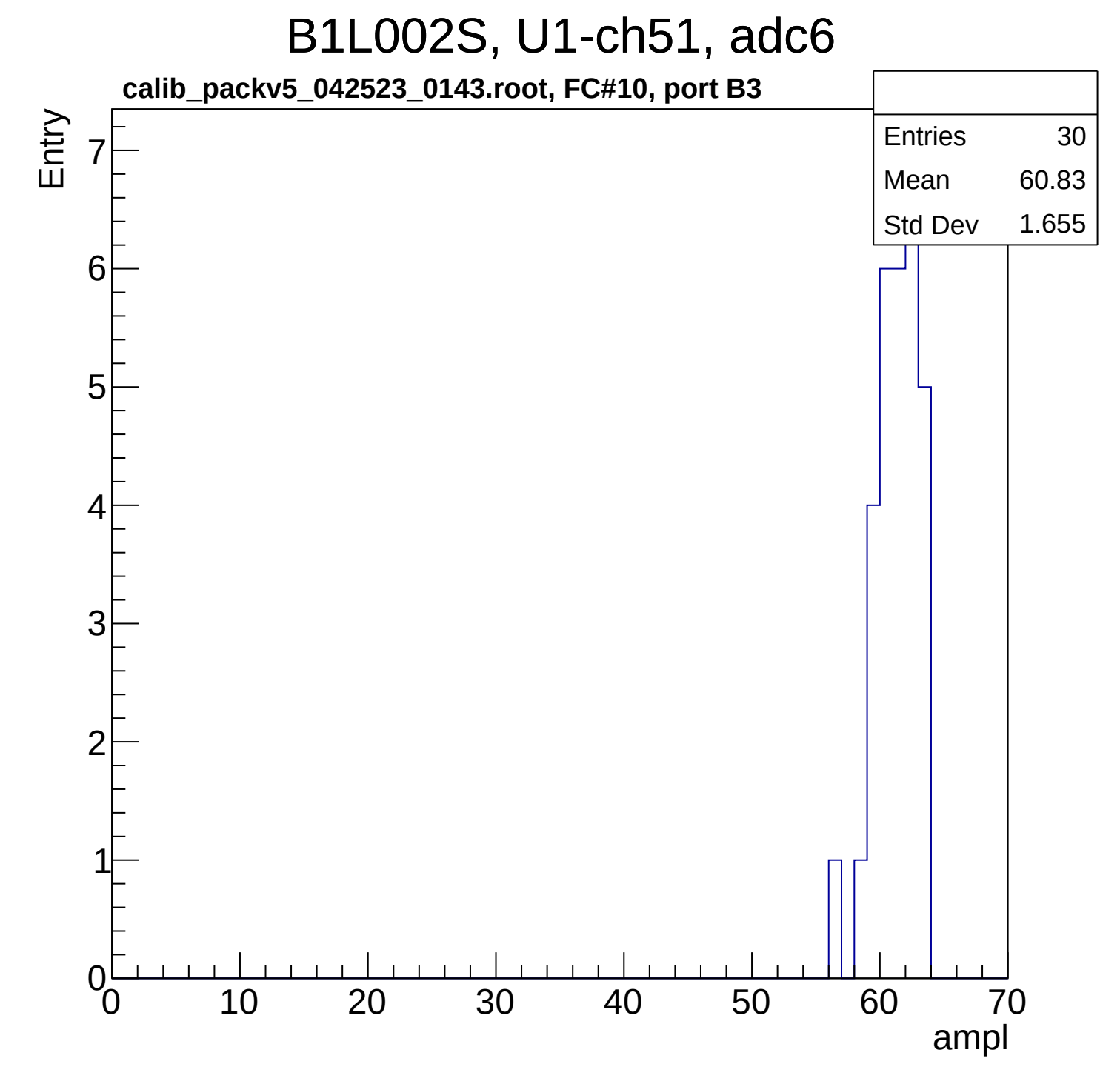
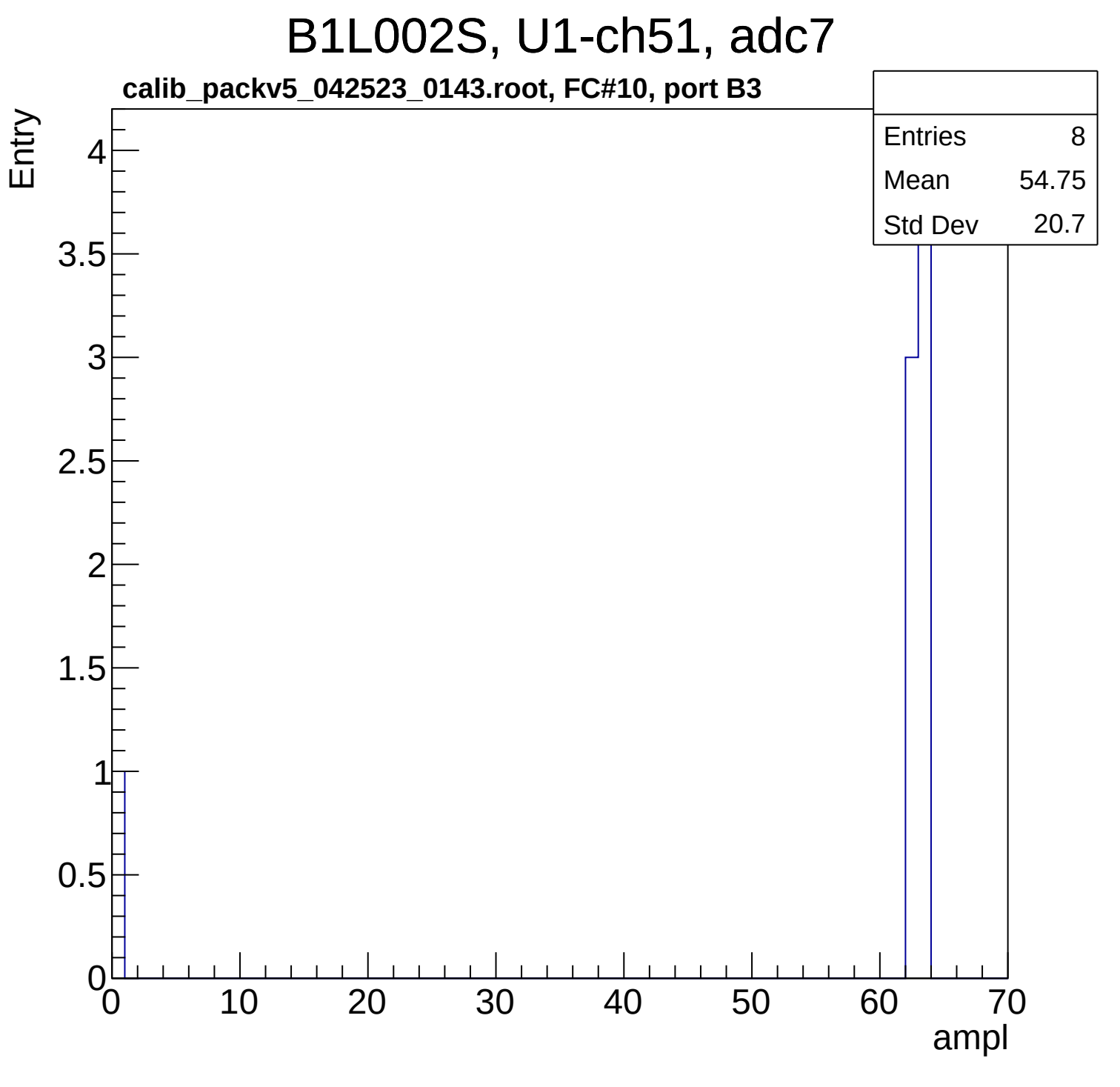

B1L002S, U1-ch51, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 60.83
Std Dev 1.655
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch51, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 54.75
Std Dev 20.7
Entry
ampl
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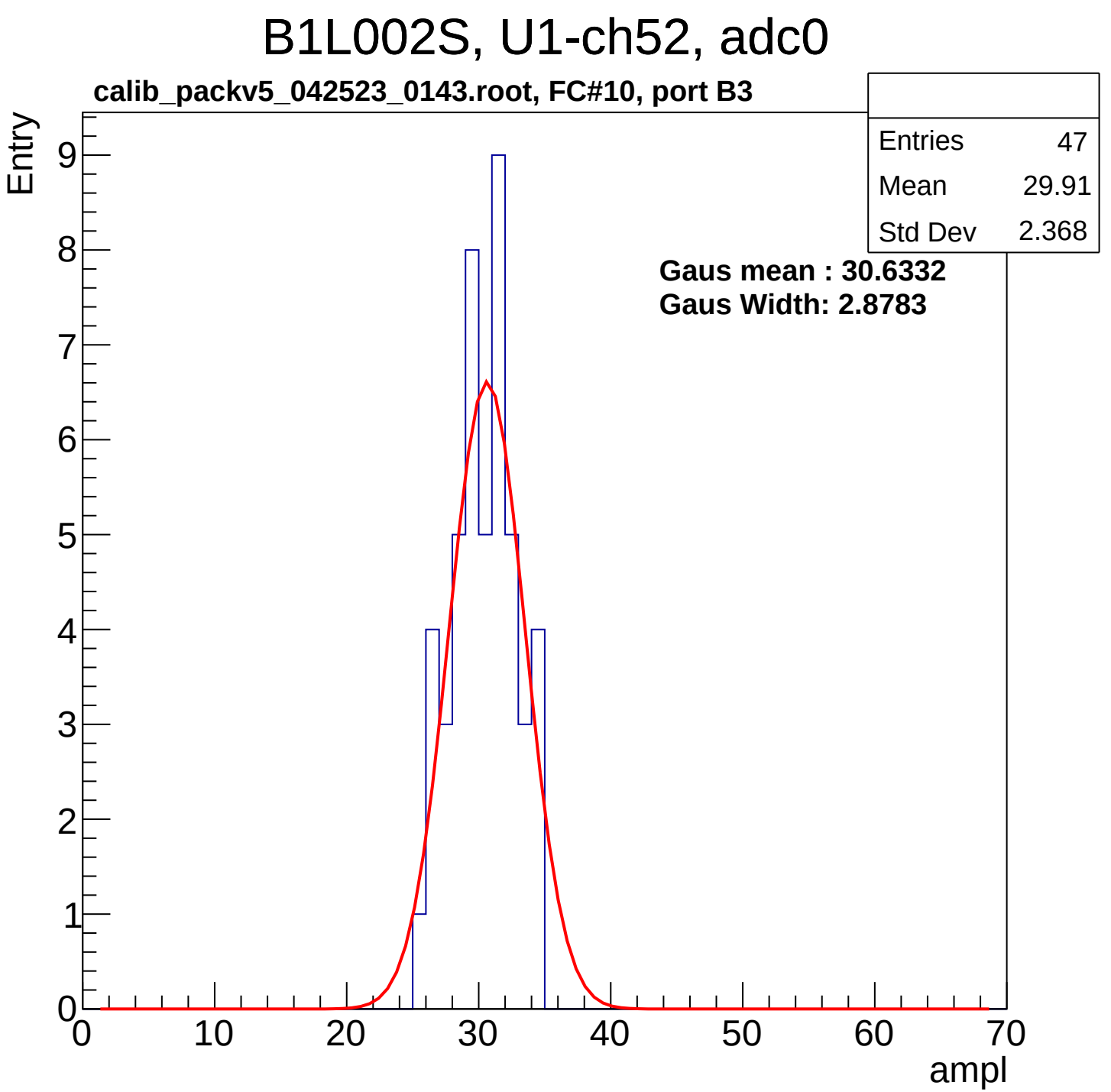
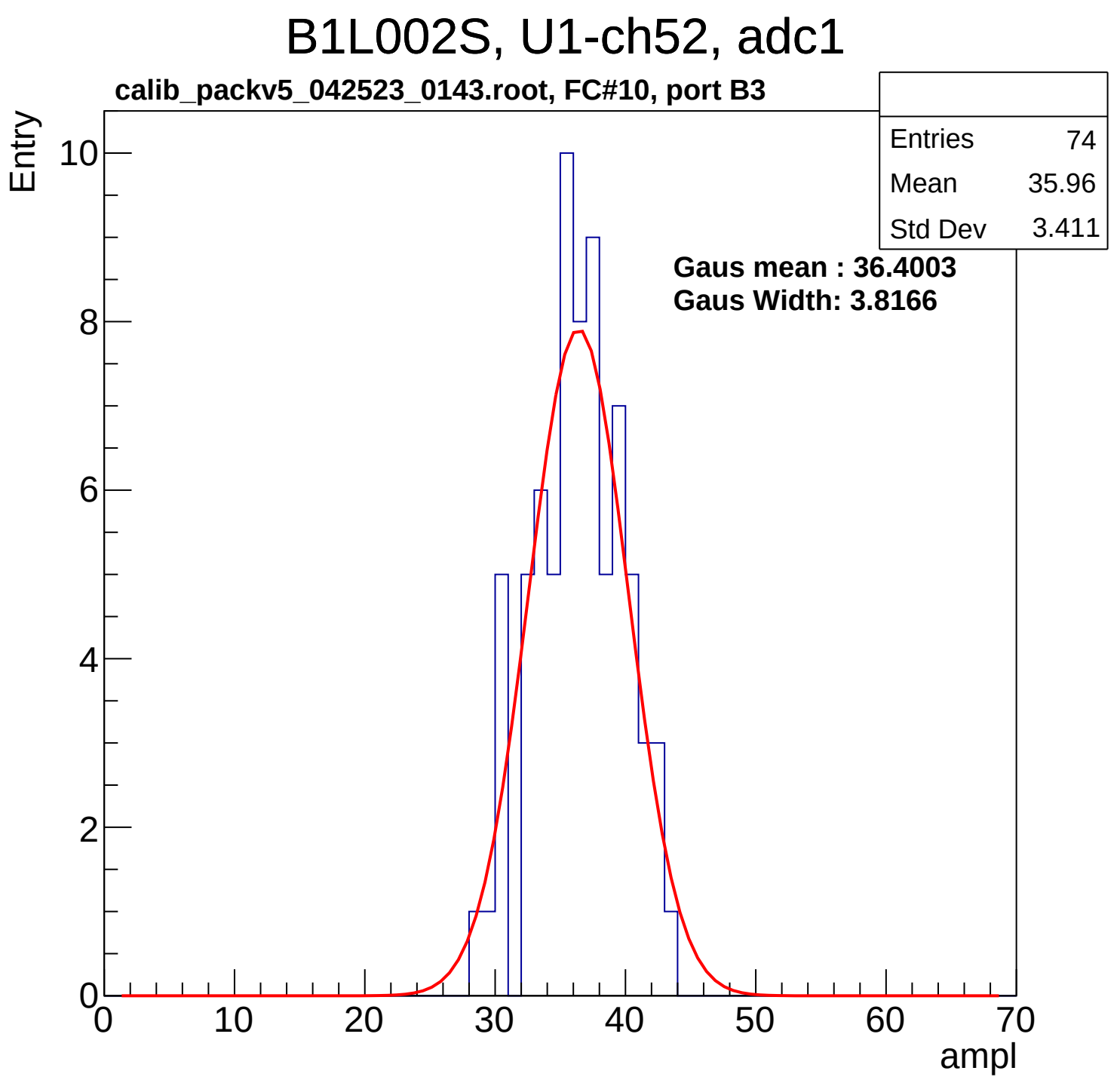
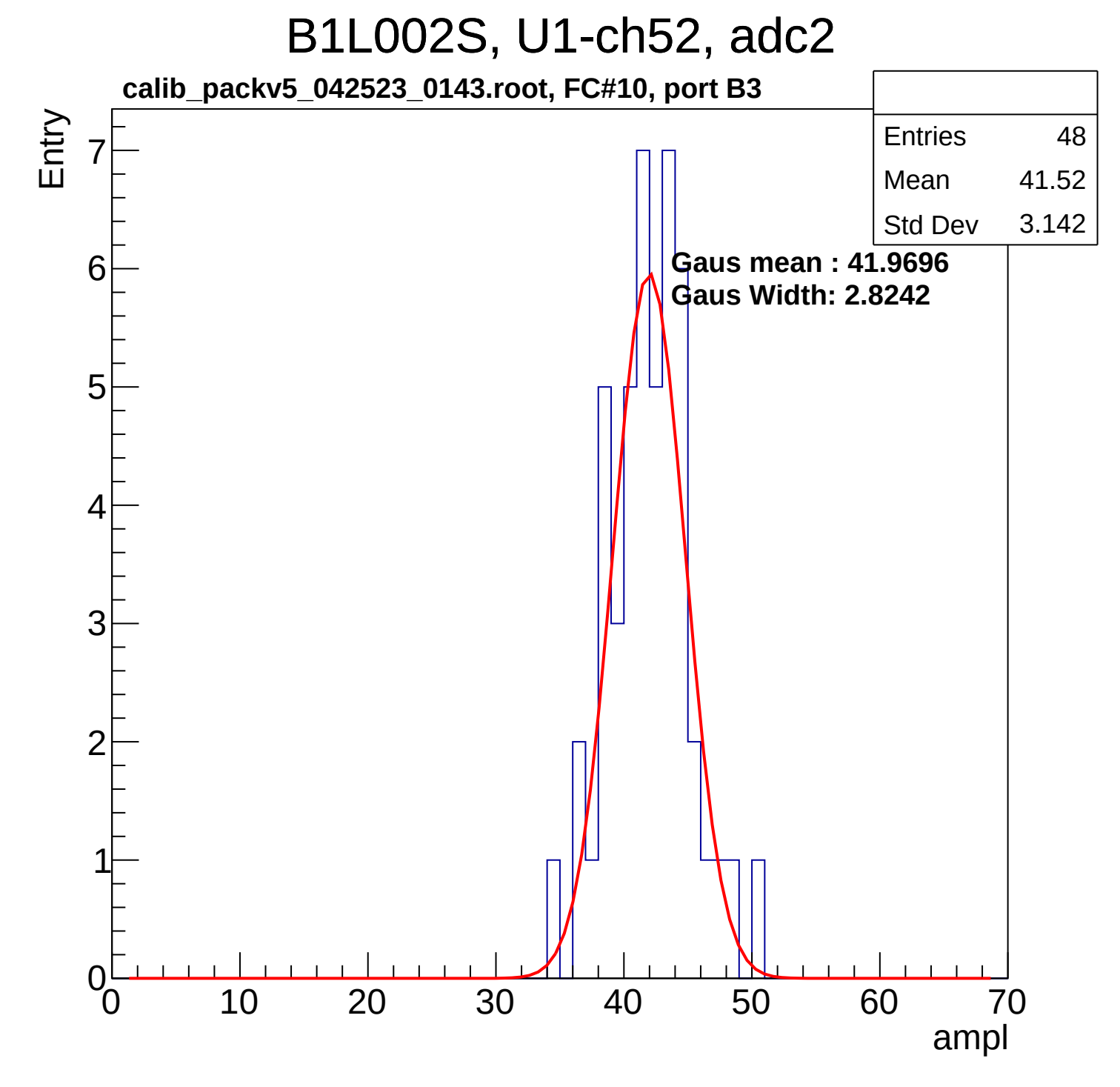
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U1-ch52, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 29.91
Std Dev 2.368
Gaus mean : 30.6332
Gaus Width: 2.8783
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch52, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.96
Std Dev 3.411
Gaus mean : 36.4003
Gaus Width: 3.8166
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch52, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.52
Std Dev 3.142
Gaus mean : 41.9696
Gaus Width: 2.8242
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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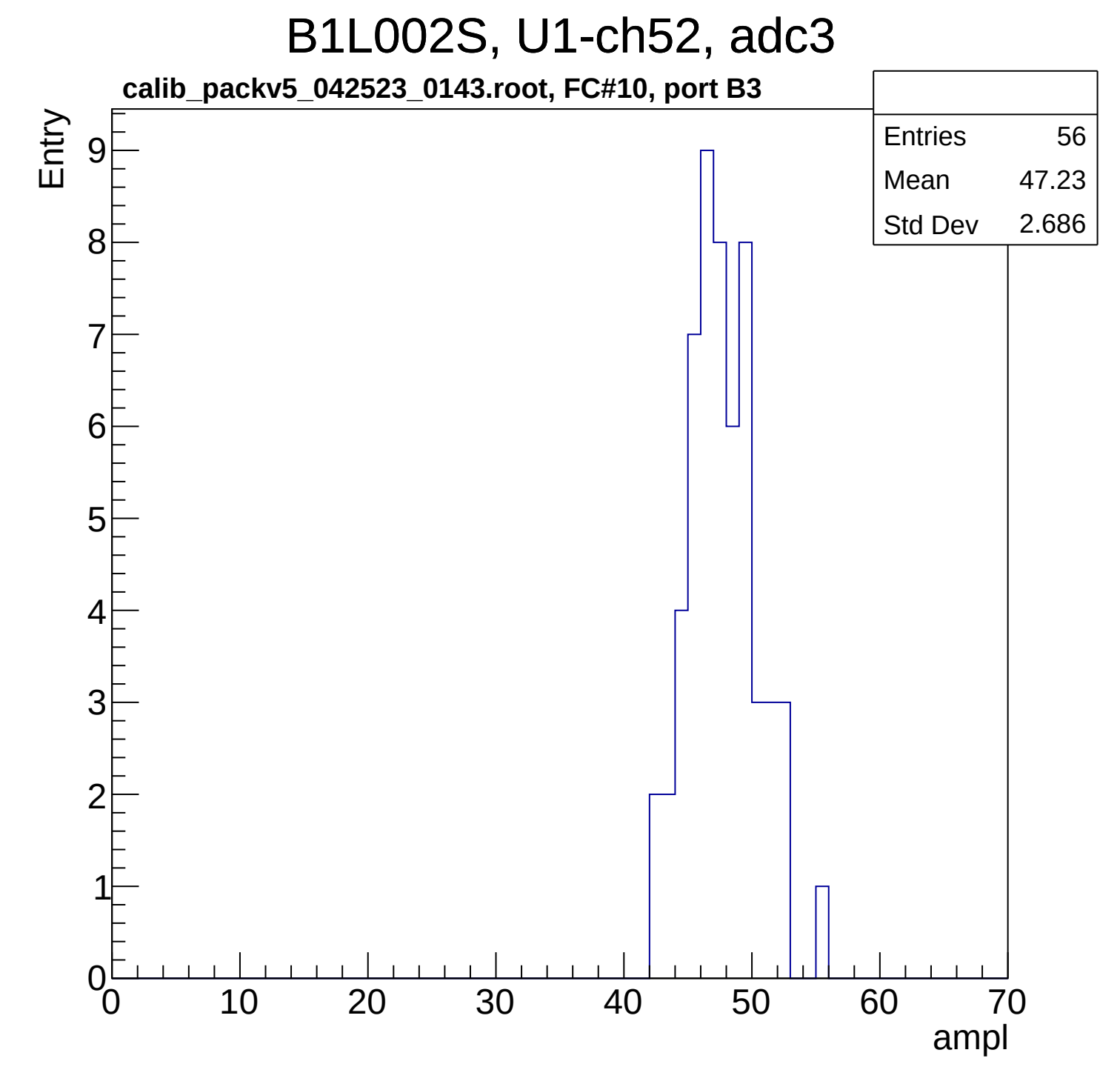
B1L002S, U1-ch52, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 47.23
Std Dev 2.686
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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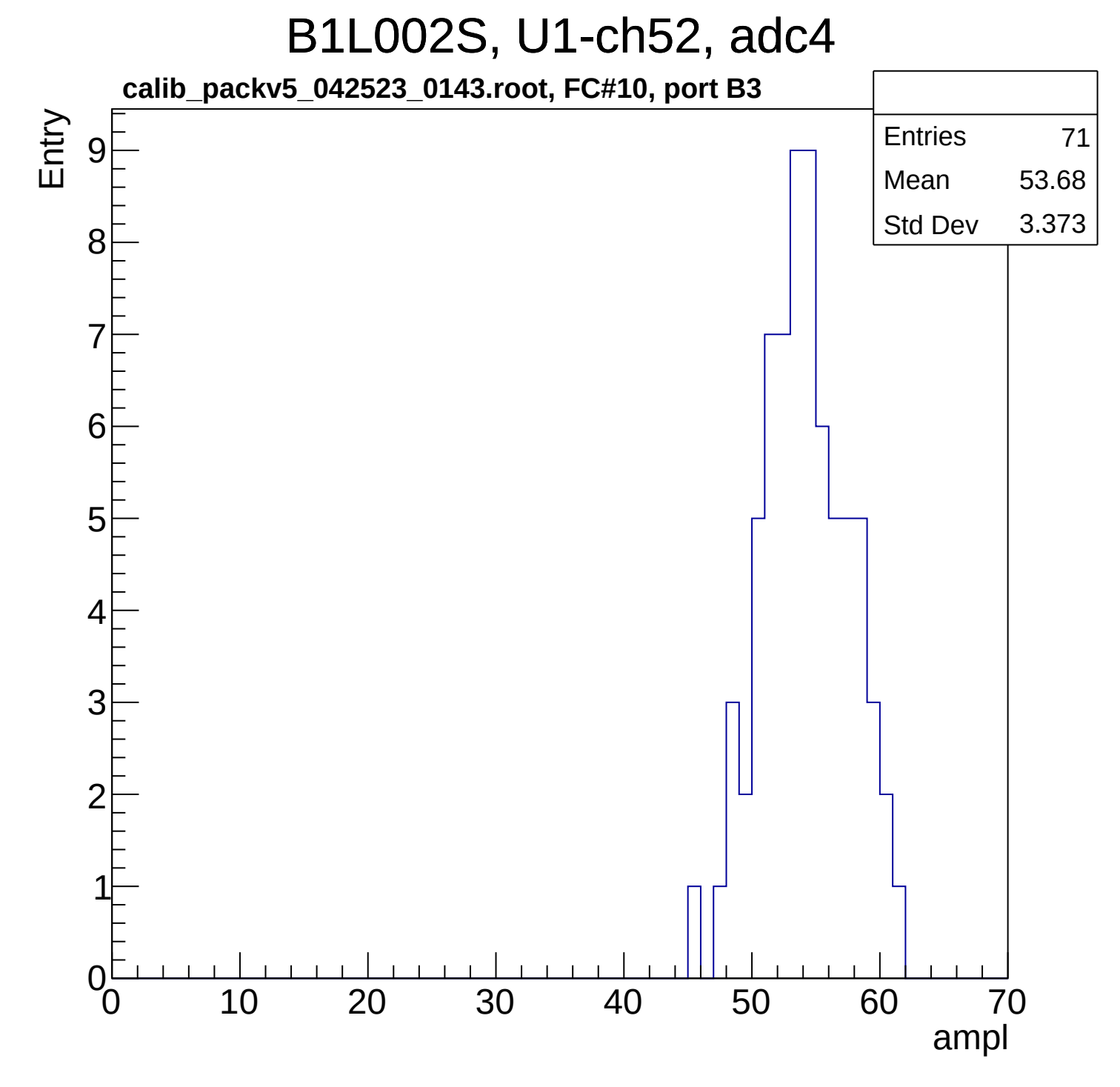
B1L002S, U1-ch52, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 53.68
Std Dev 3.373
Entry
ampl
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3
4
5
6
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8
9
0
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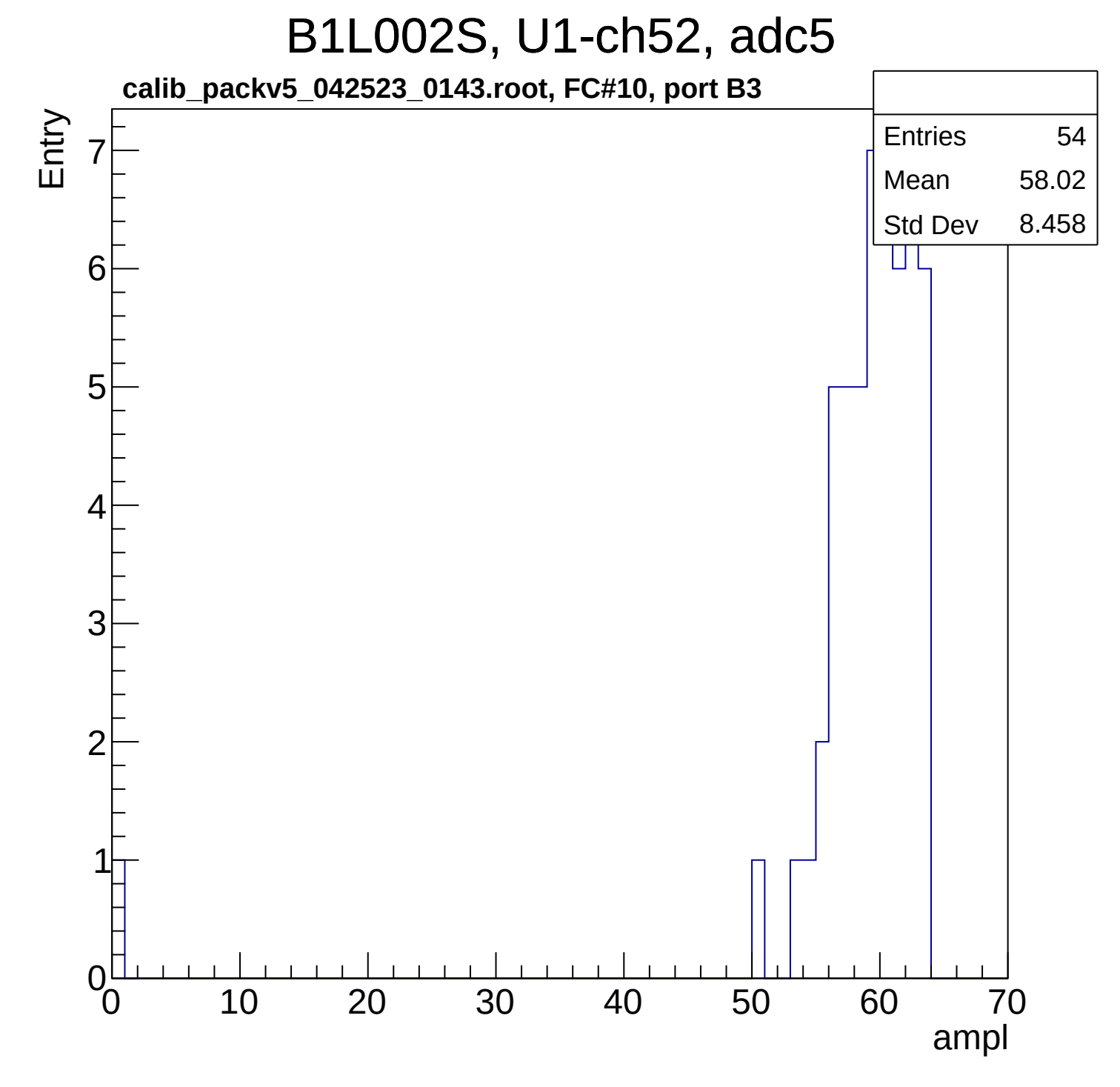
B1L002S, U1-ch52, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.02
Std Dev 8.458
Entry
ampl
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1
2
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0
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60
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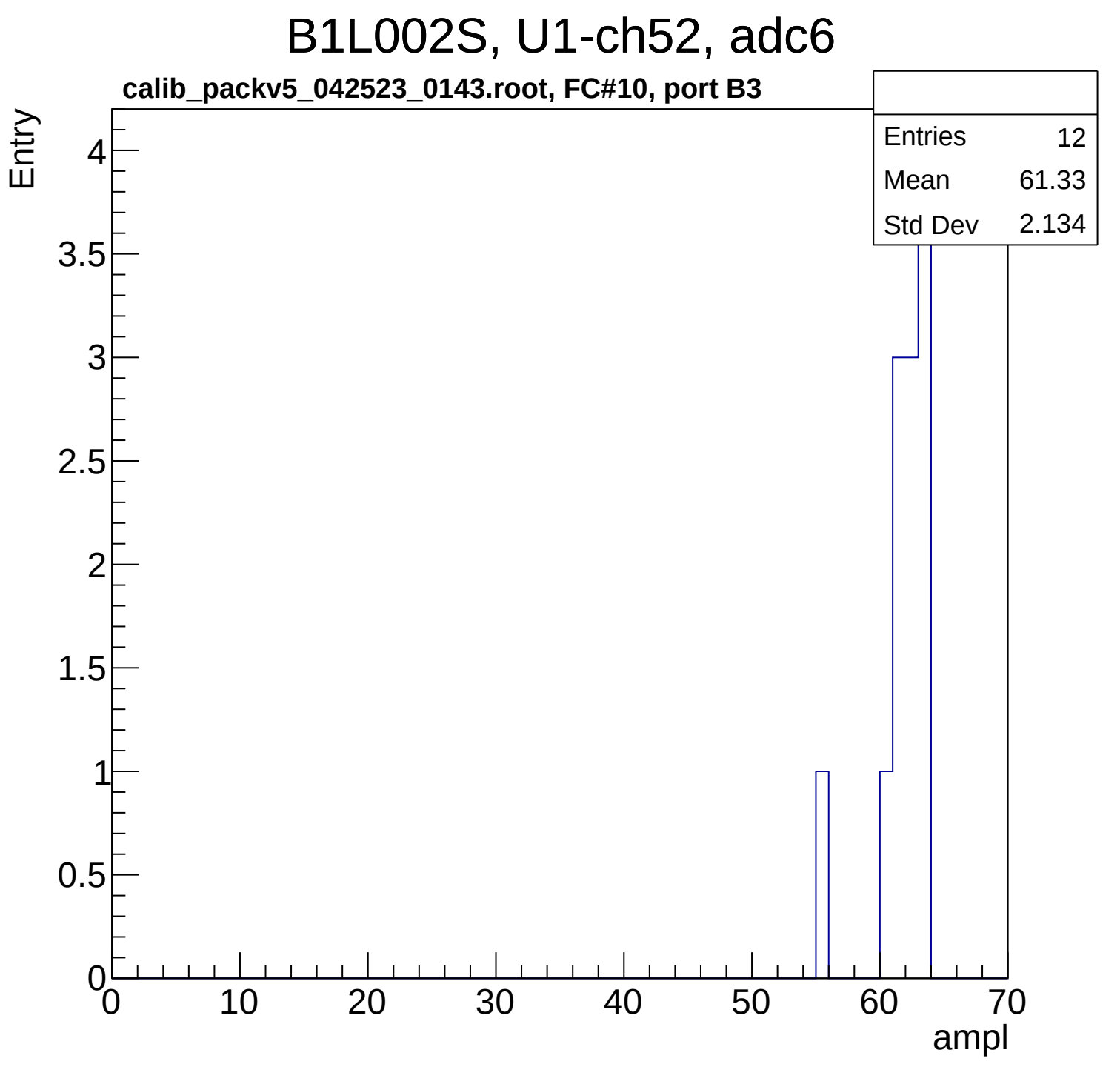
B1L002S, U1-ch52, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.33
Std Dev 2.134
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
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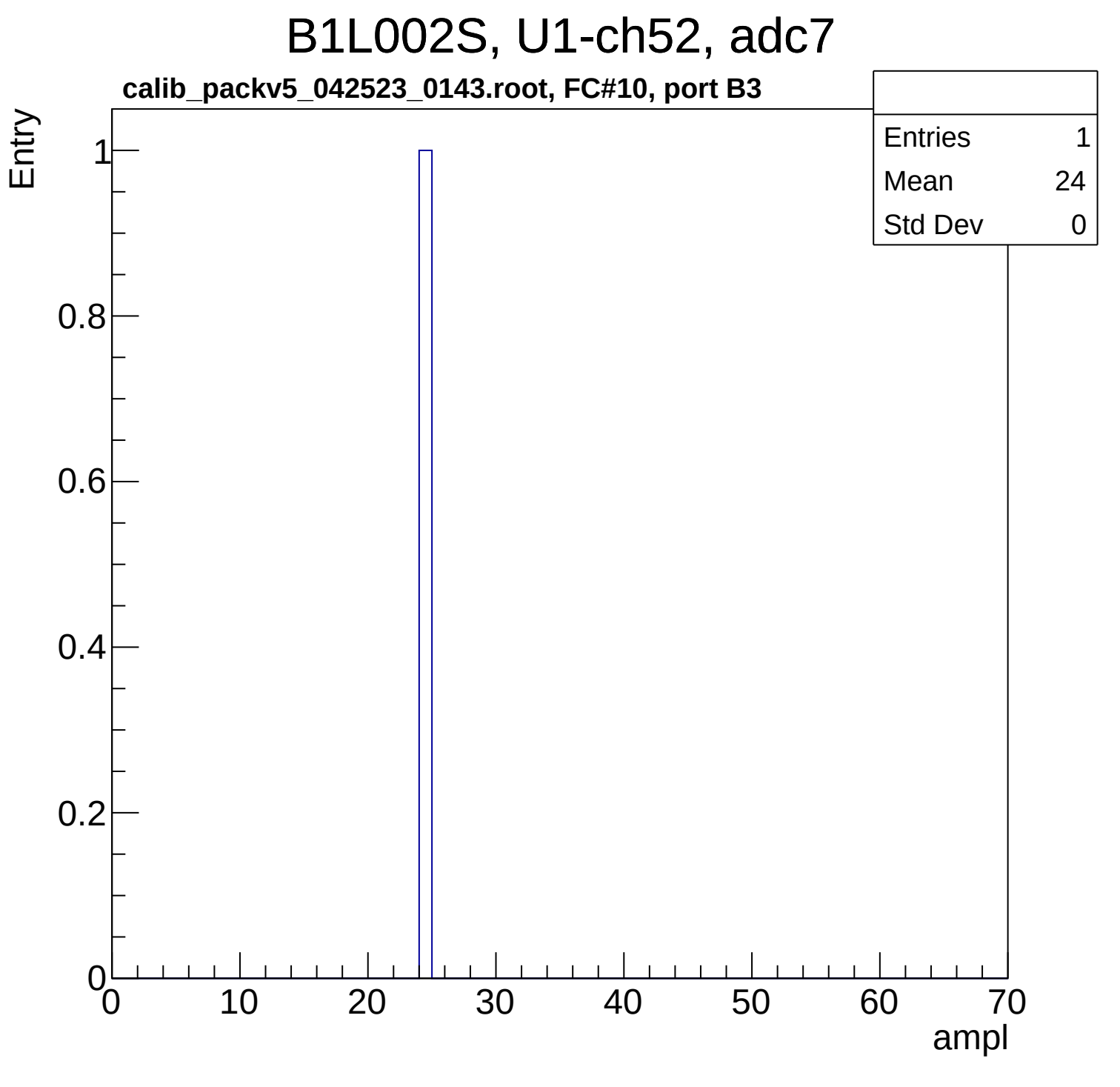
60
70


B1L002S, U1-ch52, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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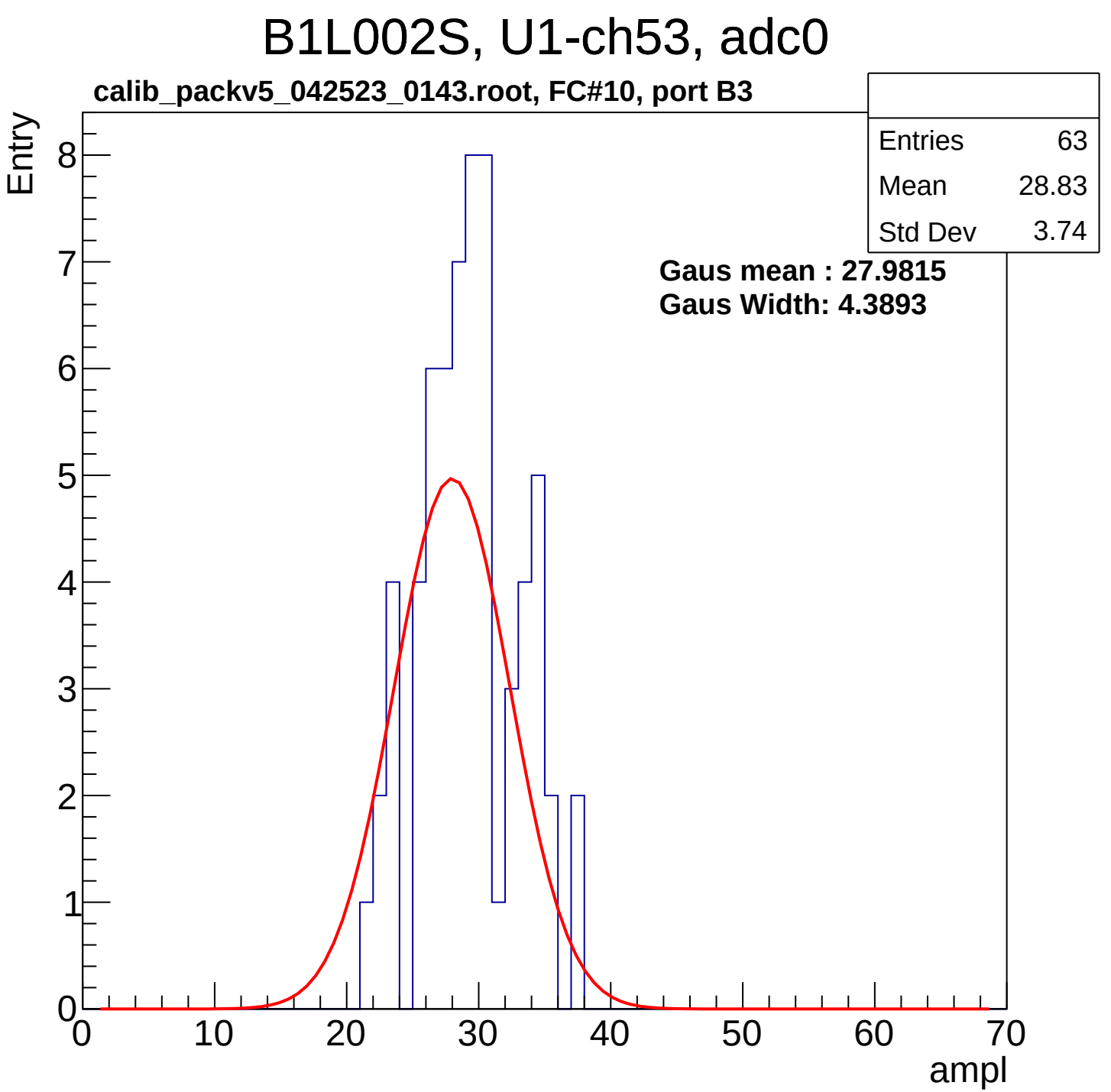
50
60
70
ampl


B1L002S, U1-ch53, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.83
Std Dev 3.74
Gaus mean : 27.9815
Gaus Width: 4.3893
Entry
ampl
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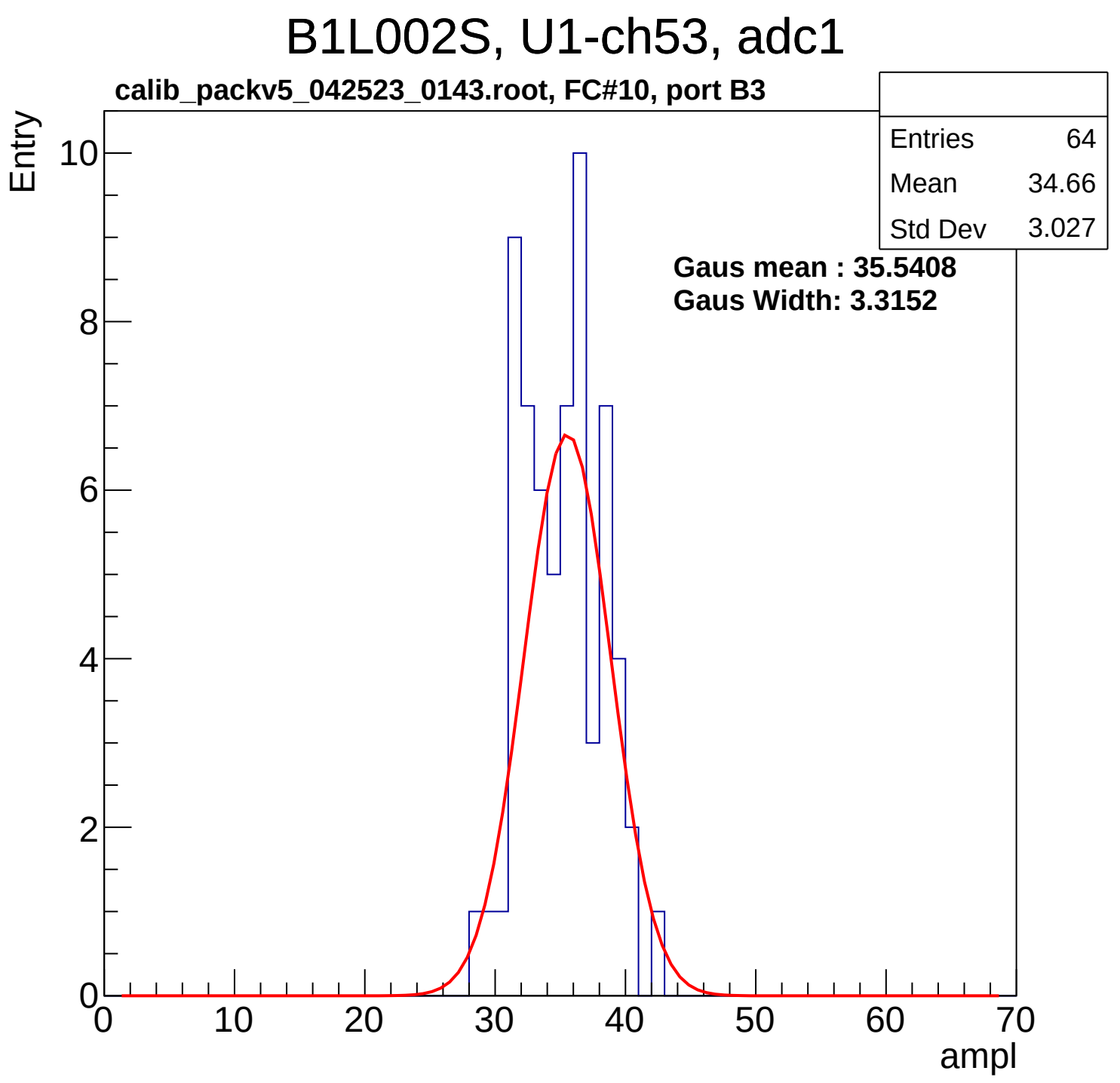
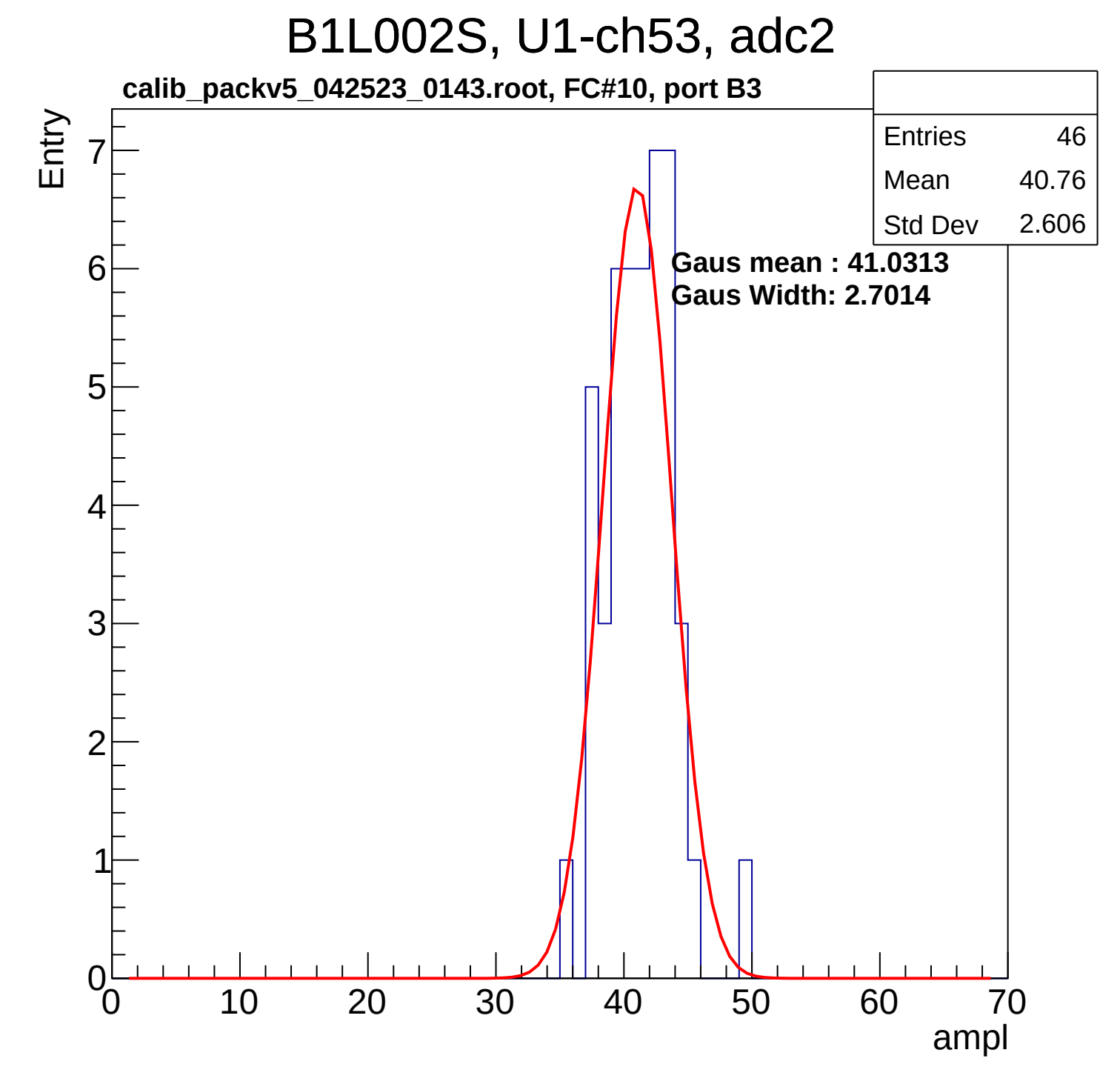
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch53, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.66
Std Dev 3.027
Gaus mean : 35.5408
Gaus Width: 3.3152
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch53, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 40.76
Std Dev 2.606
Gaus mean : 41.0313
Gaus Width: 2.7014
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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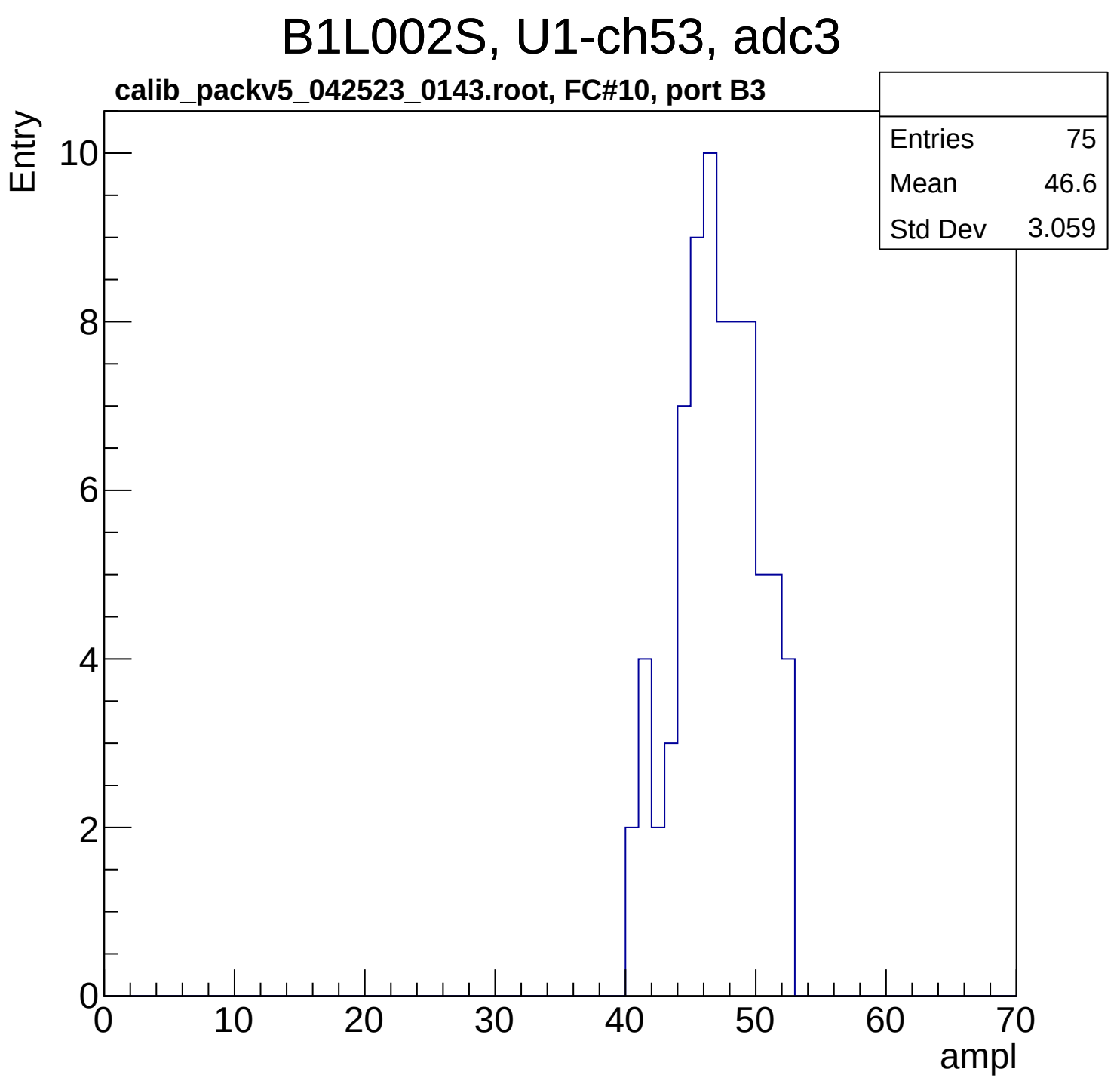
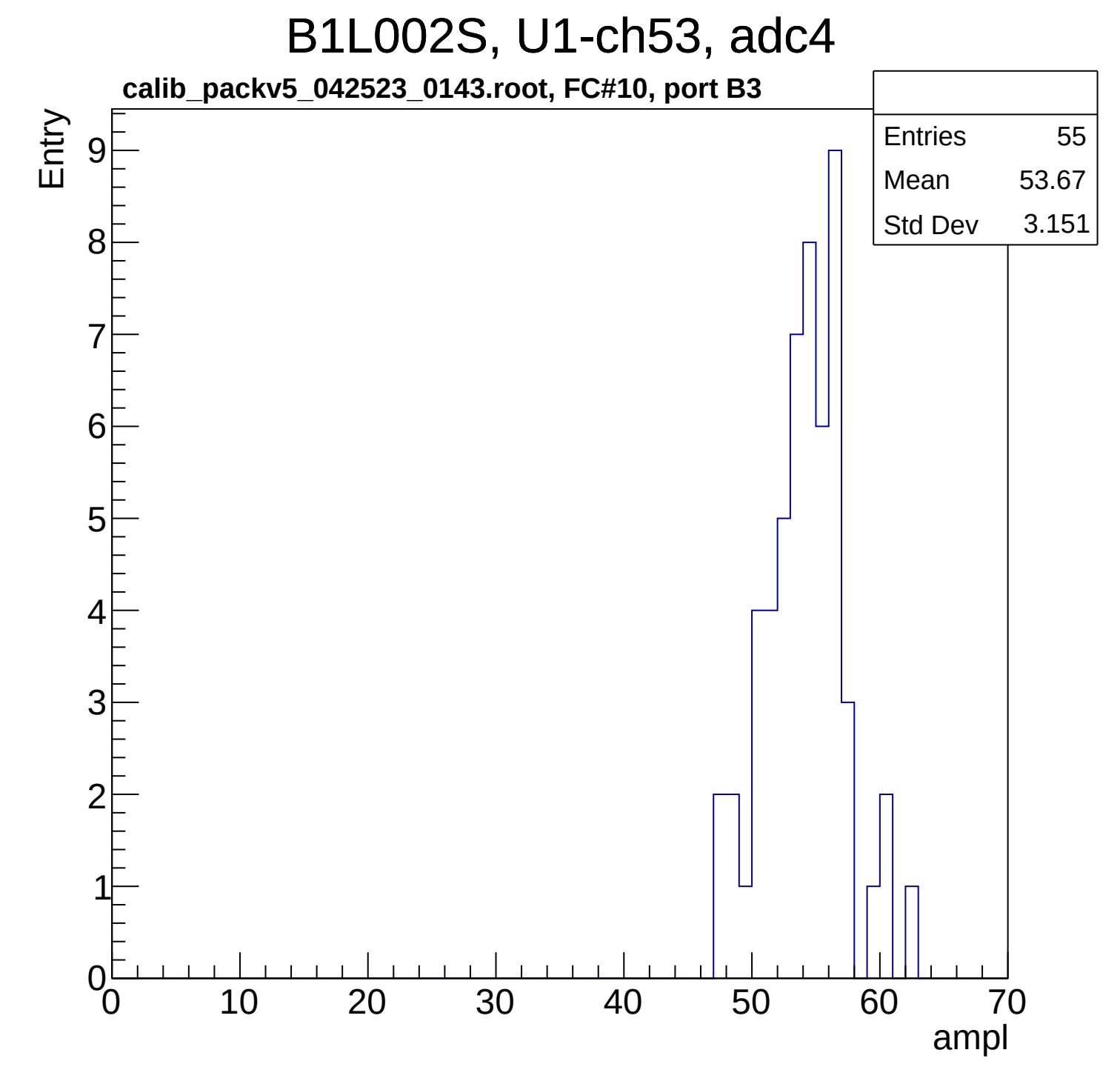

B1L002S, U1-ch53, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 46.6
Std Dev 3.059
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch53, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.67
Std Dev 3.151
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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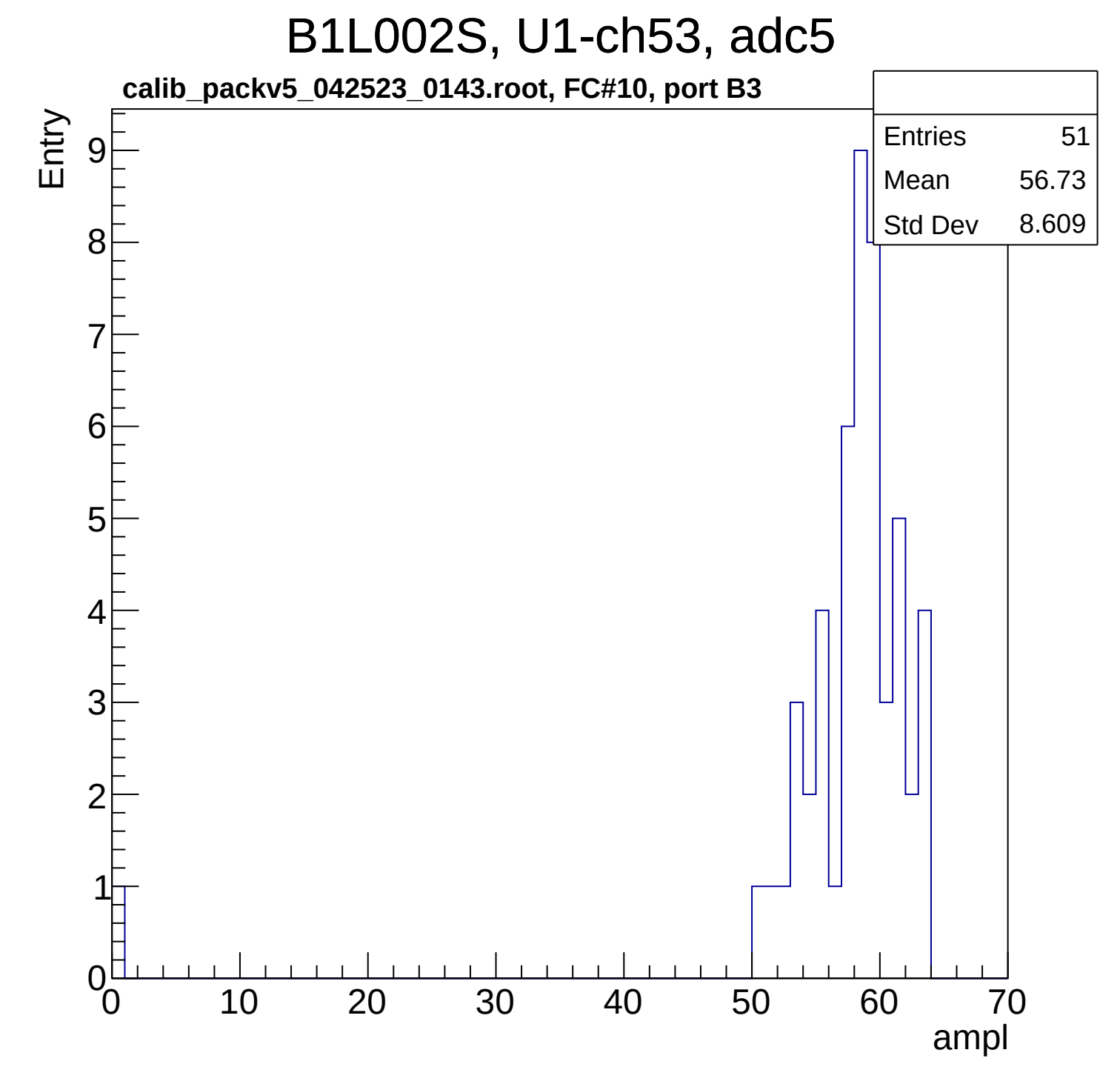
B1L002S, U1-ch53, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 56.73
Std Dev 8.609
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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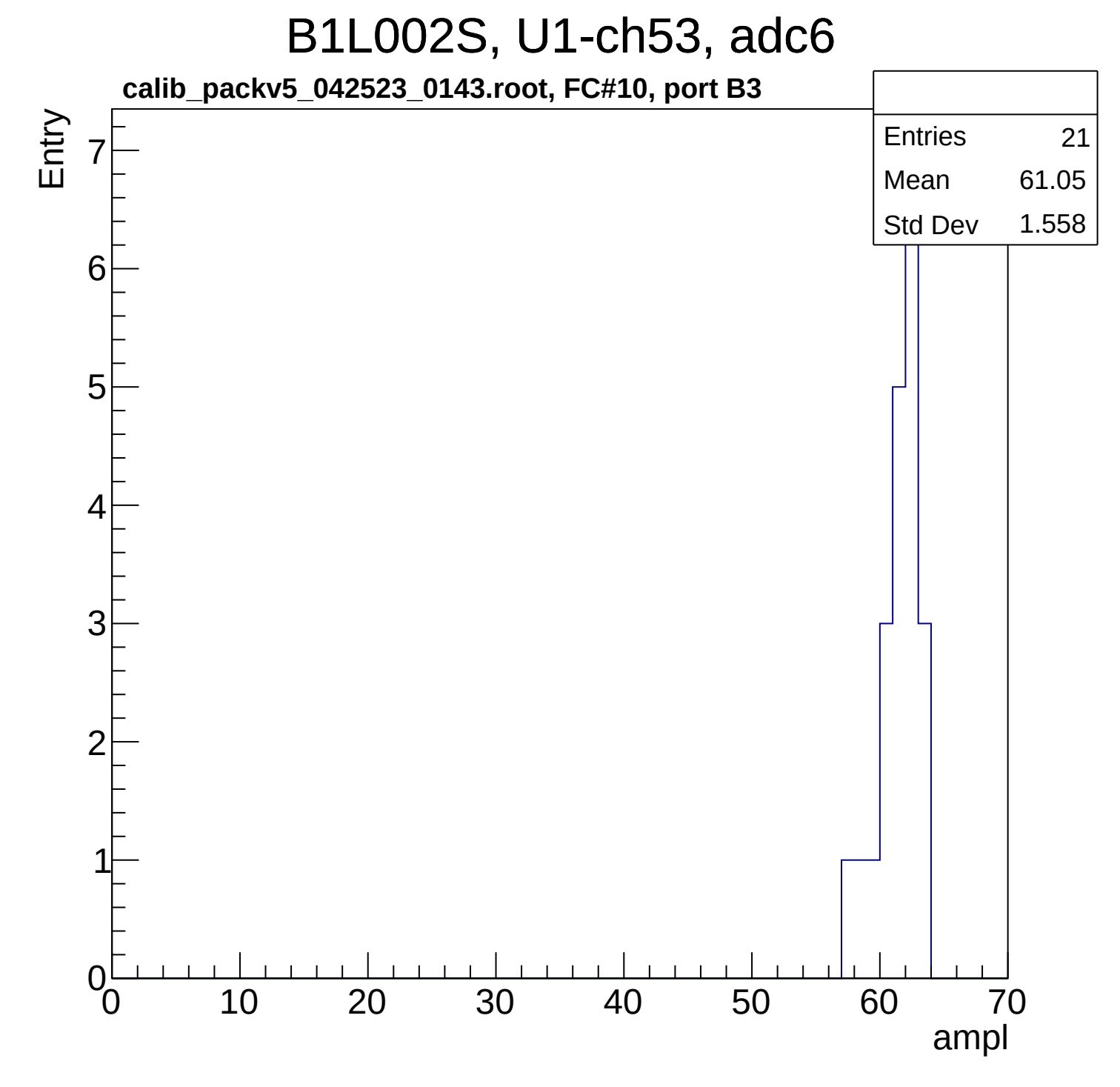
B1L002S, U1-ch53, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.05
Std Dev 1.558
Entry
ampl
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1
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3
4
5
6
7
0
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40
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60
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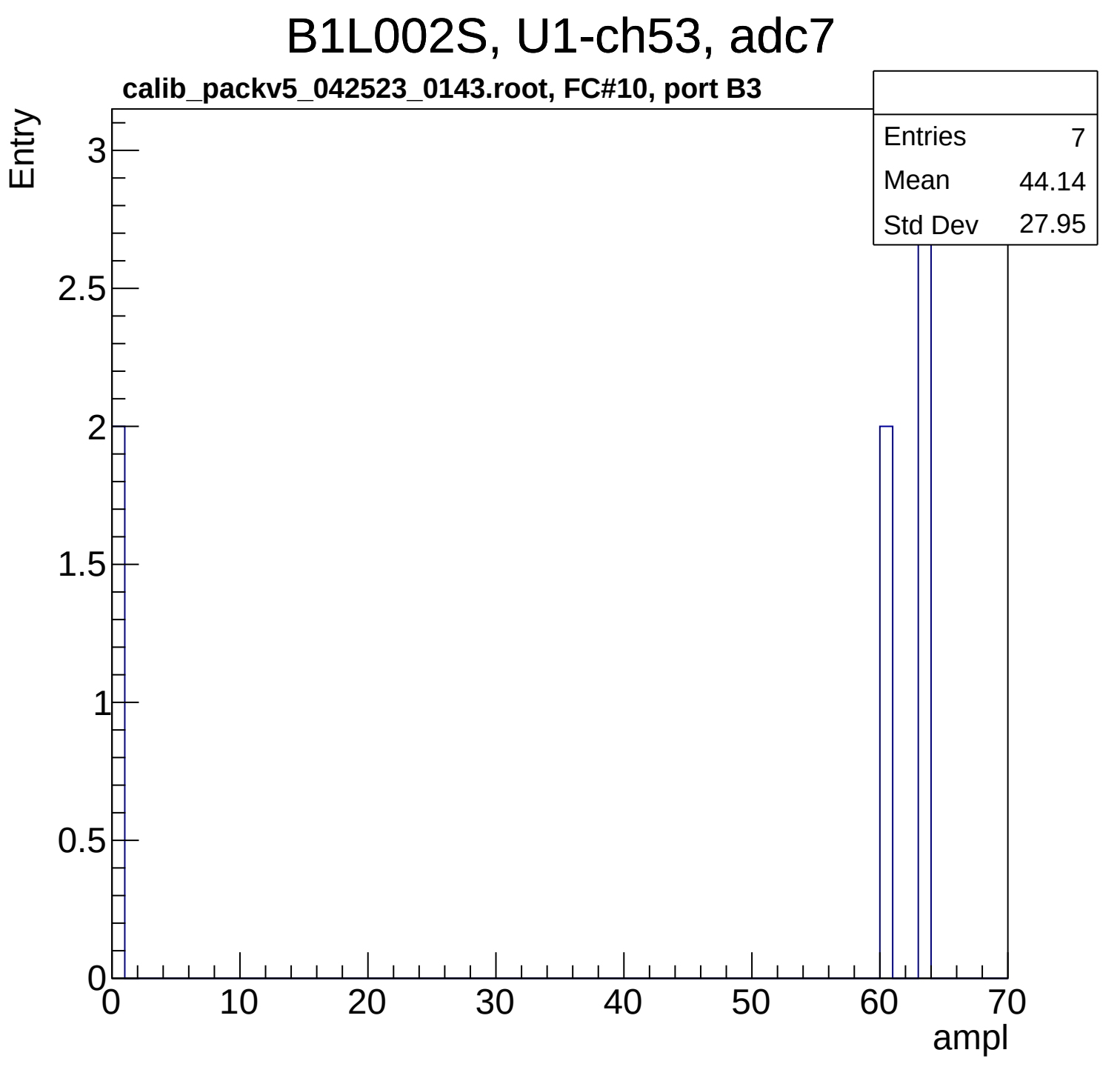
B1L002S, U1-ch53, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 44.14
Std Dev 27.95
Entry
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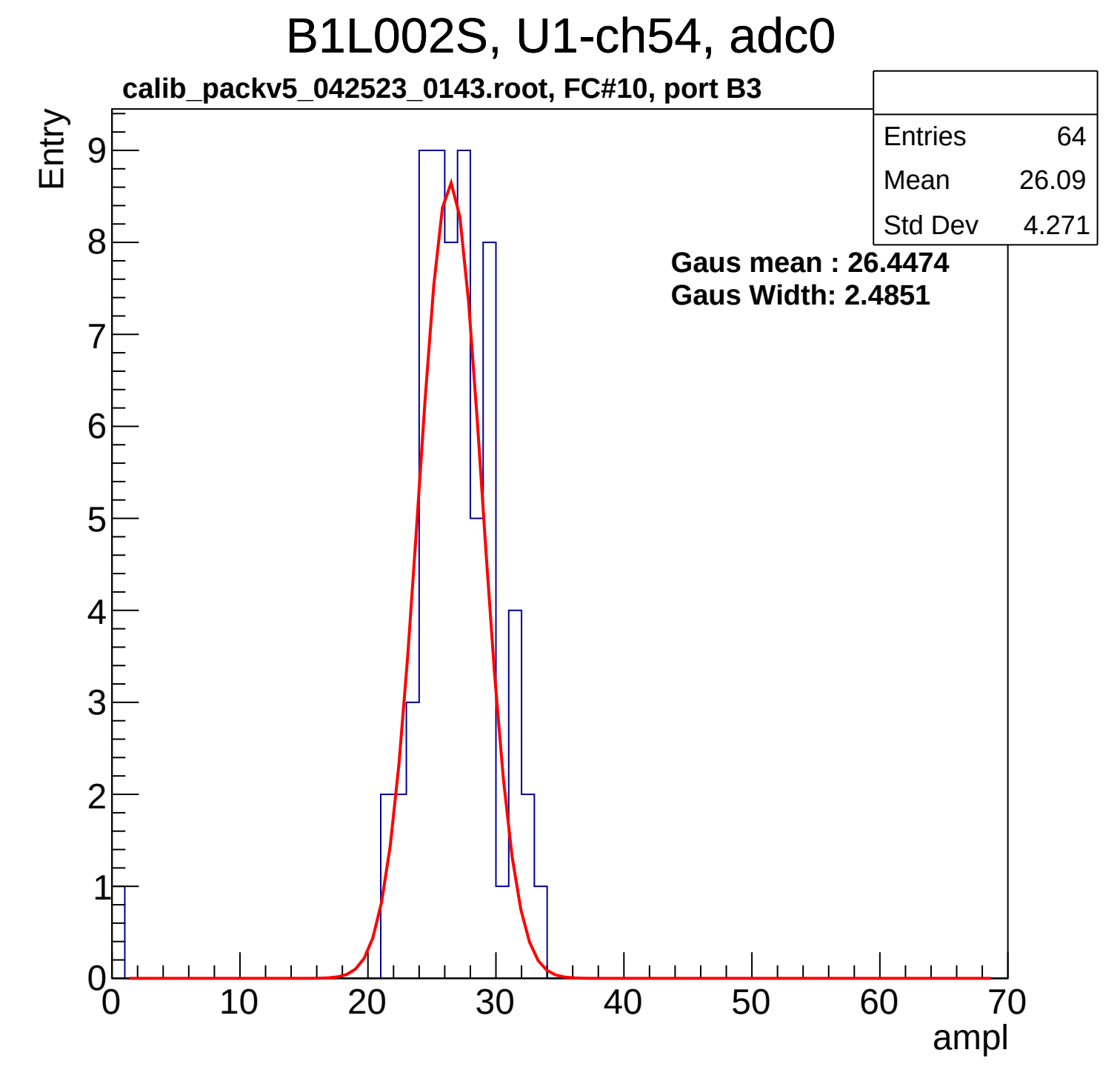
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch54, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 26.09
Std Dev 4.271
Gaus mean : 26.4474
Gaus Width: 2.4851
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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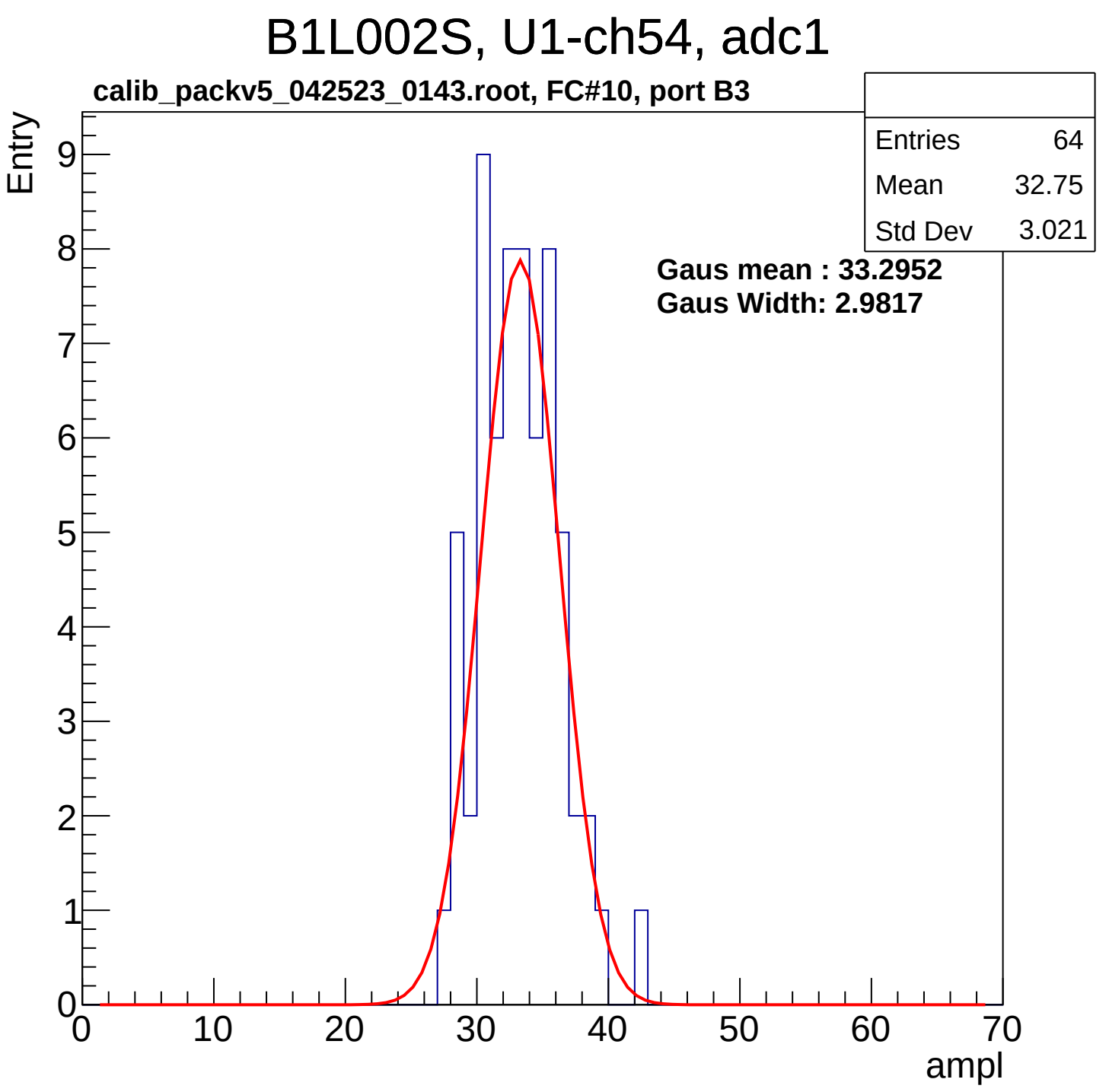
B1L002S, U1-ch54, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 32.75
Std Dev 3.021
Gaus mean : 33.2952
Gaus Width: 2.9817
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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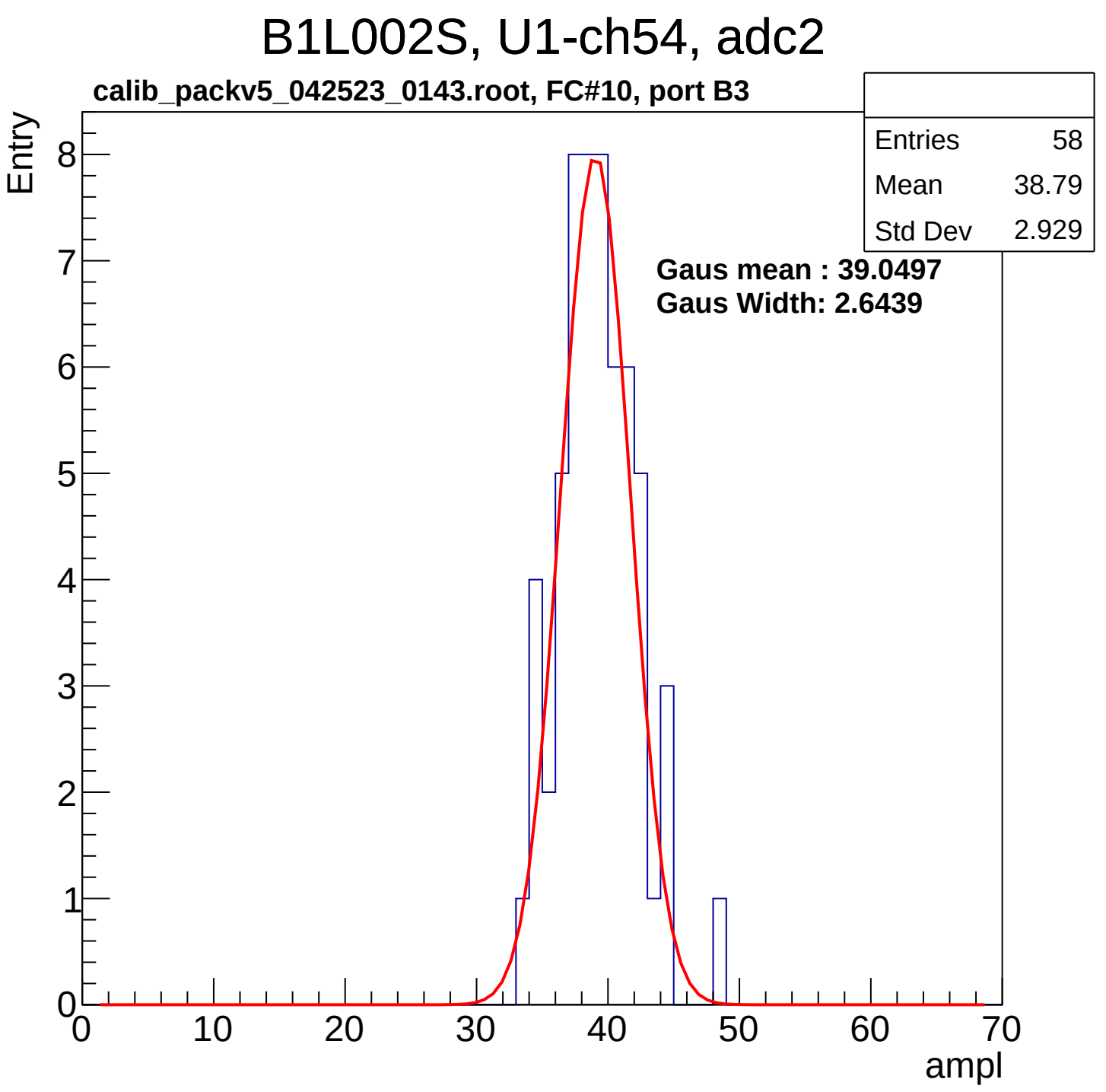
B1L002S, U1-ch54, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 38.79
Std Dev 2.929
Gaus mean : 39.0497
Gaus Width: 2.6439
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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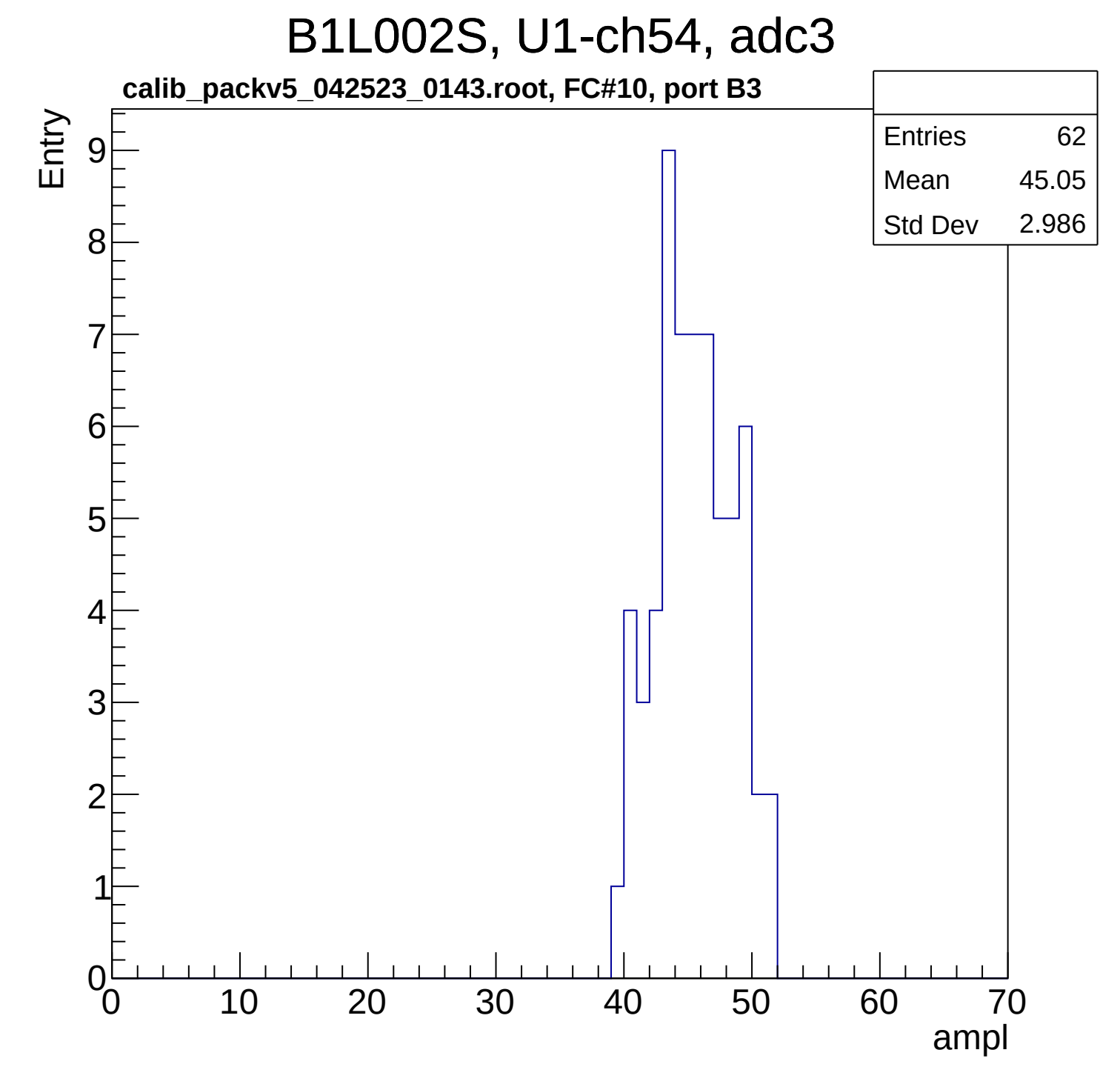
B1L002S, U1-ch54, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 45.05
Std Dev 2.986
Entry
ampl
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2
3
4
5
6
7
8
9
0
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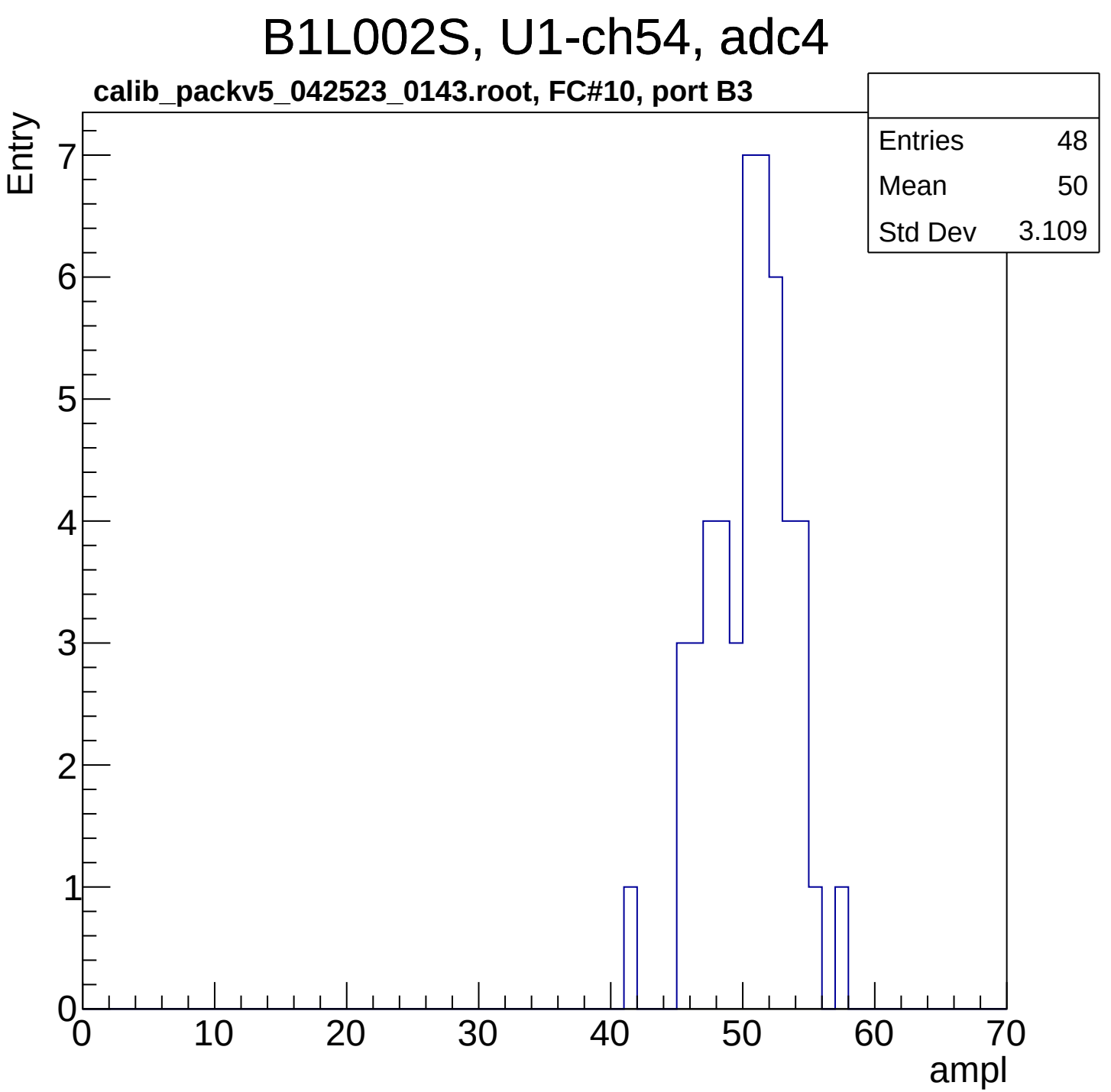
B1L002S, U1-ch54, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 50
Std Dev 3.109
Entry
ampl
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2
3
4
5
6
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0
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20
30
40
50
60
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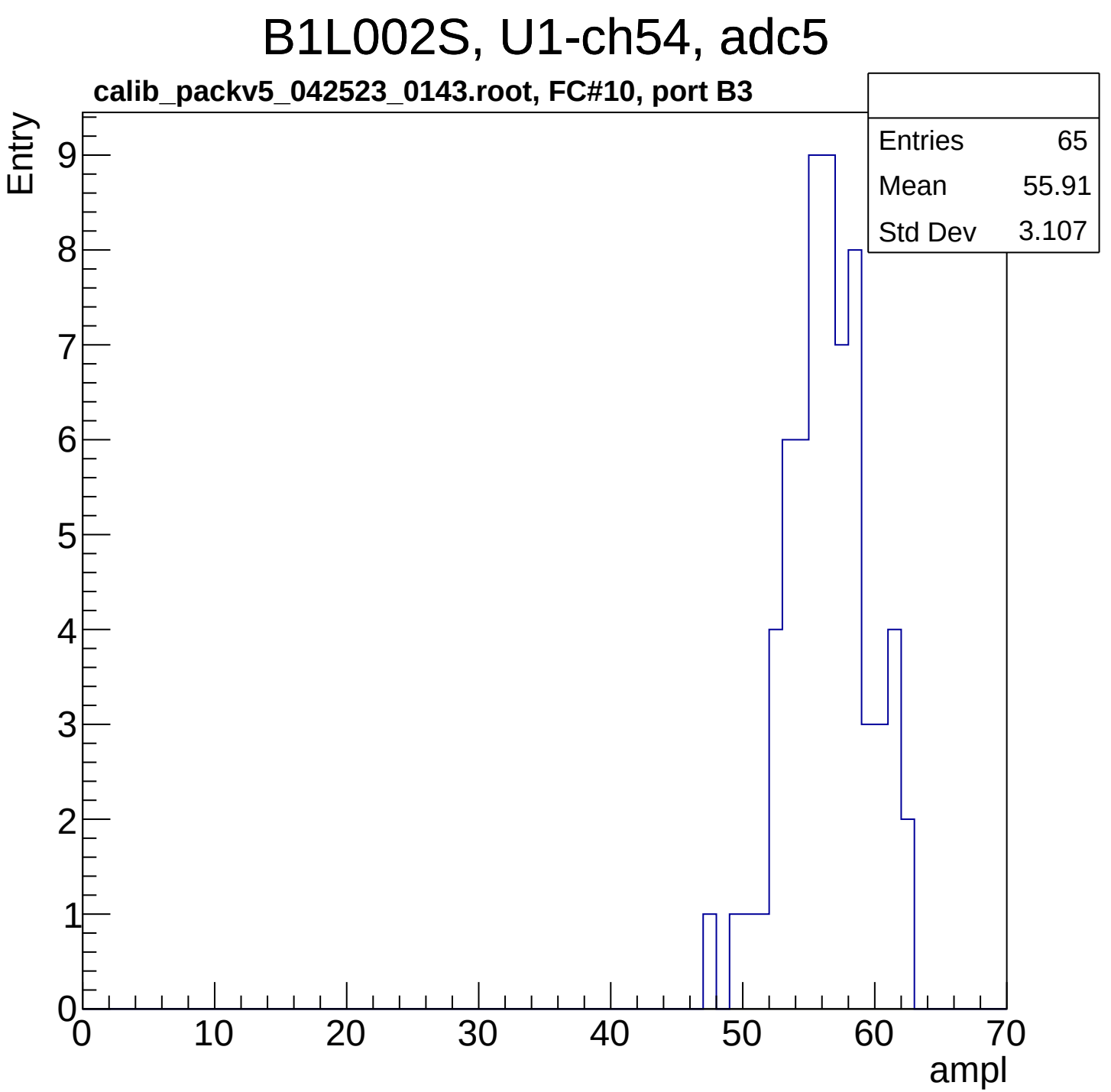
B1L002S, U1-ch54, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 55.91
Std Dev 3.107
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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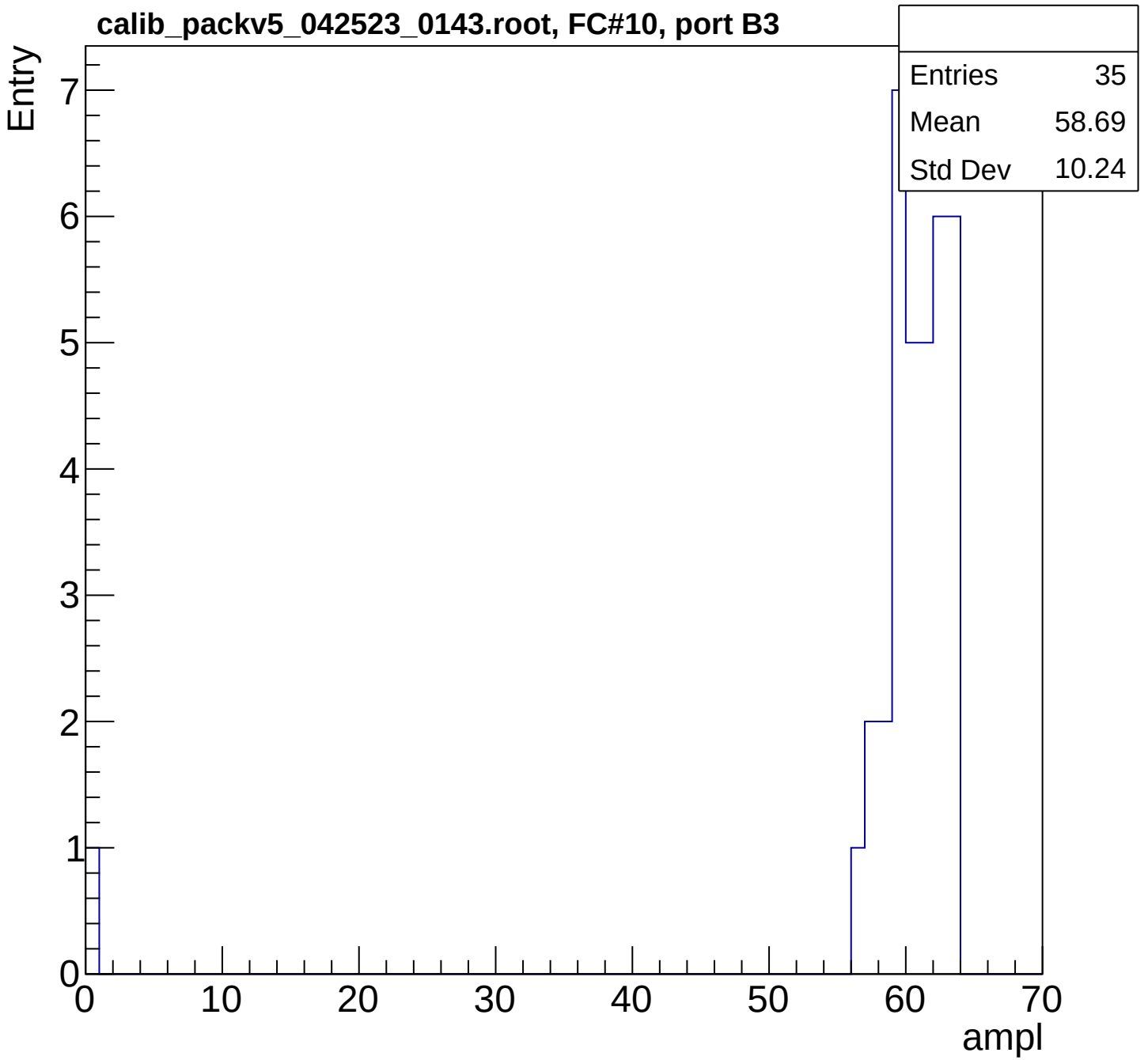
B1L002S, U1-ch54, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 58.69
Std Dev 10.24
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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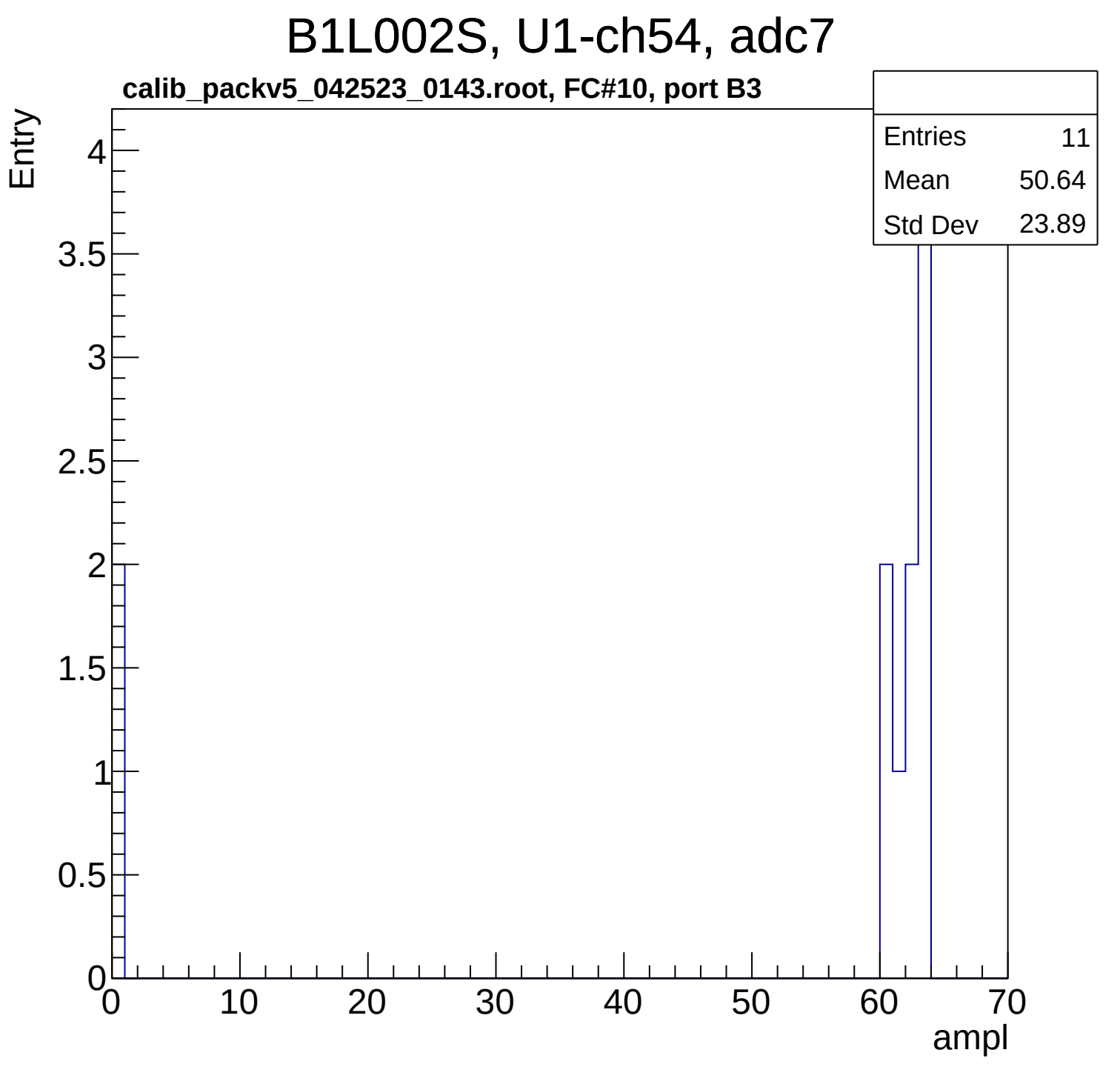
B1L002S, U1-ch54, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 50.64
Std Dev 23.89
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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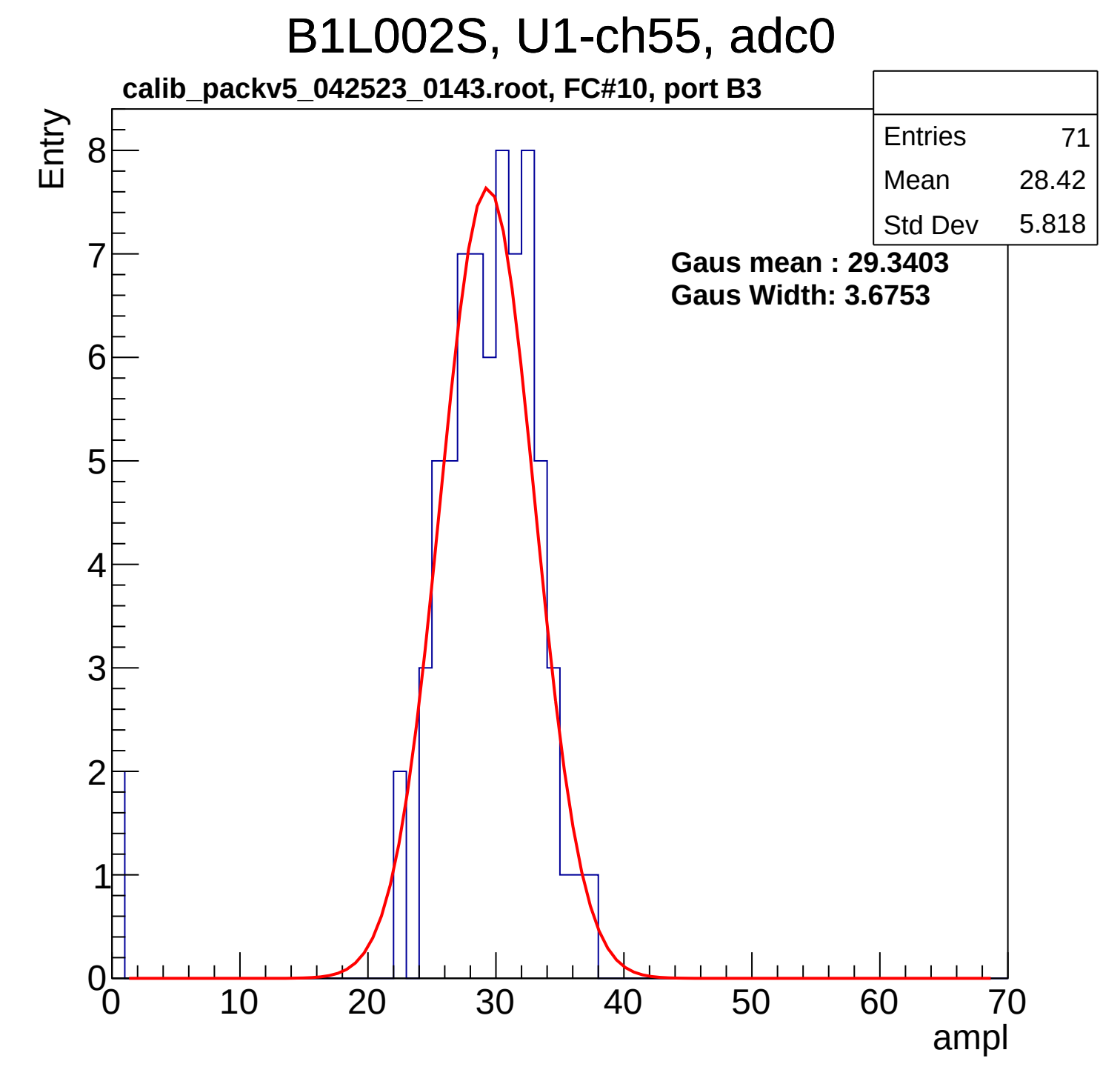
B1L002S, U1-ch55, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.42
Std Dev 5.818
Gaus mean : 29.3403
Gaus Width: 3.6753
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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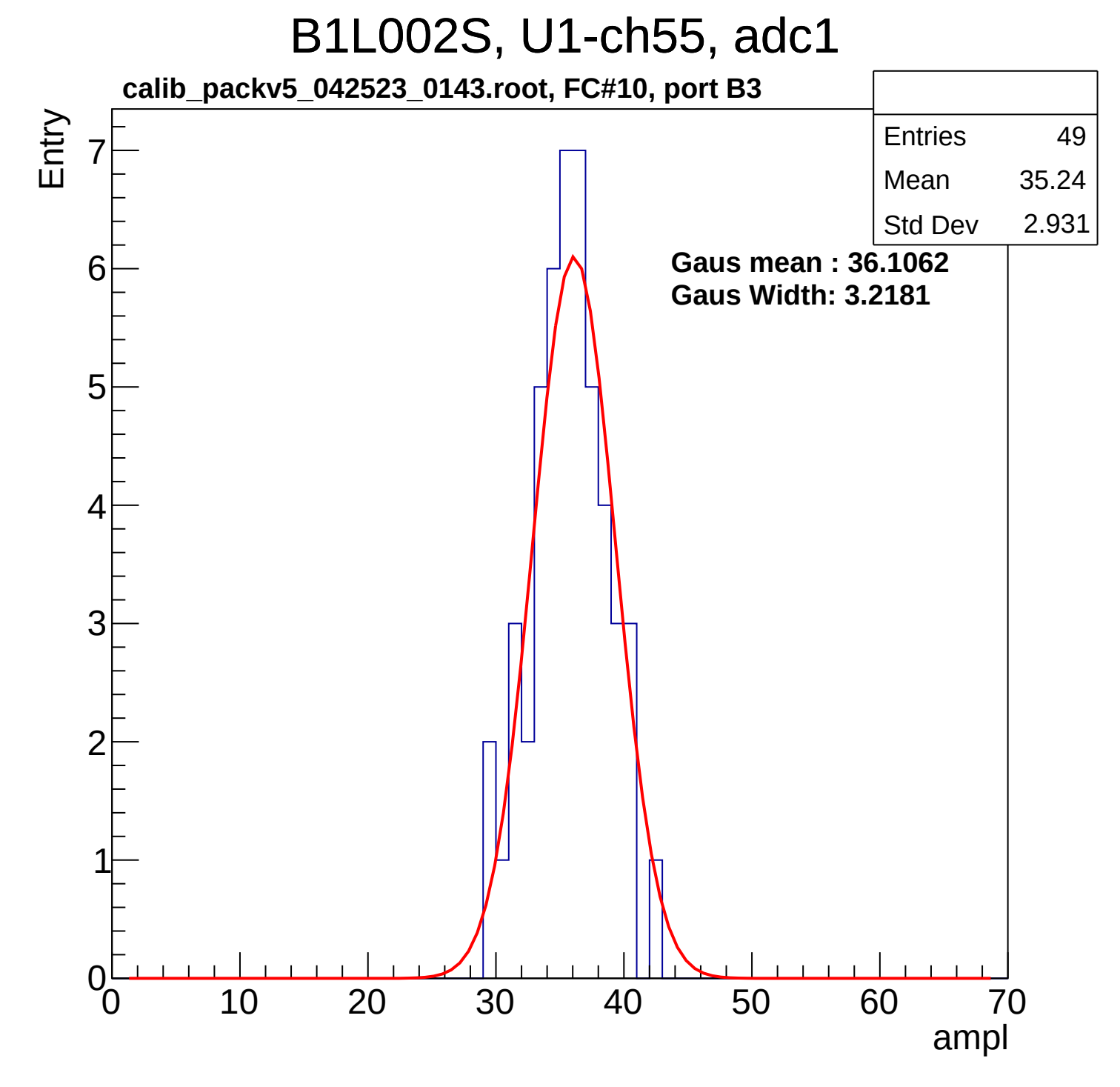
B1L002S, U1-ch55, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 35.24
Std Dev 2.931
Gaus mean : 36.1062
Gaus Width: 3.2181
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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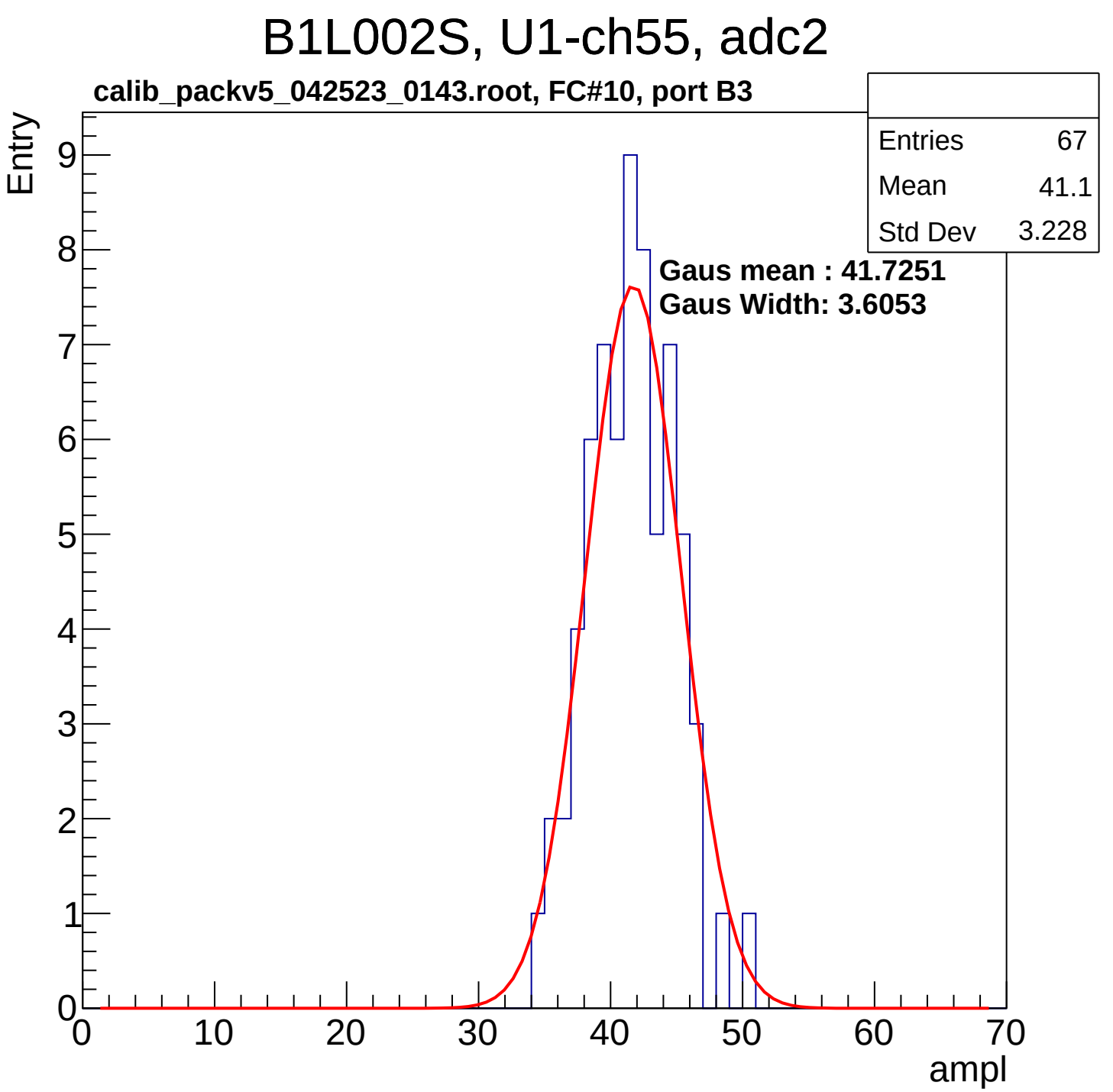
B1L002S, U1-ch55, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.1
Std Dev 3.228
Gaus mean : 41.7251
Gaus Width: 3.6053
Entry
ampl
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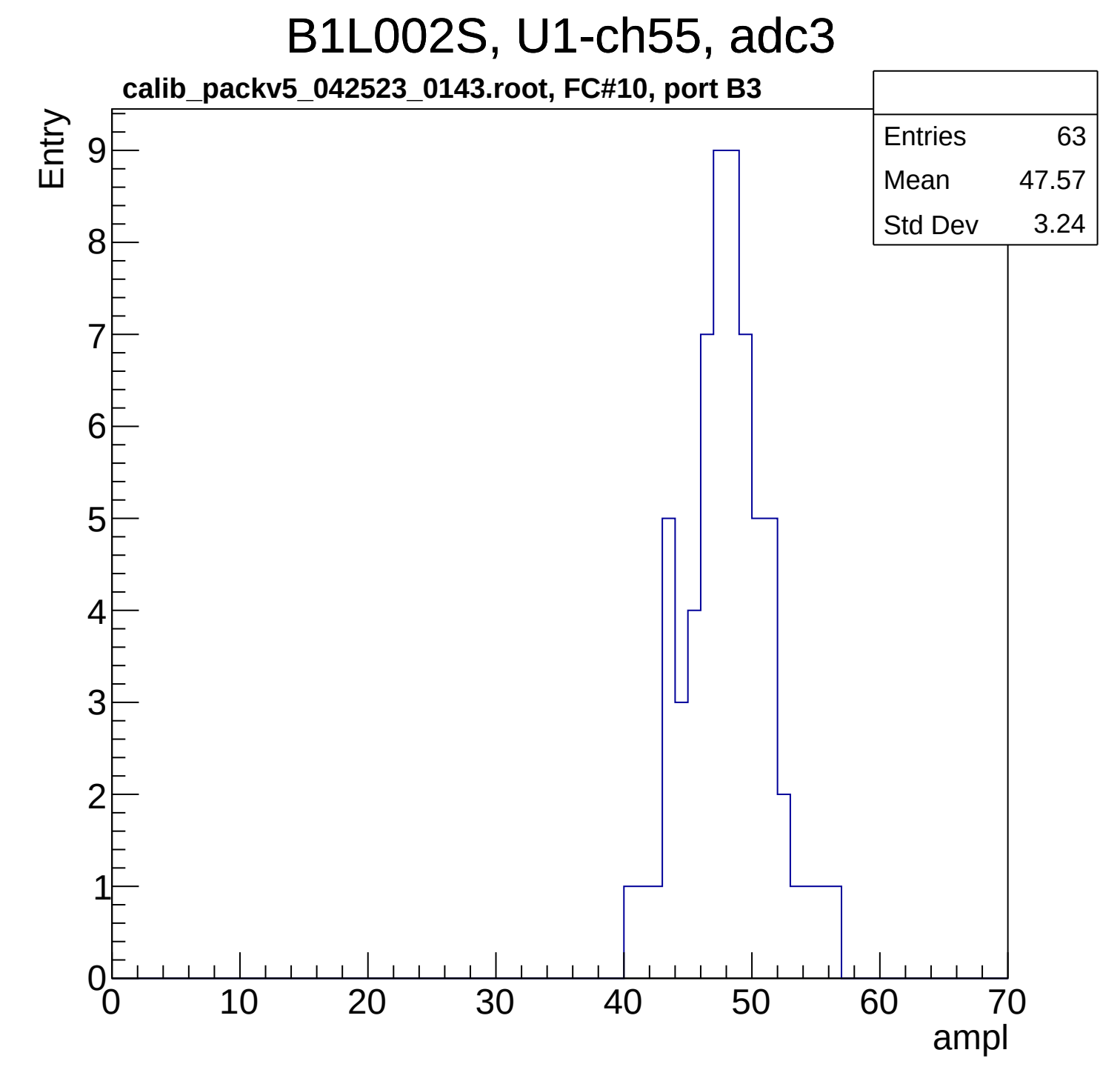
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch55, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 47.57
Std Dev 3.24
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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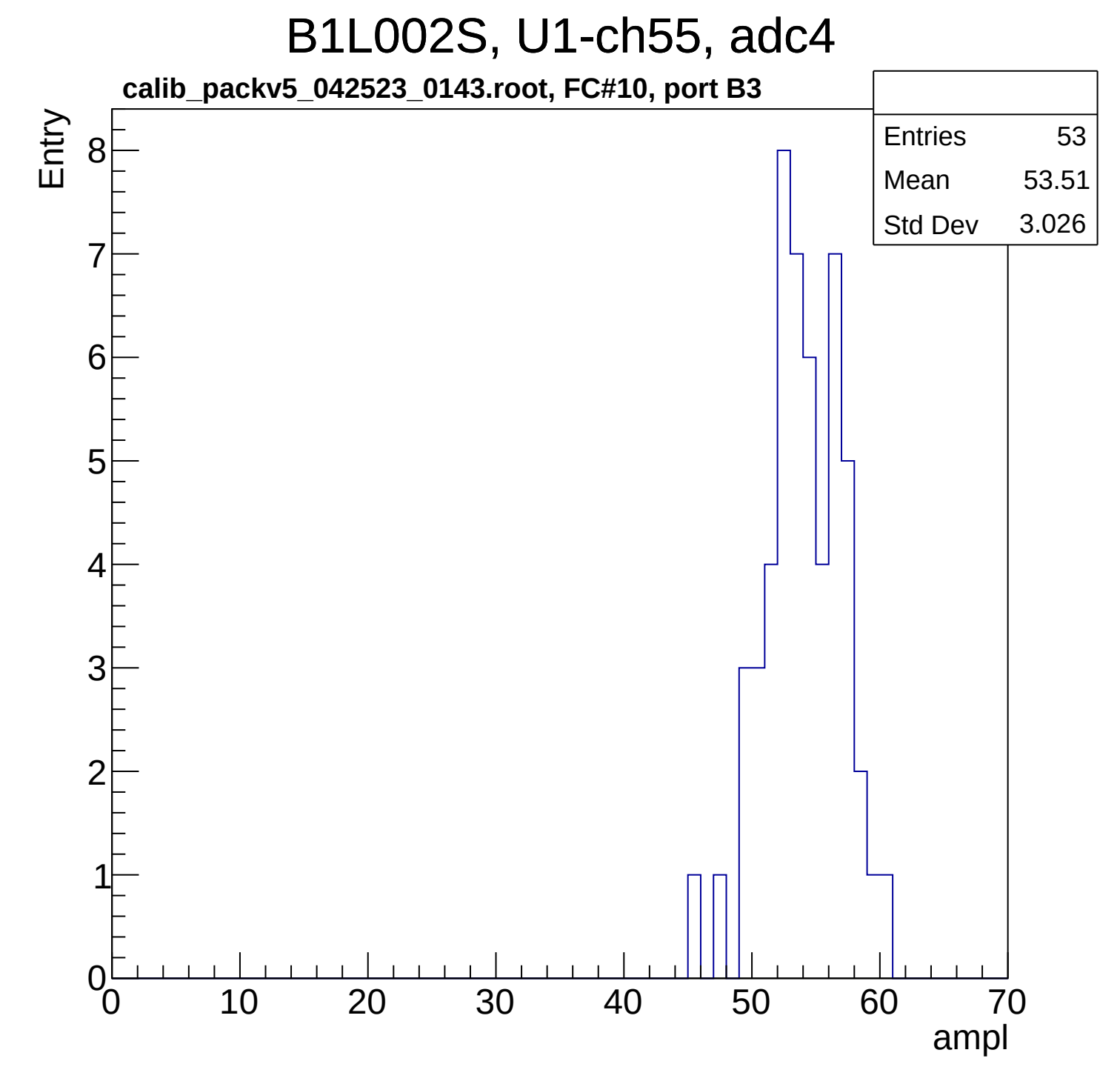
B1L002S, U1-ch55, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.51
Std Dev 3.026
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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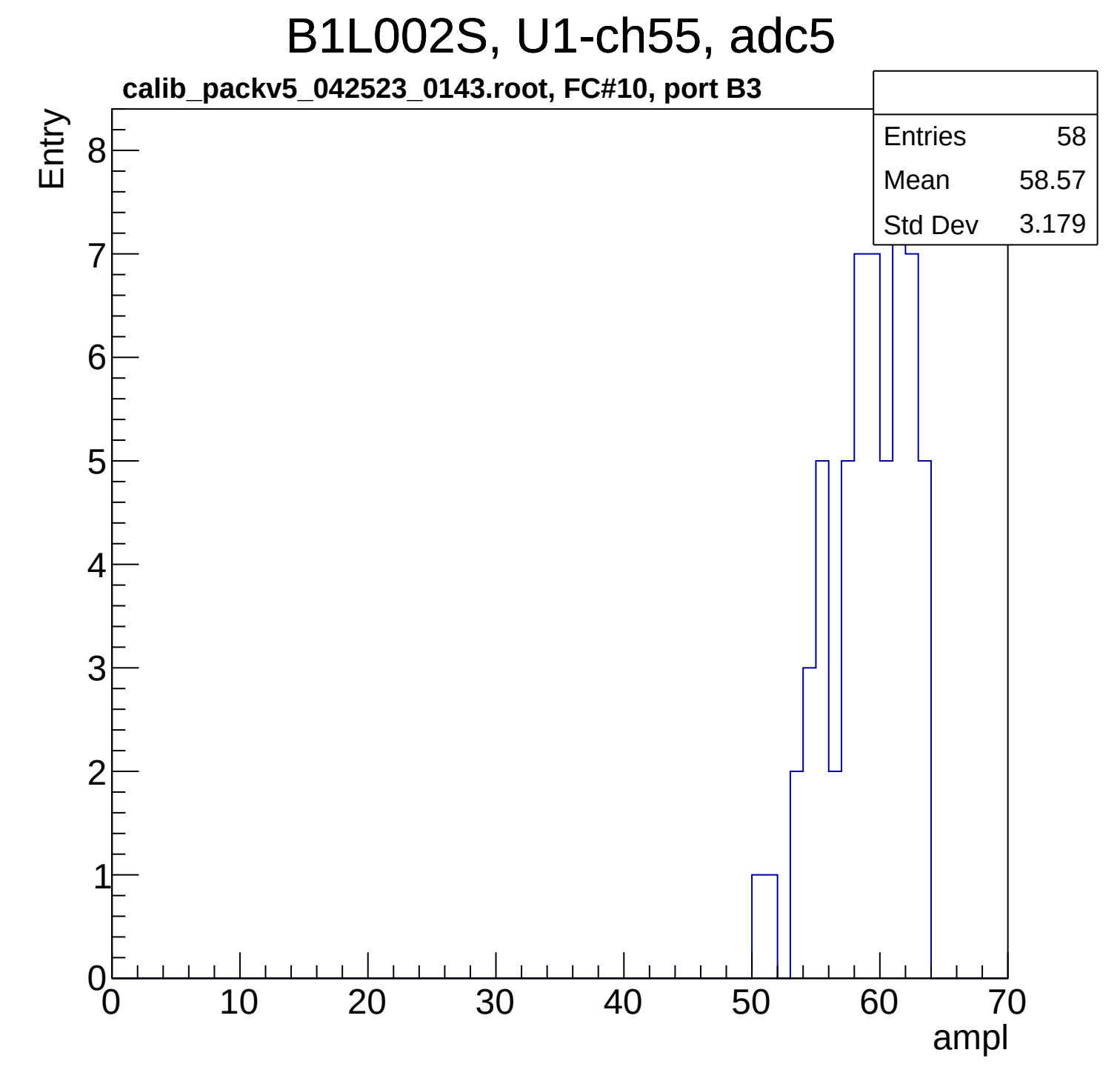
B1L002S, U1-ch55, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.57
Std Dev 3.179
Entry
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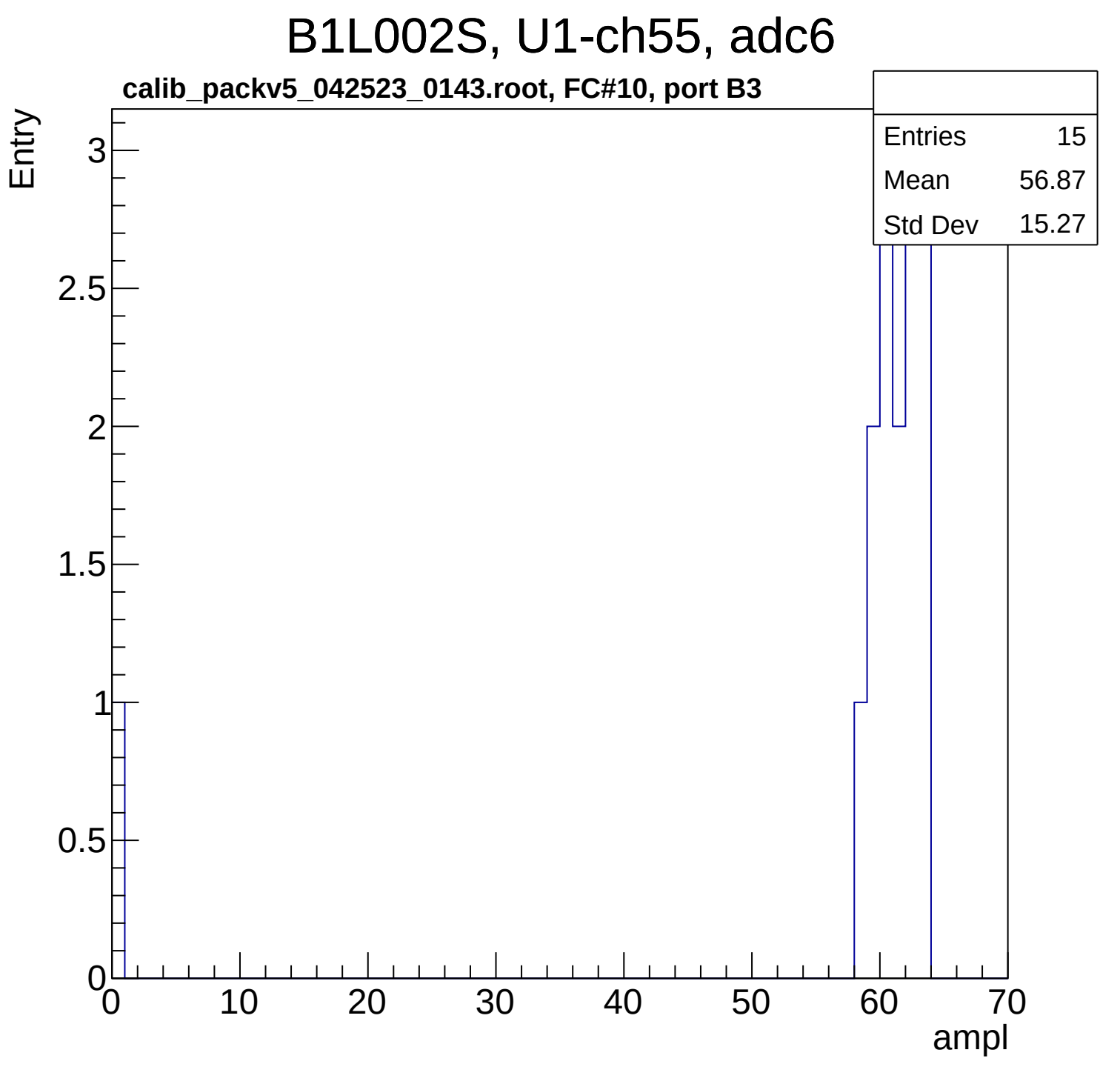
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch55, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 56.87
Std Dev 15.27
Entry
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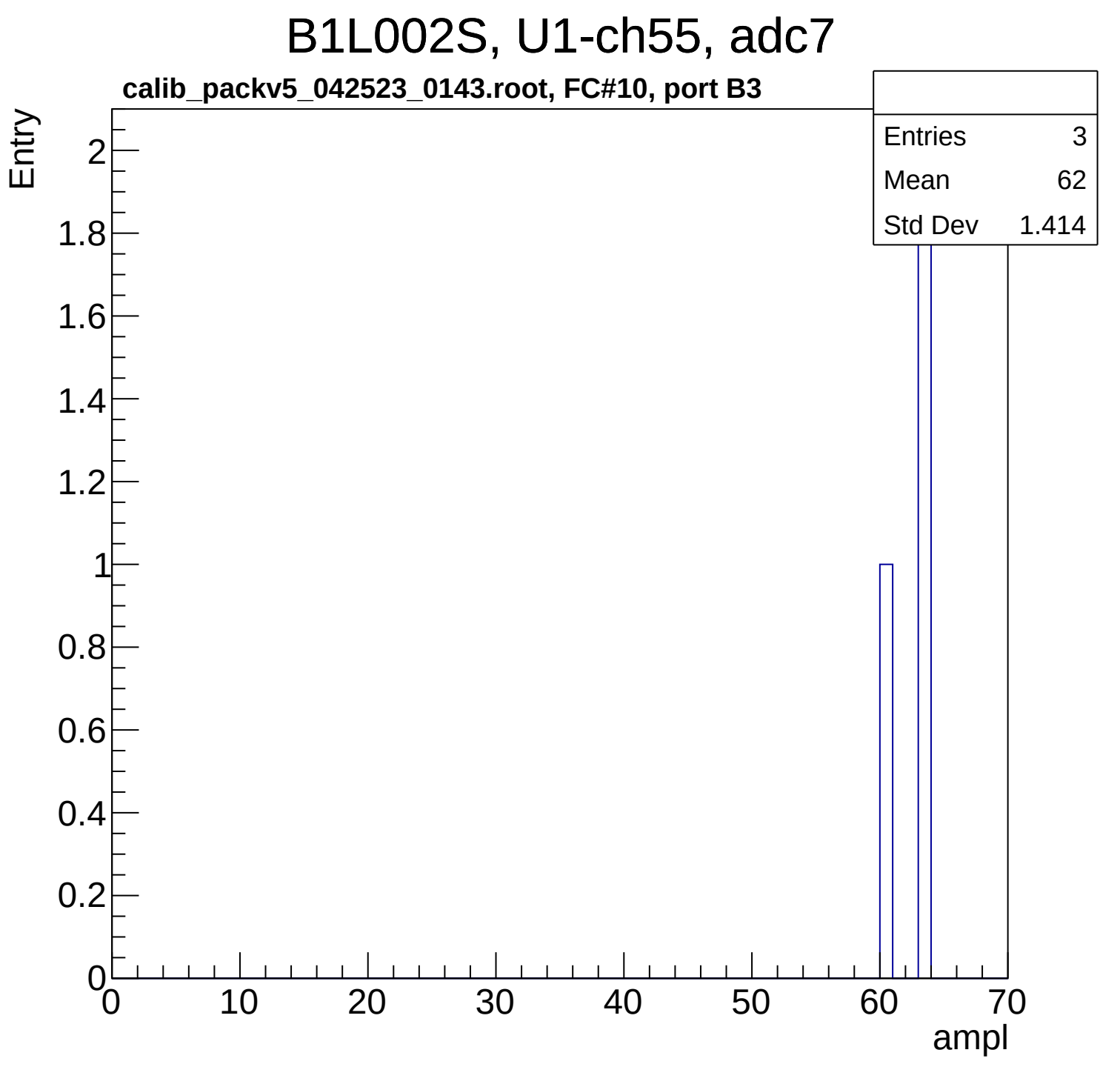
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch55, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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30
40
50
60
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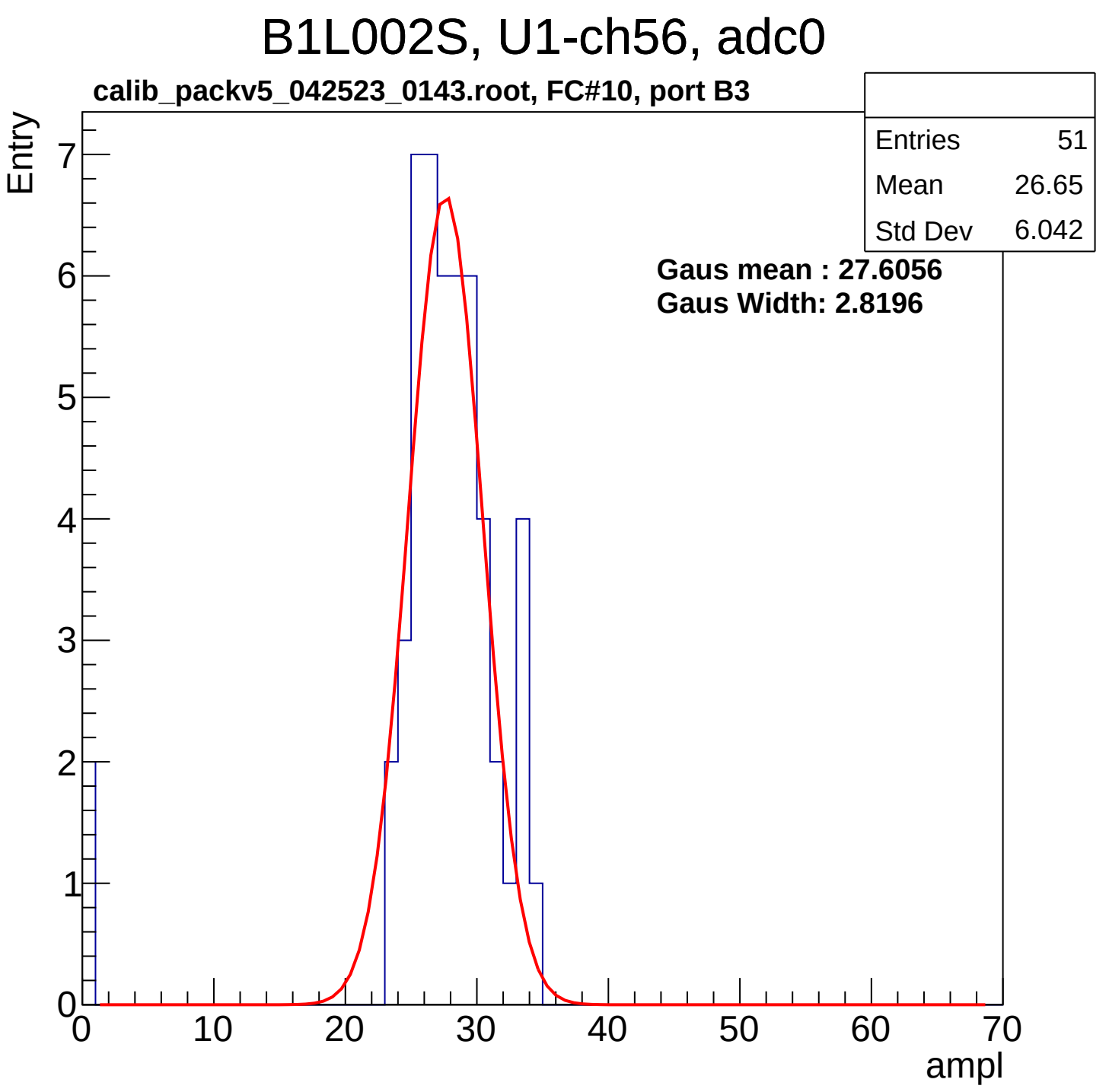
B1L002S, U1-ch56, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 26.65
Std Dev 6.042
Gaus mean : 27.6056
Gaus Width: 2.8196
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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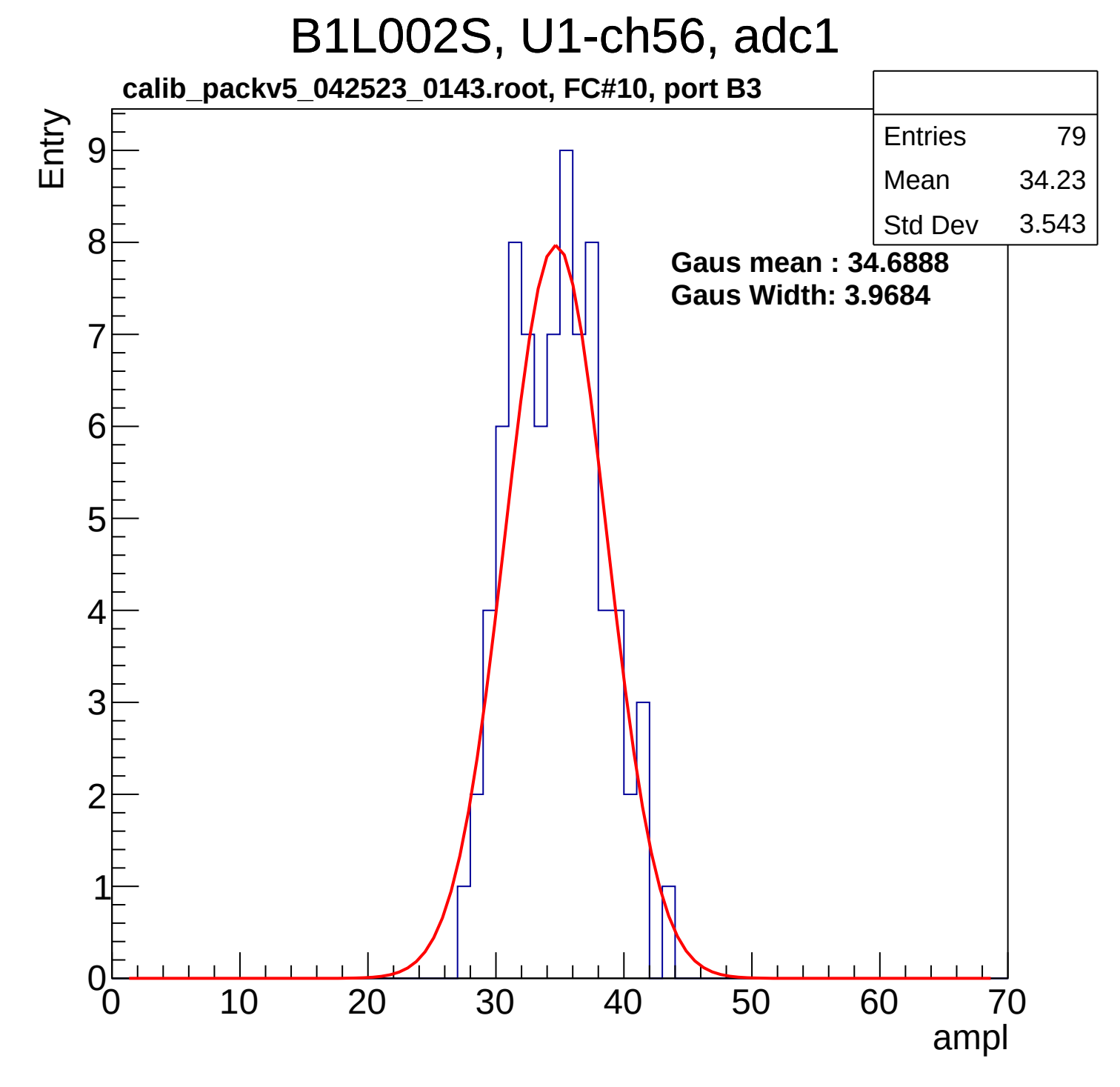
B1L002S, U1-ch56, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 34.23
Std Dev 3.543
Gaus mean : 34.6888
Gaus Width: 3.9684
Entry
ampl
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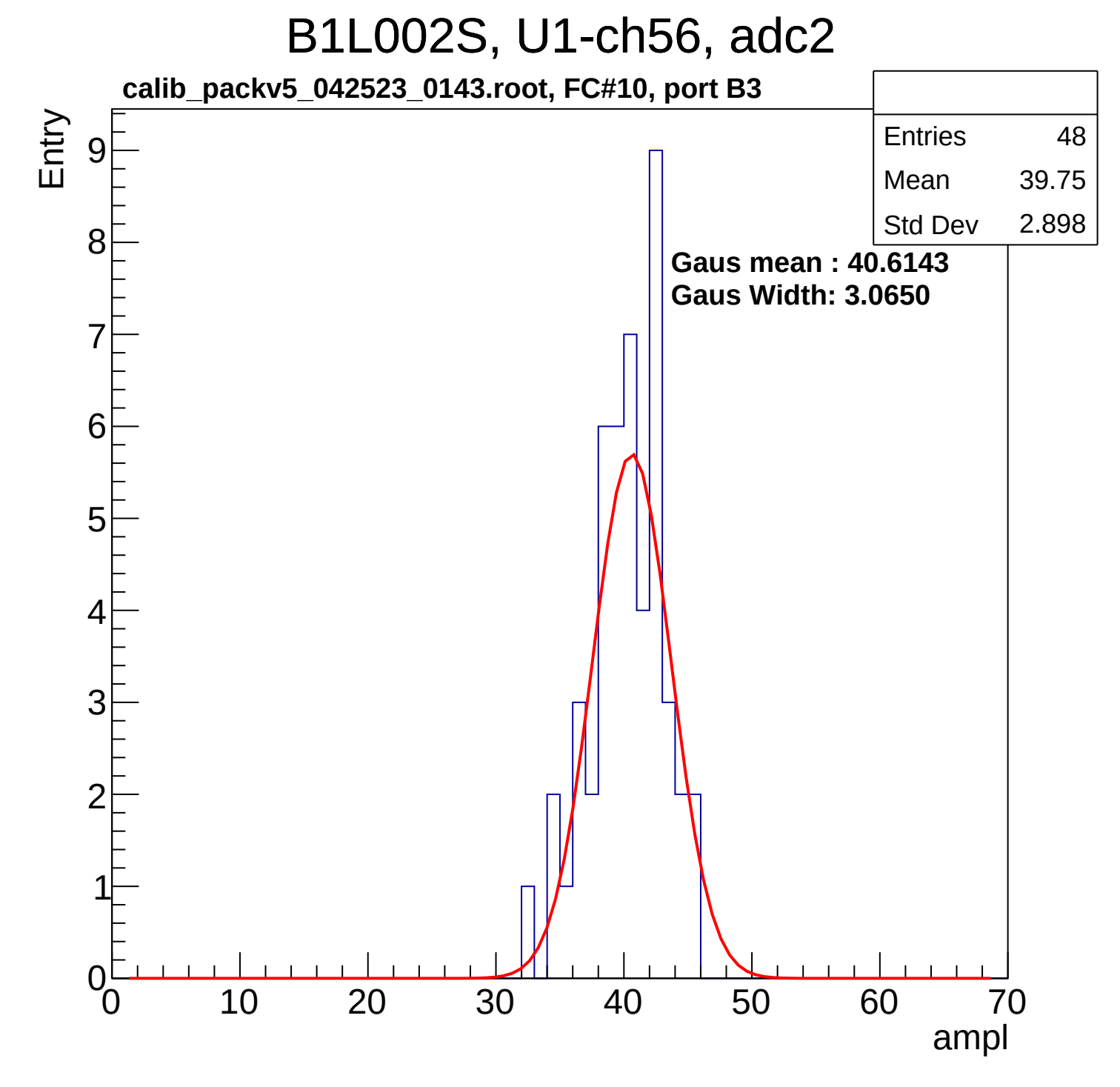
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch56, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 39.75
Std Dev 2.898
Gaus mean : 40.6143
Gaus Width: 3.0650
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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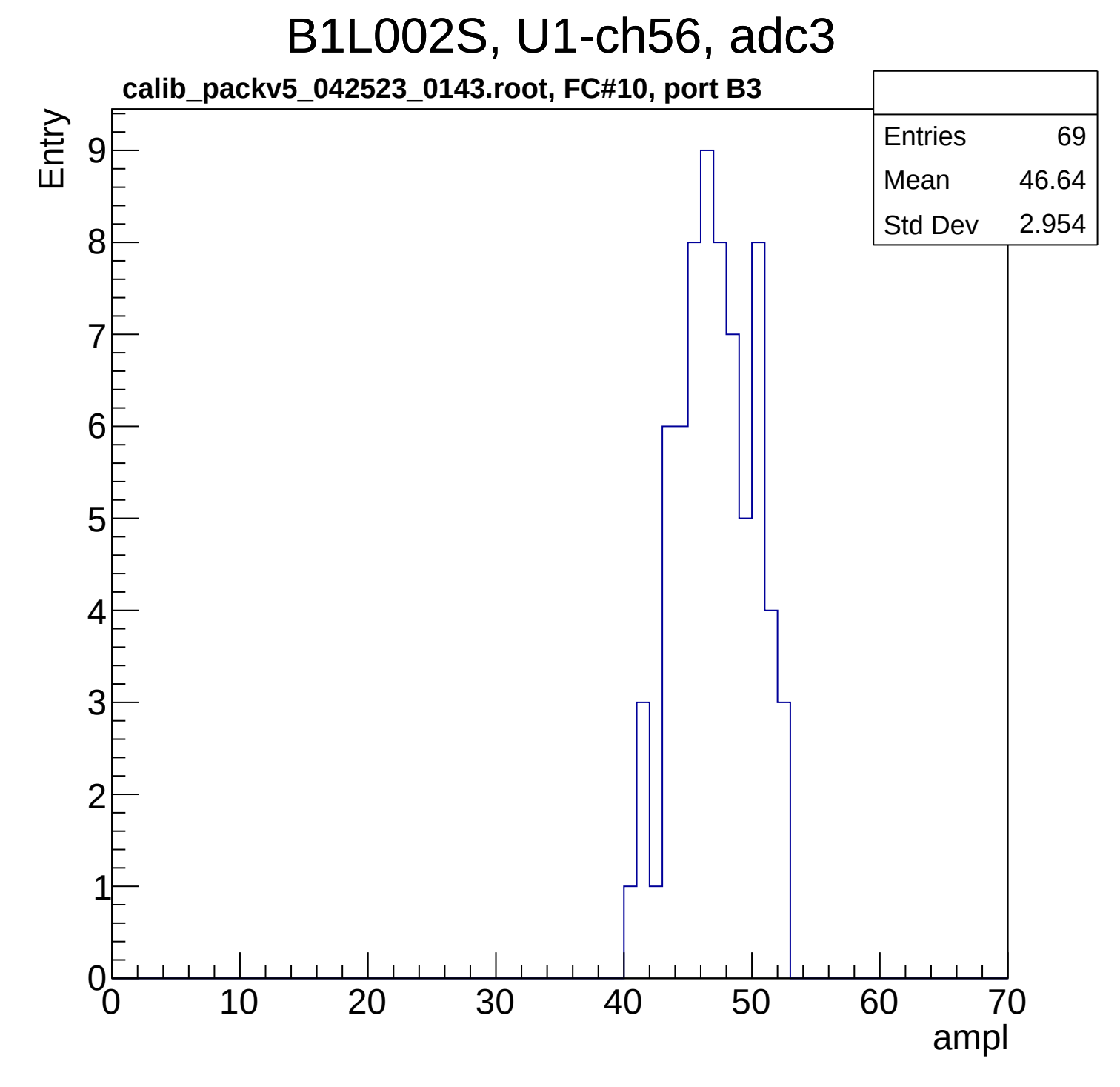
B1L002S, U1-ch56, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.64
Std Dev 2.954
Entry
ampl
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3
4
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6
7
8
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0
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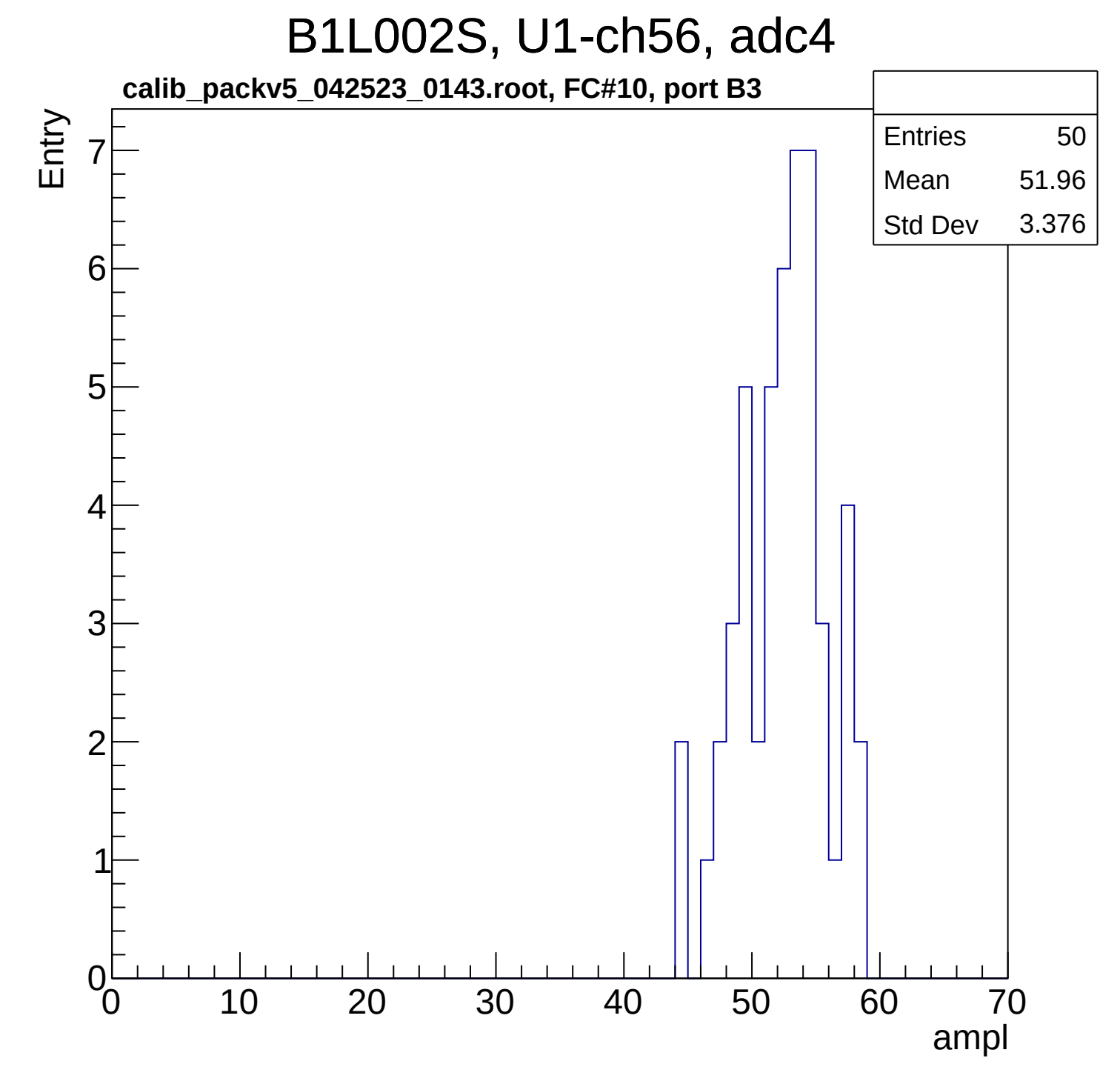
B1L002S, U1-ch56, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 51.96
Std Dev 3.376
Entry
ampl
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4
5
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0
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20
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60
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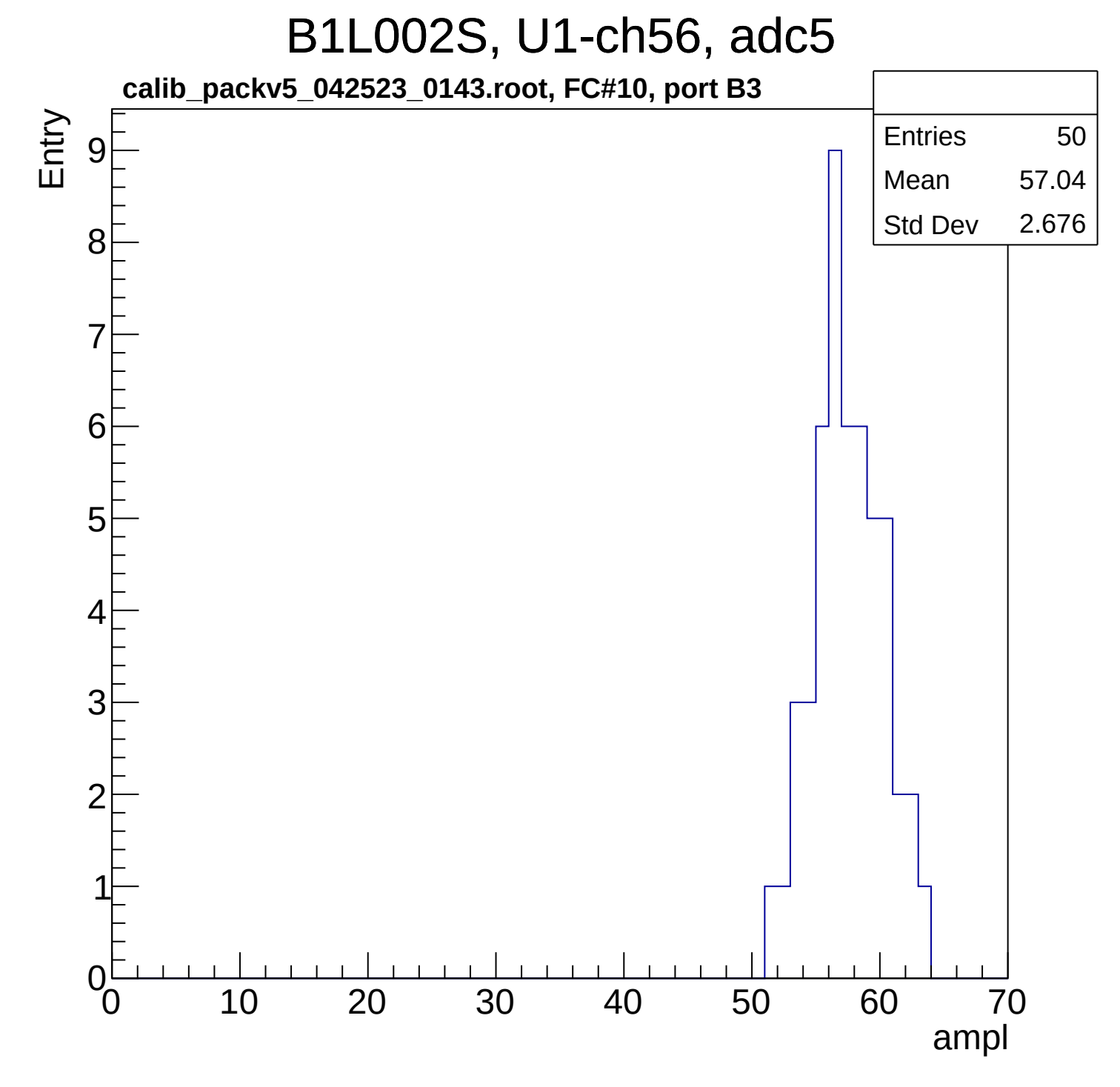
B1L002S, U1-ch56, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57.04
Std Dev 2.676
Entry
ampl
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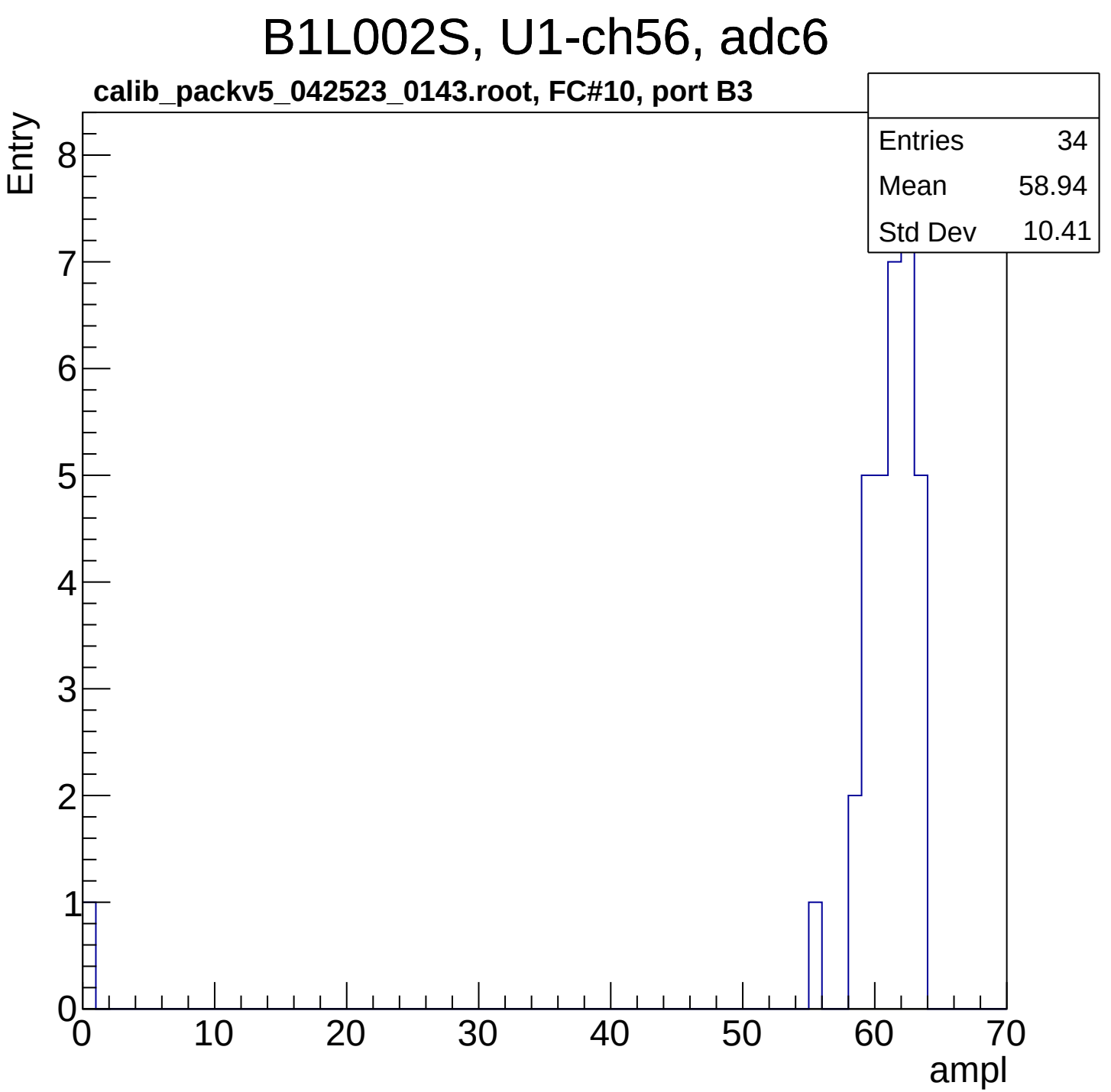
B1L002S, U1-ch56, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.94
Std Dev 10.41
Entry
ampl
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5
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7
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0
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20
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60
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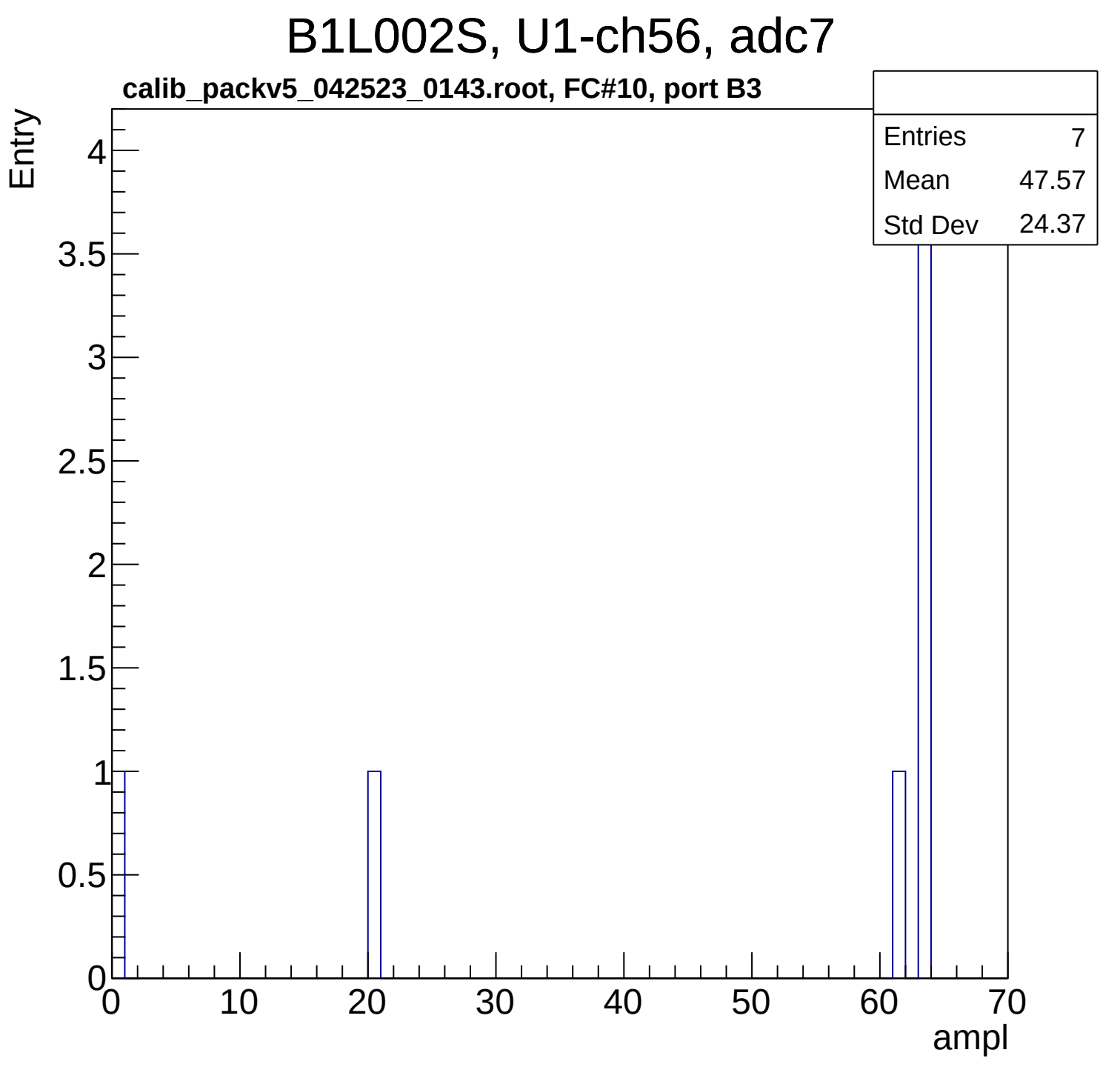
B1L002S, U1-ch56, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 47.57
Std Dev 24.37
Entry
ampl
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1.5
2
2.5
3
3.5
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0
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60
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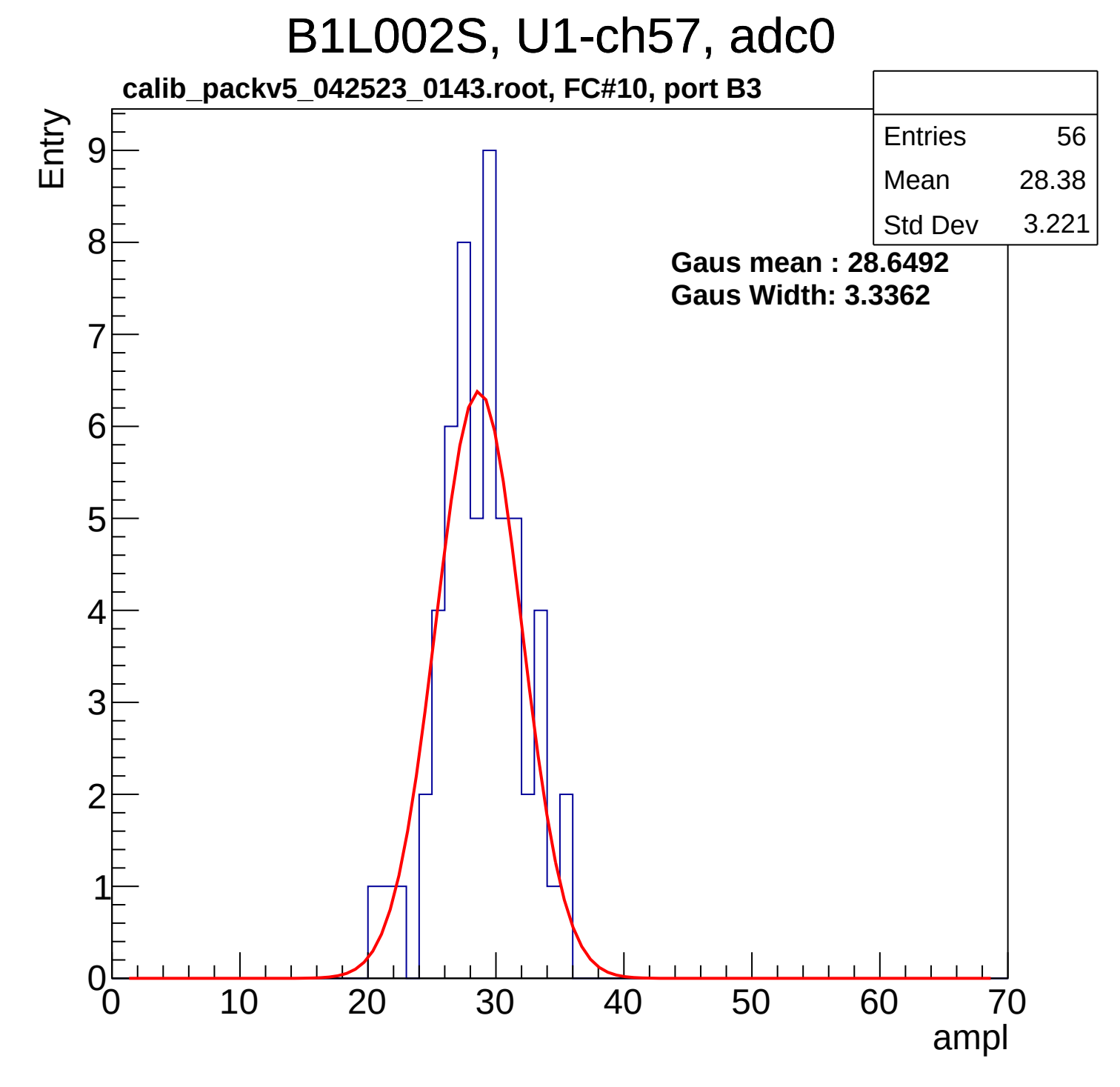
B1L002S, U1-ch57, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 28.38
Std Dev 3.221
Gaus mean : 28.6492
Gaus Width: 3.3362
Entry
ampl
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2
3
4
5
6
7
8
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0
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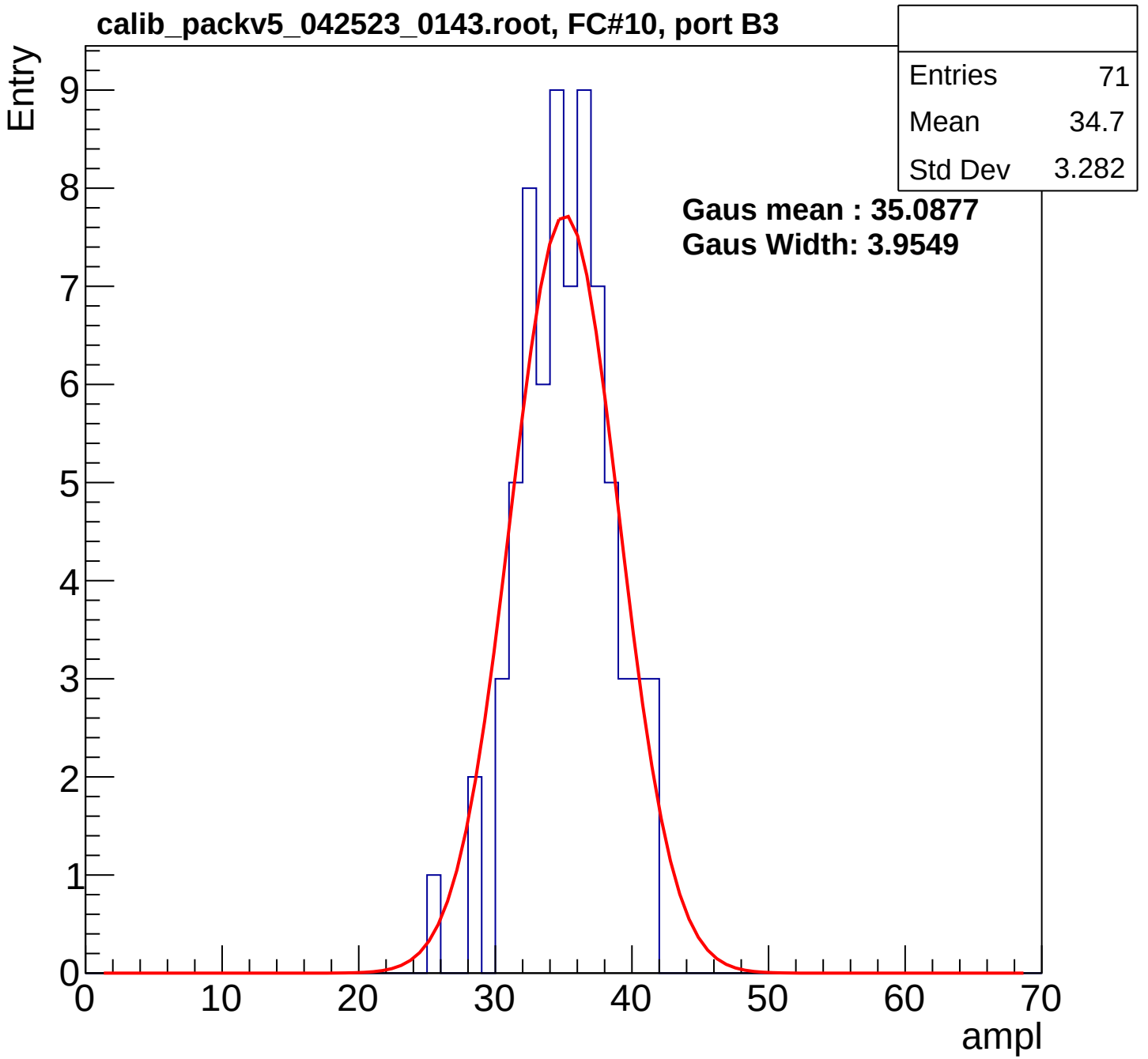
B1L002S, U1-ch57, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.7
Std Dev 3.282
Gaus mean : 35.0877
Gaus Width: 3.9549
Entry
ampl
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2
3
4
5
6
7
8
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0
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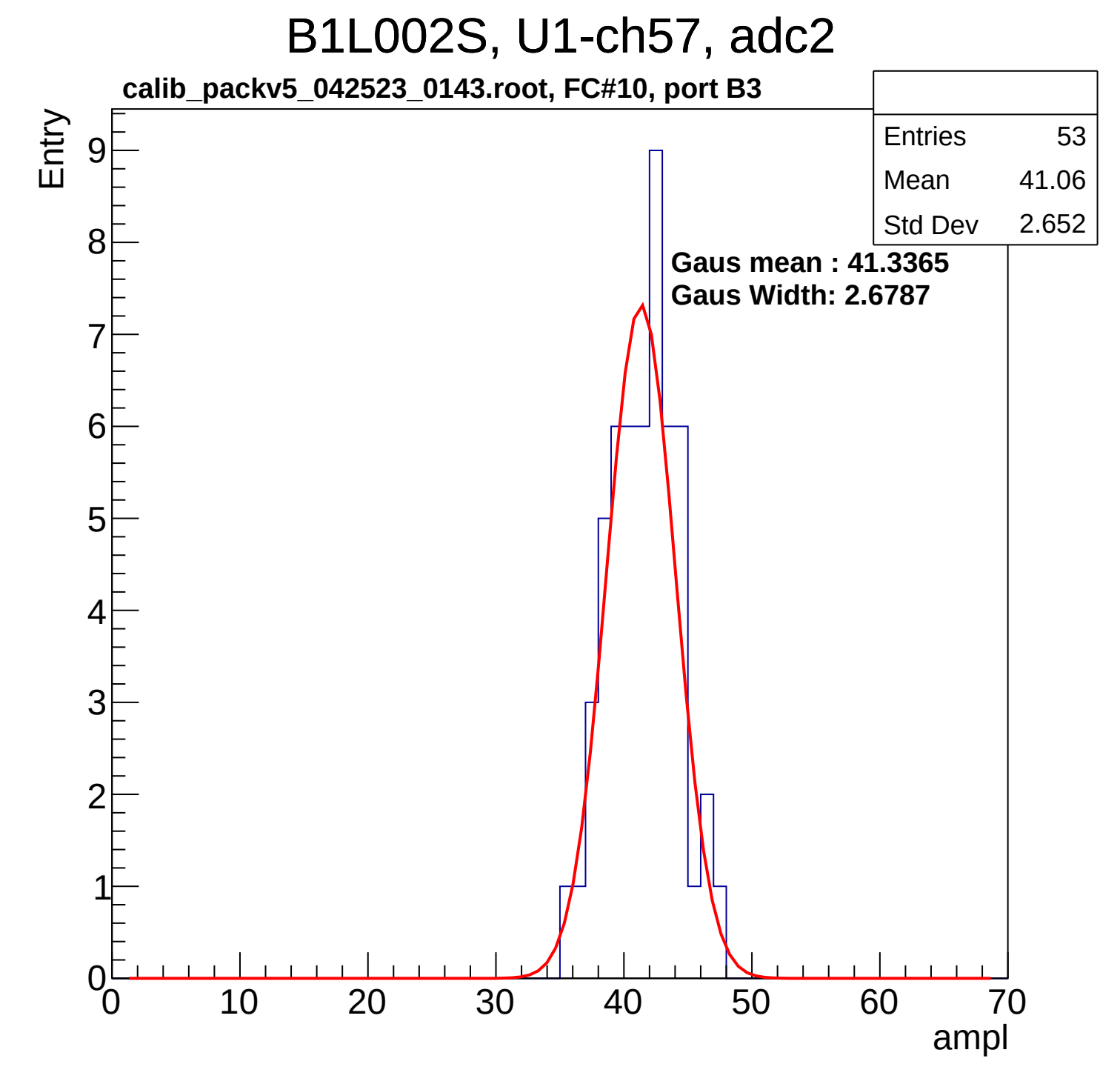
B1L002S, U1-ch57, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.06
Std Dev 2.652
Gaus mean : 41.3365
Gaus Width: 2.6787
Entry
ampl
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2
3
4
5
6
7
8
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0
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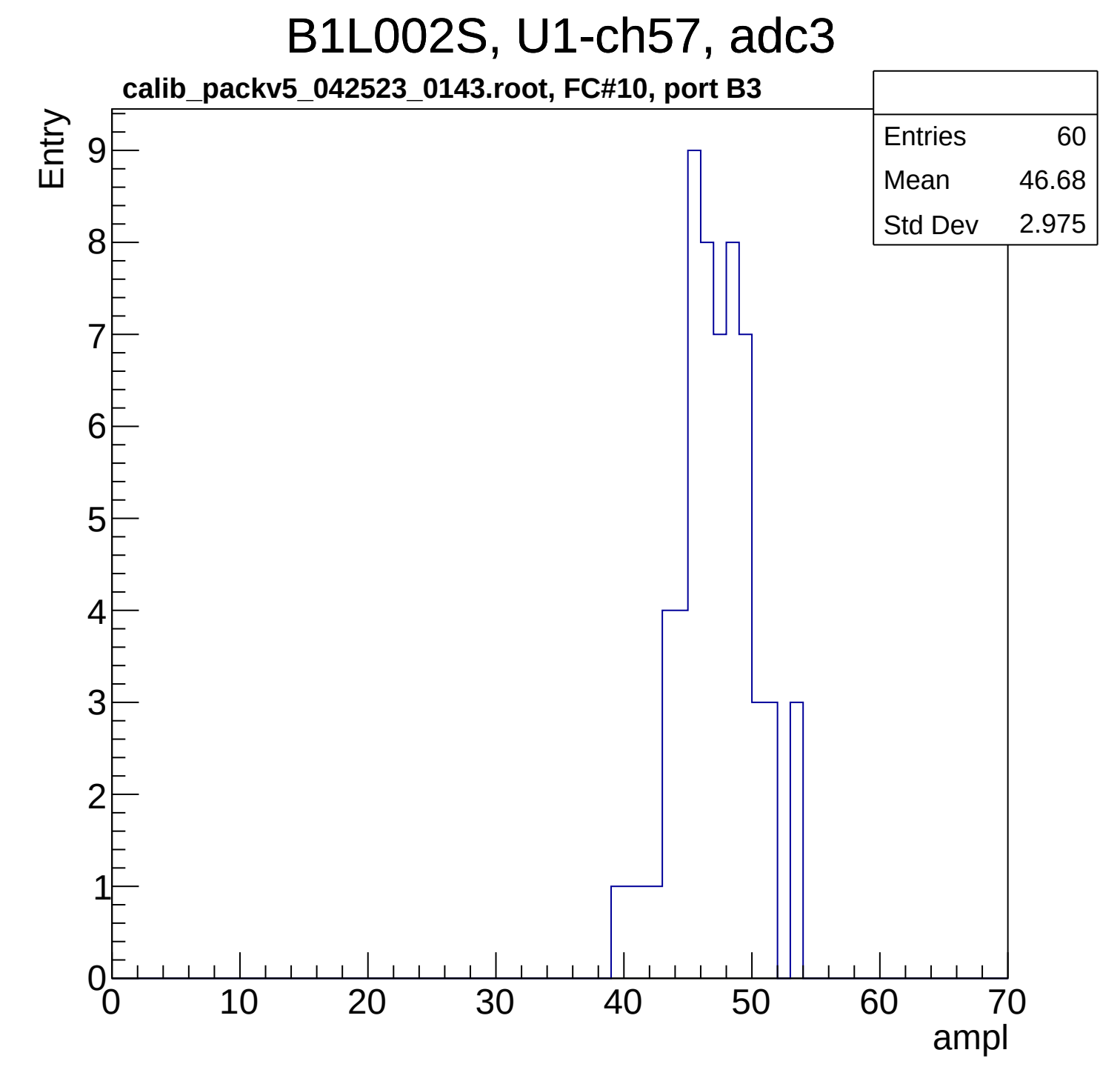
B1L002S, U1-ch57, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 46.68
Std Dev 2.975
Entry
ampl
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4
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0
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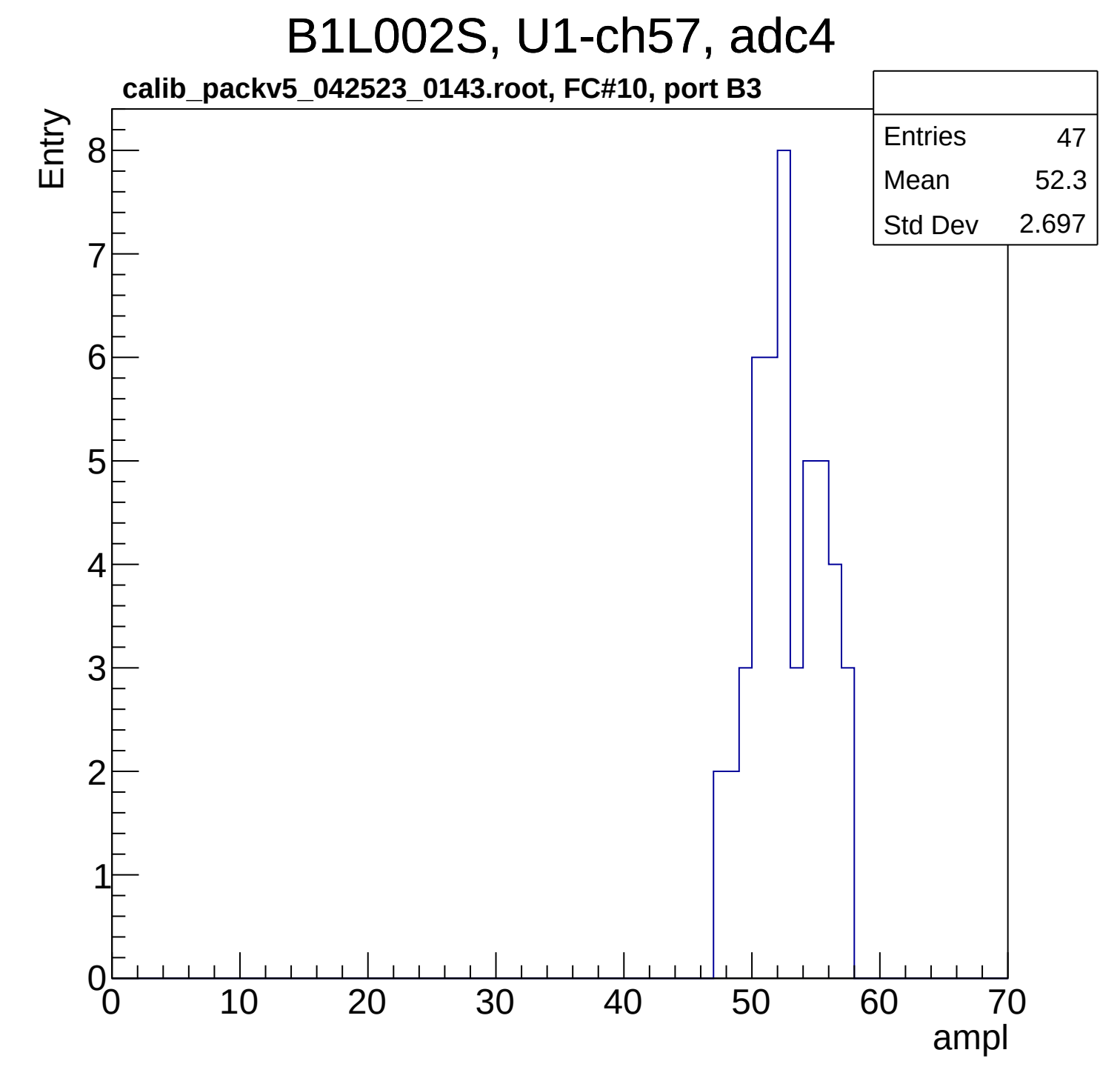
B1L002S, U1-ch57, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 52.3
Std Dev 2.697
Entry
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3
4
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6
7
8
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30
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50
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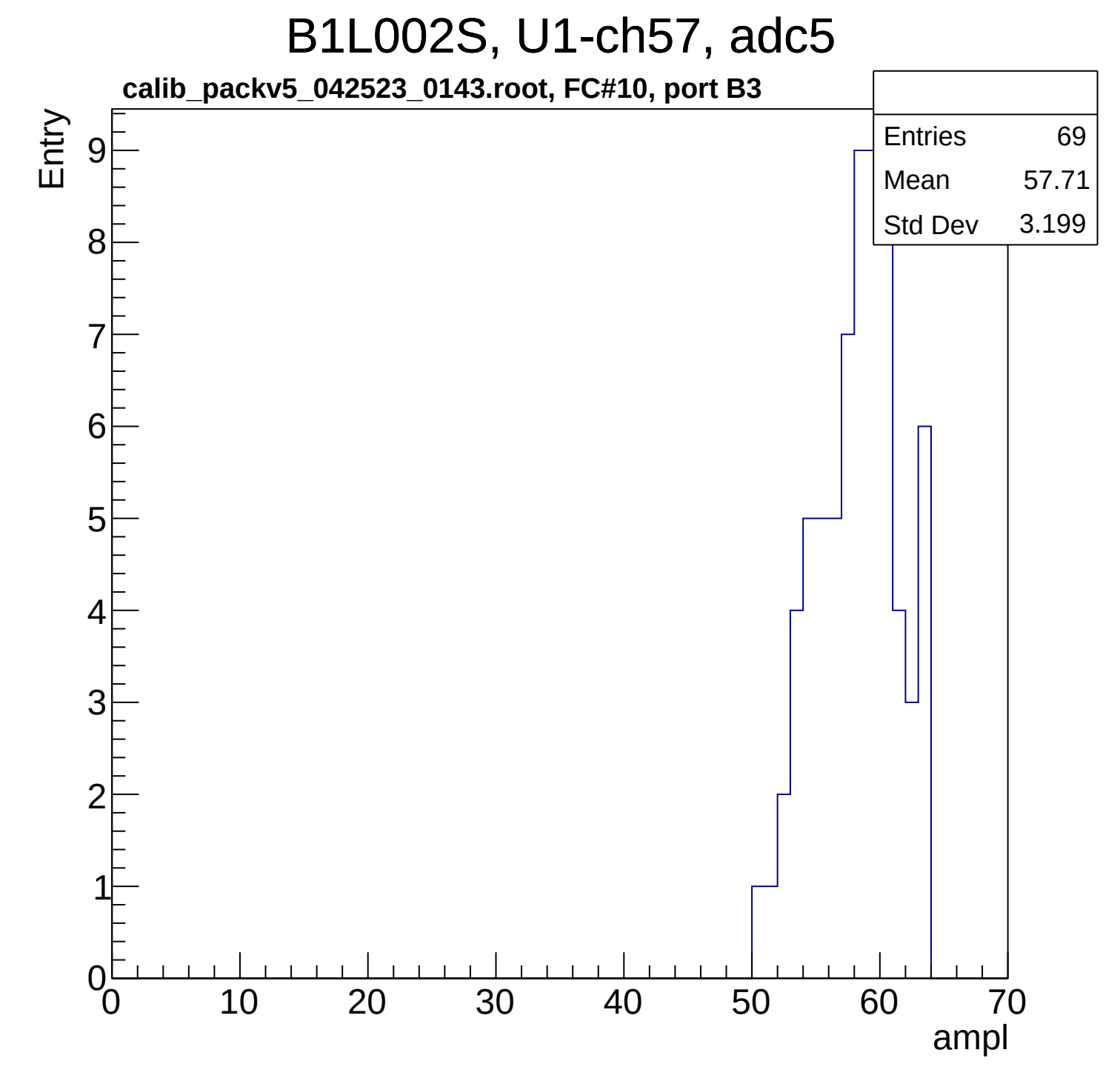
70
ampl


B1L002S, U1-ch57, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 57.71
Std Dev 3.199
Entry
ampl
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3
4
5
6
7
8
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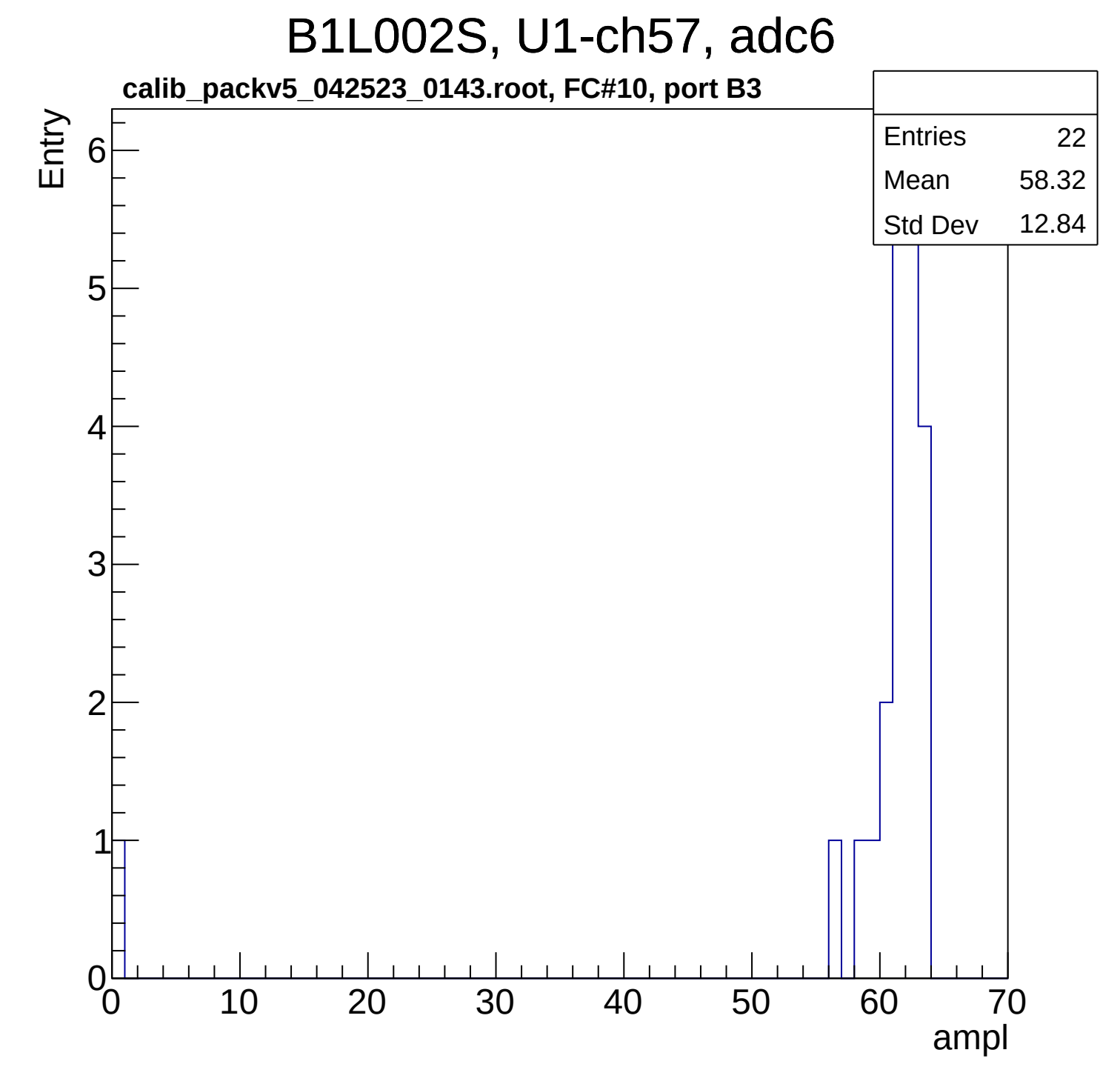
B1L002S, U1-ch57, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.32
Std Dev 12.84
Entry
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2
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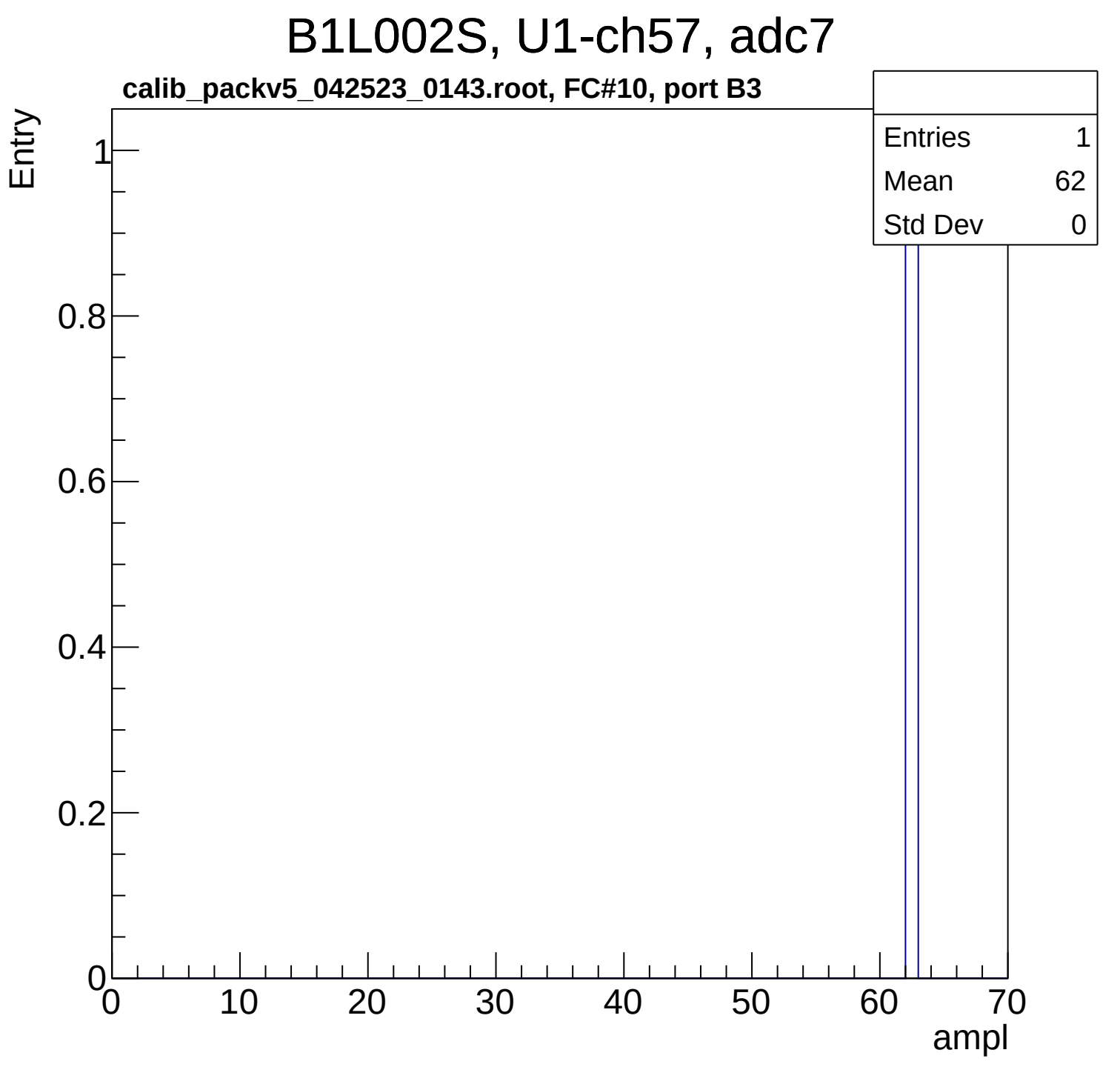
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch57, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
20
30
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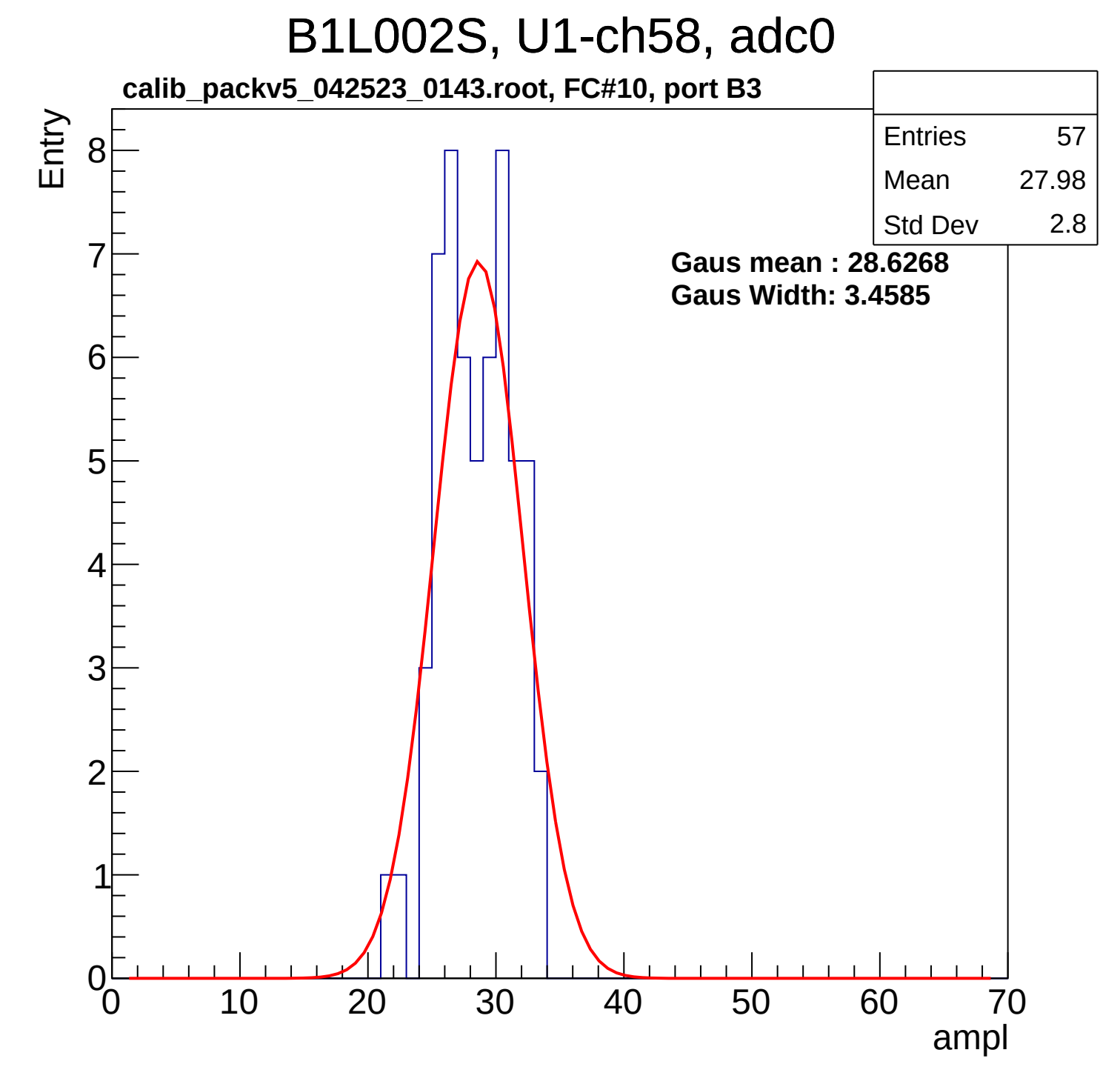
50
60
70
ampl


B1L002S, U1-ch58, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.98
Std Dev 2.8
Gaus mean : 28.6268
Gaus Width: 3.4585
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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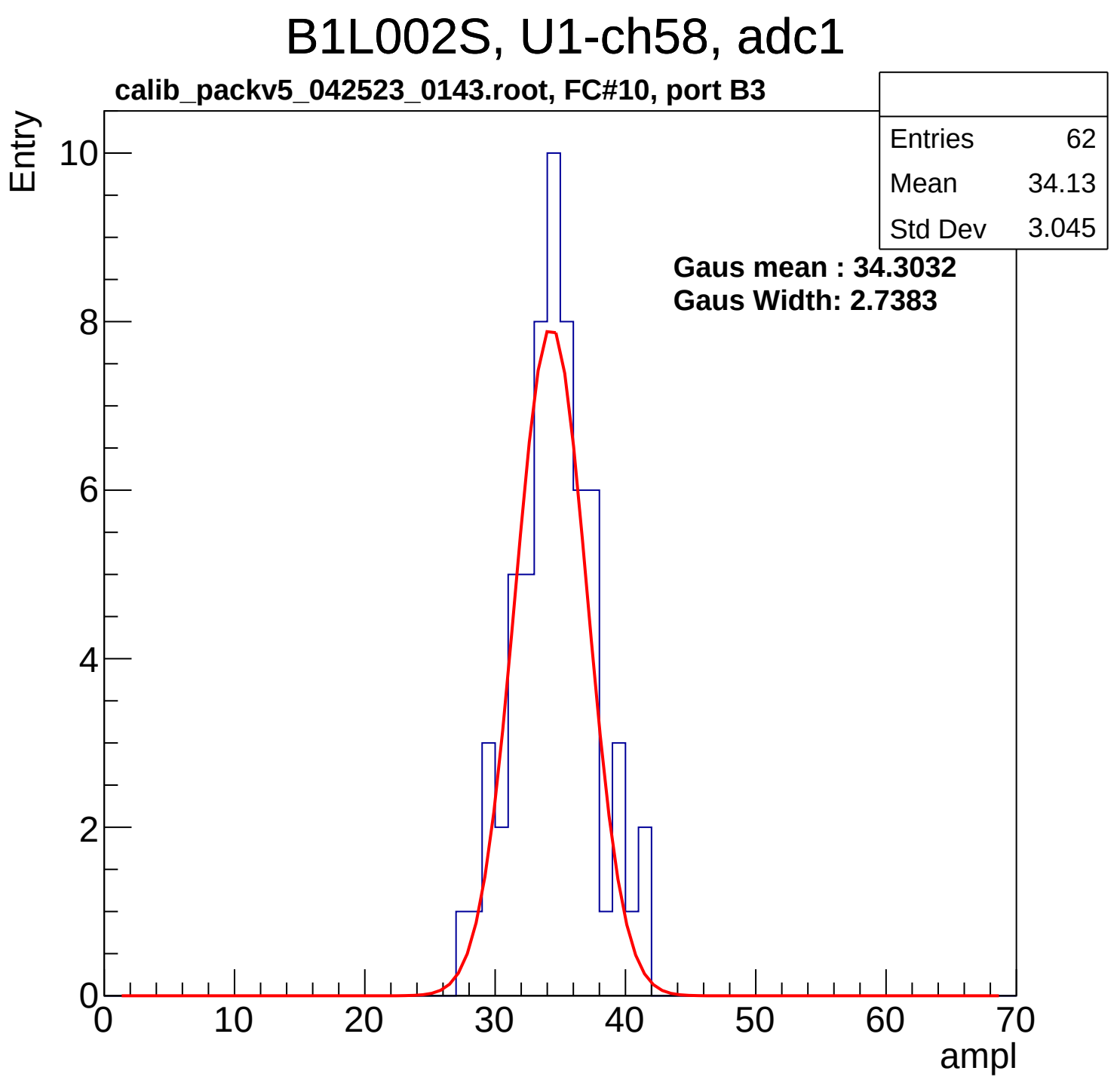
B1L002S, U1-ch58, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.13
Std Dev 3.045
Gaus mean : 34.3032
Gaus Width: 2.7383
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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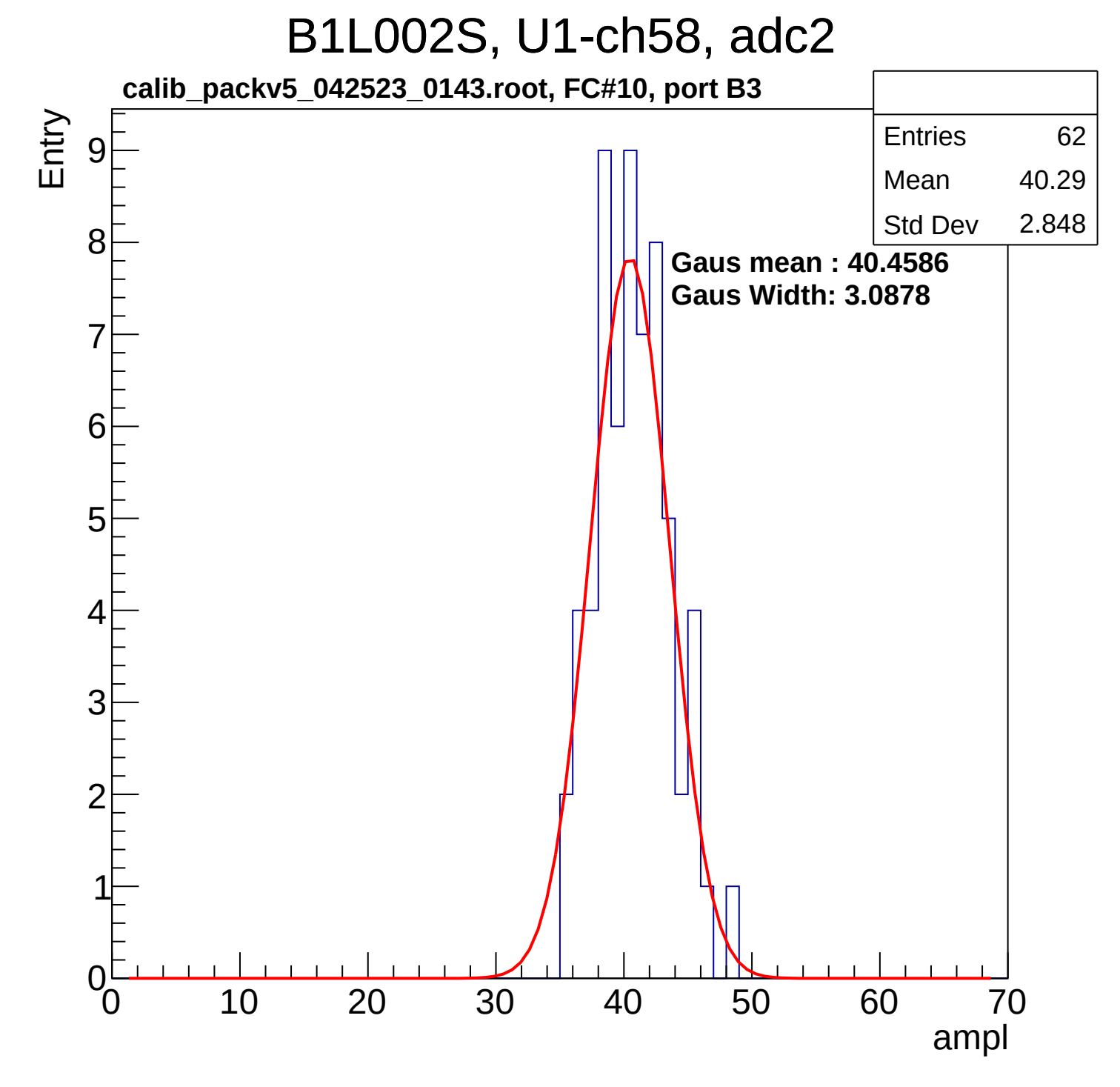
B1L002S, U1-ch58, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.29
Std Dev 2.848
Gaus mean : 40.4586
Gaus Width: 3.0878
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
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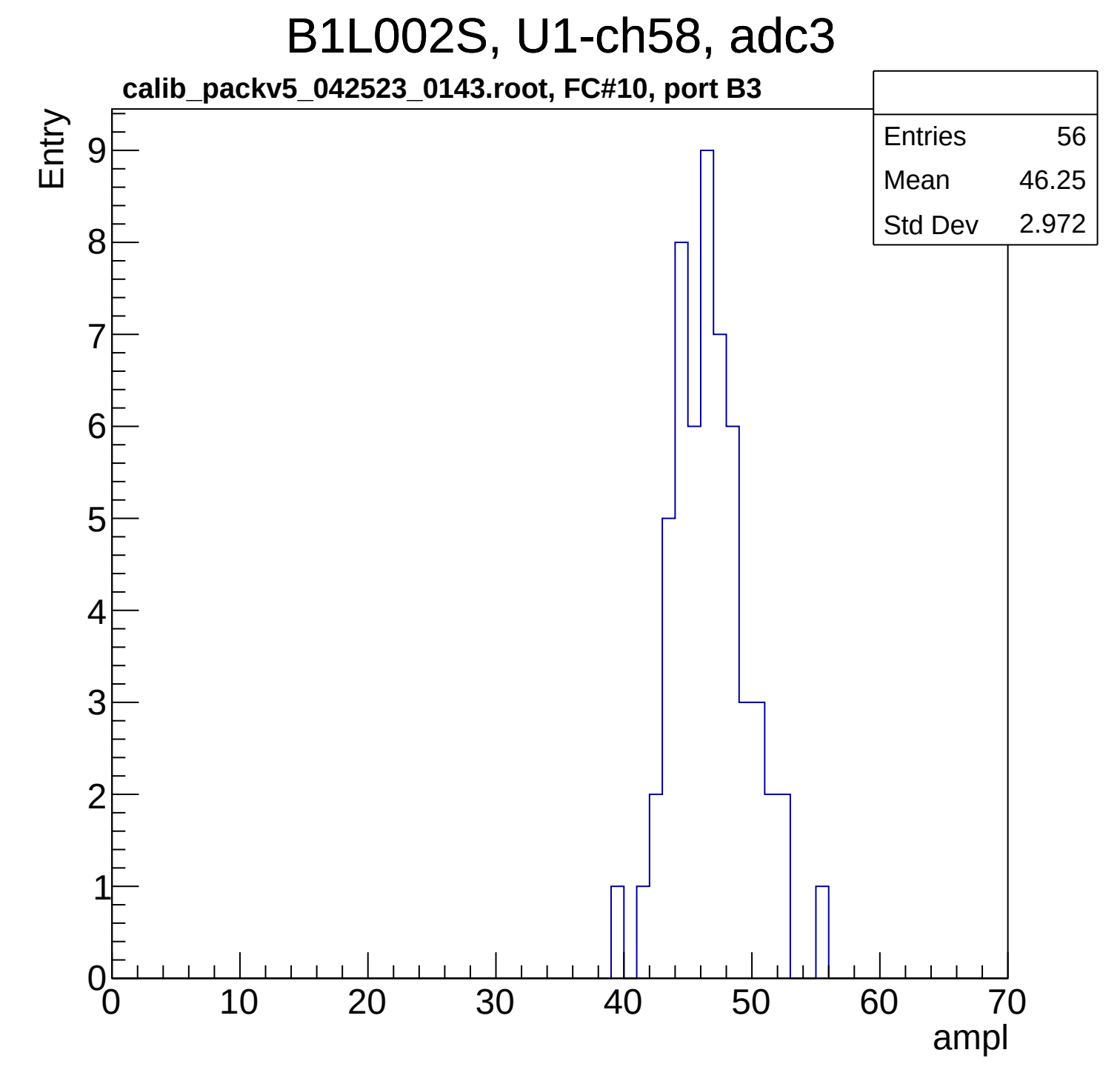
B1L002S, U1-ch58, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.25
Std Dev 2.972
Entry
ampl
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3
4
5
6
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0
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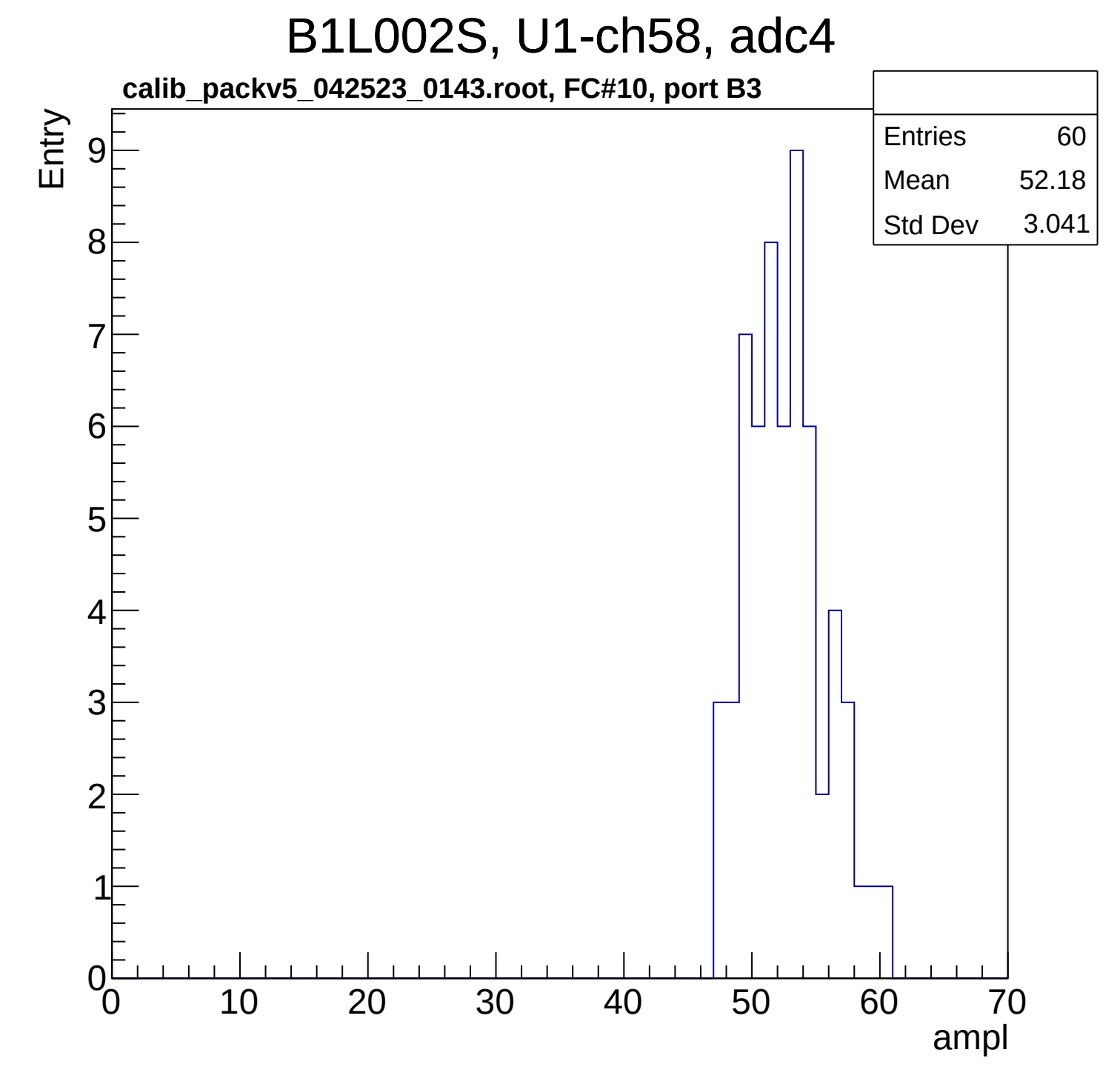
B1L002S, U1-ch58, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.18
Std Dev 3.041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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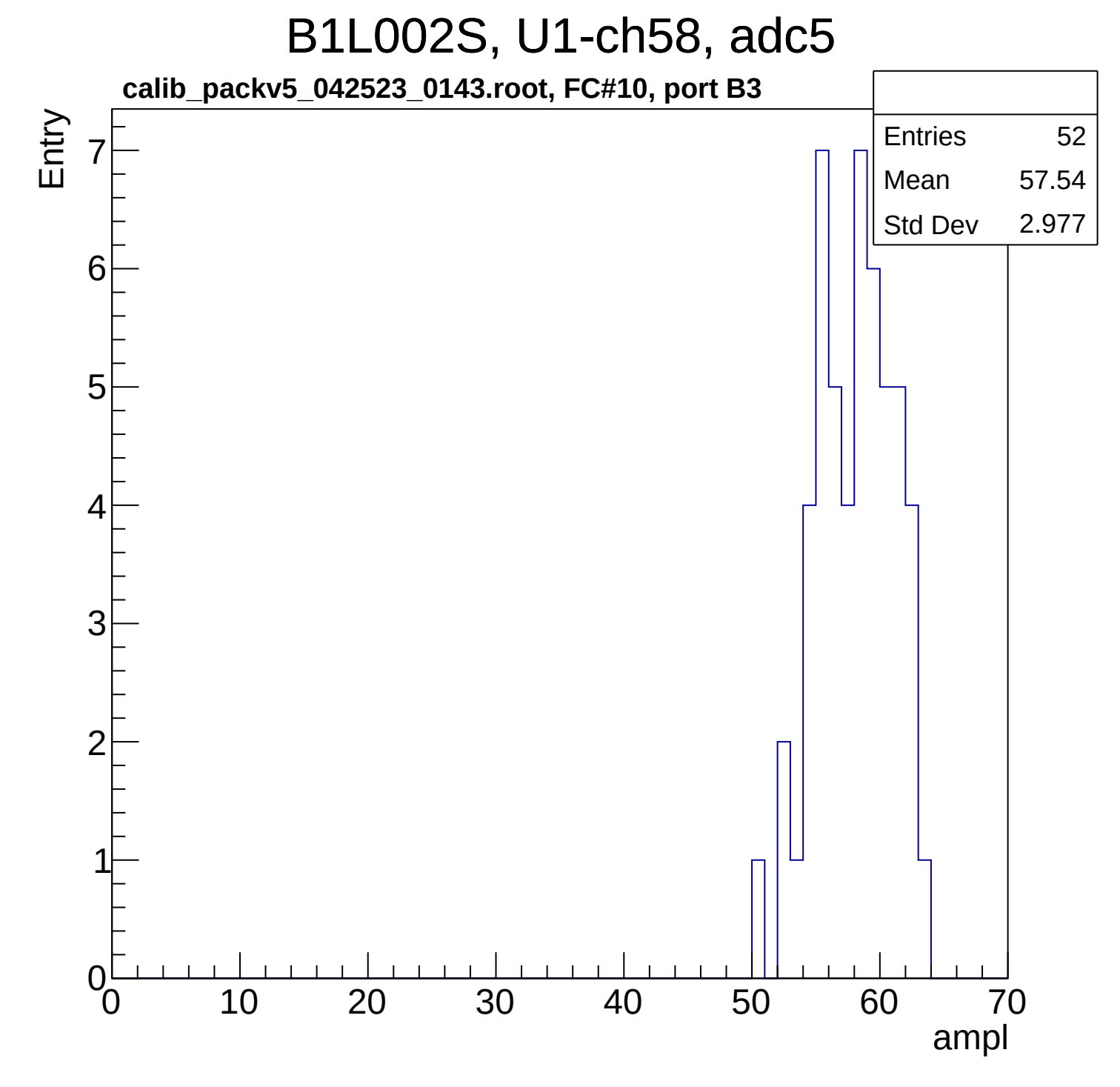
B1L002S, U1-ch58, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.54
Std Dev 2.977
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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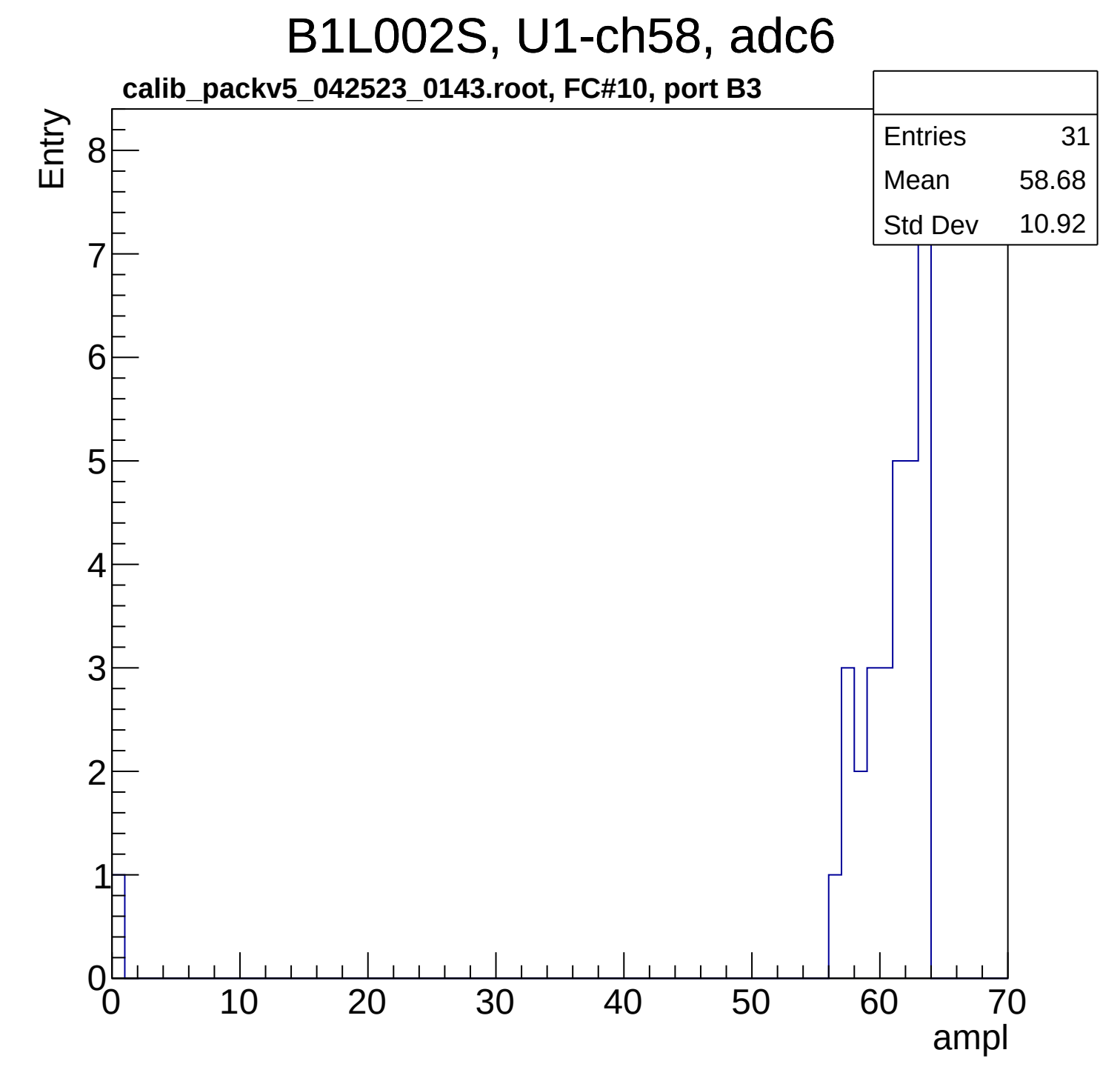
B1L002S, U1-ch58, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.68
Std Dev 10.92
Entry
ampl
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2
3
4
5
6
7
8
0
10
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60
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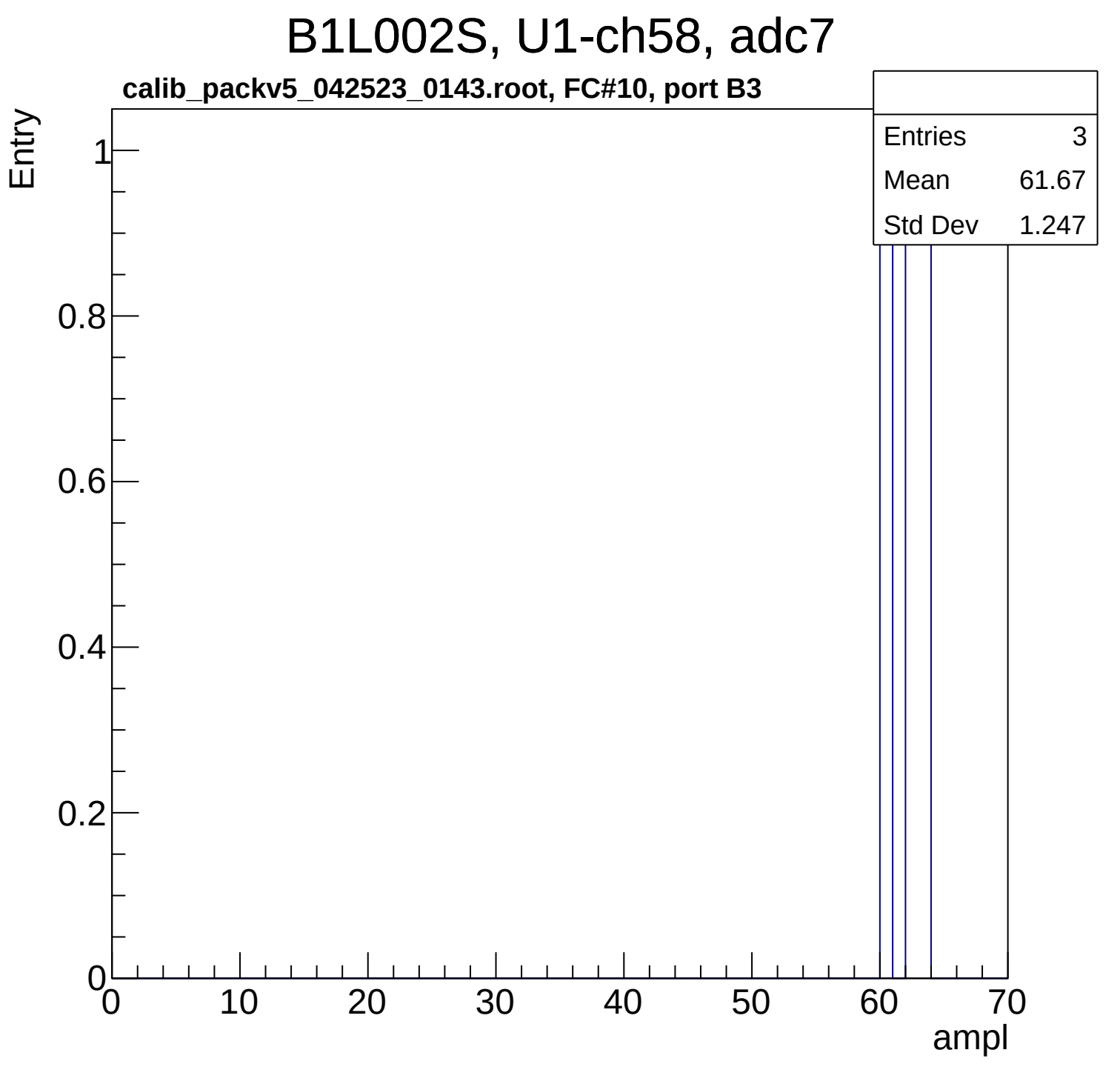
B1L002S, U1-ch58, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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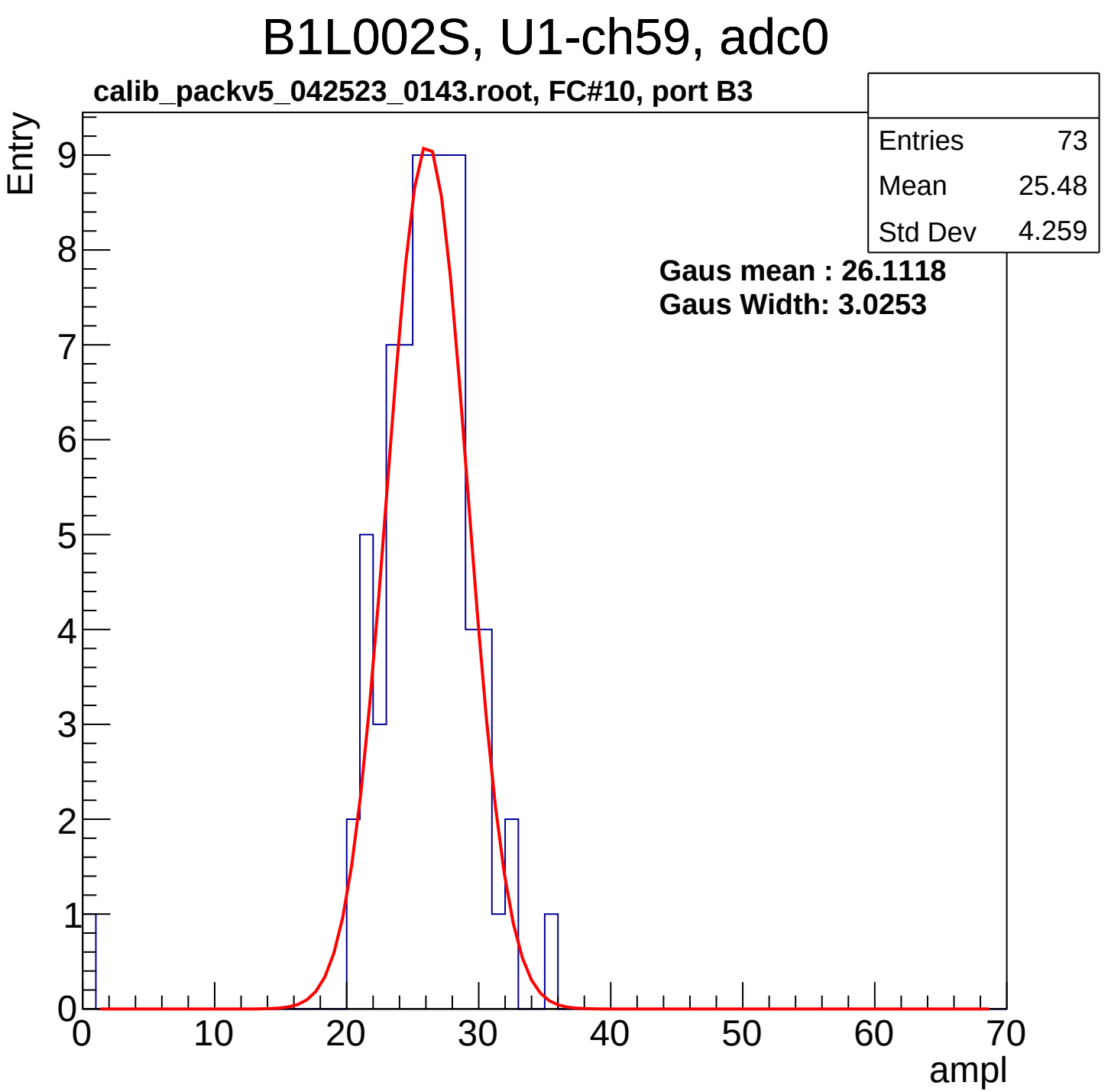
B1L002S, U1-ch59, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 25.48
Std Dev 4.259
Gaus mean : 26.1118
Gaus Width: 3.0253
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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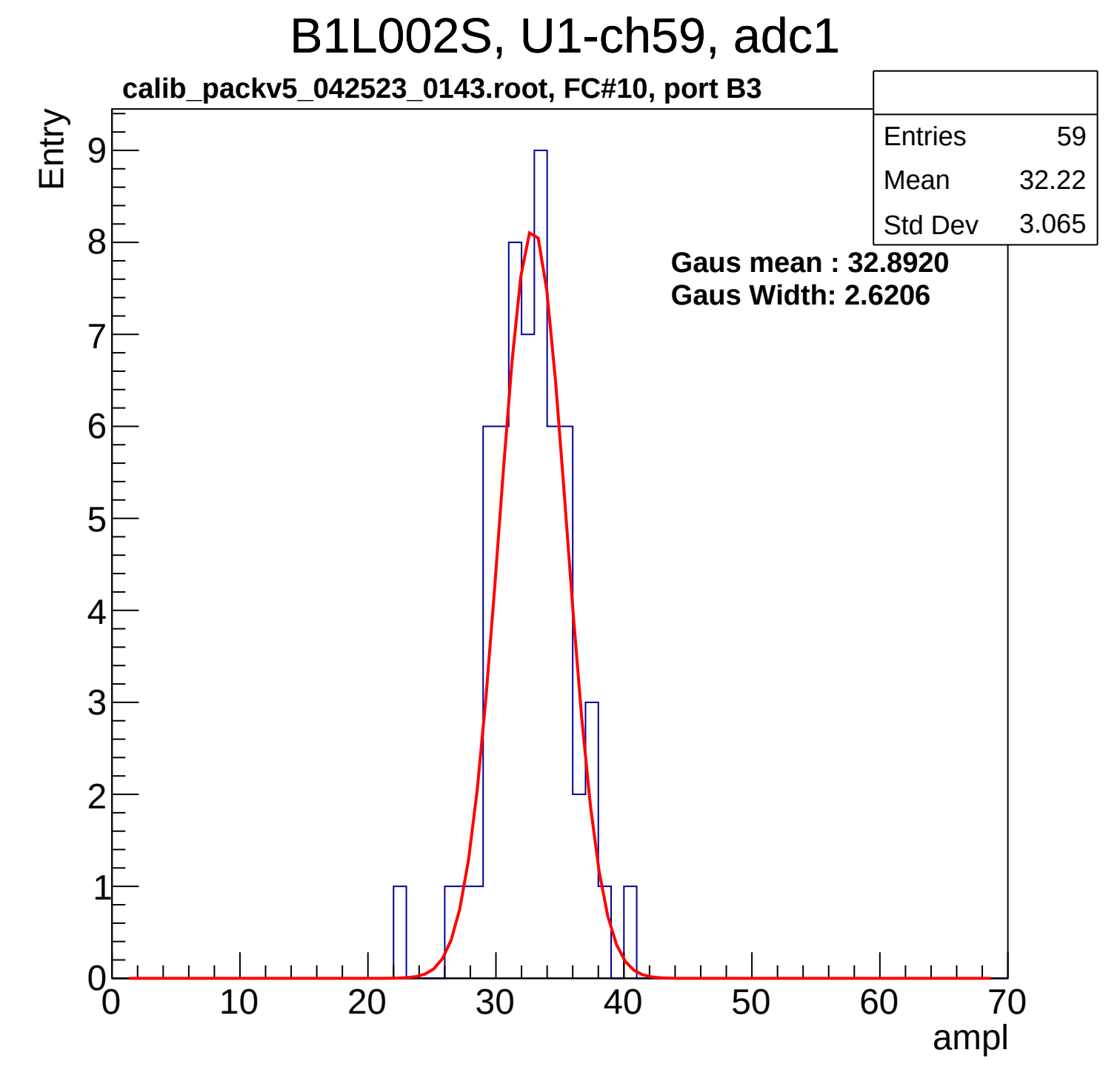
B1L002S, U1-ch59, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 32.22
Std Dev 3.065
Gaus mean : 32.8920
Gaus Width: 2.6206
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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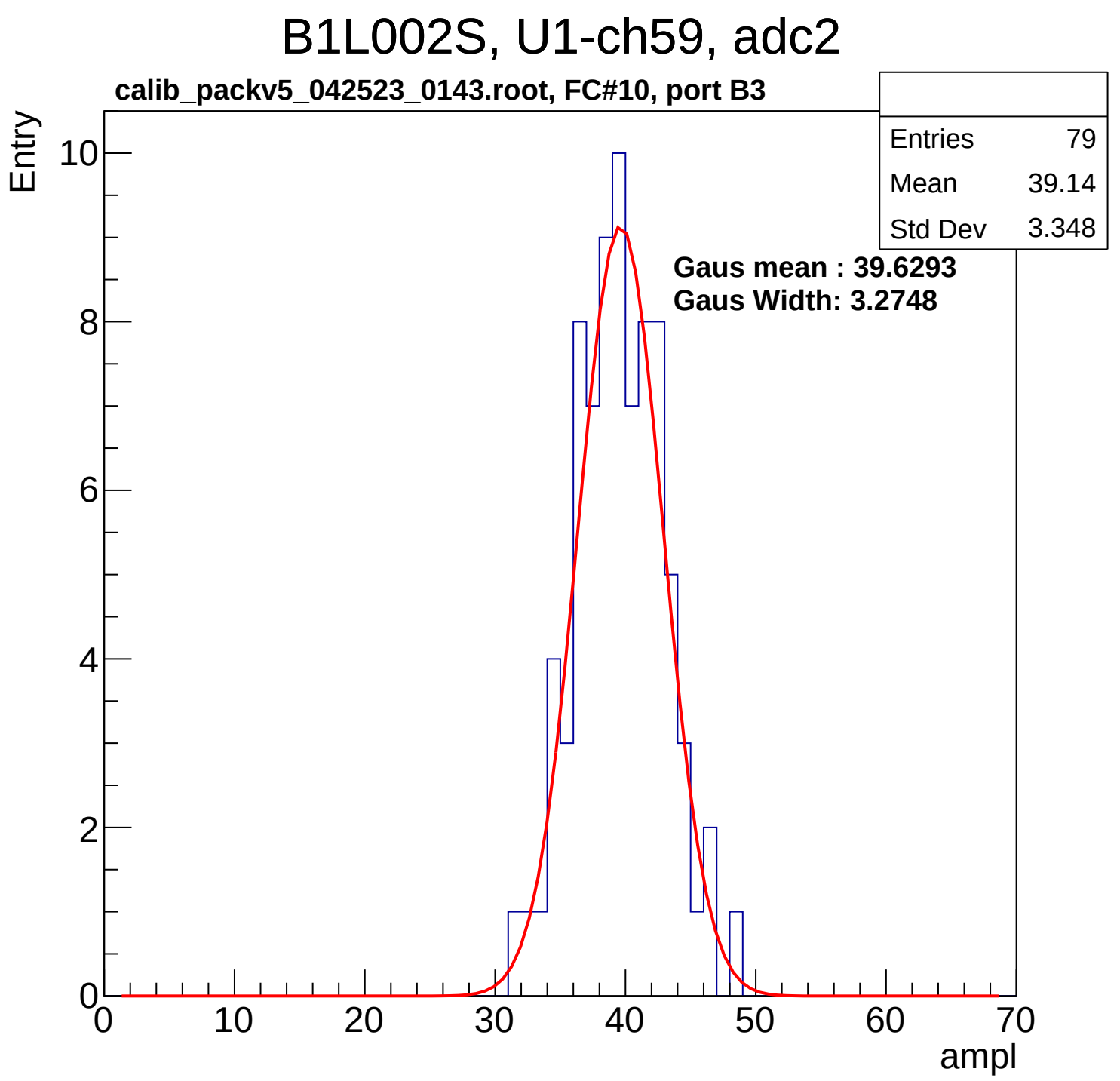
B1L002S, U1-ch59, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 39.14
Std Dev 3.348
Gaus mean : 39.6293
Gaus Width: 3.2748
Entry
ampl
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2
4
6
8
10
0
10
20
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40
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60
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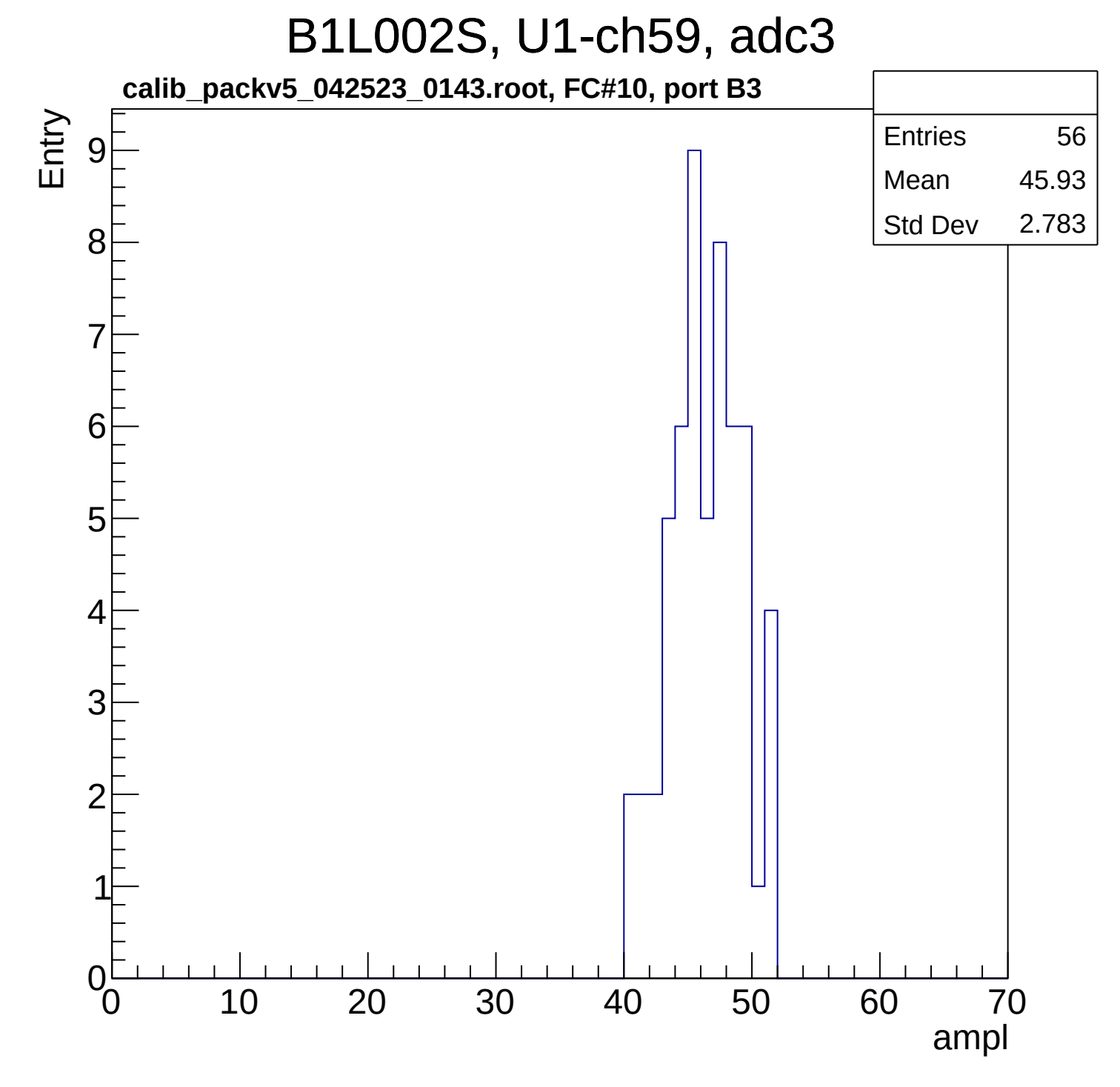
B1L002S, U1-ch59, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 45.93
Std Dev 2.783
Entry
ampl
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2
3
4
5
6
7
8
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0
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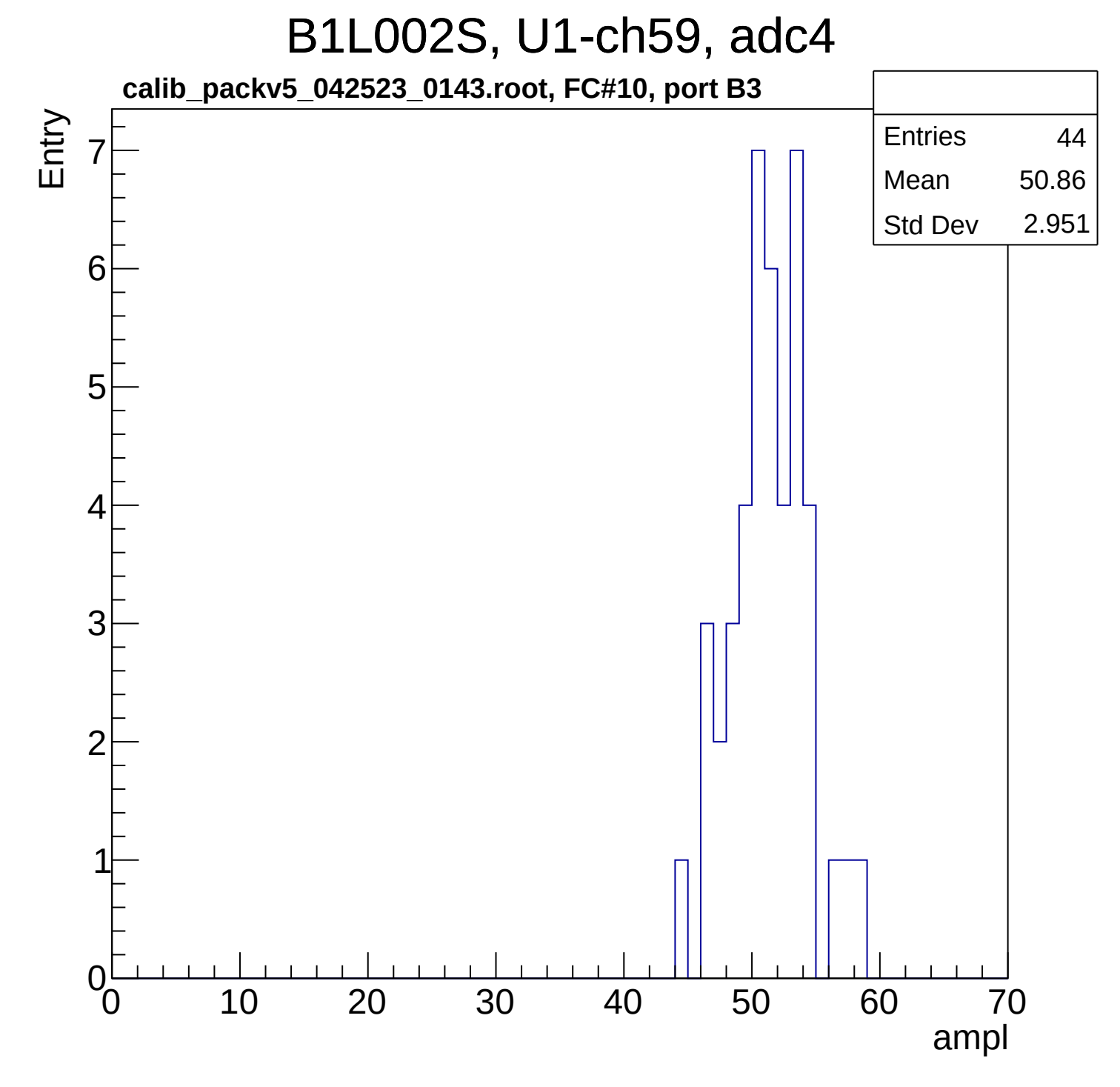
B1L002S, U1-ch59, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 50.86
Std Dev 2.951
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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60
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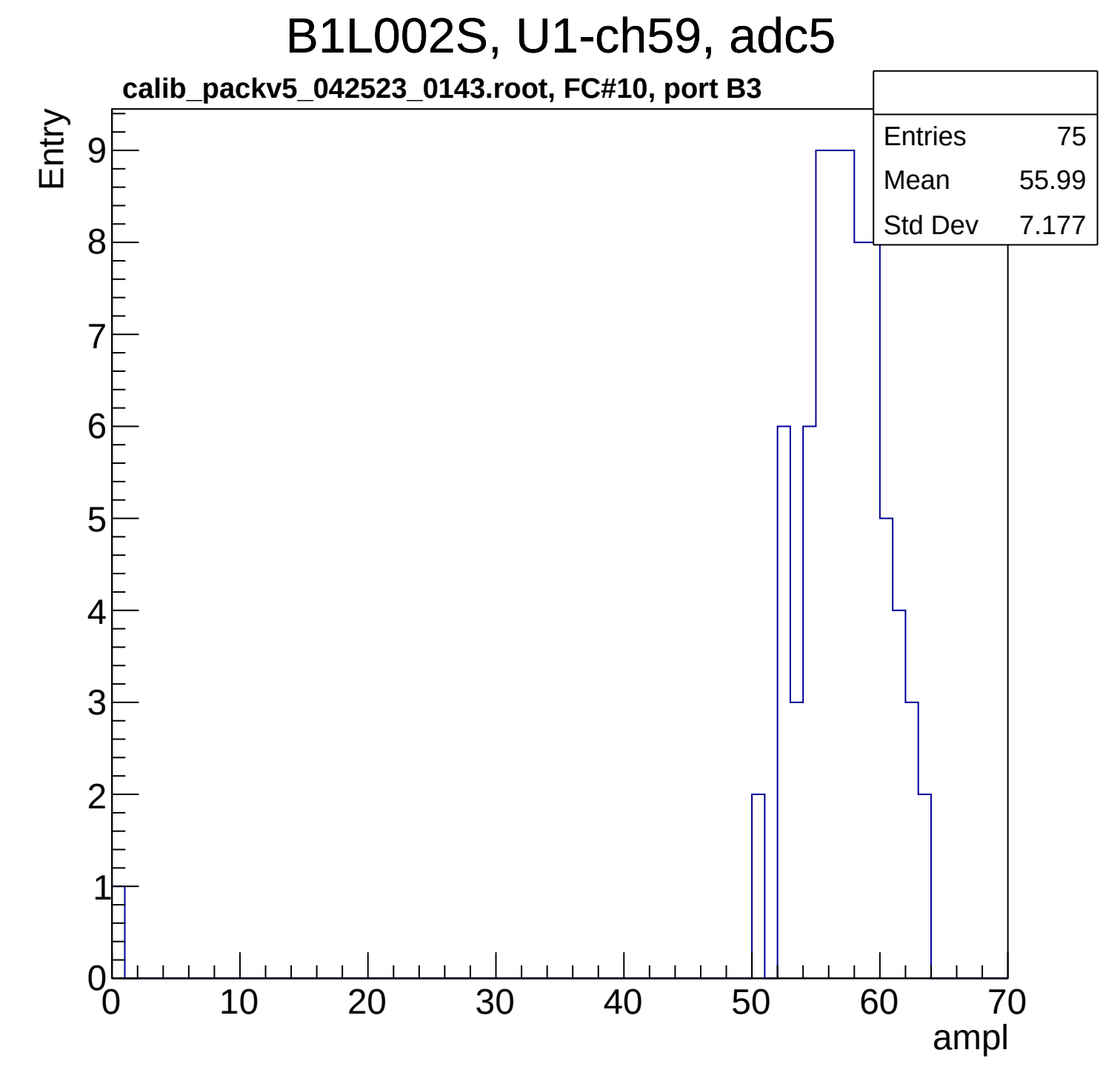
B1L002S, U1-ch59, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 55.99
Std Dev 7.177
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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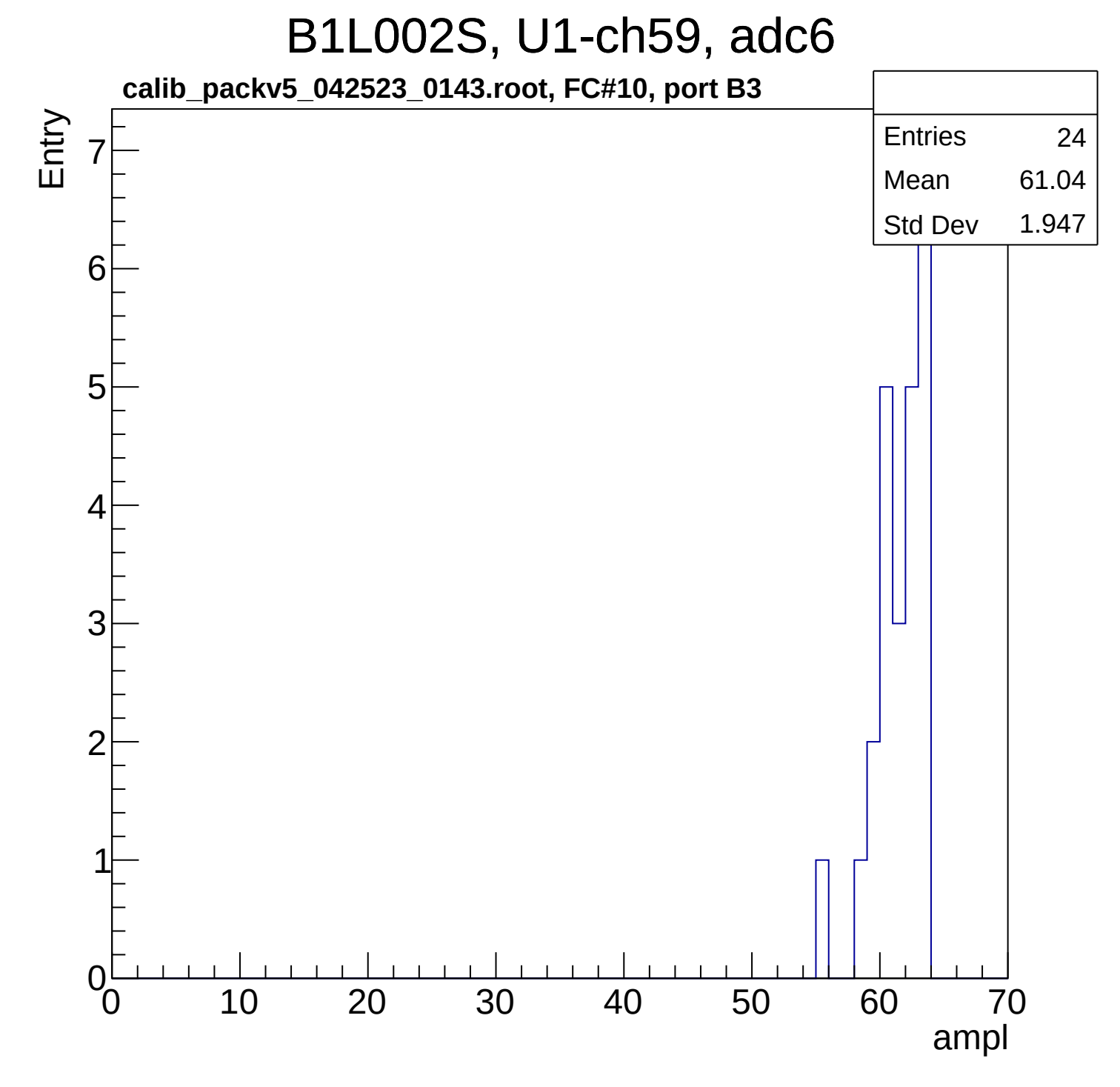
B1L002S, U1-ch59, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61.04
Std Dev 1.947
Entry
ampl
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2
3
4
5
6
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0
10
20
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40
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60
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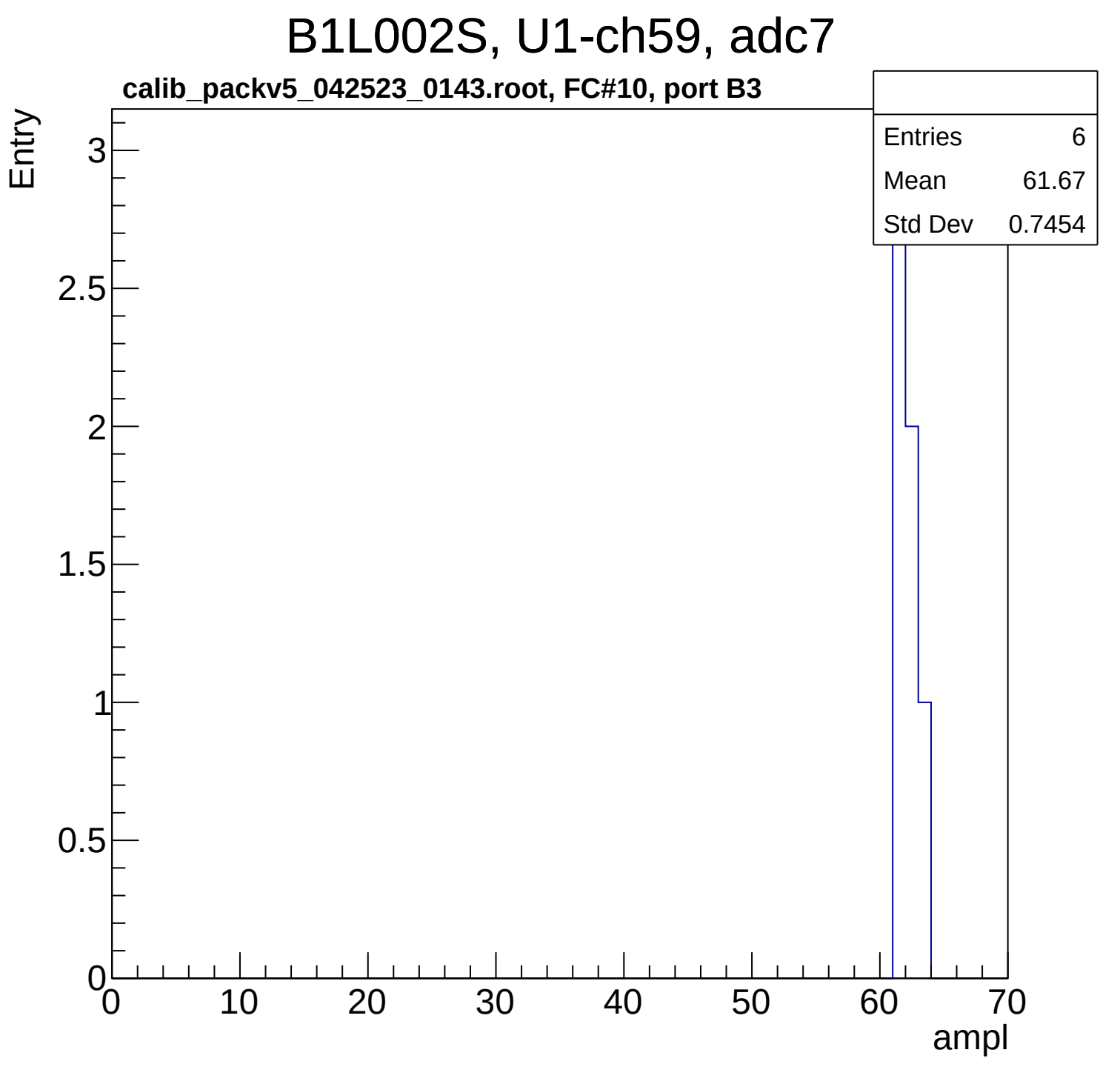
B1L002S, U1-ch59, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 61.67
Std Dev 0.7454
Entry
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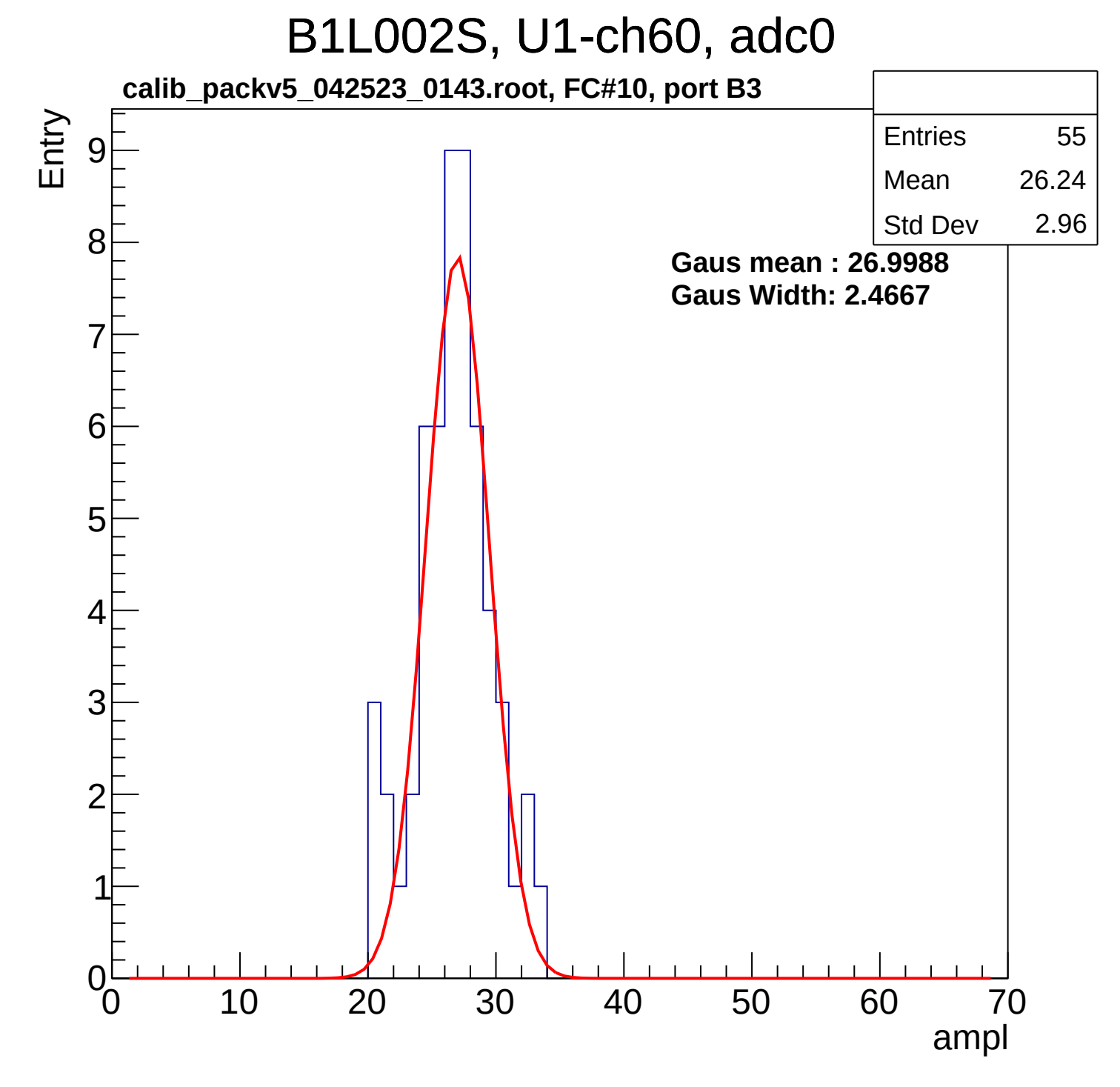
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch60, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 26.24
Std Dev 2.96
Gaus mean : 26.9988
Gaus Width: 2.4667
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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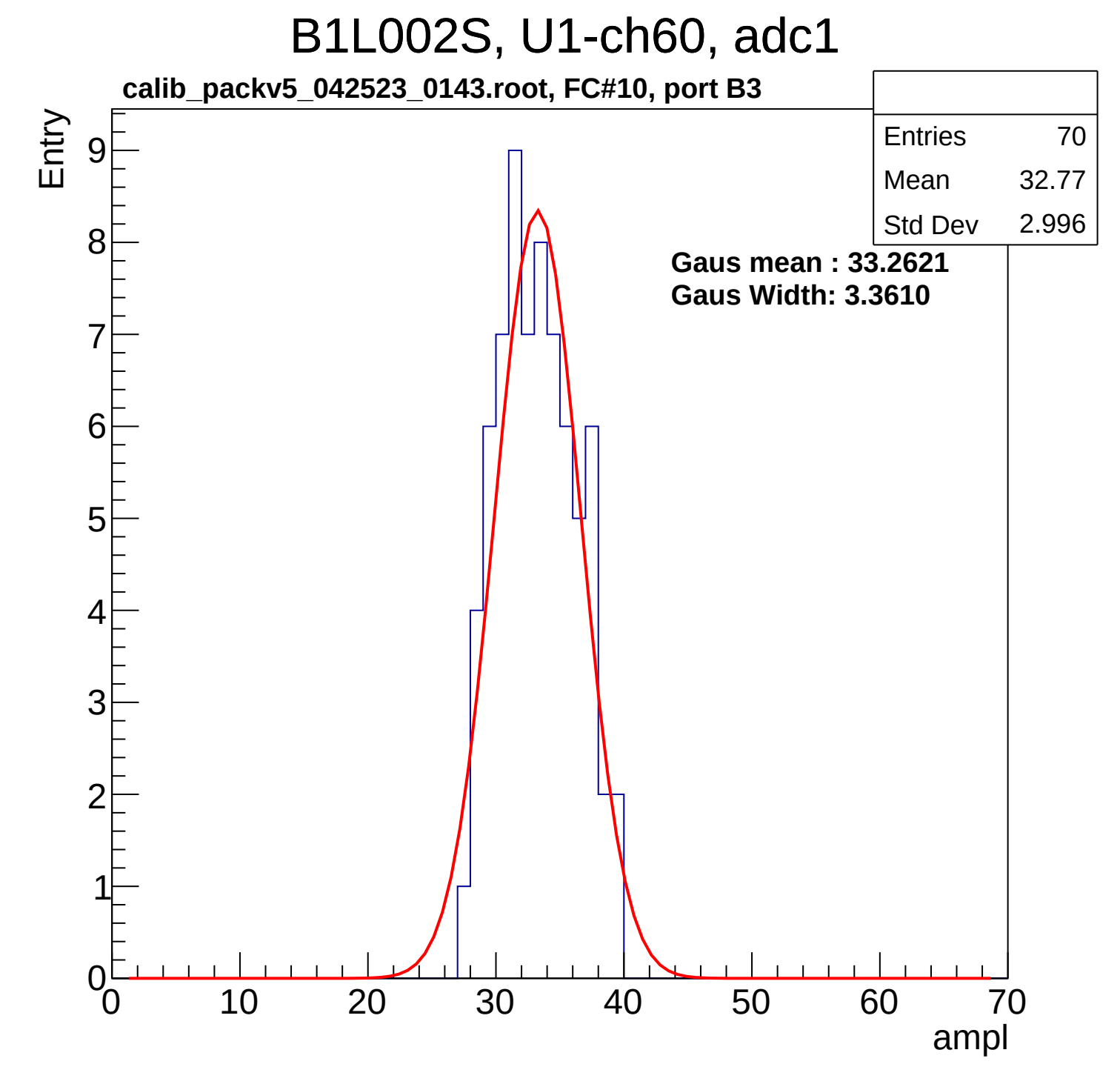
B1L002S, U1-ch60, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 32.77
Std Dev 2.996
Gaus mean : 33.2621
Gaus Width: 3.3610
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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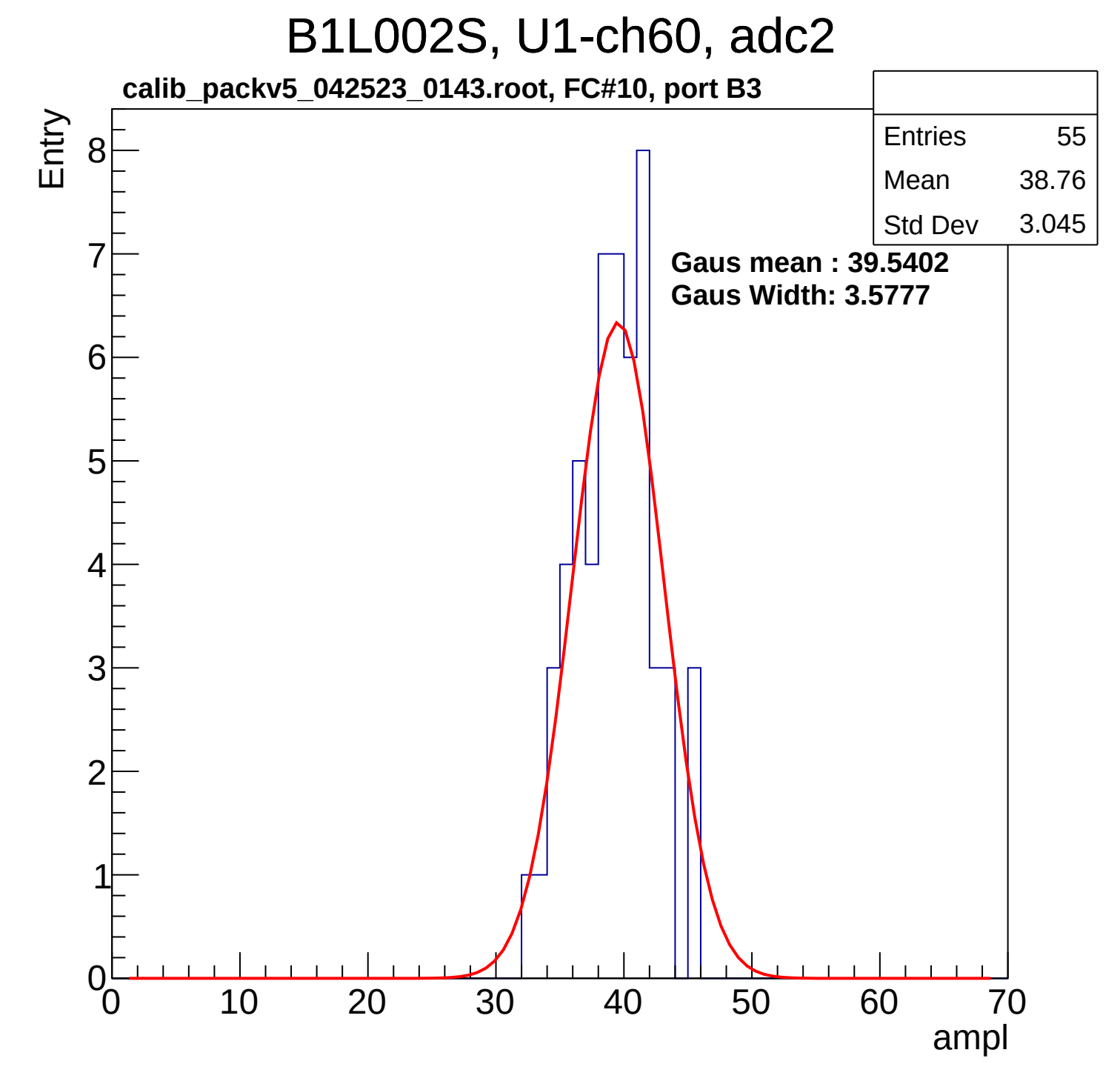
B1L002S, U1-ch60, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 38.76
Std Dev 3.045
Gaus mean : 39.5402
Gaus Width: 3.5777
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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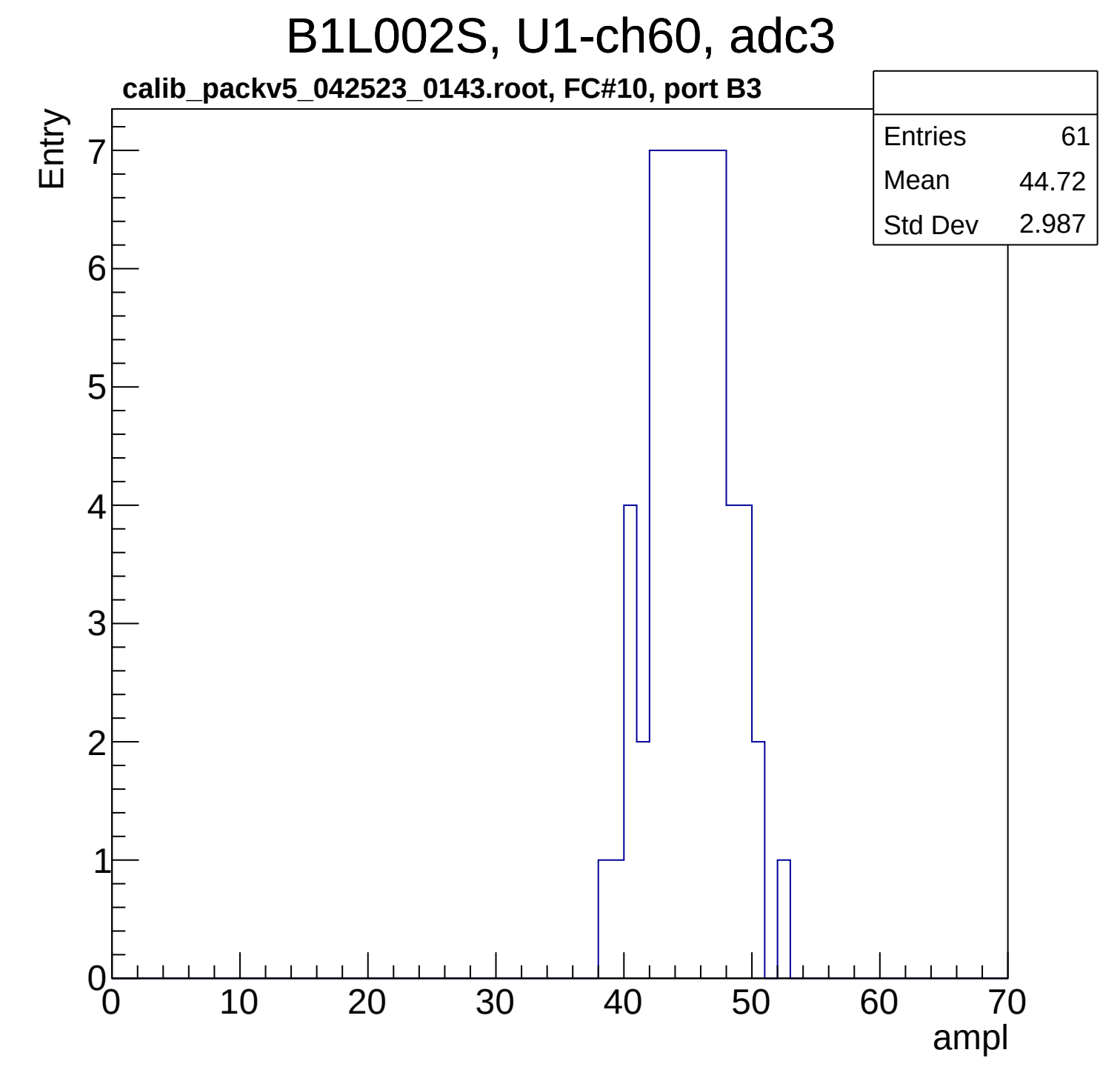
B1L002S, U1-ch60, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 44.72
Std Dev 2.987
Entry
ampl
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4
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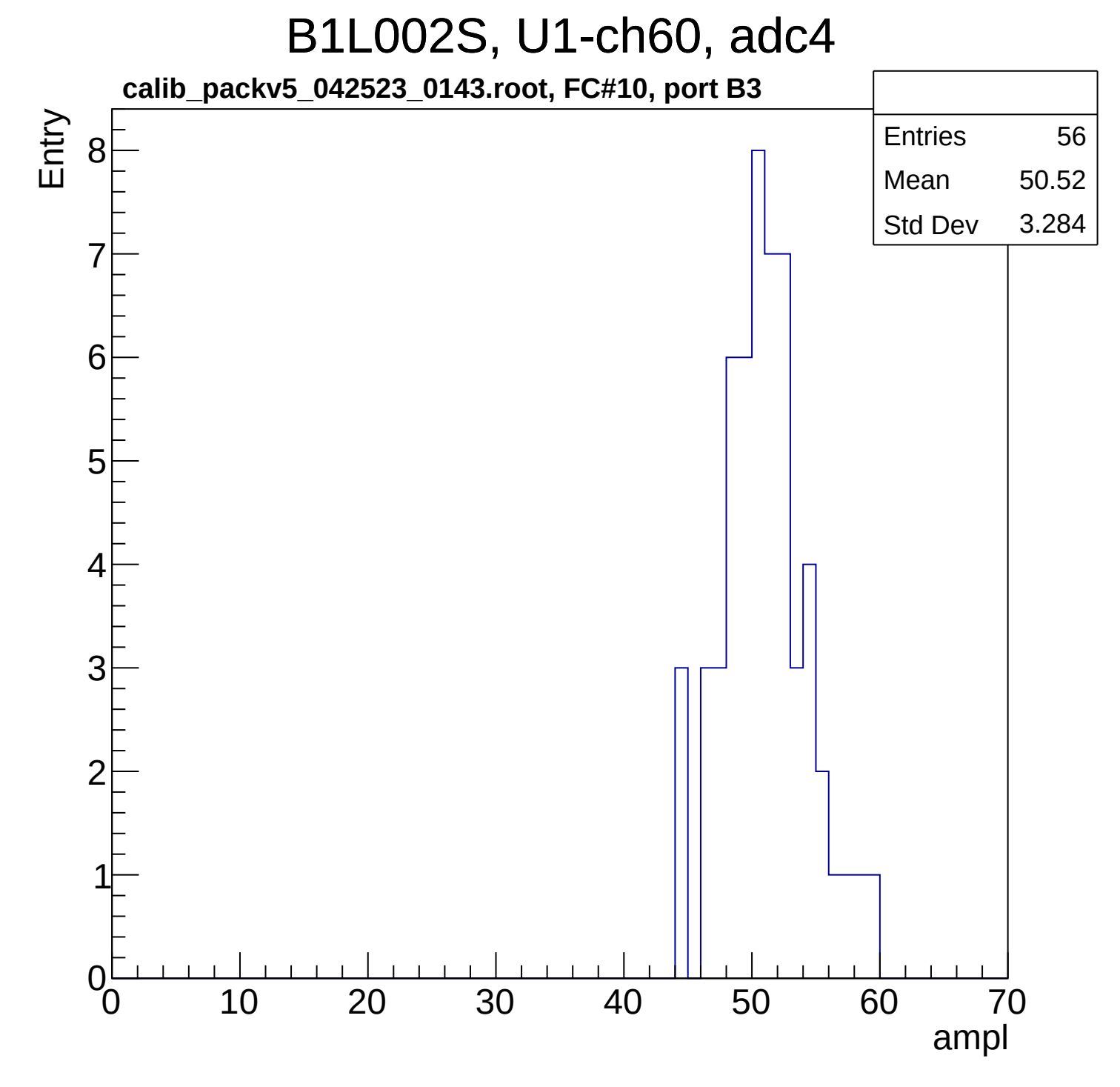
B1L002S, U1-ch60, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 50.52
Std Dev 3.284
Entry
ampl
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4
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0
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60
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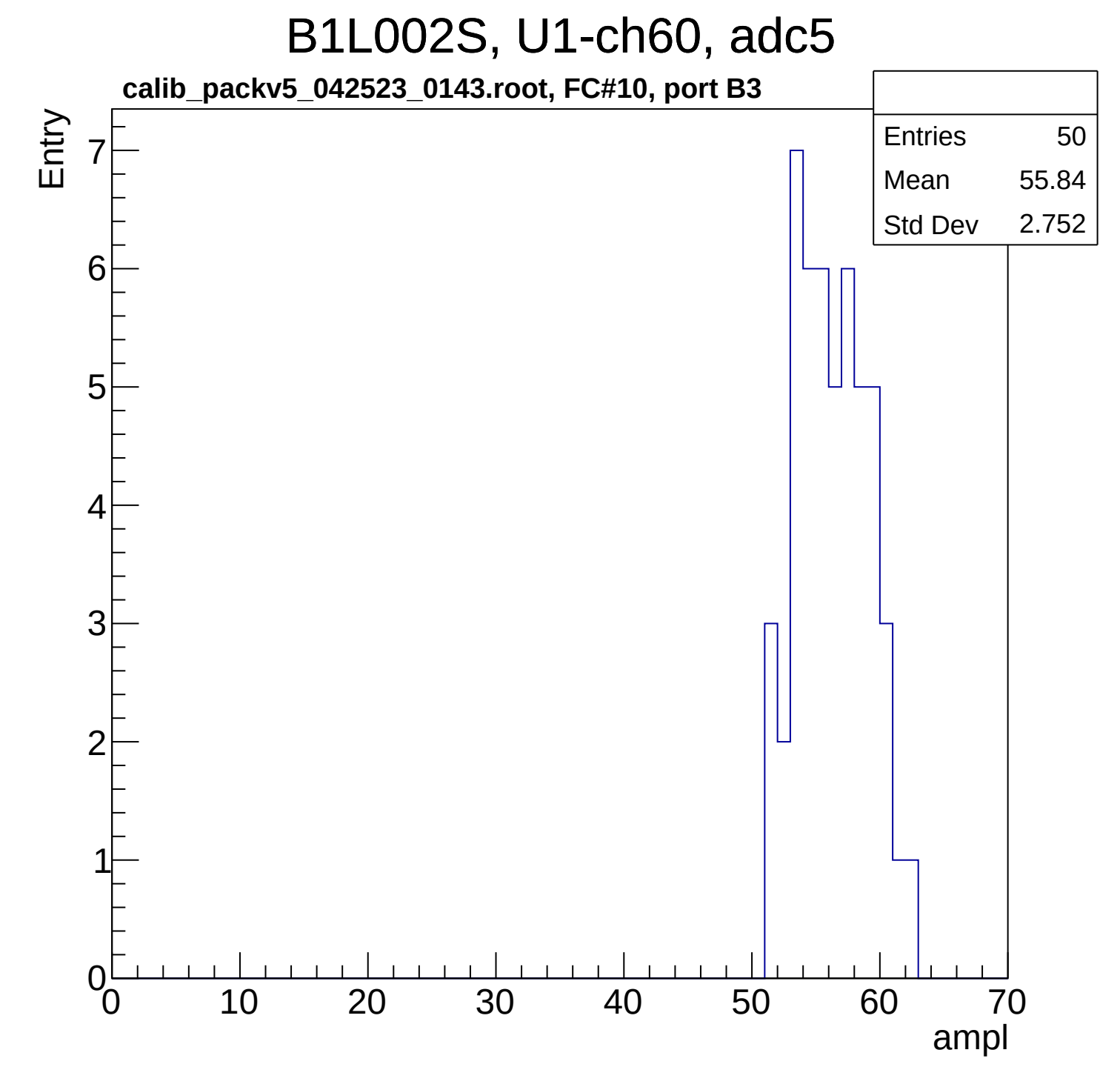
B1L002S, U1-ch60, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 55.84
Std Dev 2.752
Entry
ampl
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0
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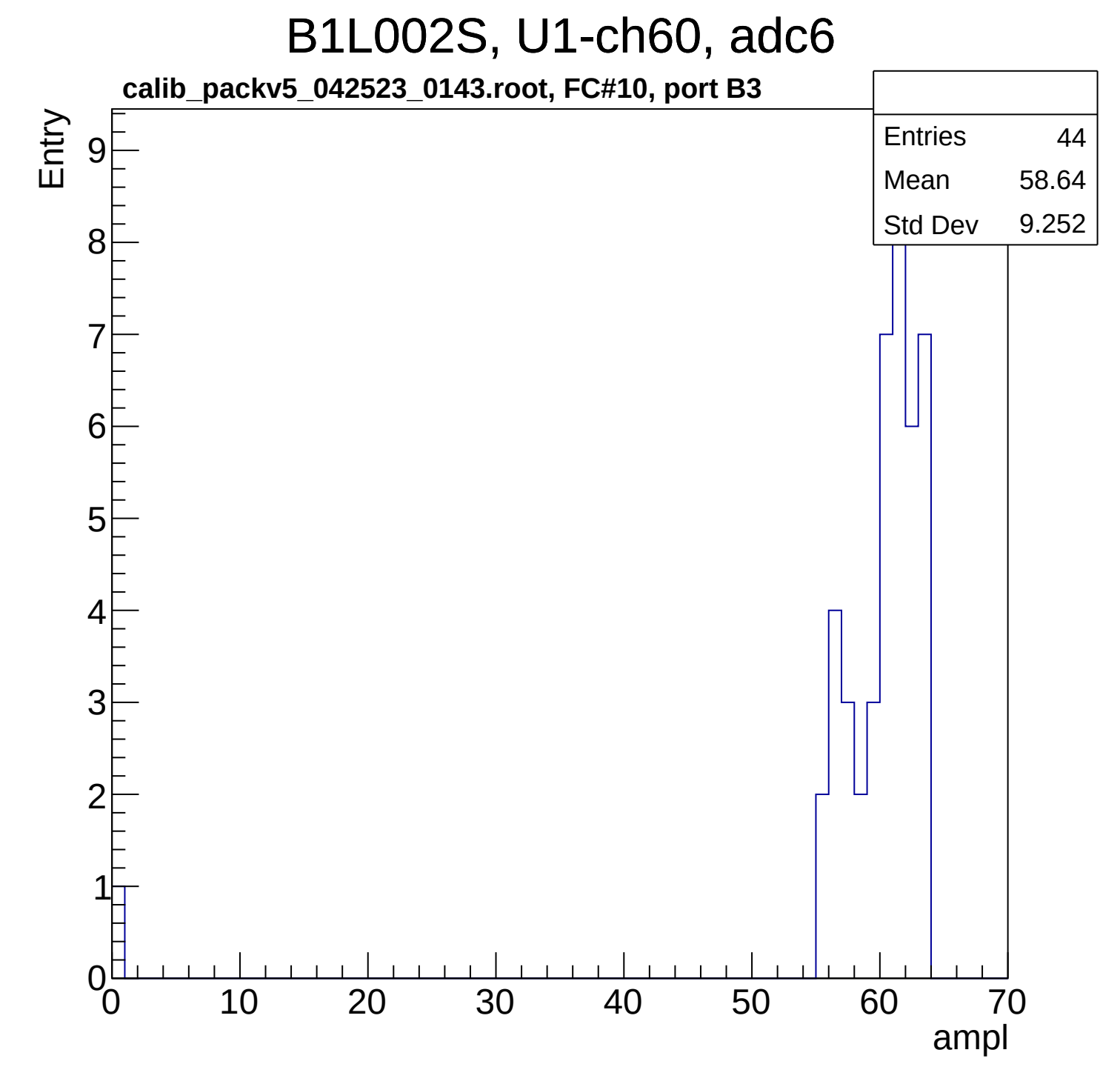
B1L002S, U1-ch60, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.64
Std Dev 9.252
Entry
ampl
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0
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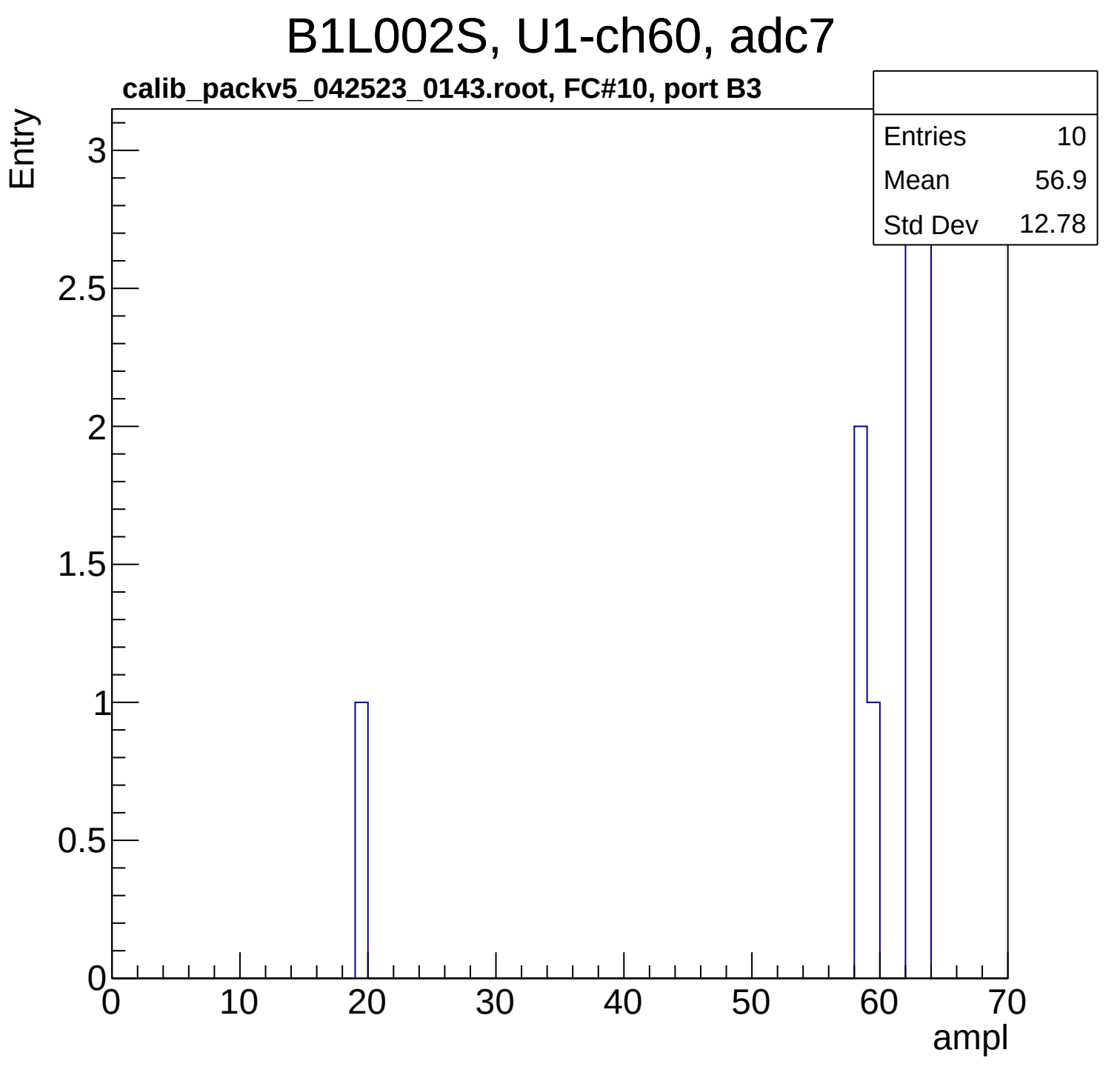
B1L002S, U1-ch60, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 56.9
Std Dev 12.78
Entry
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1
1.5
2
2.5
3
0
10
20
30
40
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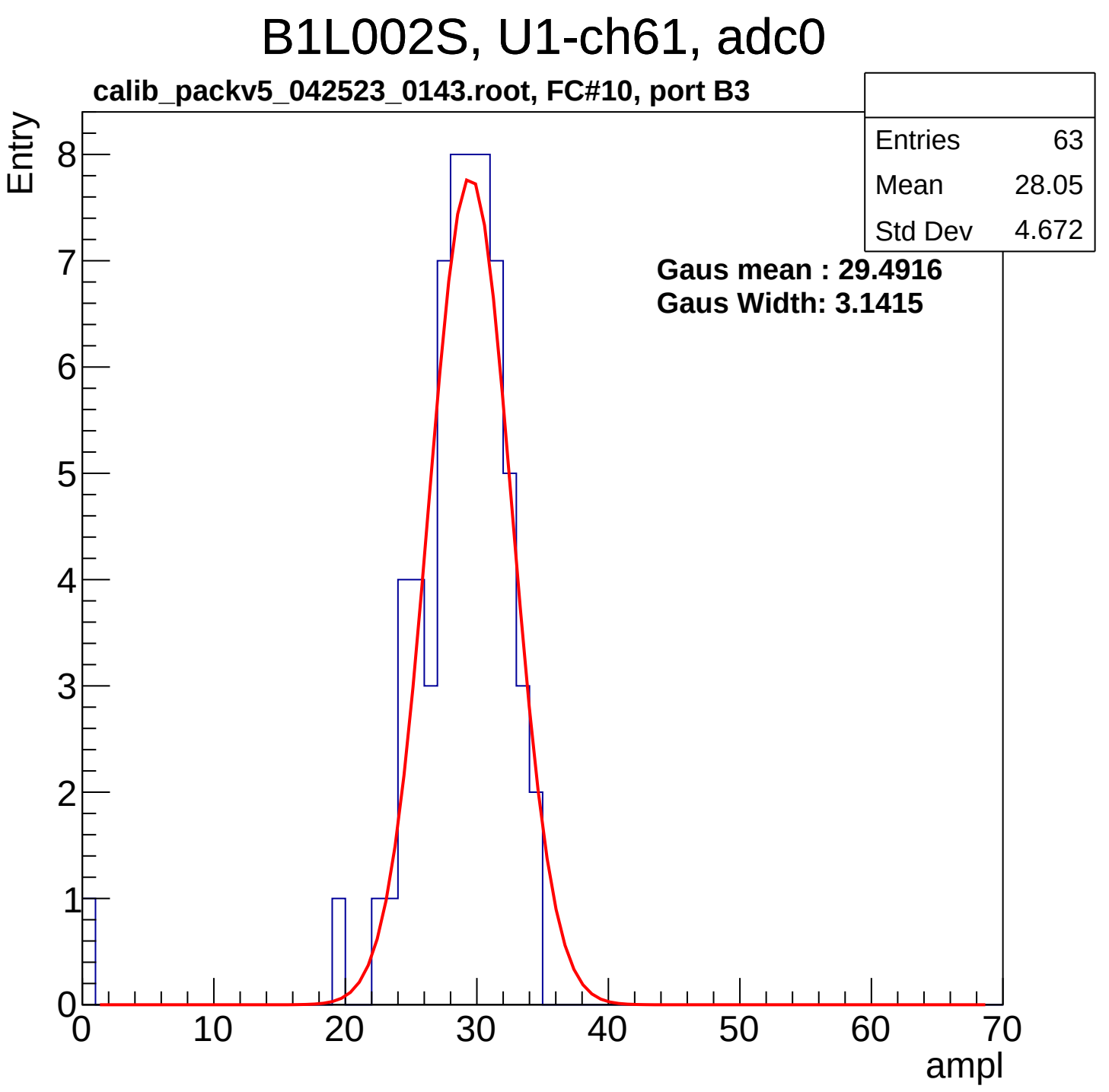
70
ampl


B1L002S, U1-ch61, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.05
Std Dev 4.672
Gaus mean : 29.4916
Gaus Width: 3.1415
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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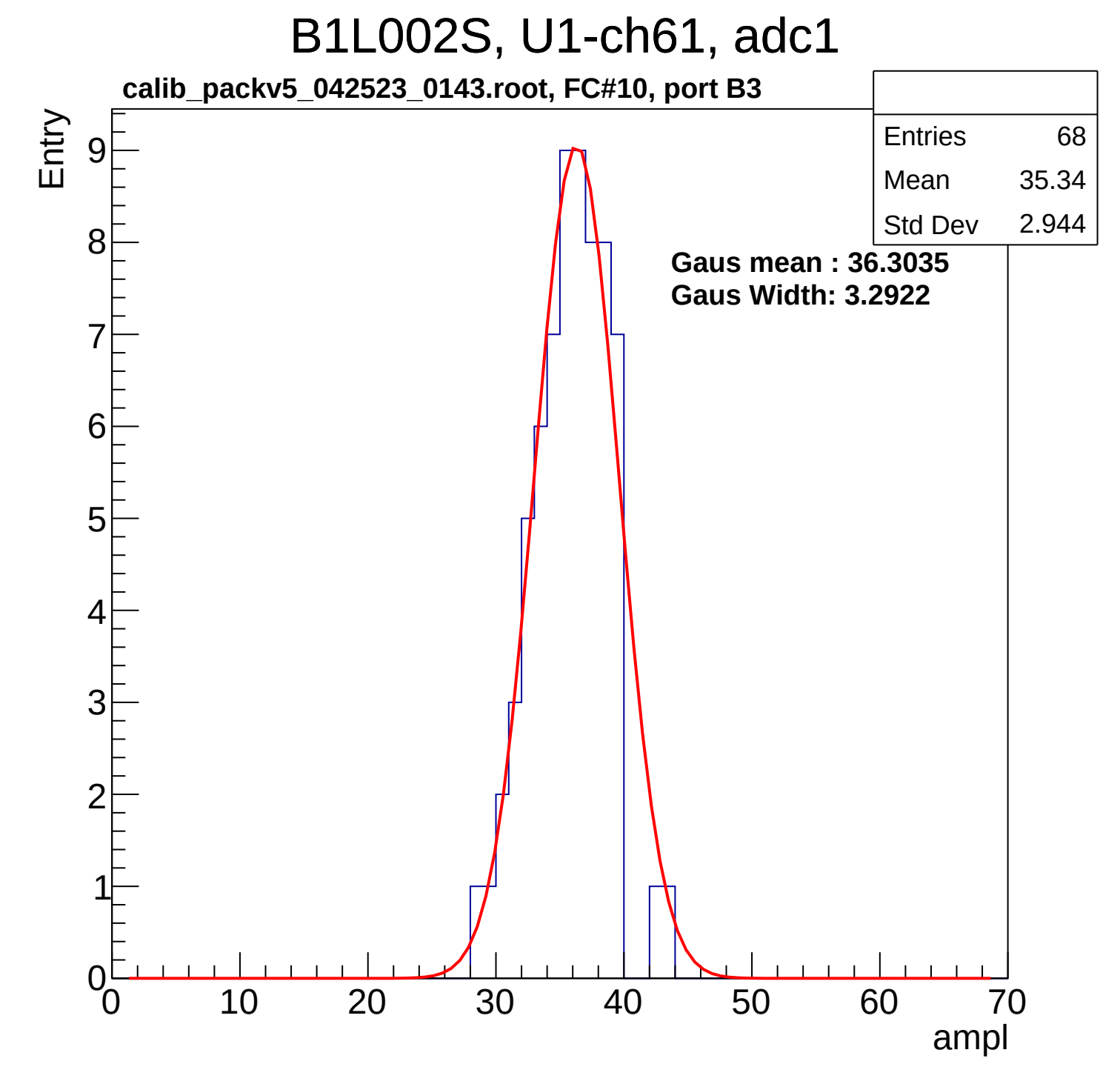
B1L002S, U1-ch61, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.34
Std Dev 2.944
Gaus mean : 36.3035
Gaus Width: 3.2922
Entry
ampl
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3
4
5
6
7
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0
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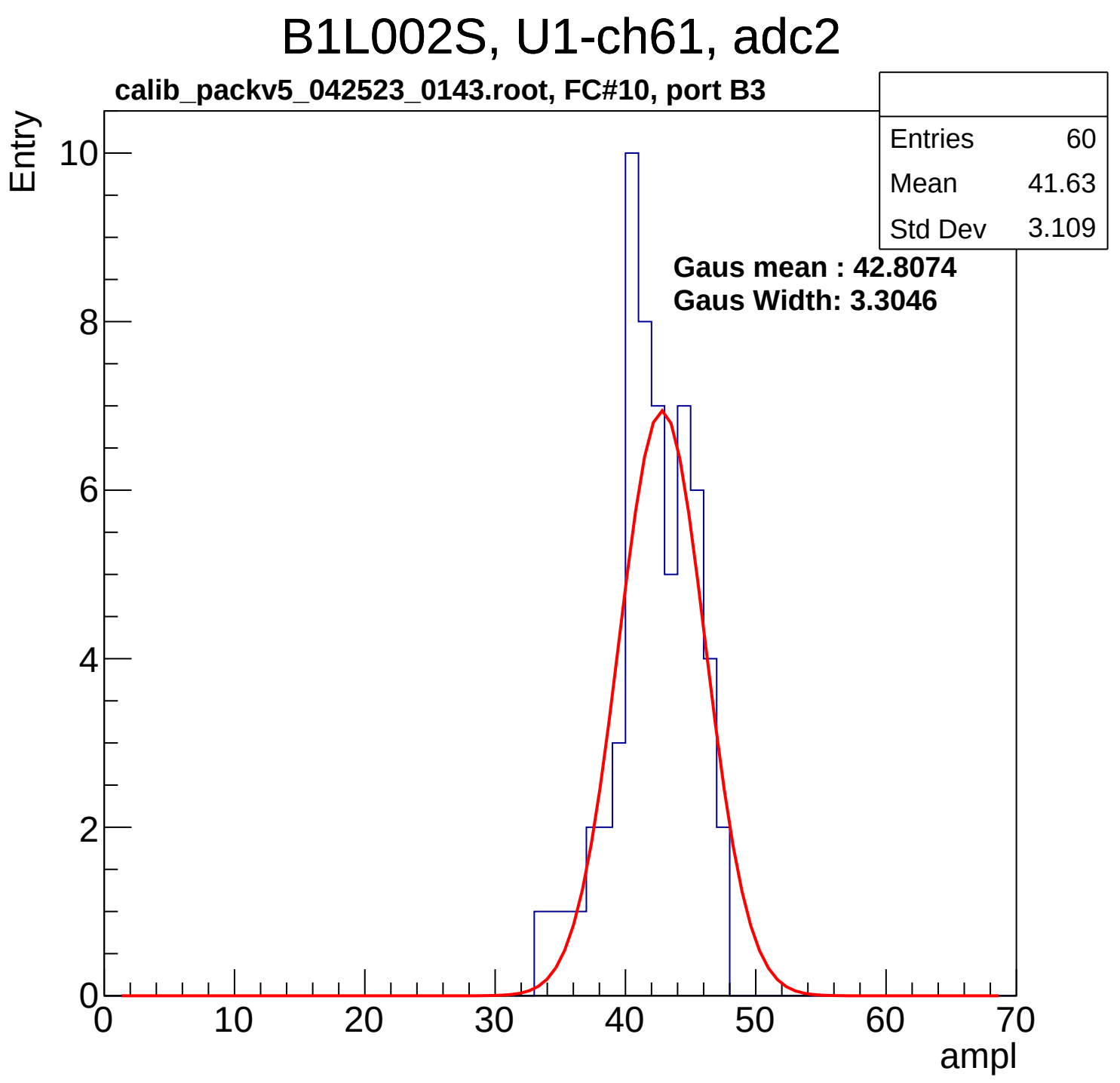
B1L002S, U1-ch61, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.63
Std Dev 3.109
Gaus mean : 42.8074
Gaus Width: 3.3046
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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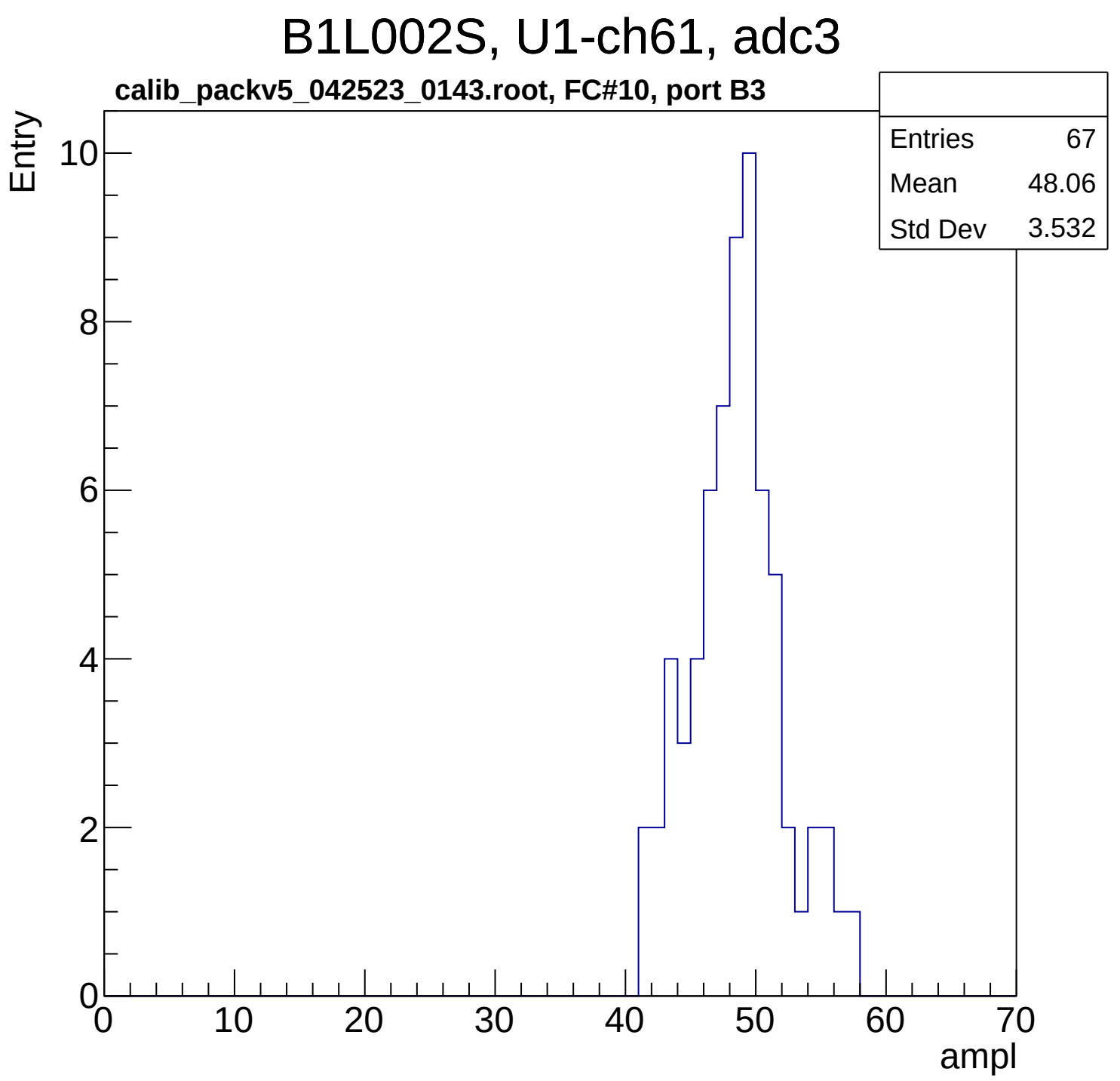
B1L002S, U1-ch61, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 48.06
Std Dev 3.532
Entry
ampl
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4
6
8
10
0
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20
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60
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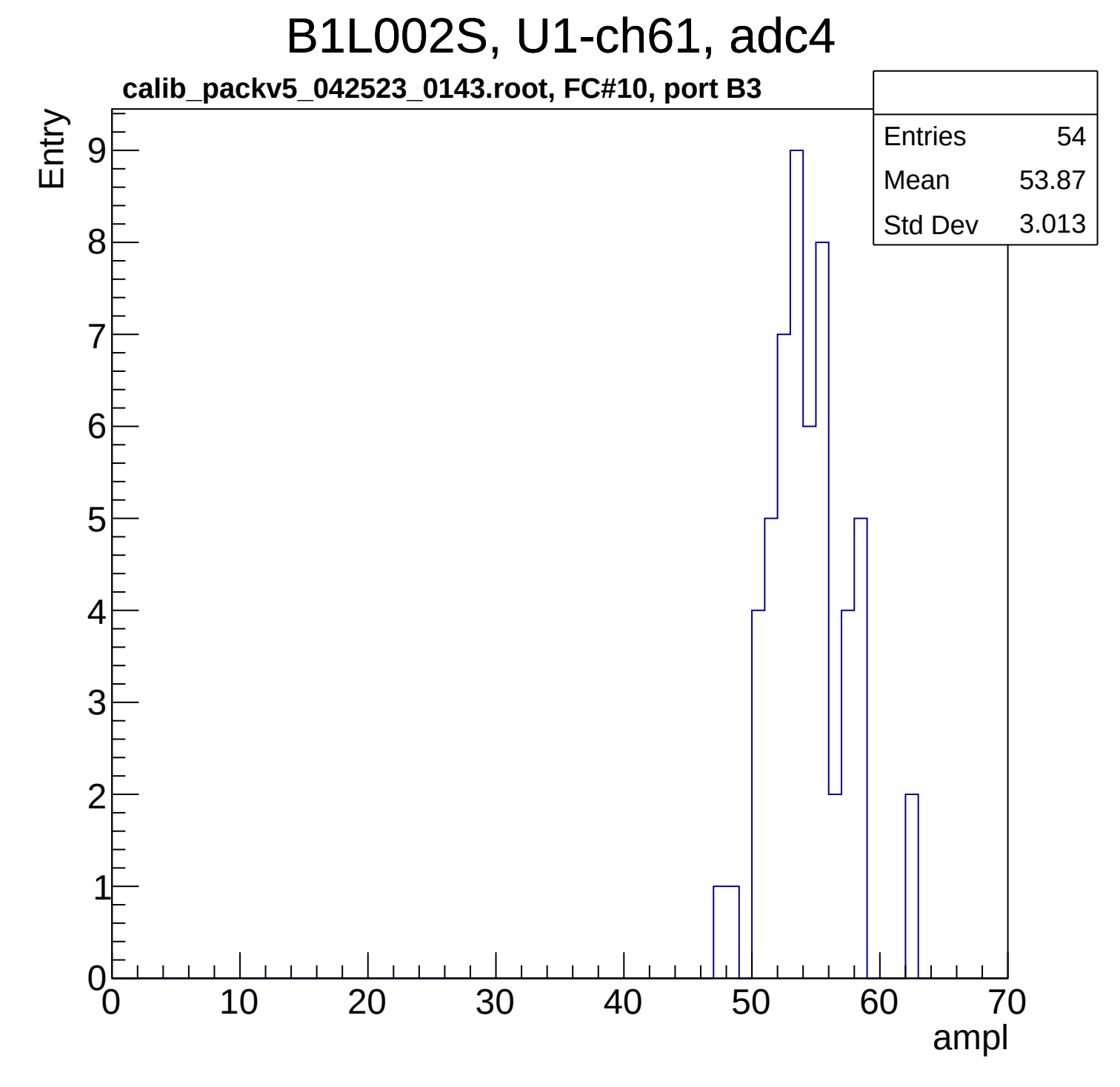
B1L002S, U1-ch61, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.87
Std Dev 3.013
Entry
ampl
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2
3
4
5
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8
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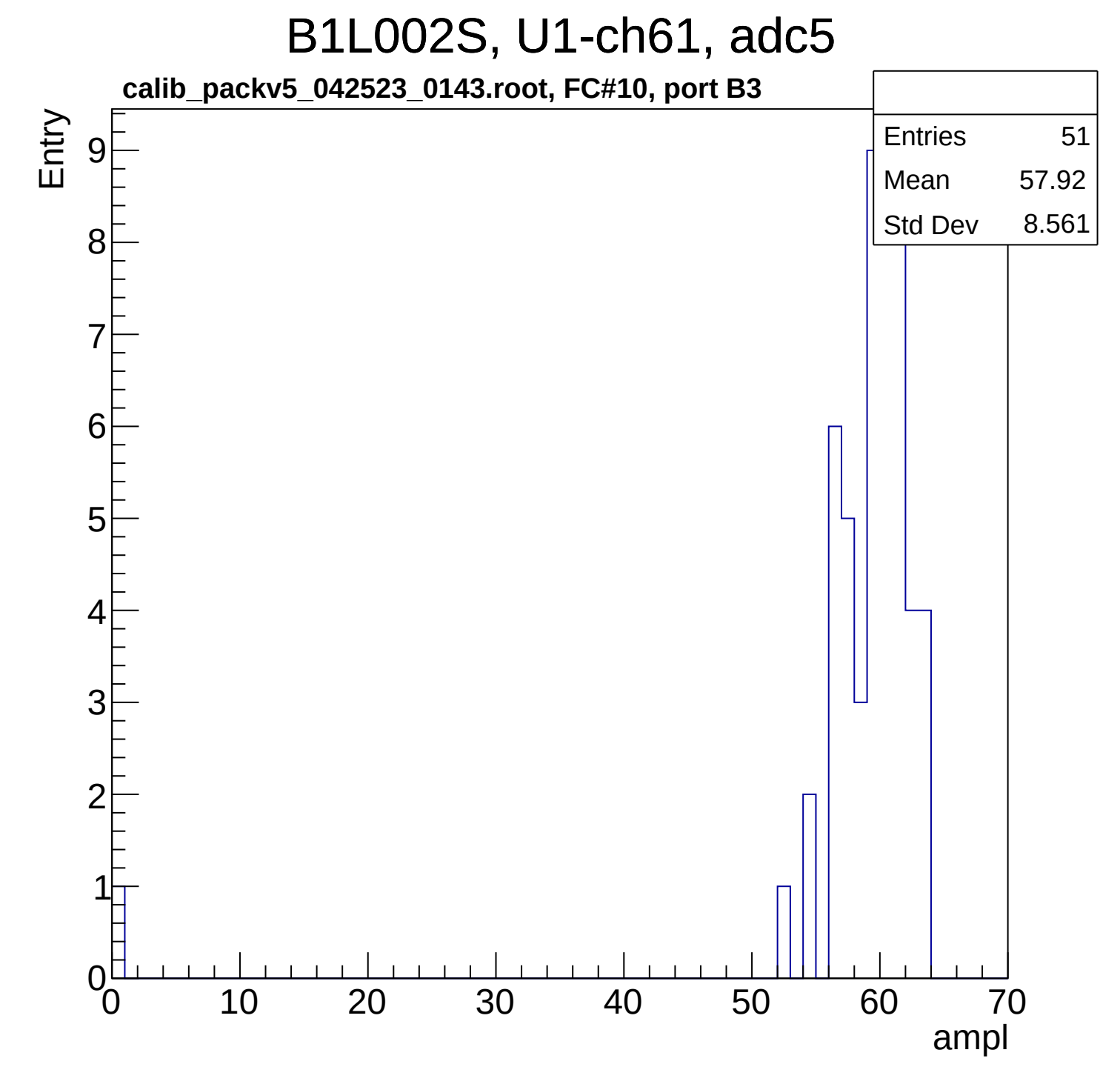
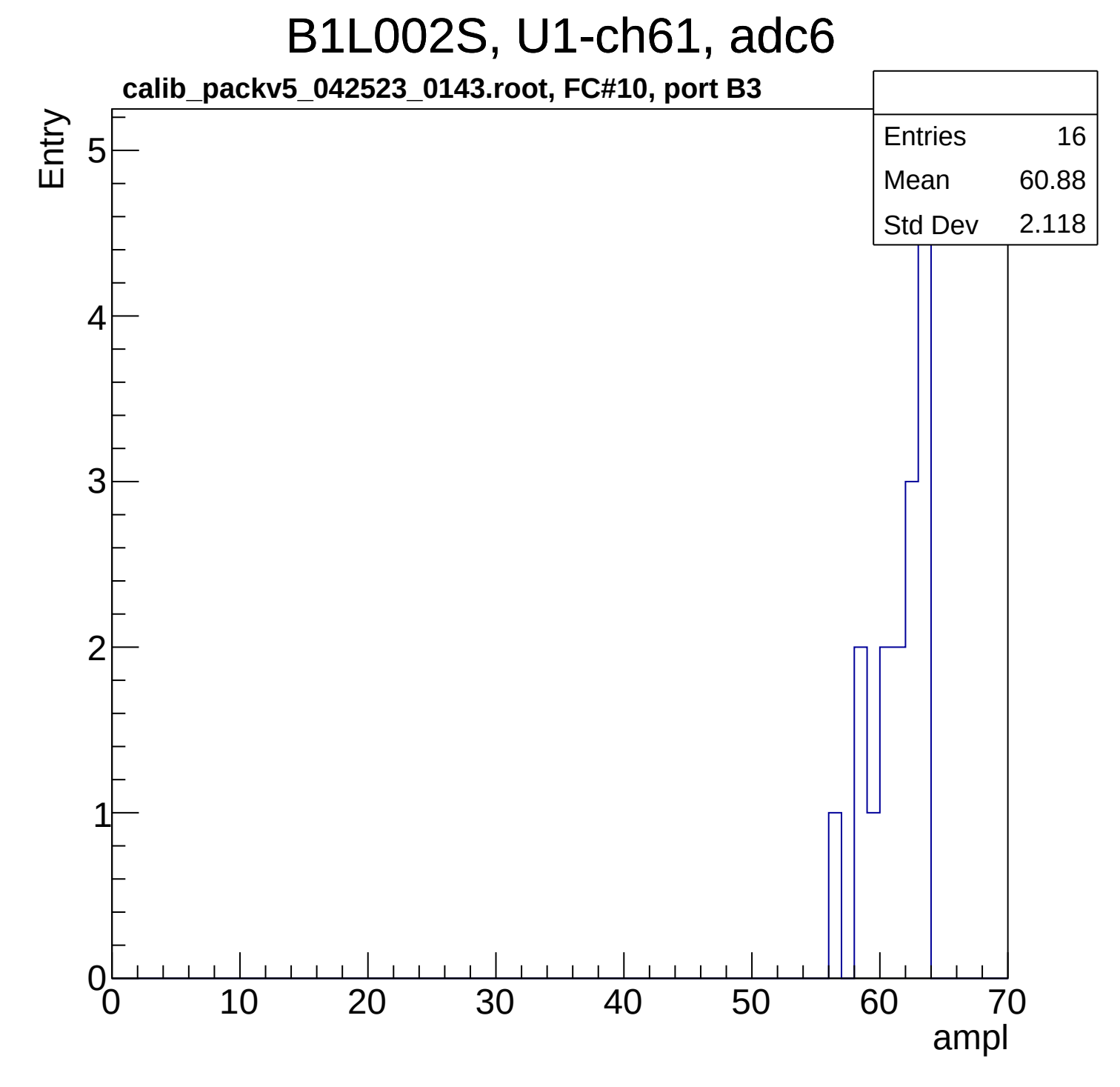

B1L002S, U1-ch61, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.92
Std Dev 8.561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch61, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 60.88
Std Dev 2.118
Entry
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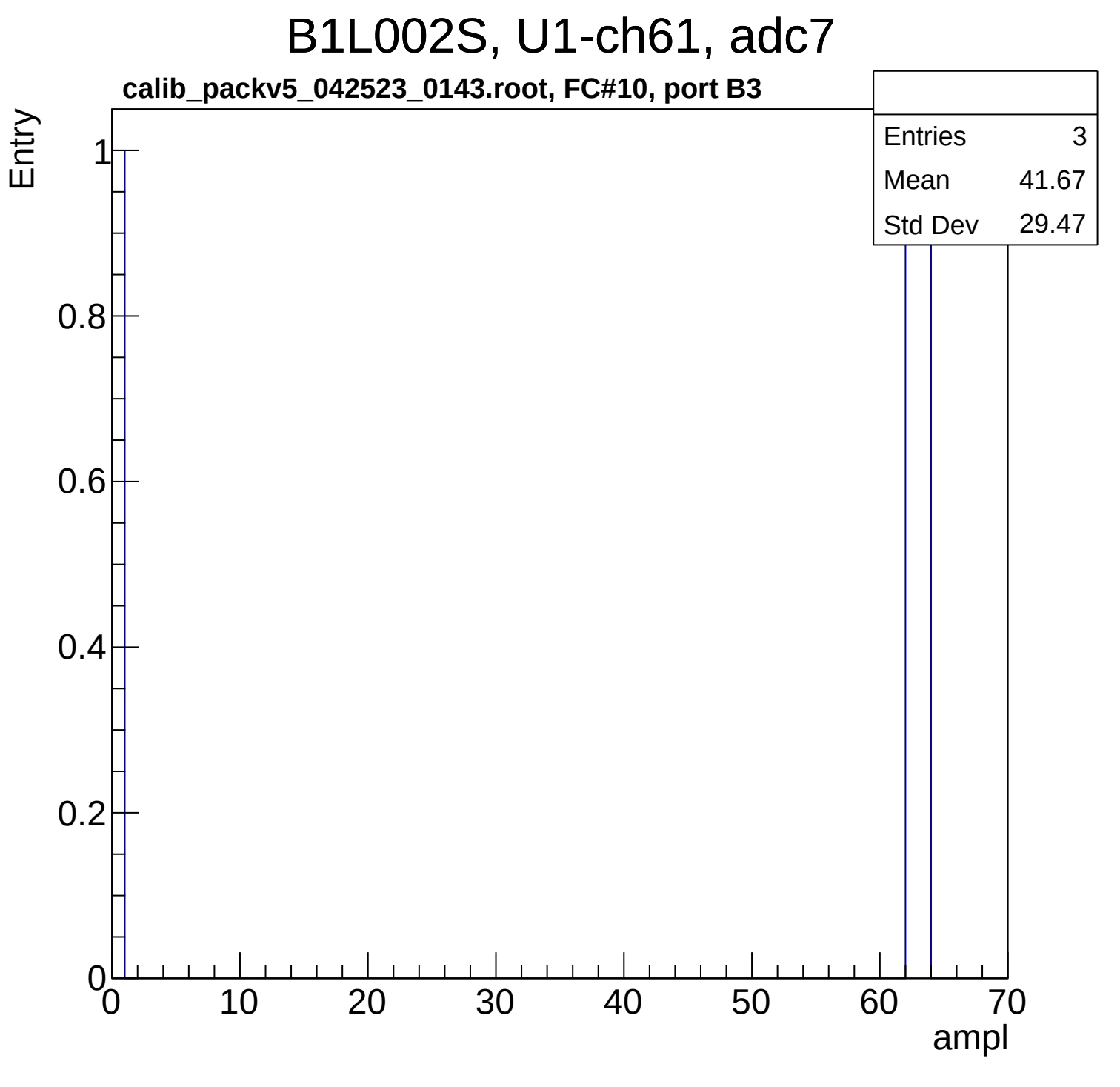
ampl


B1L002S, U1-ch61, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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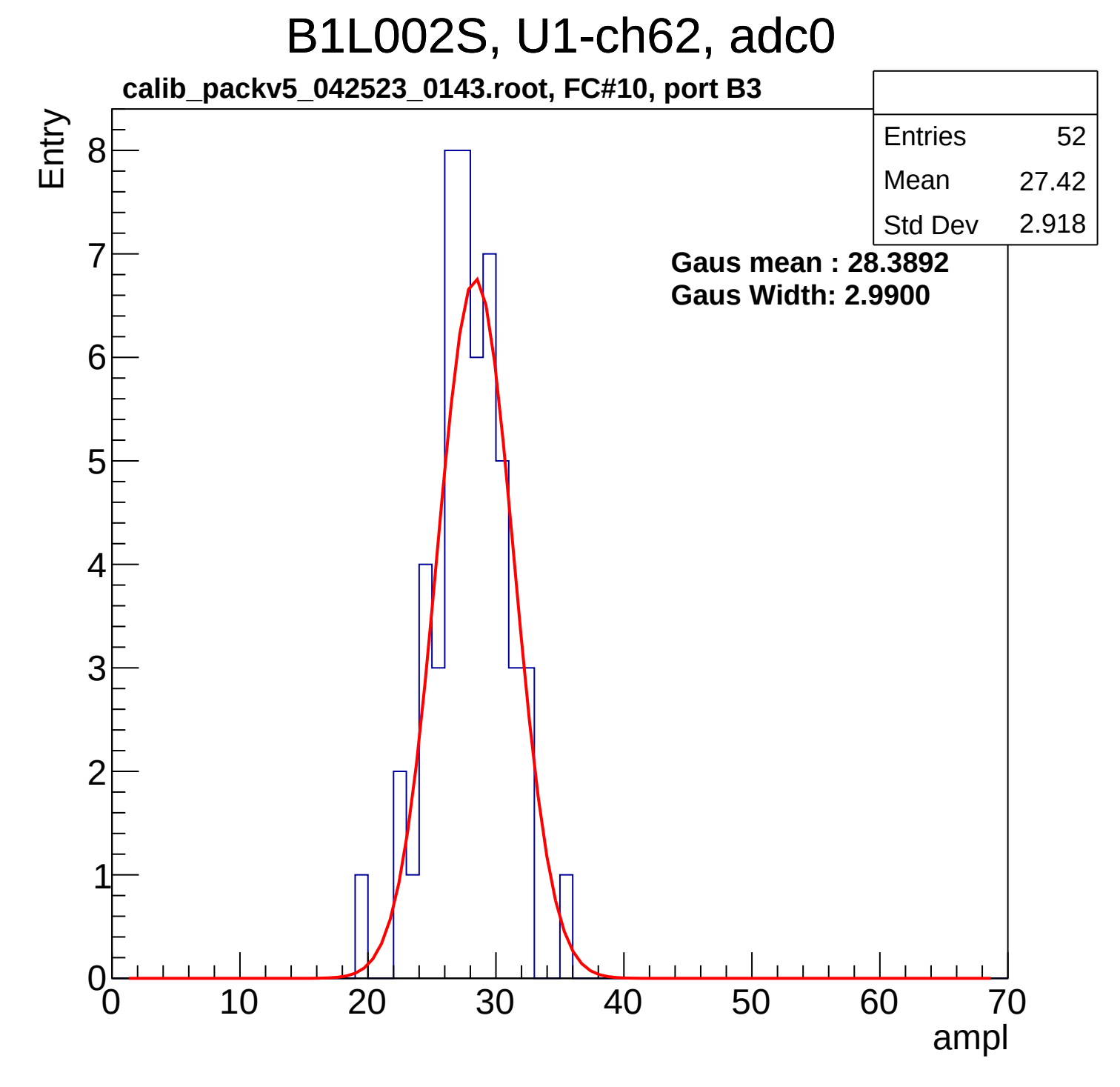
ampl


B1L002S, U1-ch62, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 27.42
Std Dev 2.918
Gaus mean : 28.3892
Gaus Width: 2.9900
Entry
ampl
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2
3
4
5
6
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0
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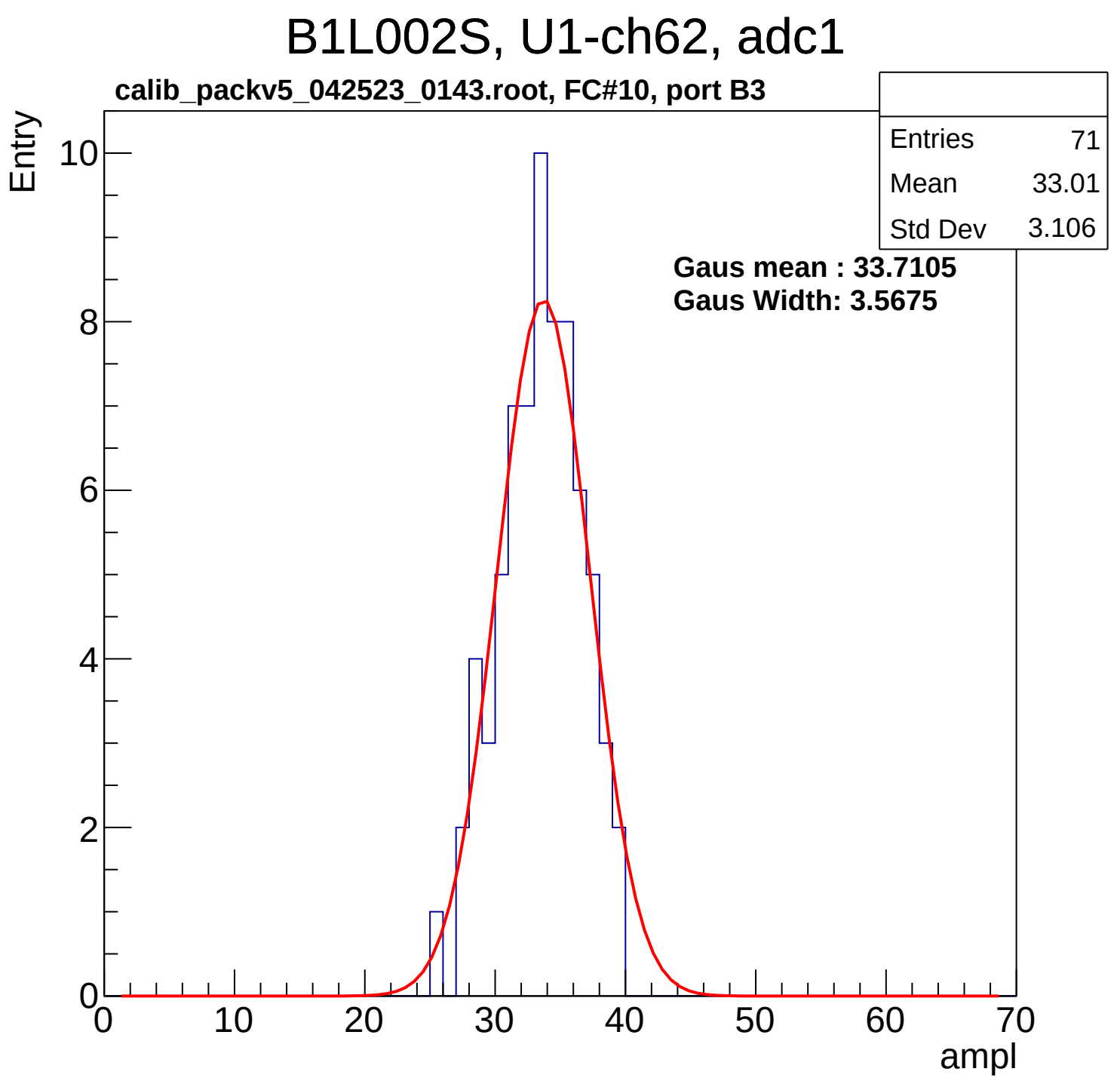
B1L002S, U1-ch62, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 33.01
Std Dev 3.106
Gaus mean : 33.7105
Gaus Width: 3.5675
Entry
ampl
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60
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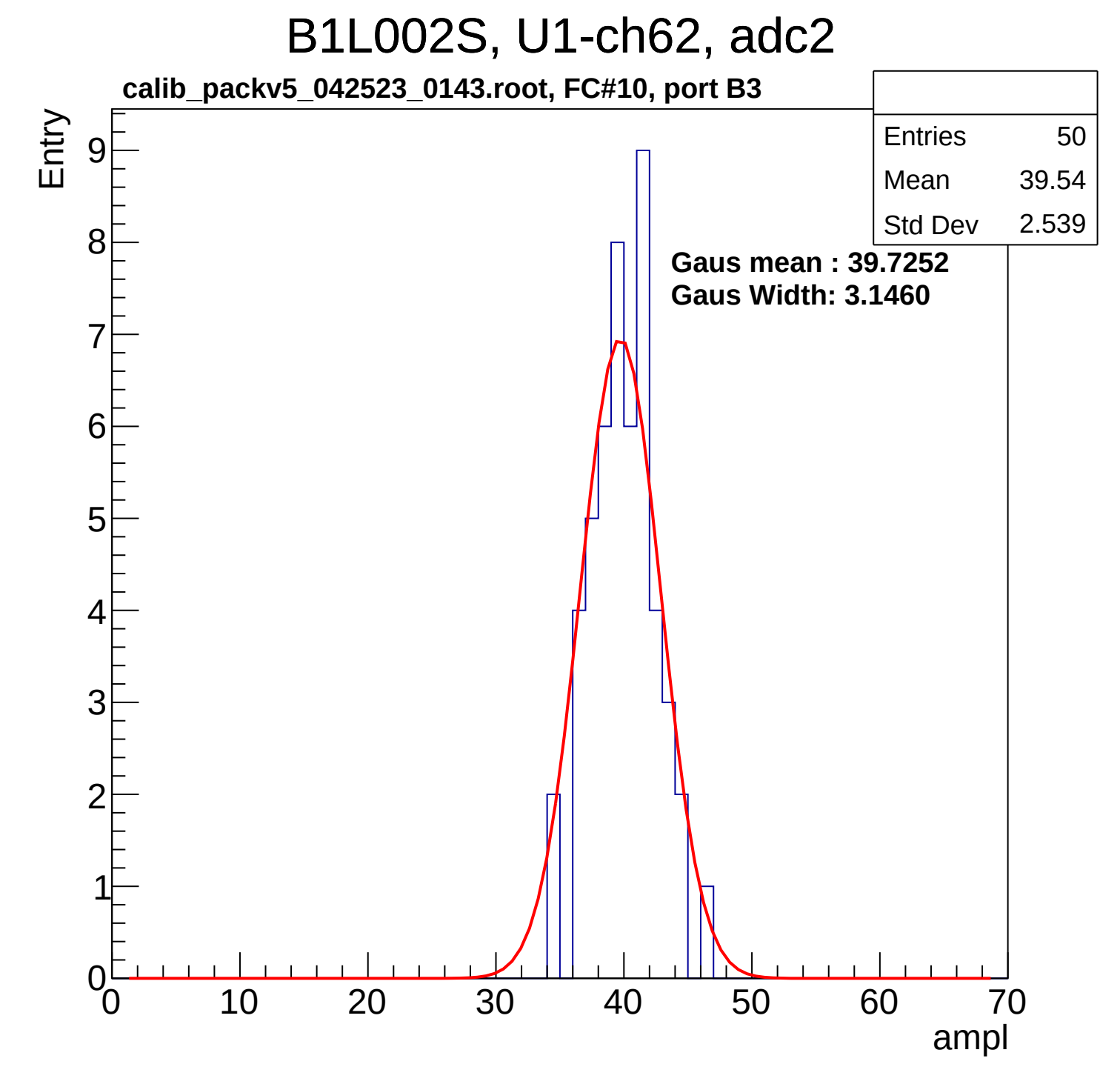
B1L002S, U1-ch62, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 39.54
Std Dev 2.539
Gaus mean : 39.7252
Gaus Width: 3.1460
Entry
ampl
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4
5
6
7
8
9
0
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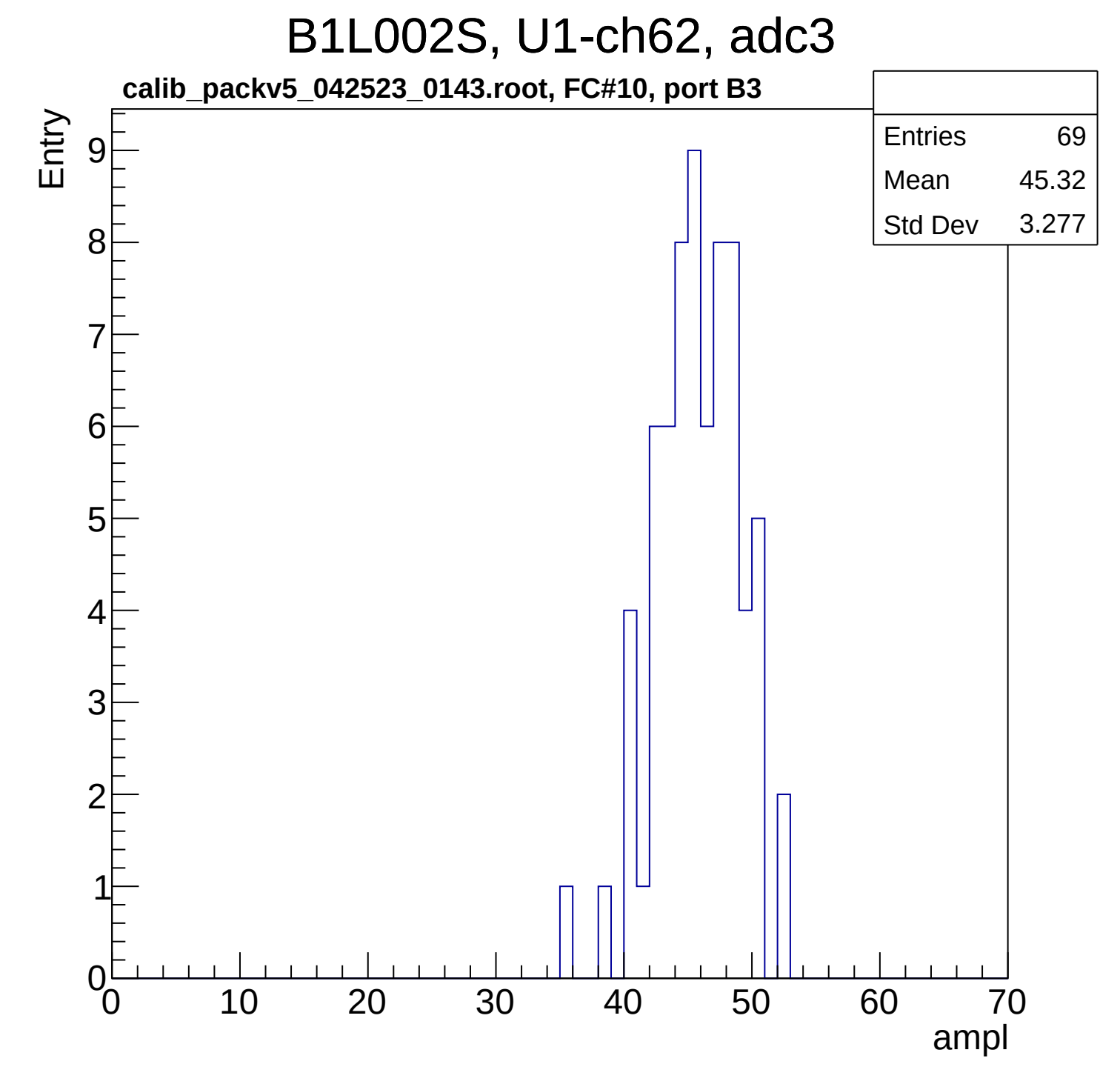
B1L002S, U1-ch62, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 45.32
Std Dev 3.277
Entry
ampl
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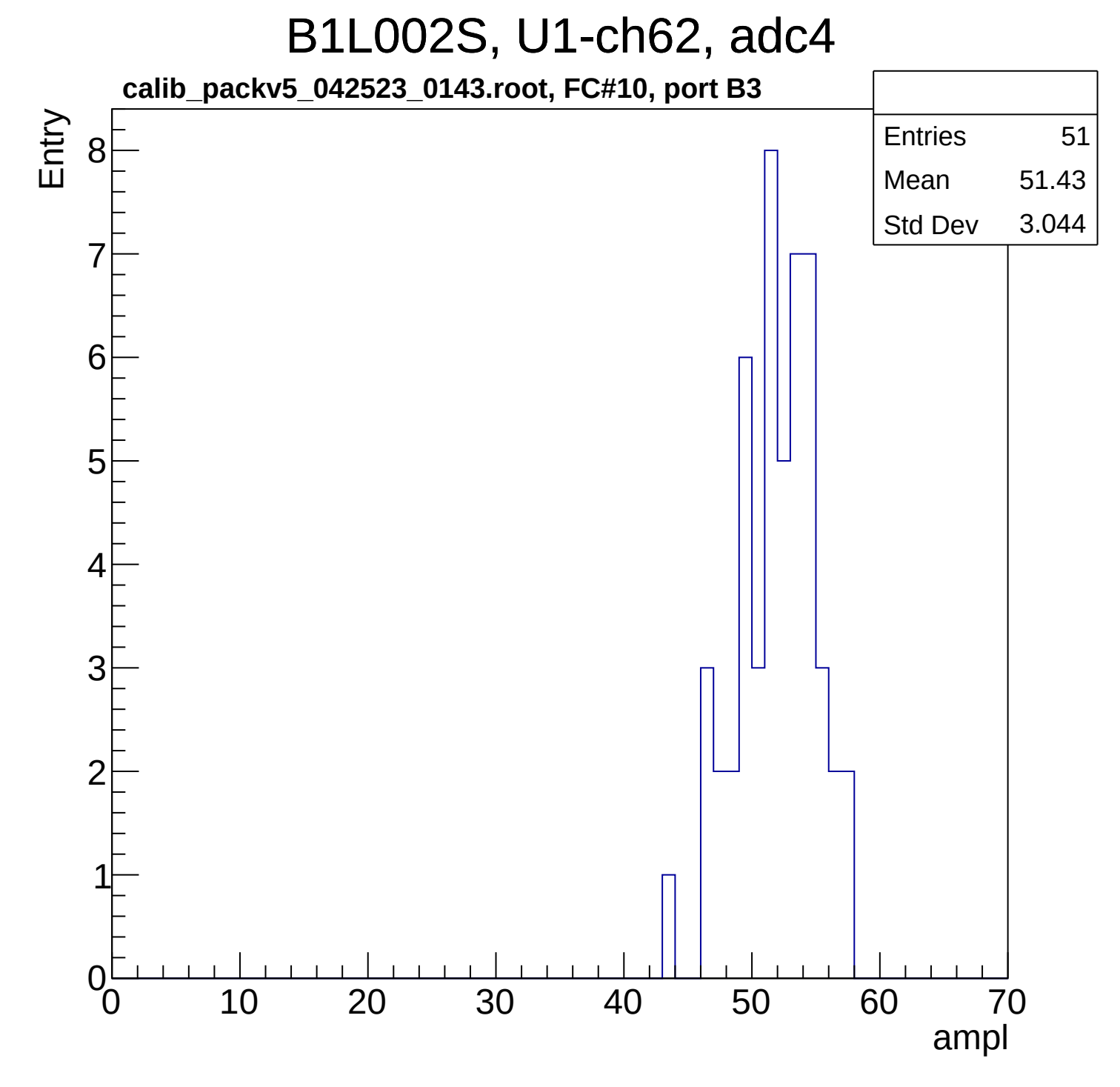
B1L002S, U1-ch62, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 51.43
Std Dev 3.044
Entry
ampl
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2
3
4
5
6
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0
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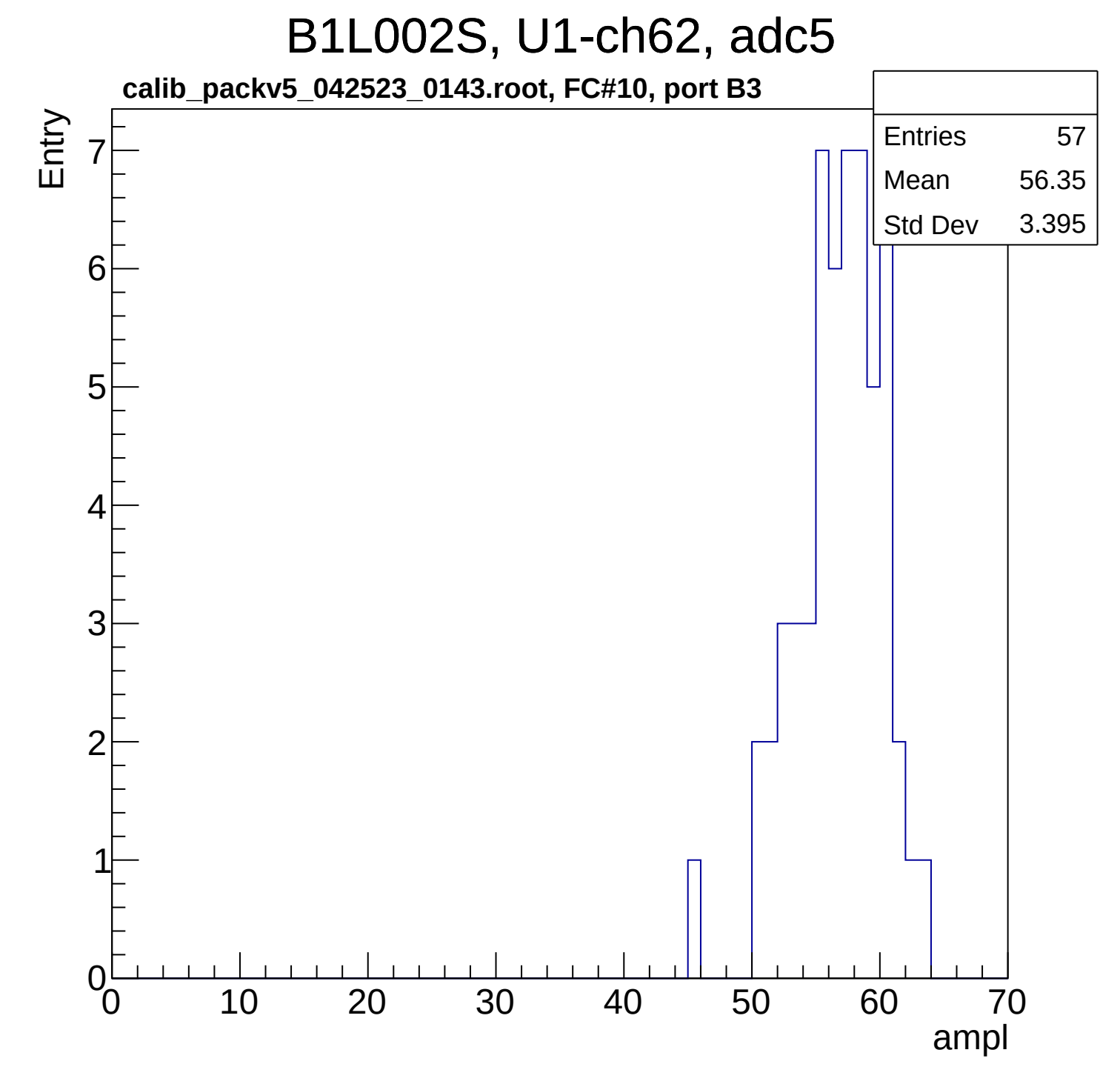
B1L002S, U1-ch62, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 56.35
Std Dev 3.395
Entry
ampl
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4
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0
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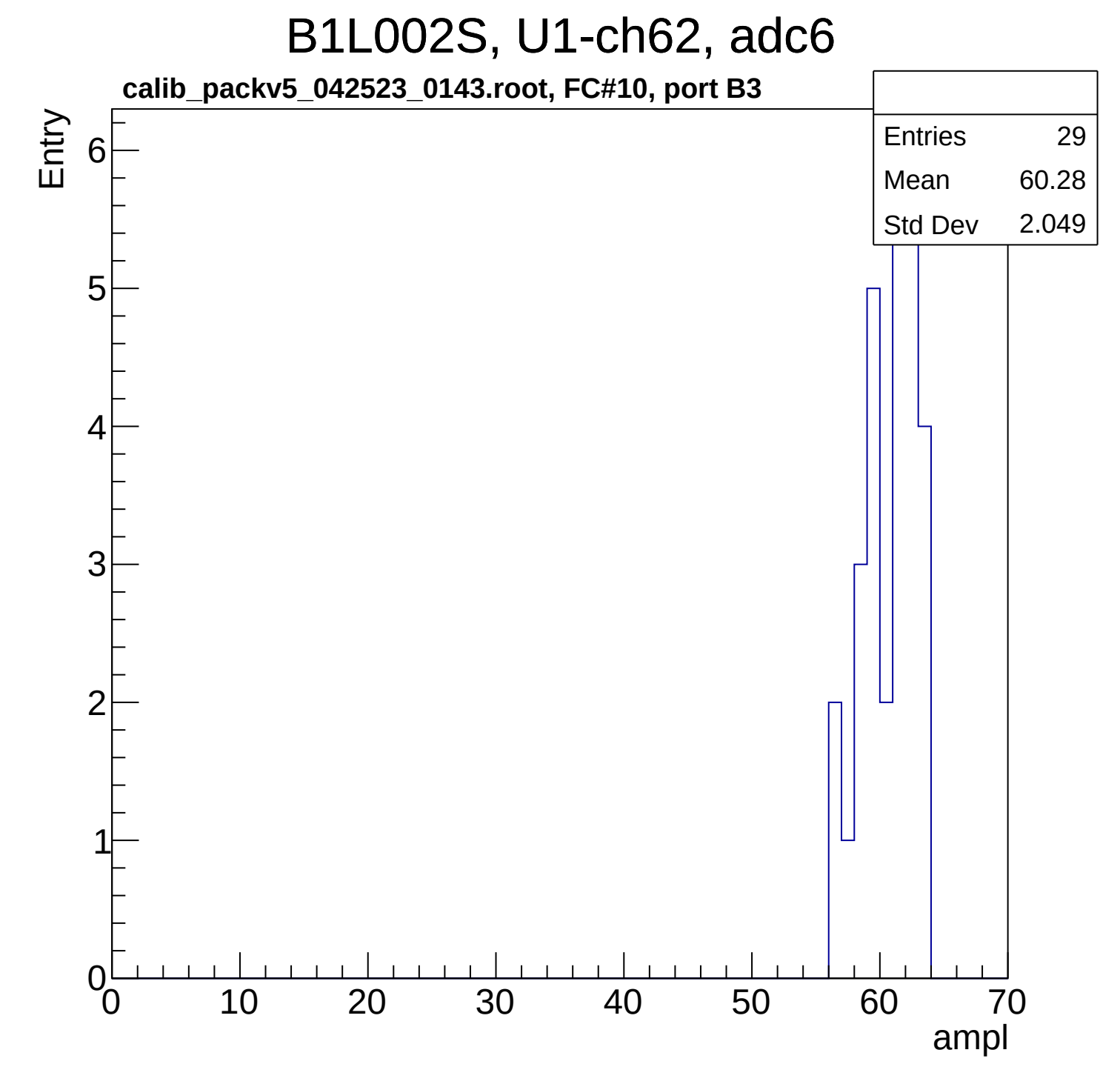
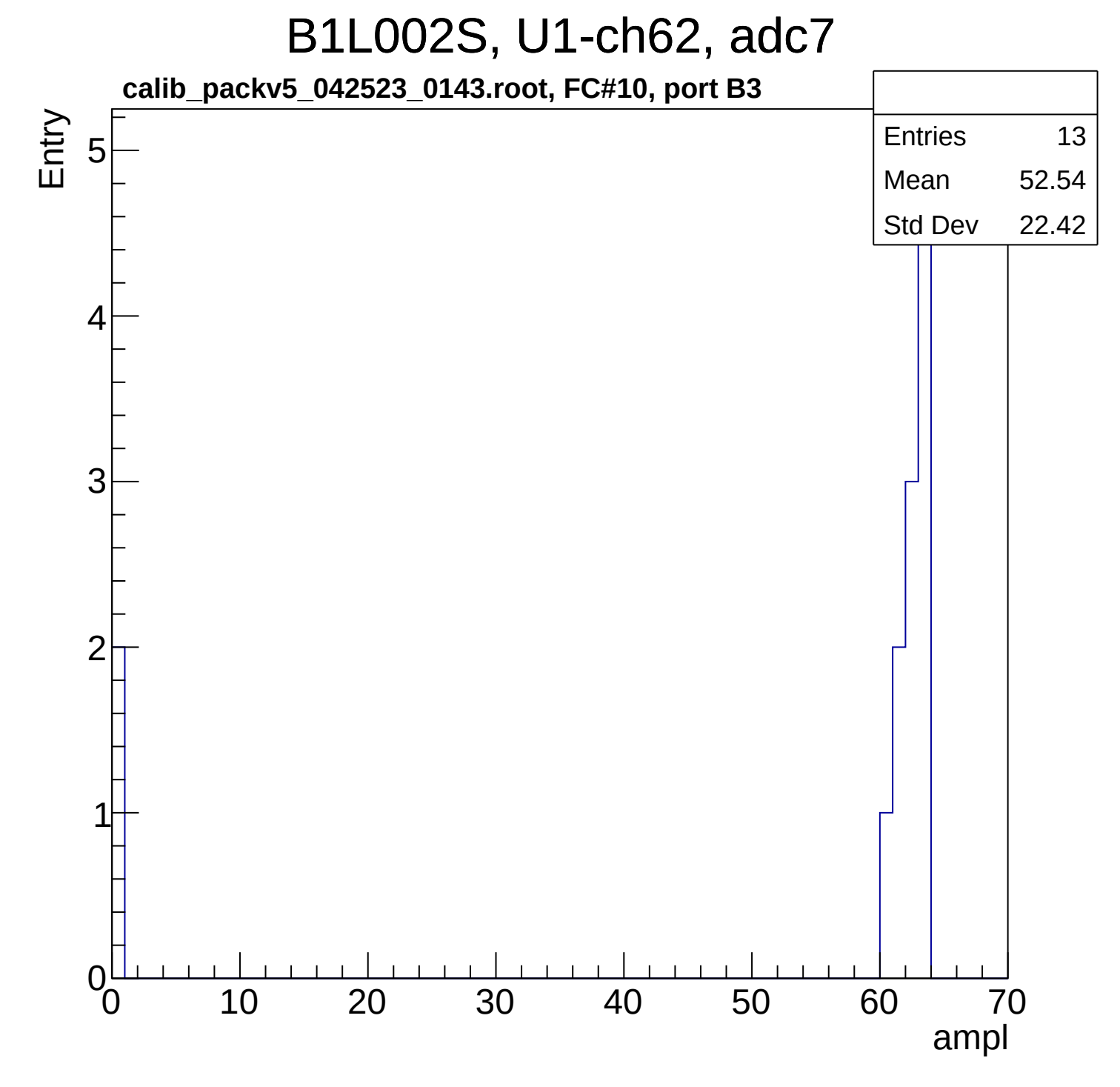
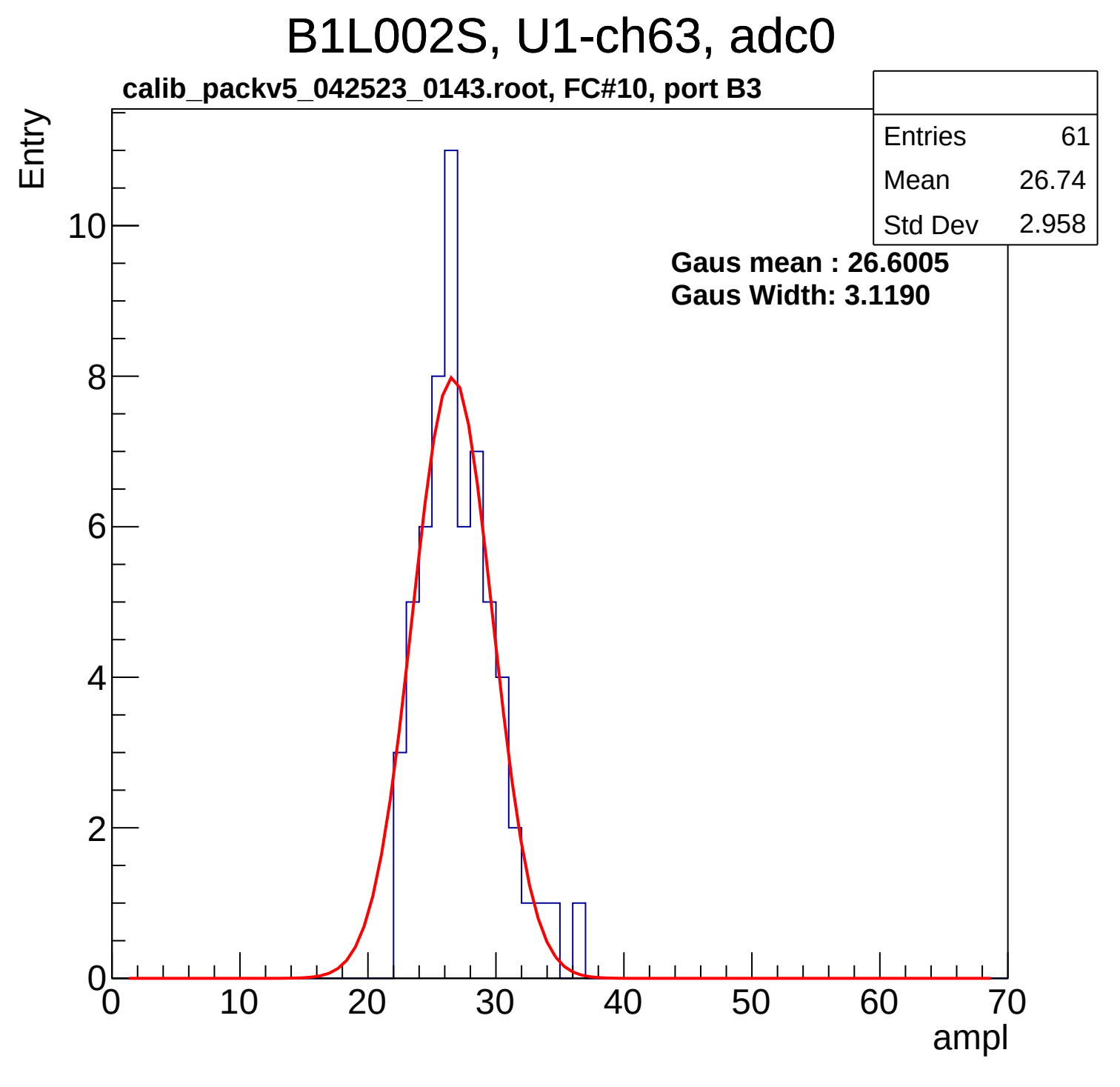

B1L002S, U1-ch62, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 60.28
Std Dev 2.049
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch62, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 52.54
Std Dev 22.42
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch63, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 26.74
Std Dev 2.958
Gaus mean : 26.6005
Gaus Width: 3.1190
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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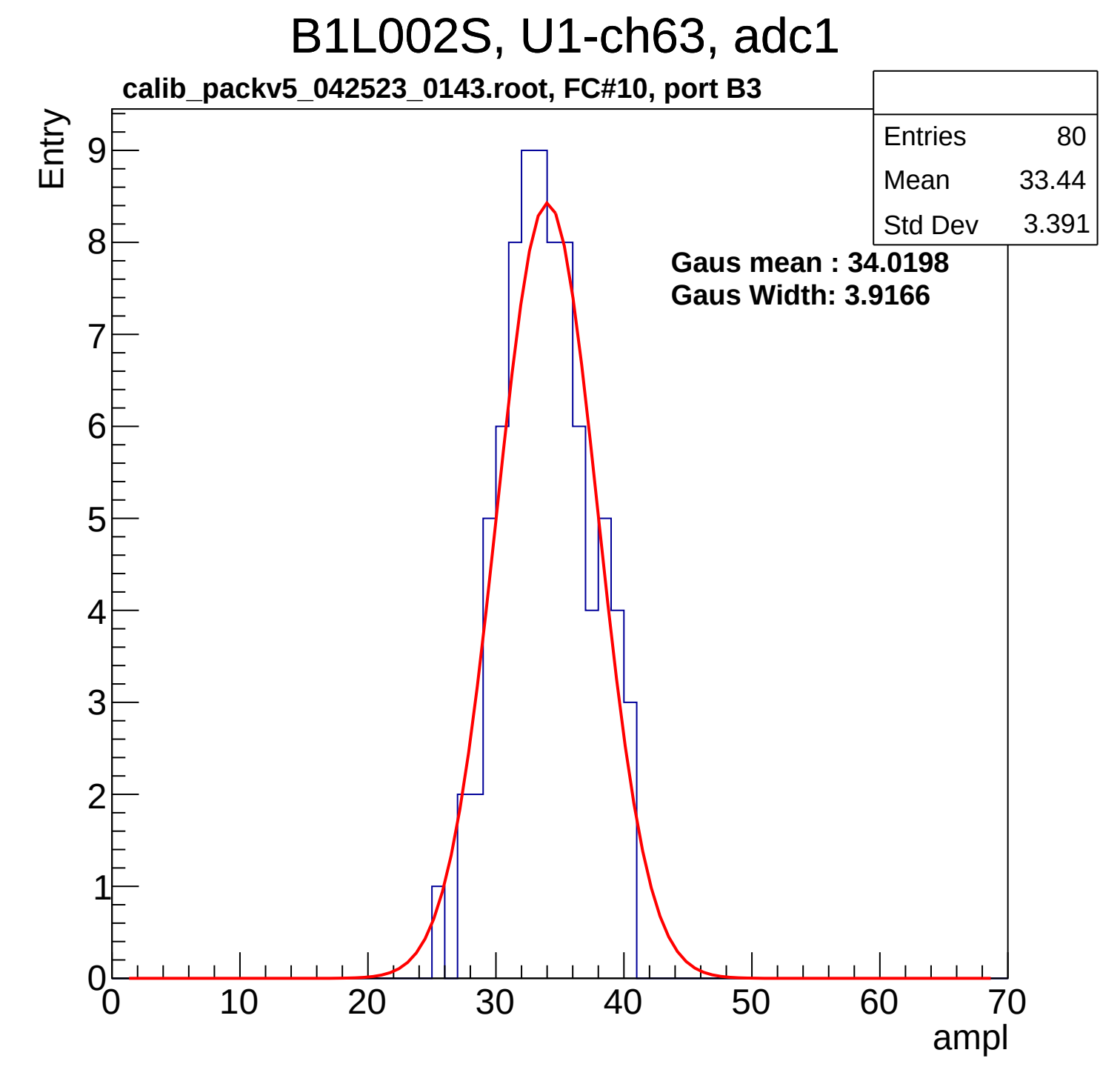
B1L002S, U1-ch63, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 33.44
Std Dev 3.391
Gaus mean : 34.0198
Gaus Width: 3.9166
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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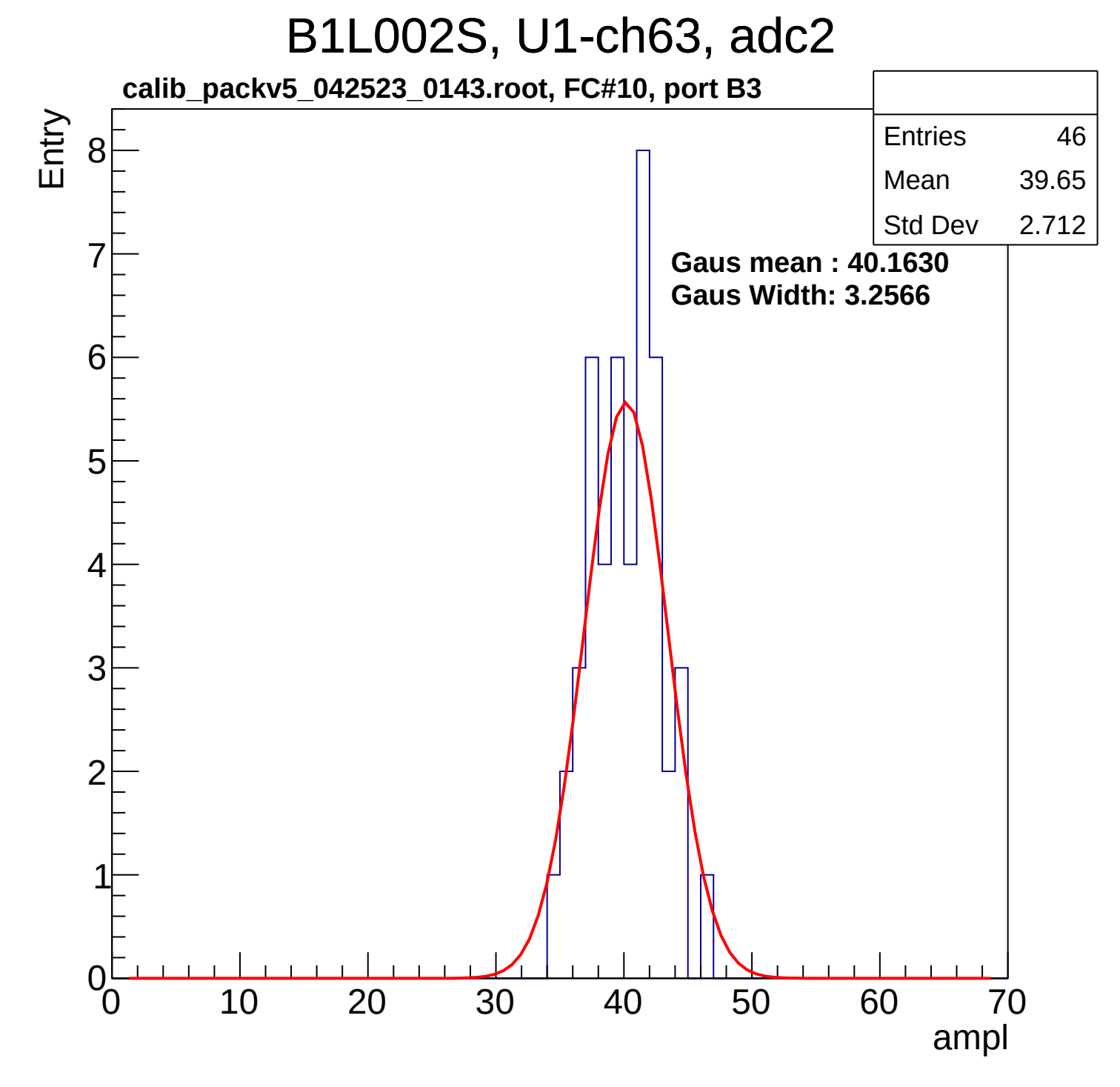
B1L002S, U1-ch63, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 39.65
Std Dev 2.712
Gaus mean : 40.1630
Gaus Width: 3.2566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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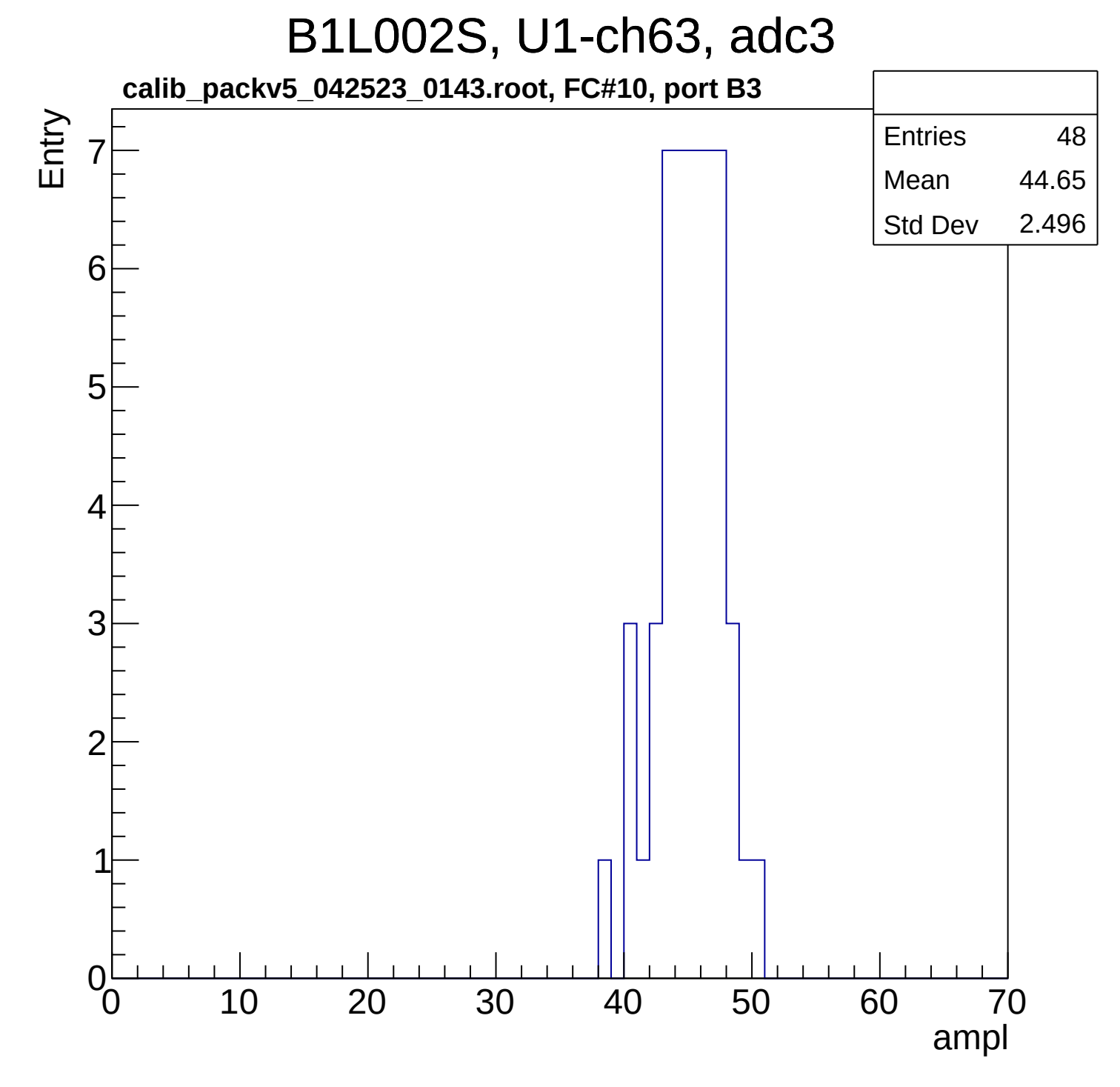
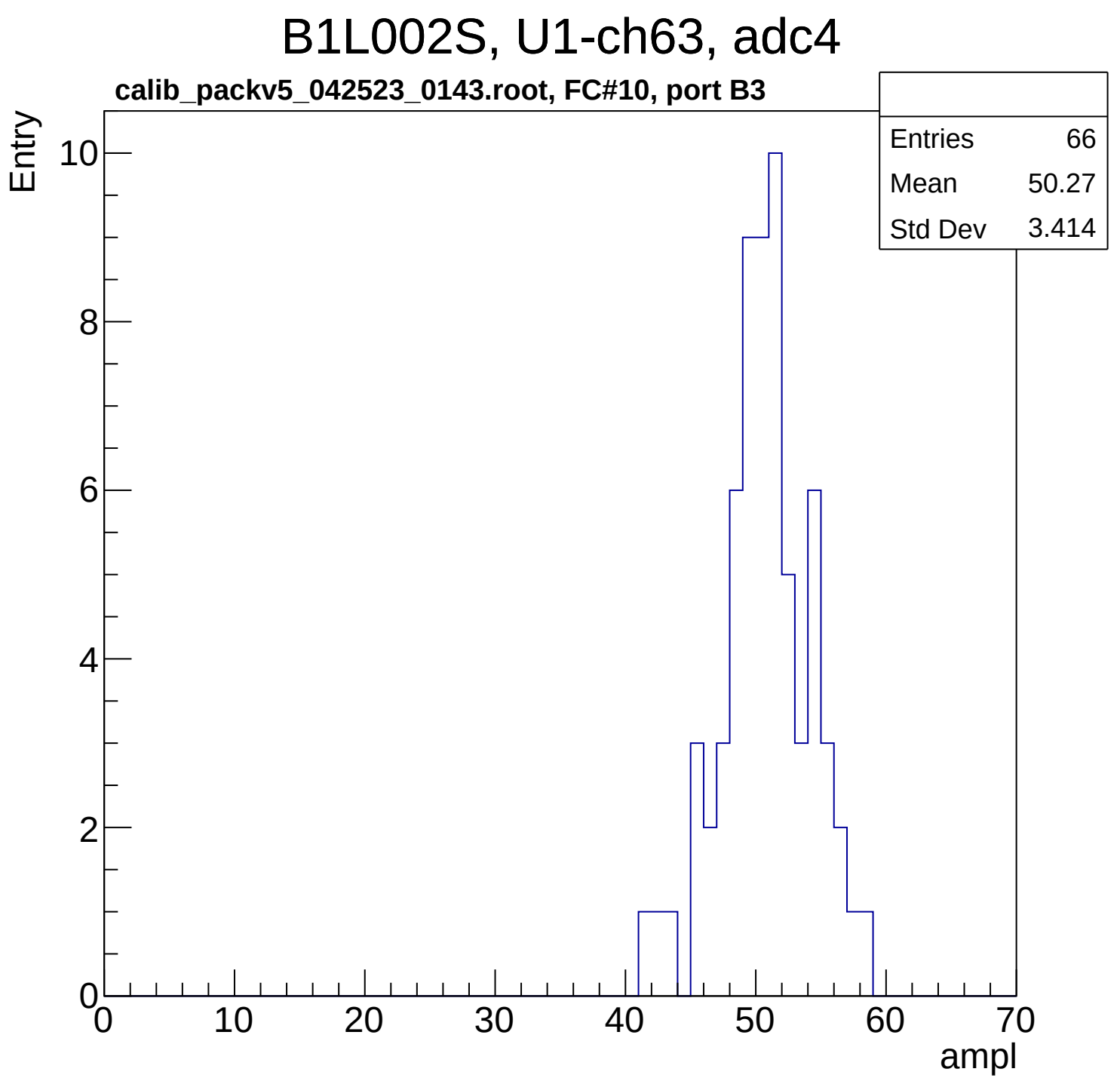
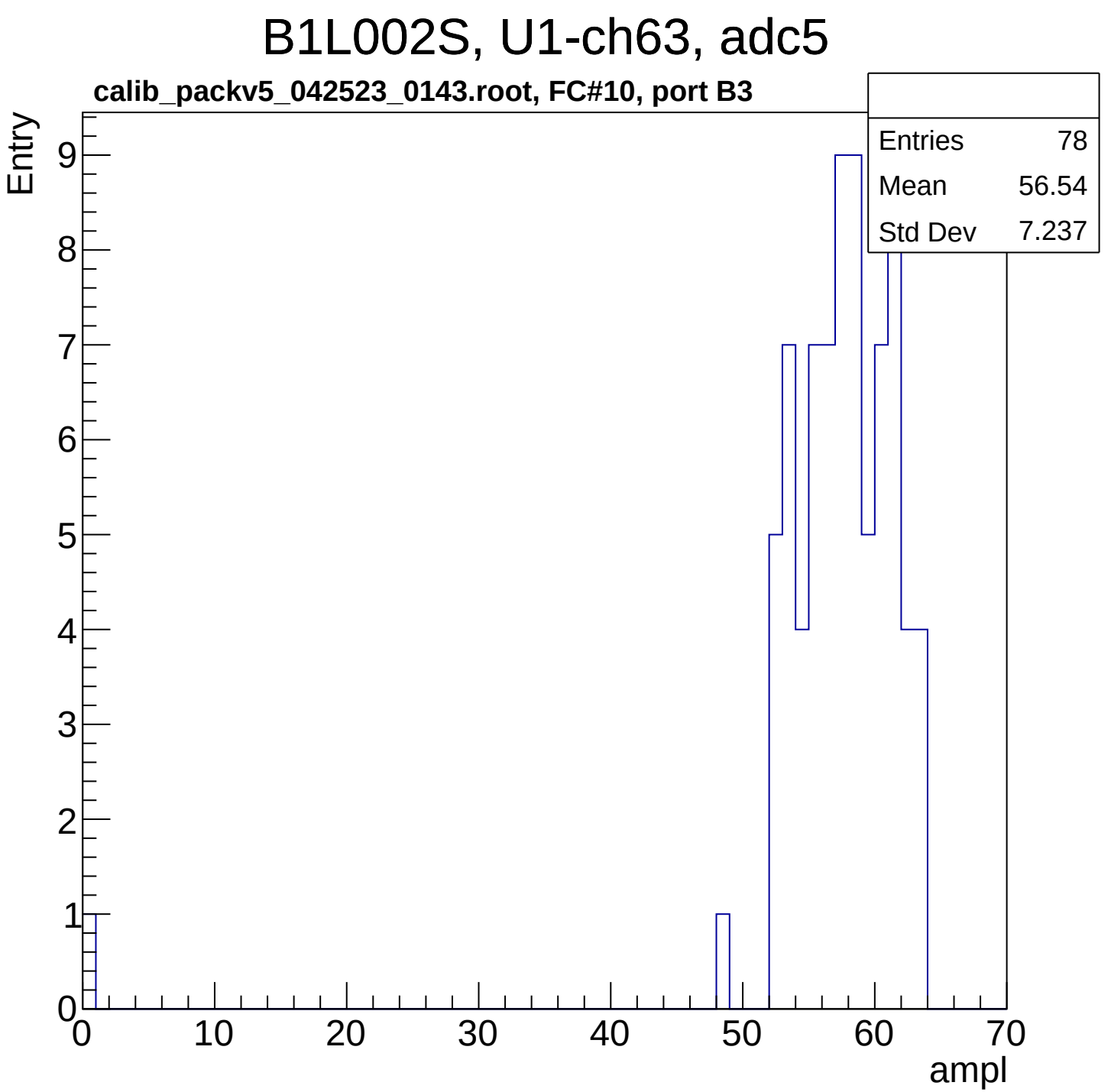

B1L002S, U1-ch63, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 44.65
Std Dev 2.496
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch63, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 50.27
Std Dev 3.414
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch63, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 56.54
Std Dev 7.237
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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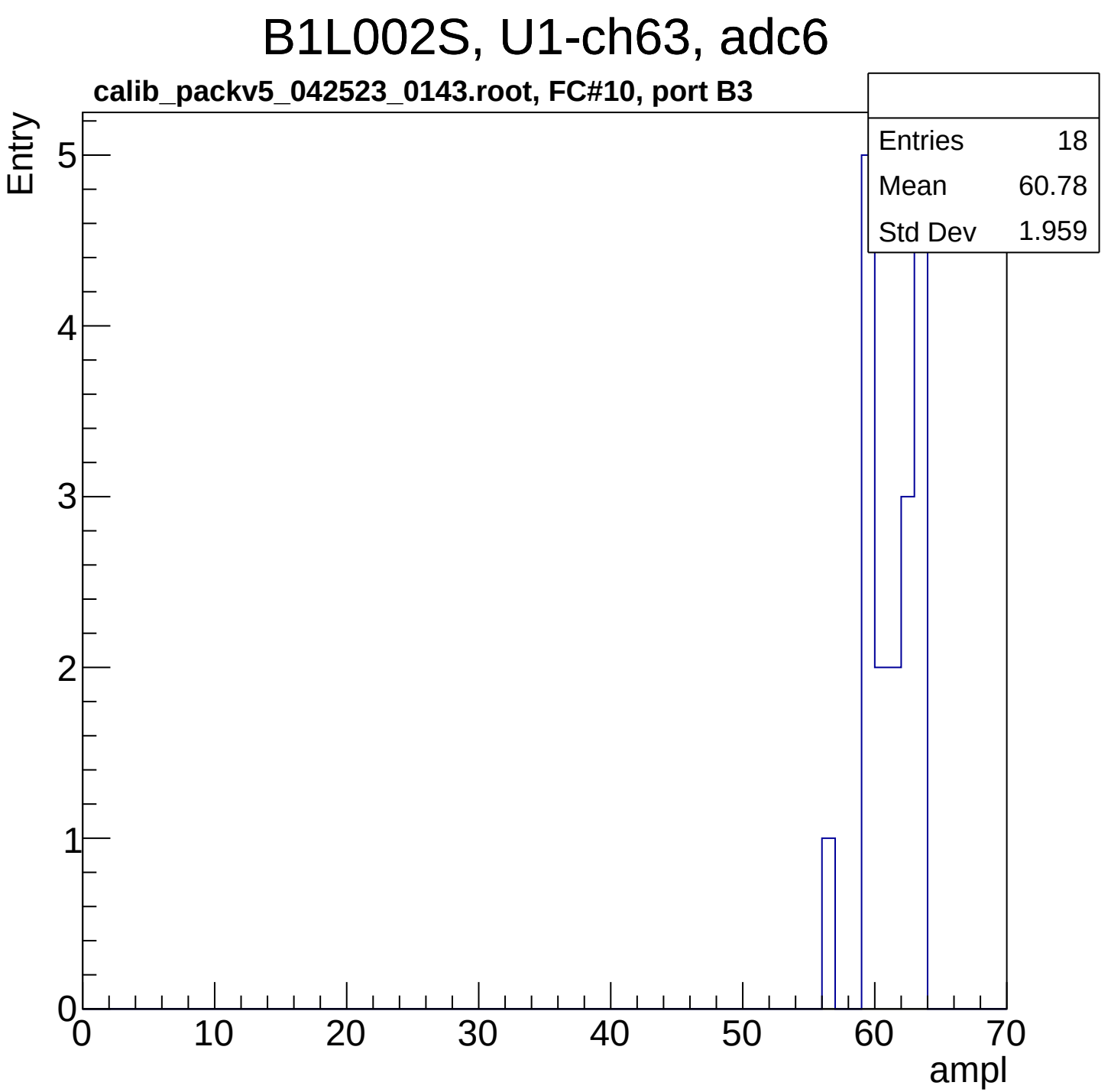
B1L002S, U1-ch63, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 60.78
Std Dev 1.959
Entry
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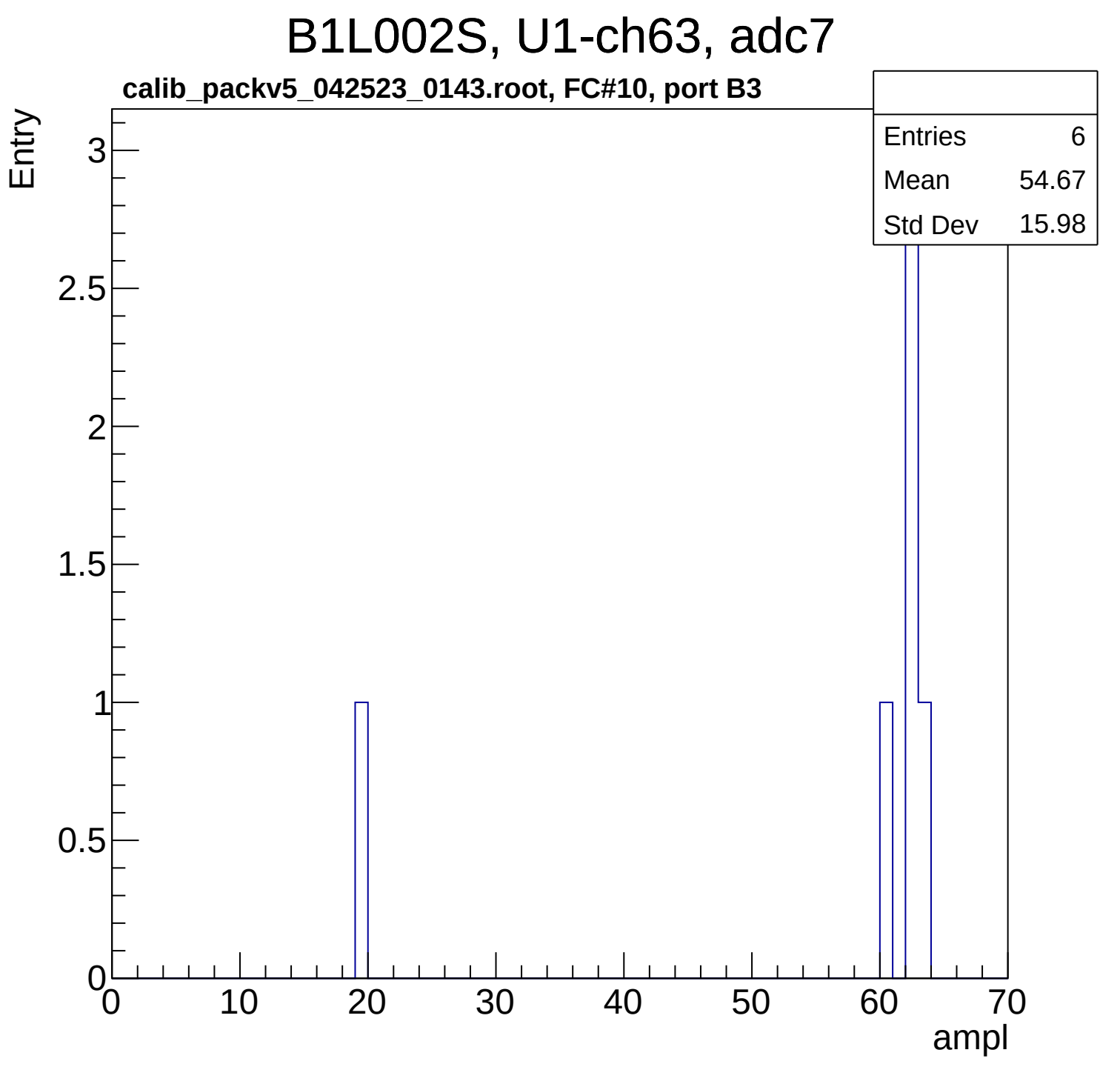
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch63, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 54.67
Std Dev 15.98
Entry
ampl
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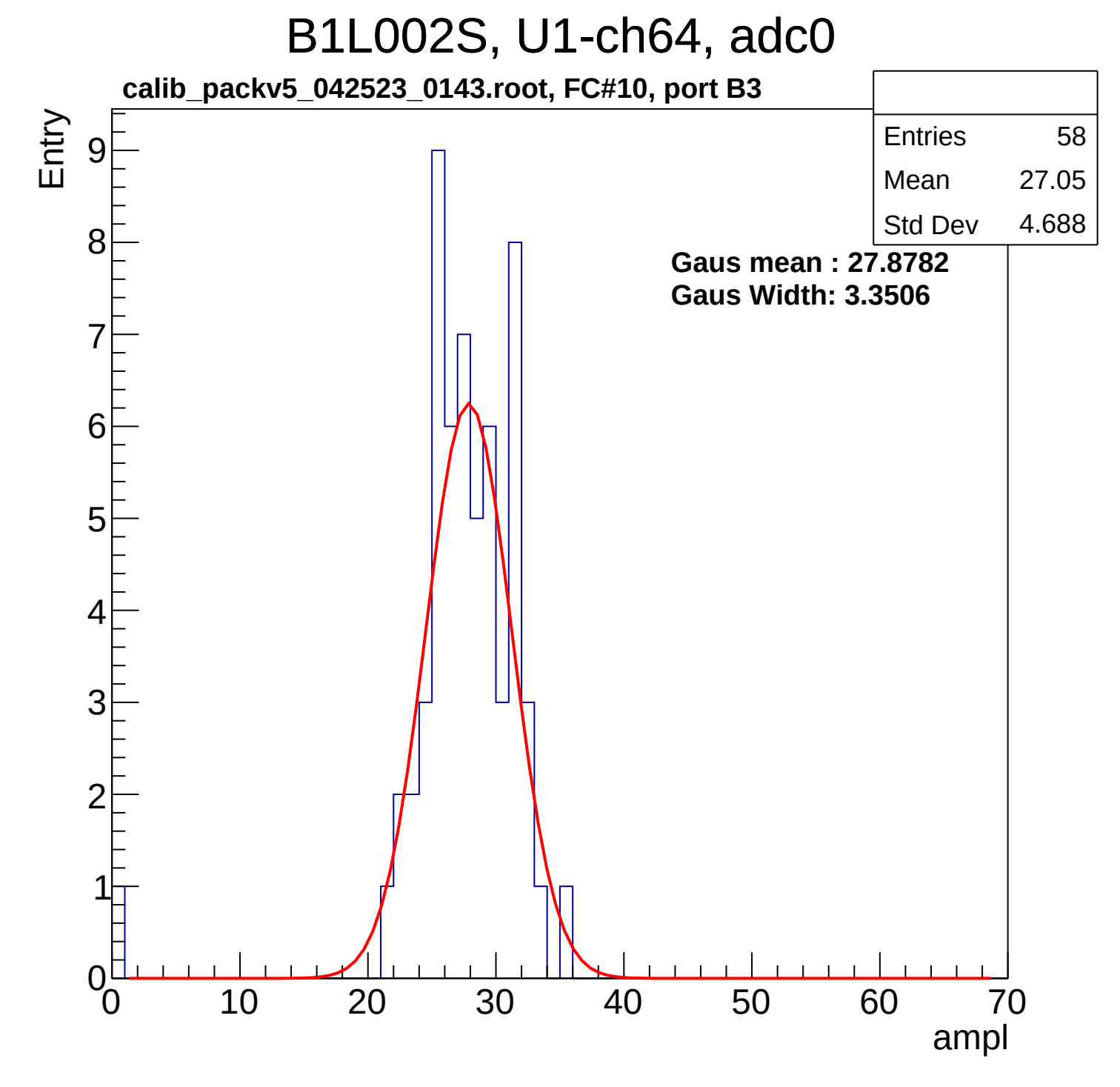
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U1-ch64, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.05
Std Dev 4.688
Gaus mean : 27.8782
Gaus Width: 3.3506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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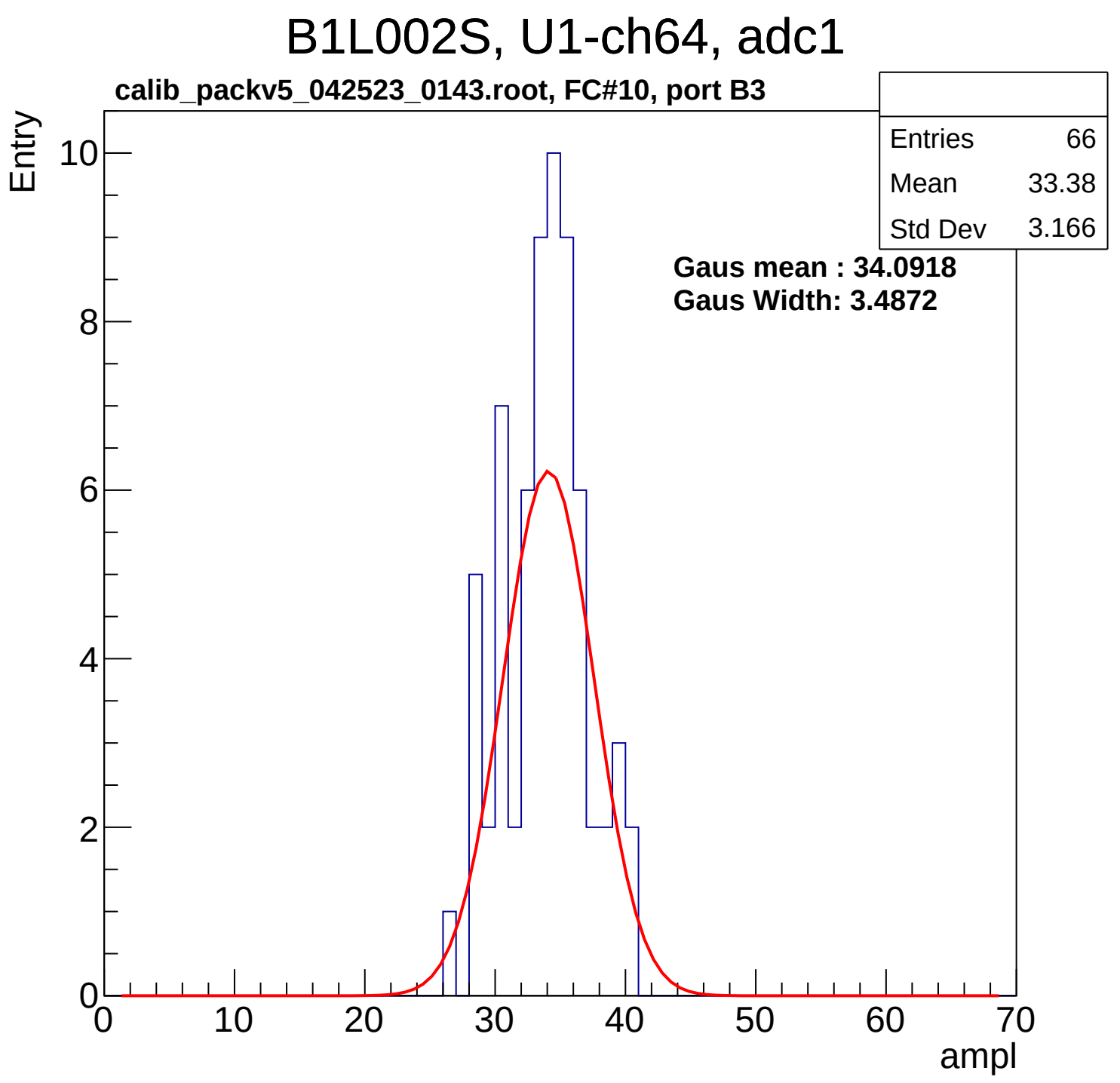
60
70


B1L002S, U1-ch64, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.38
Std Dev 3.166
Gaus mean : 34.0918
Gaus Width: 3.4872
Entry
ampl
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2
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0
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20
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60
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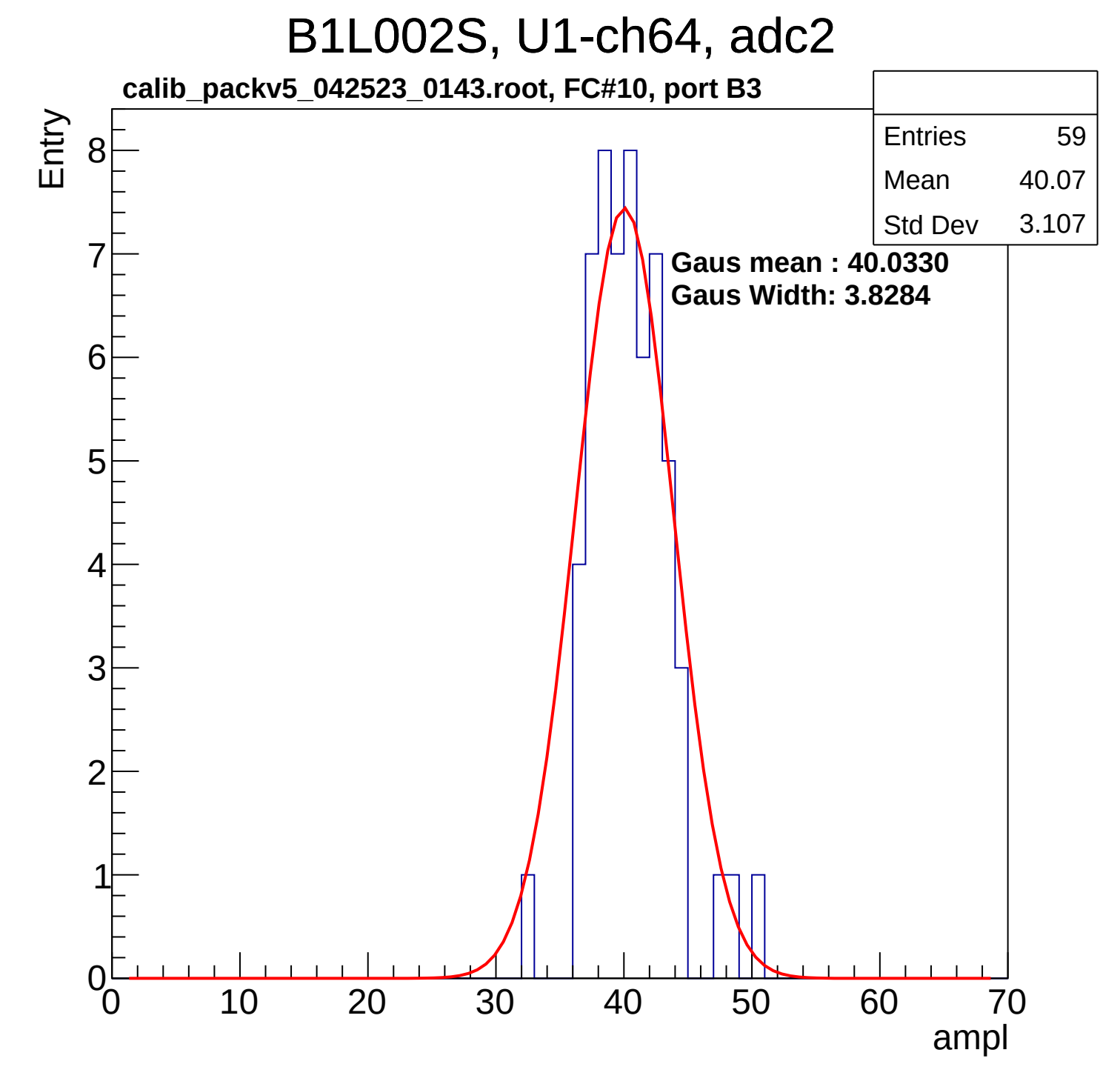
B1L002S, U1-ch64, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.07
Std Dev 3.107
Gaus mean : 40.0330
Gaus Width: 3.8284
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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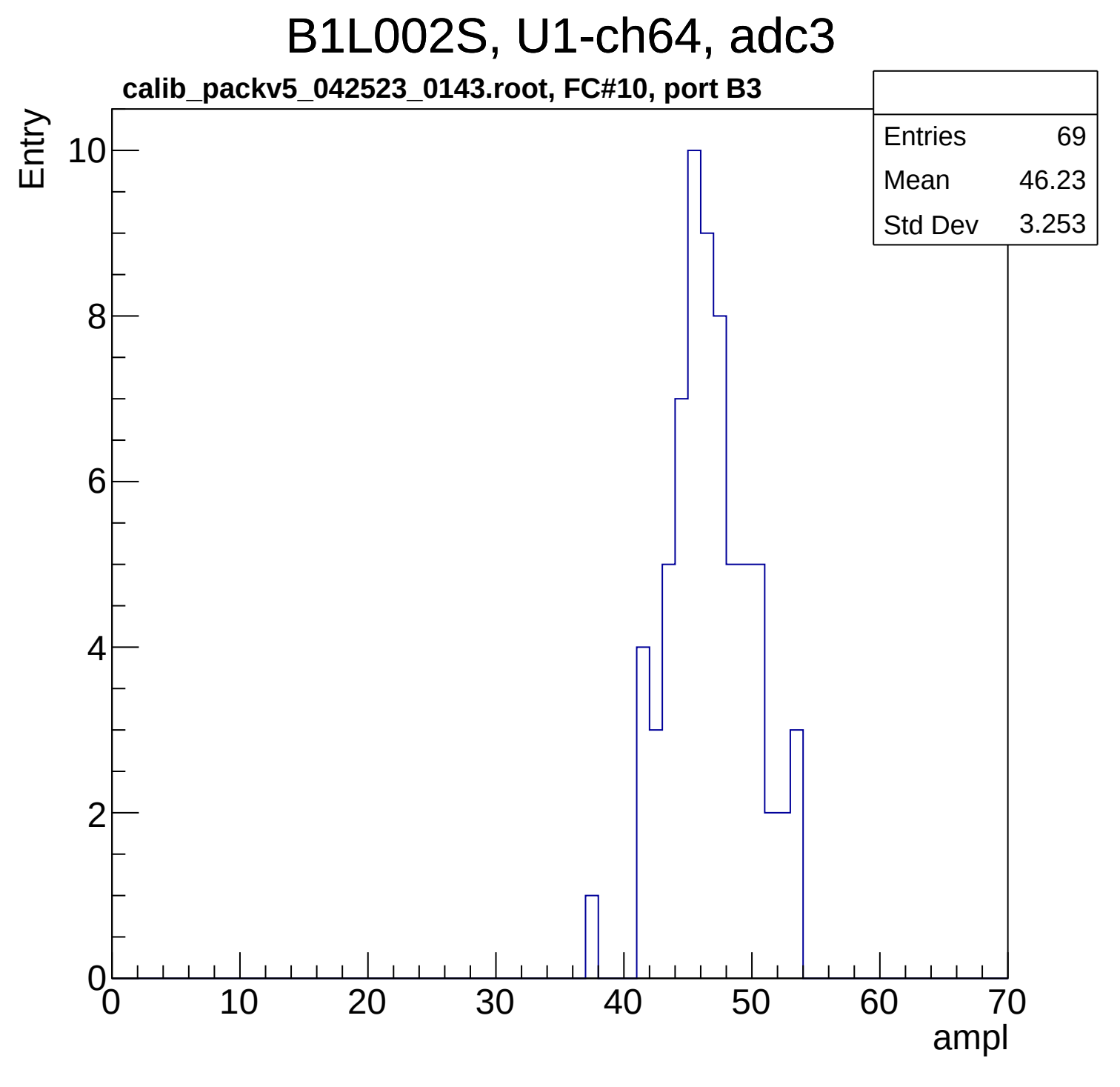
40
50
60
70


B1L002S, U1-ch64, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.23
Std Dev 3.253
Entry
ampl
0
2
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10
0
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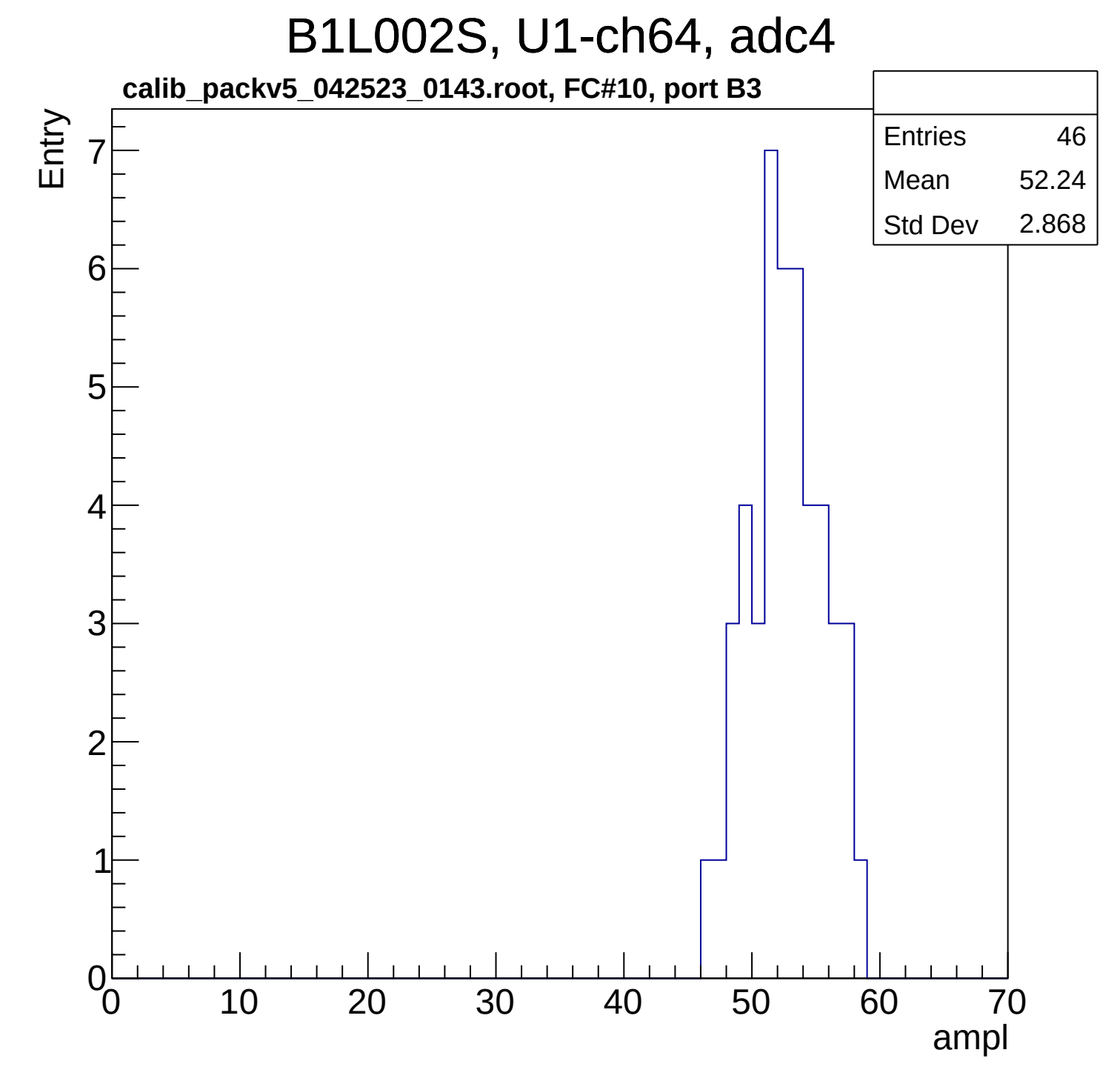
40
50
60
70


B1L002S, U1-ch64, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 52.24
Std Dev 2.868
Entry
ampl
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1
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3
4
5
6
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0
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20
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40
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60
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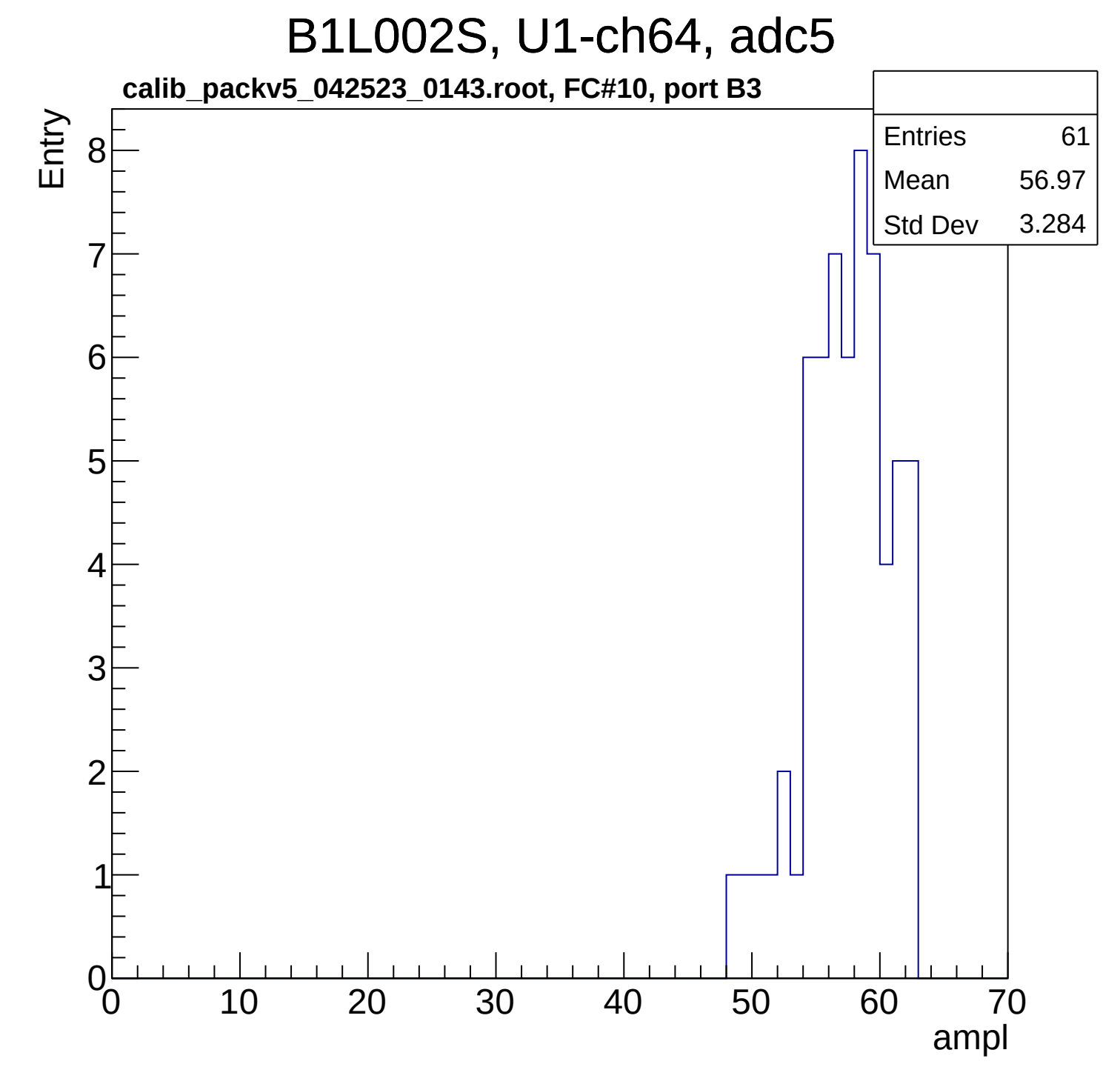
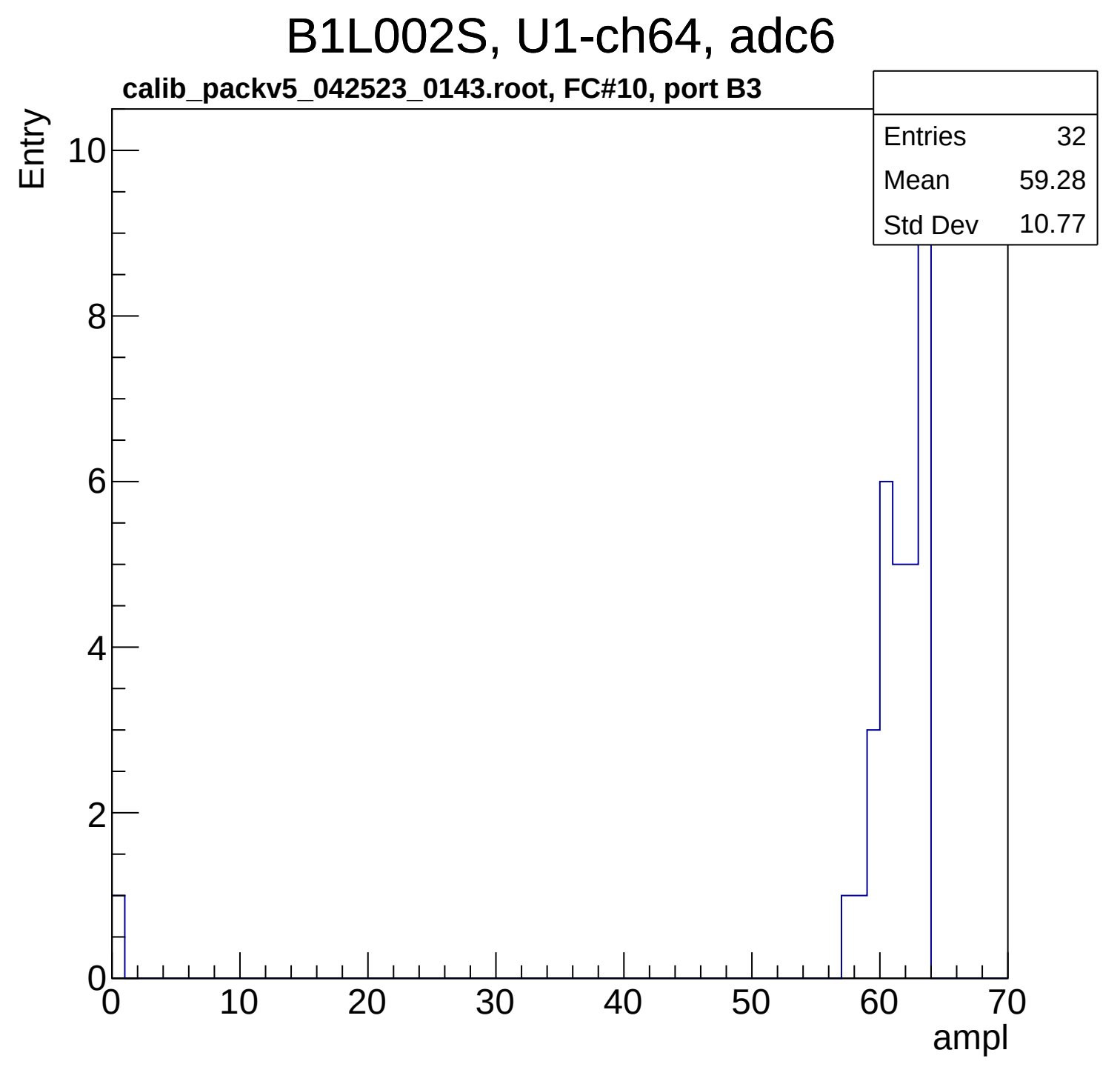

B1L002S, U1-ch64, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 56.97
Std Dev 3.284
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch64, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 59.28
Std Dev 10.77
Entry
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8
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10
20
30
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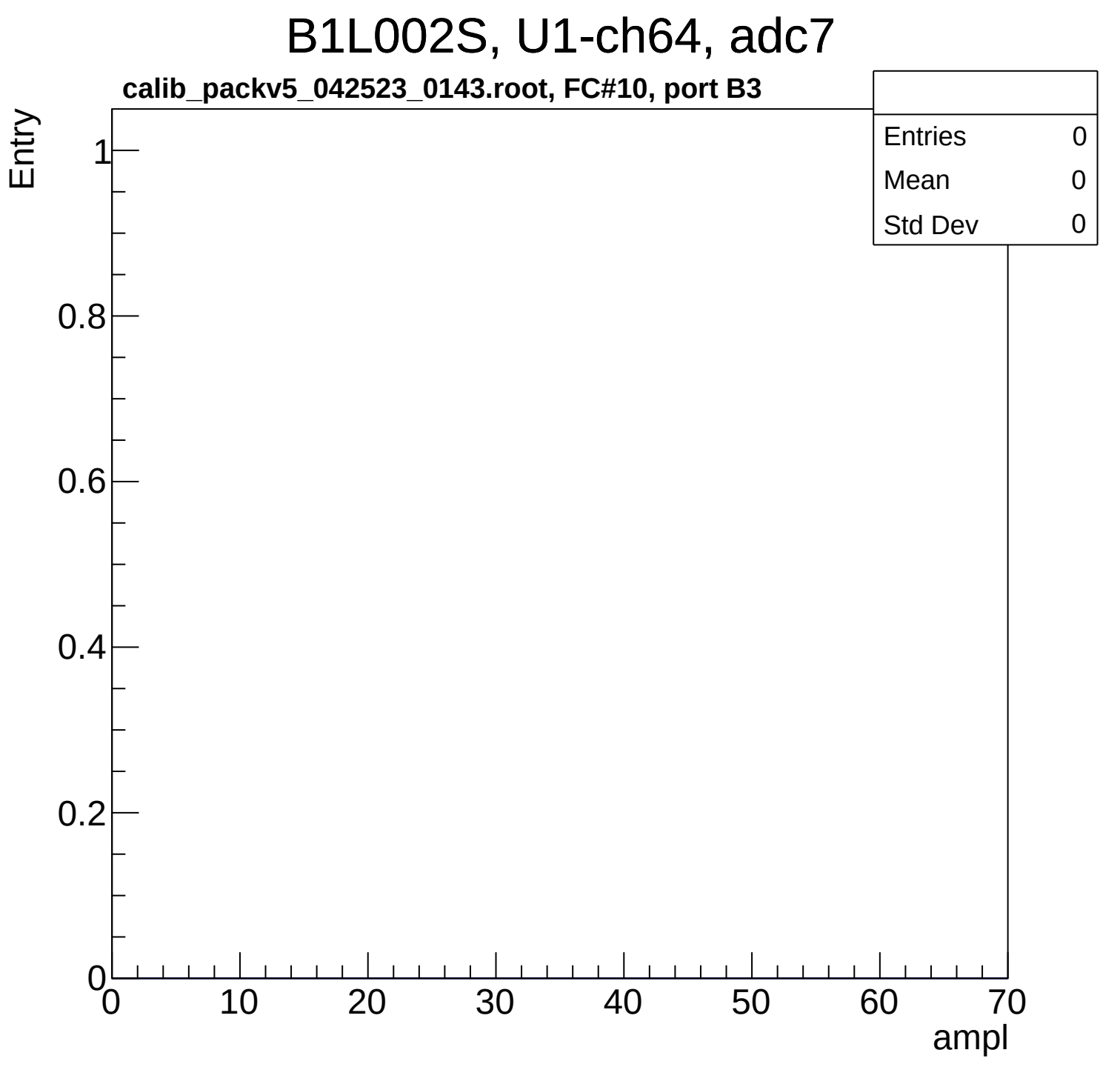
50
60
70
ampl


B1L002S, U1-ch64, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.4
0.2
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10
20
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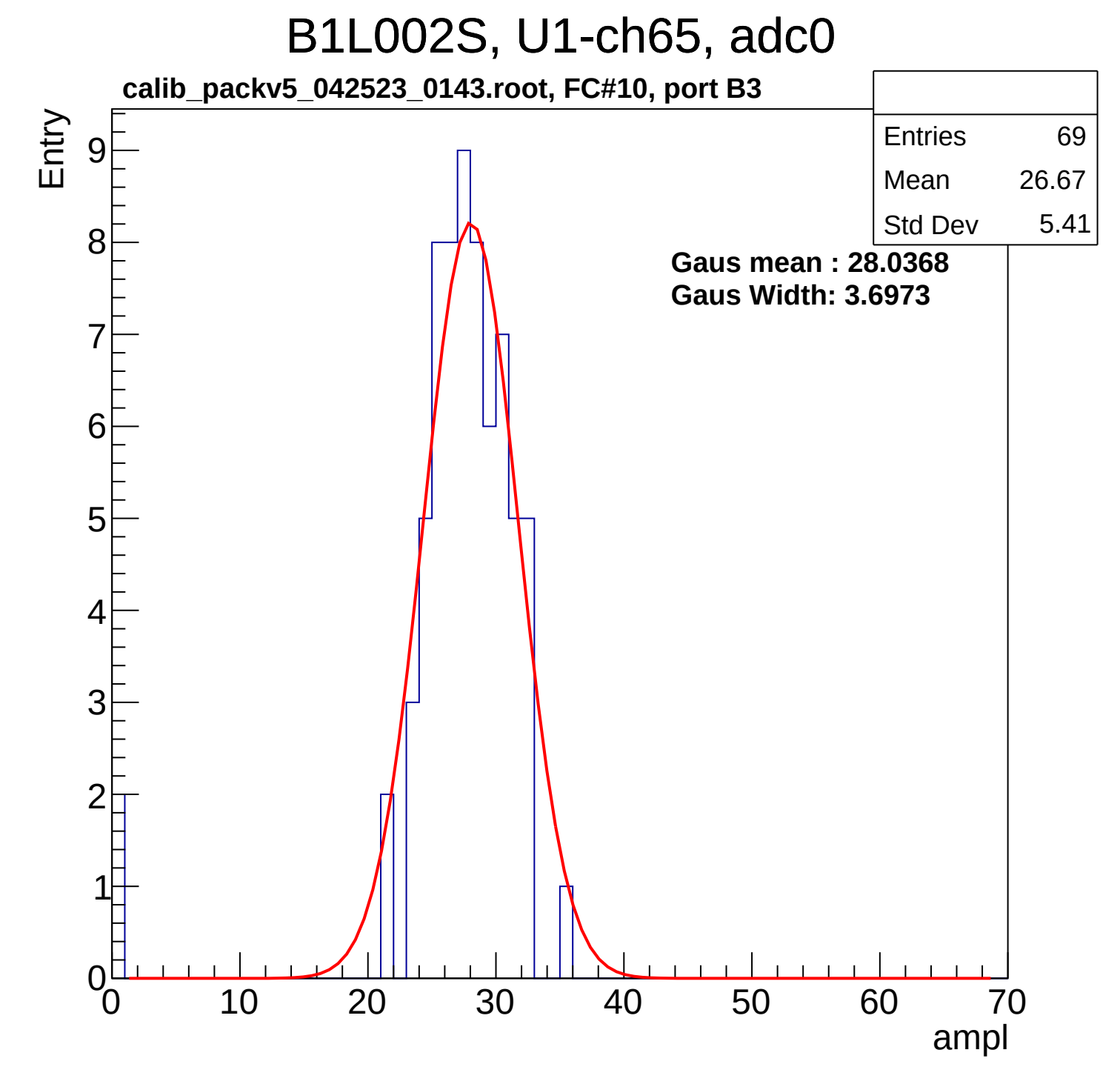
40
50
60
70
ampl


B1L002S, U1-ch65, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 26.67
Std Dev 5.41
Gaus mean : 28.0368
Gaus Width: 3.6973
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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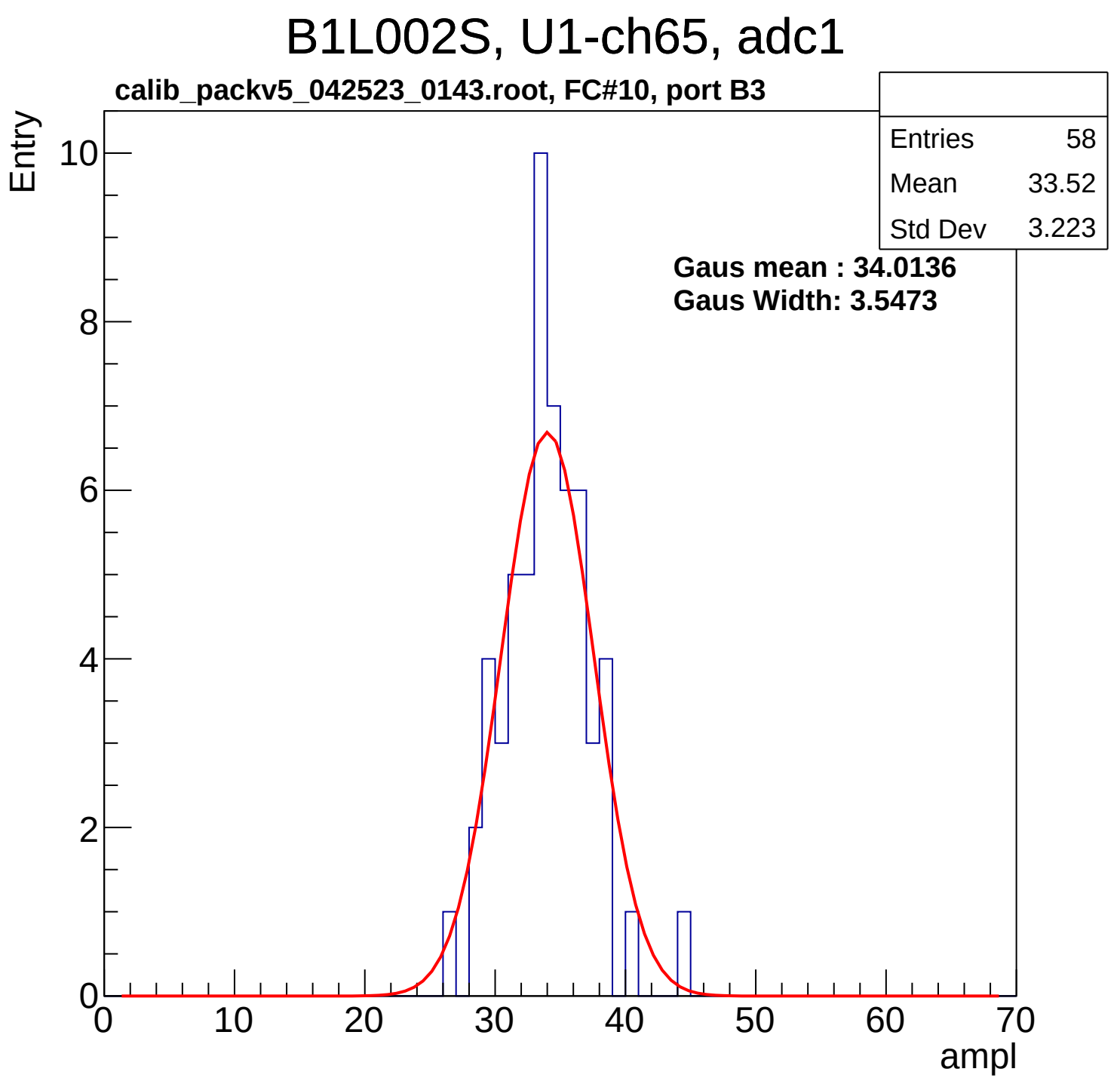
B1L002S, U1-ch65, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 33.52
Std Dev 3.223
Gaus mean : 34.0136
Gaus Width: 3.5473
Entry
ampl
0
2
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6
8
10
0
10
20
30
40
50
60
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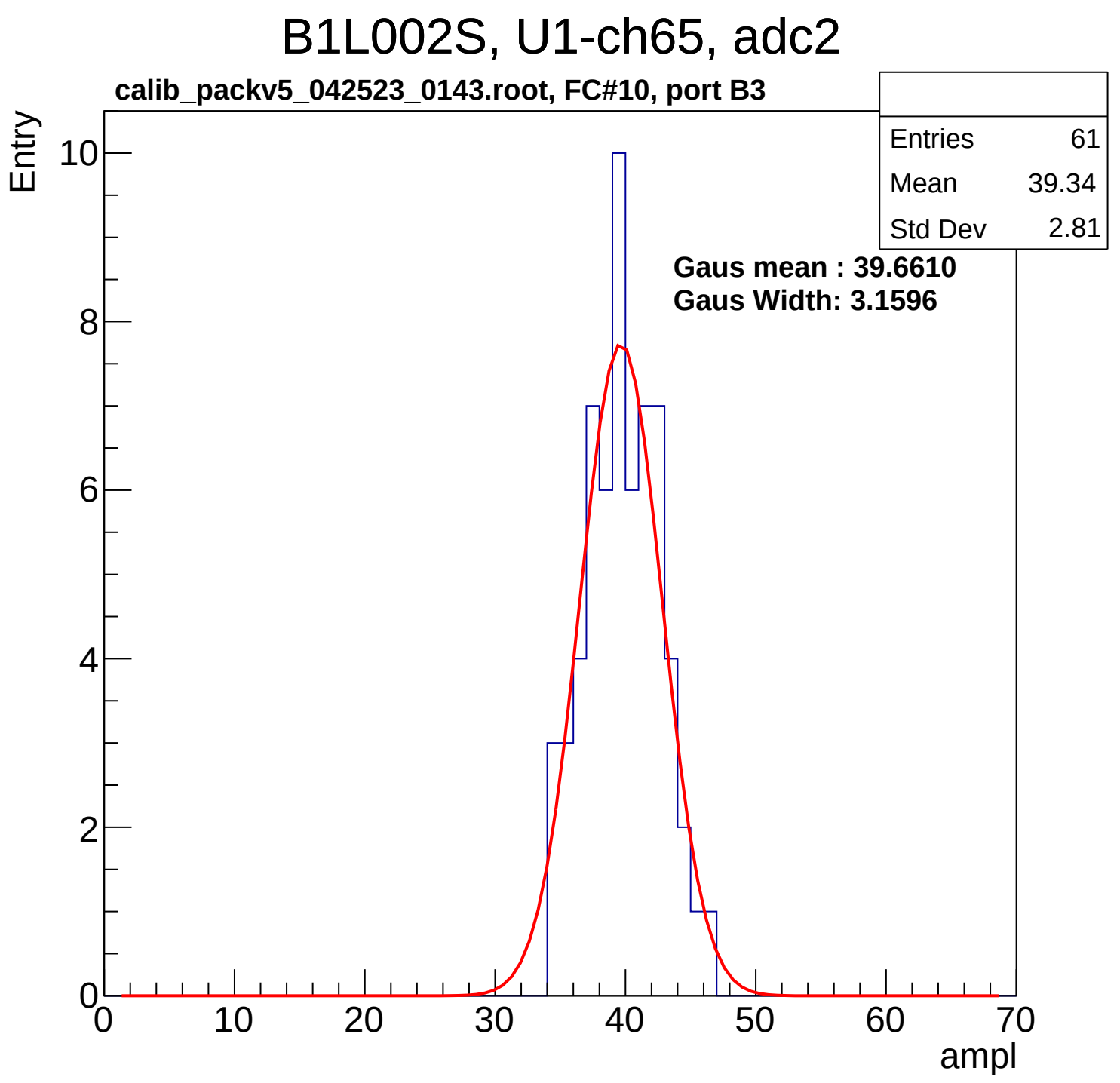
B1L002S, U1-ch65, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 39.34
Std Dev 2.81
Gaus mean : 39.6610
Gaus Width: 3.1596
Entry
ampl
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0
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40
50
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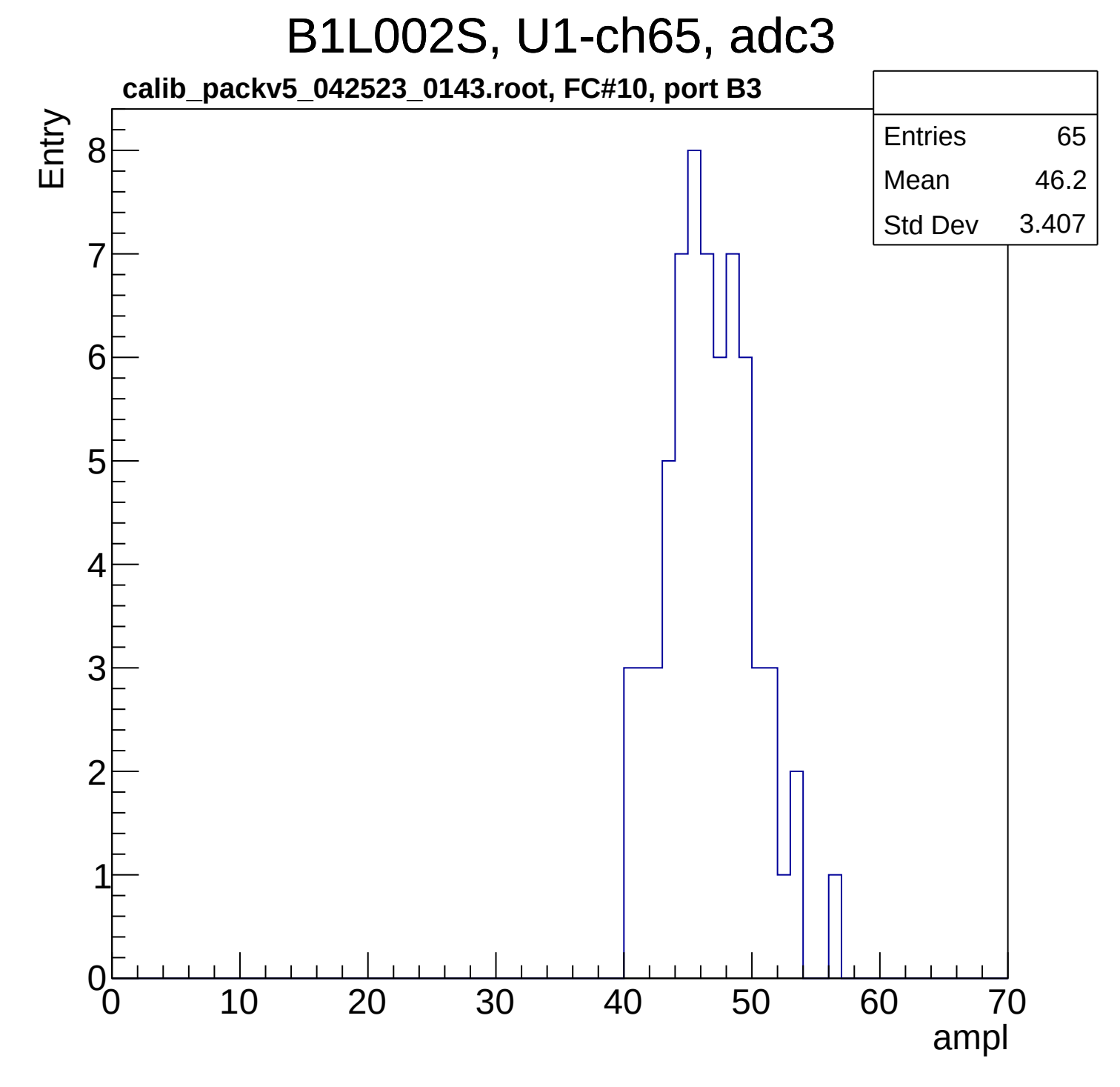
B1L002S, U1-ch65, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.2
Std Dev 3.407
Entry
ampl
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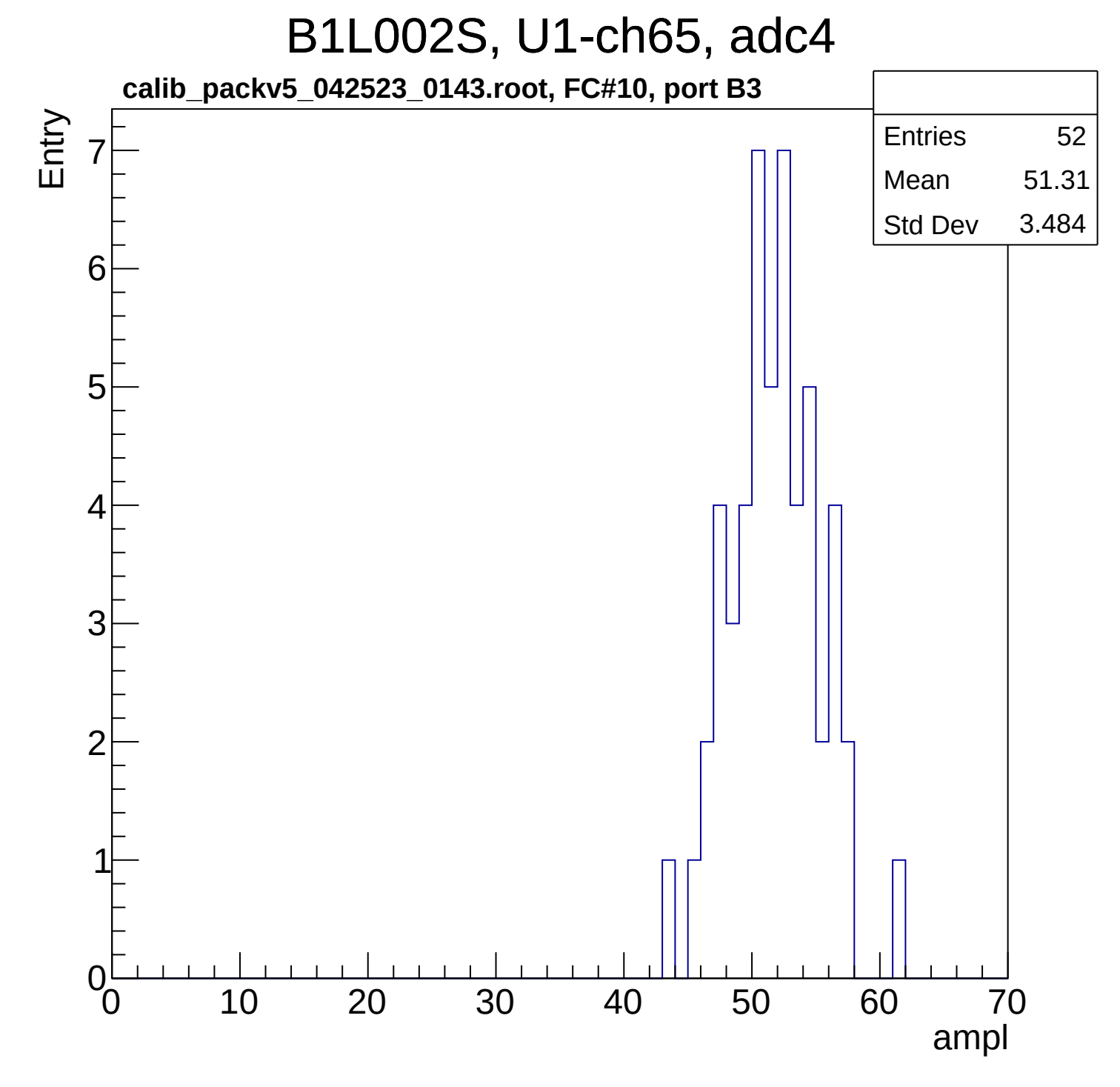
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch65, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 51.31
Std Dev 3.484
Entry
ampl
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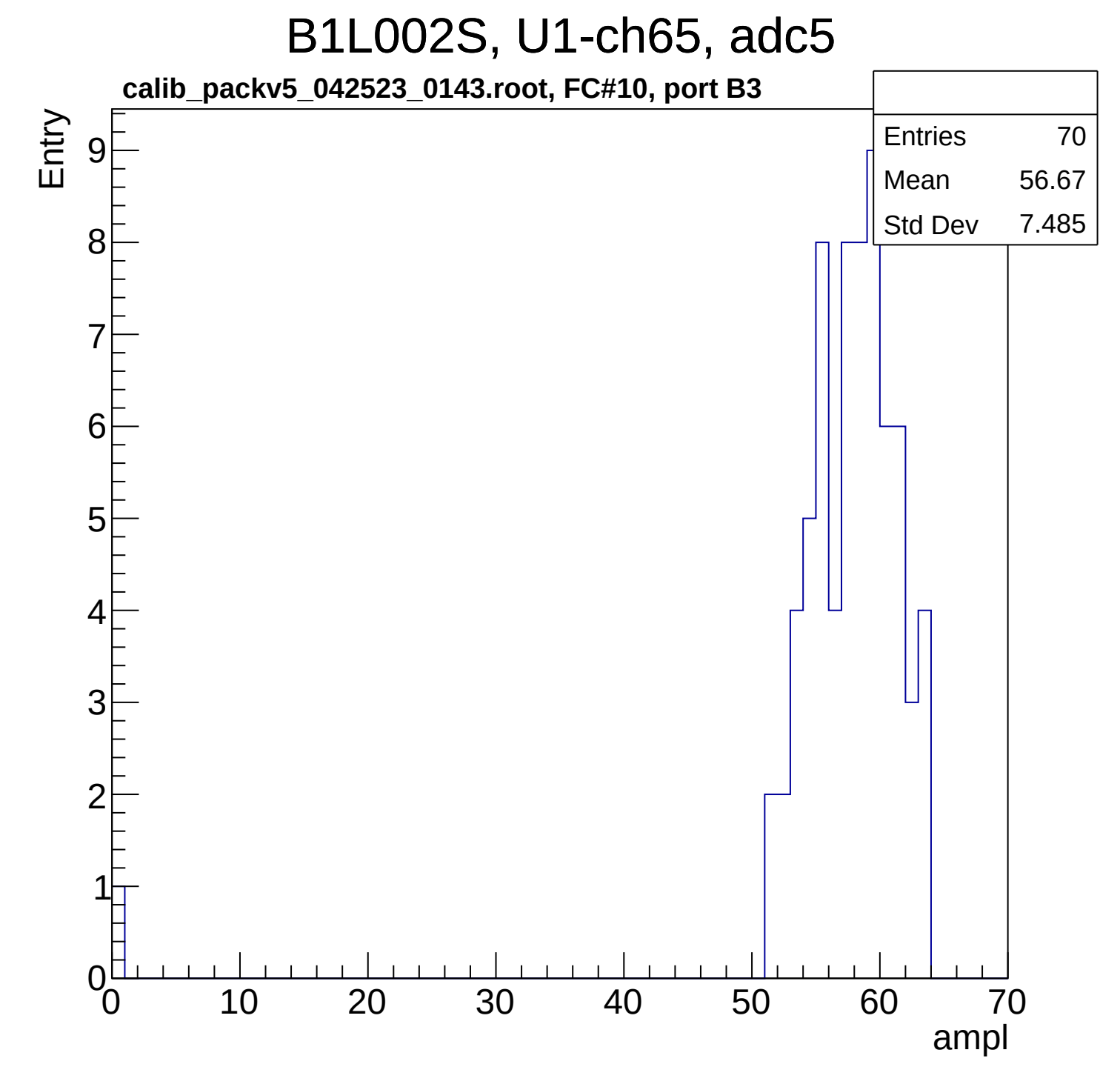
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch65, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 56.67
Std Dev 7.485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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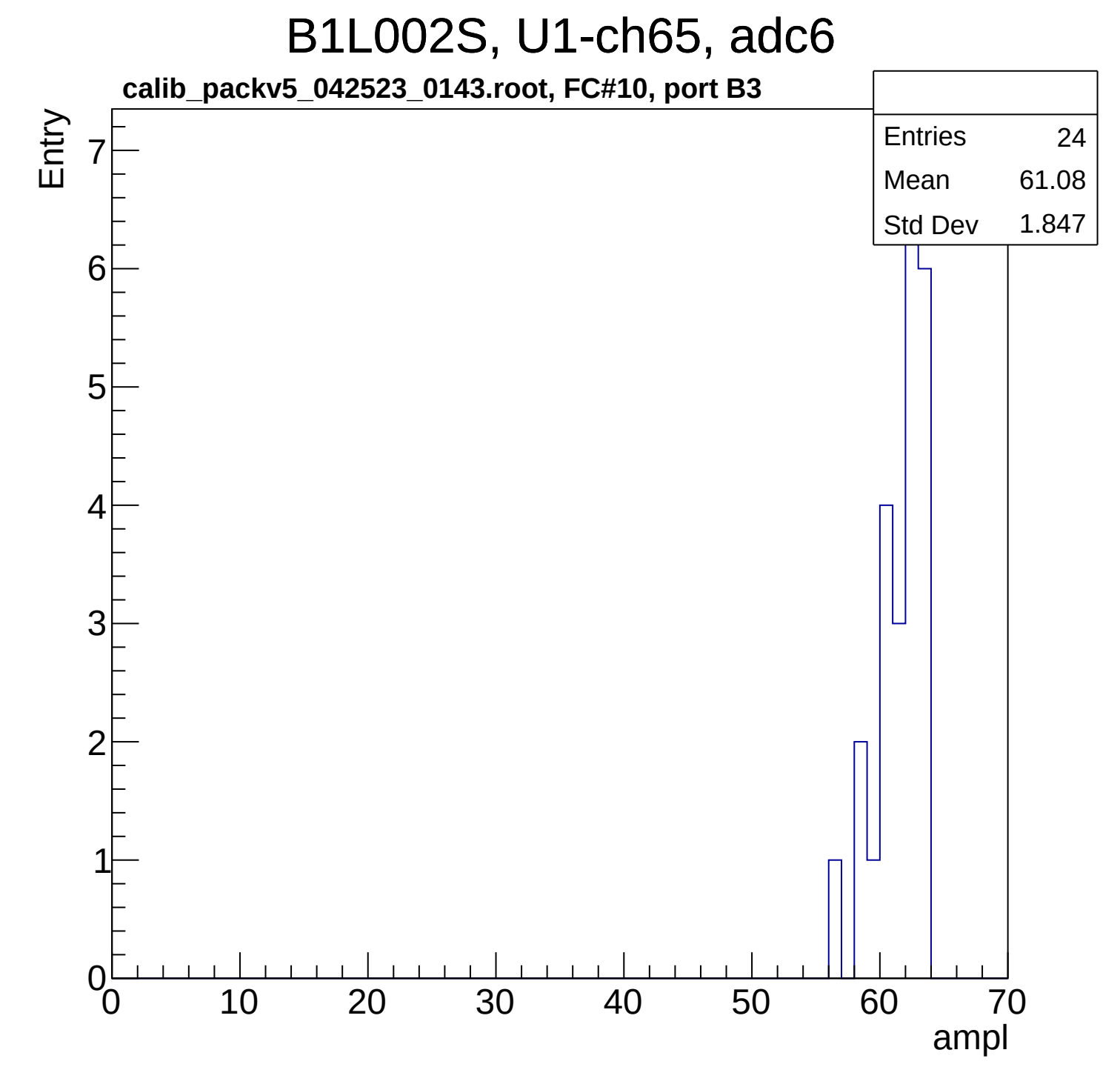
B1L002S, U1-ch65, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61.08
Std Dev 1.847
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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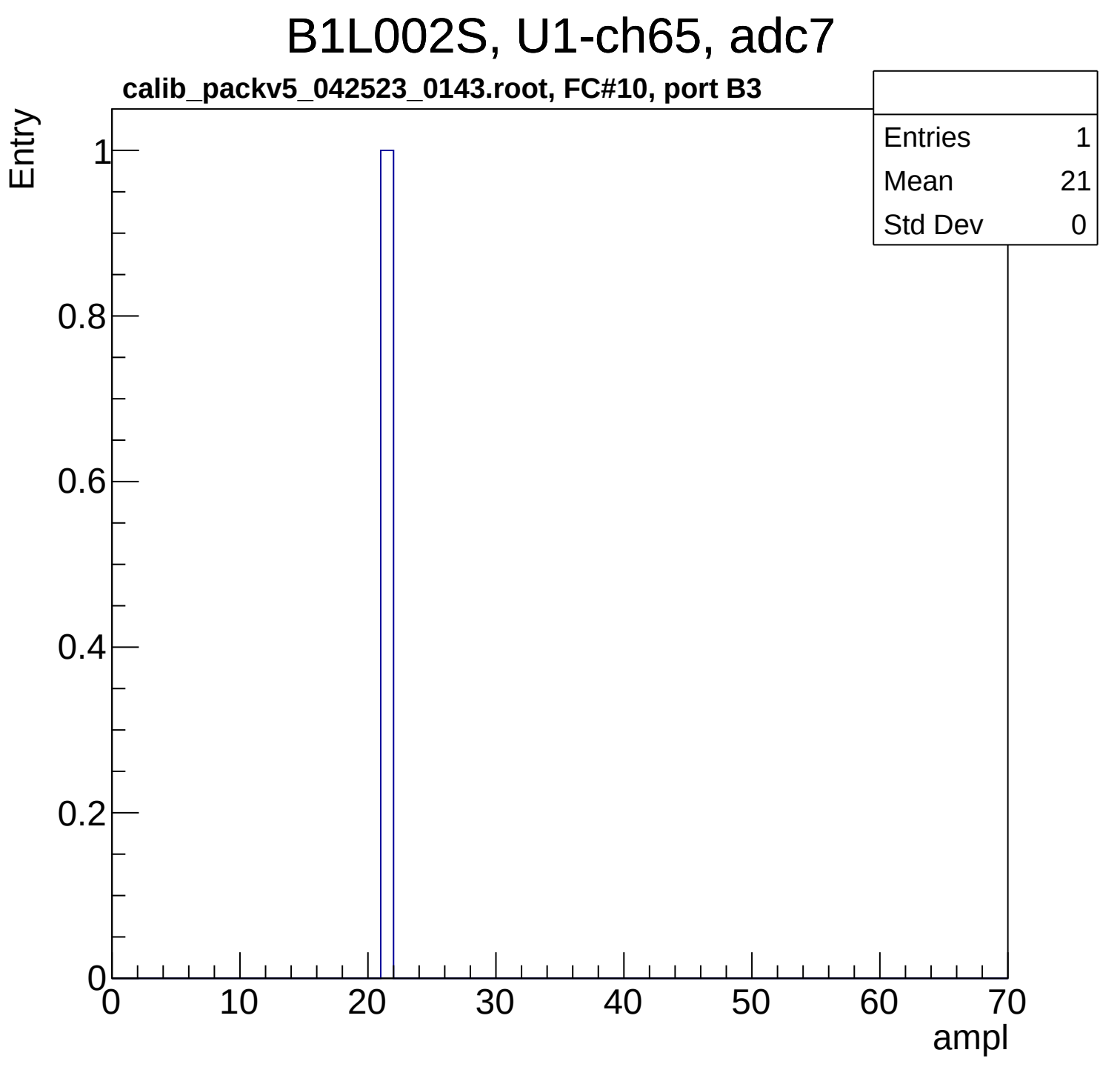
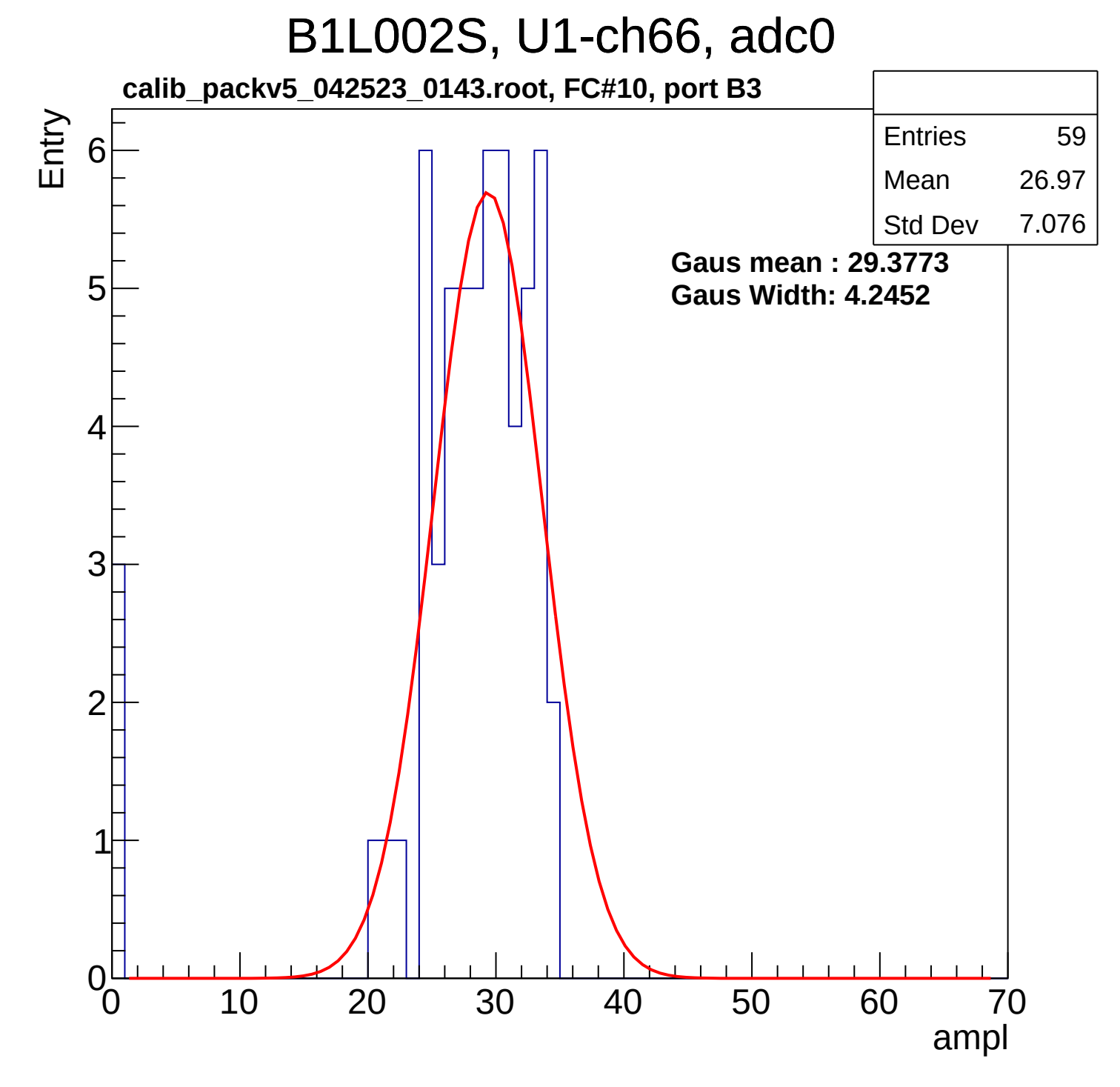
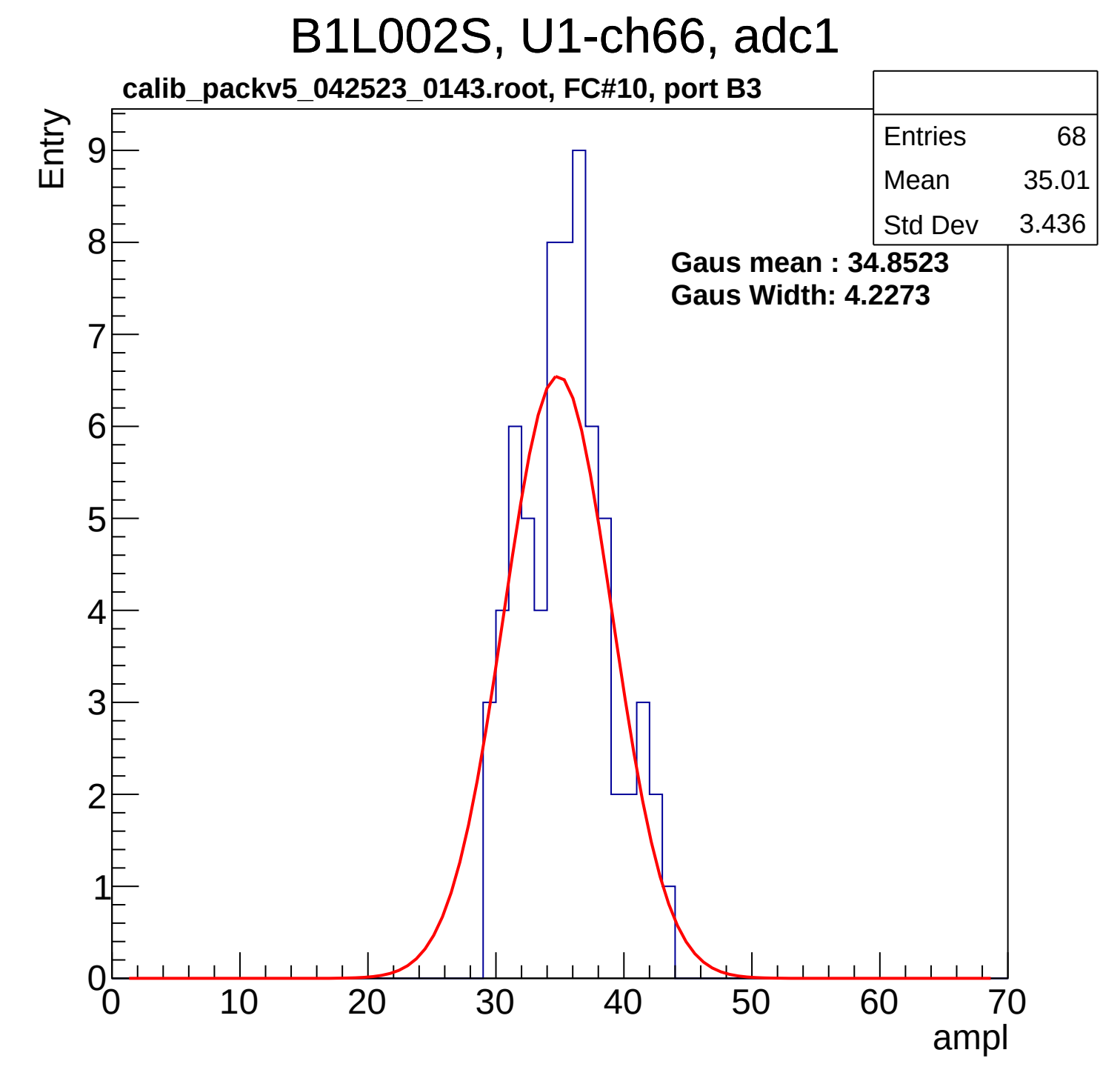

B1L002S, U1-ch65, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch66, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 26.97
Std Dev 7.076
Gaus mean : 29.3773
Gaus Width: 4.2452
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U1-ch66, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.01
Std Dev 3.436
Gaus mean : 34.8523
Gaus Width: 4.2273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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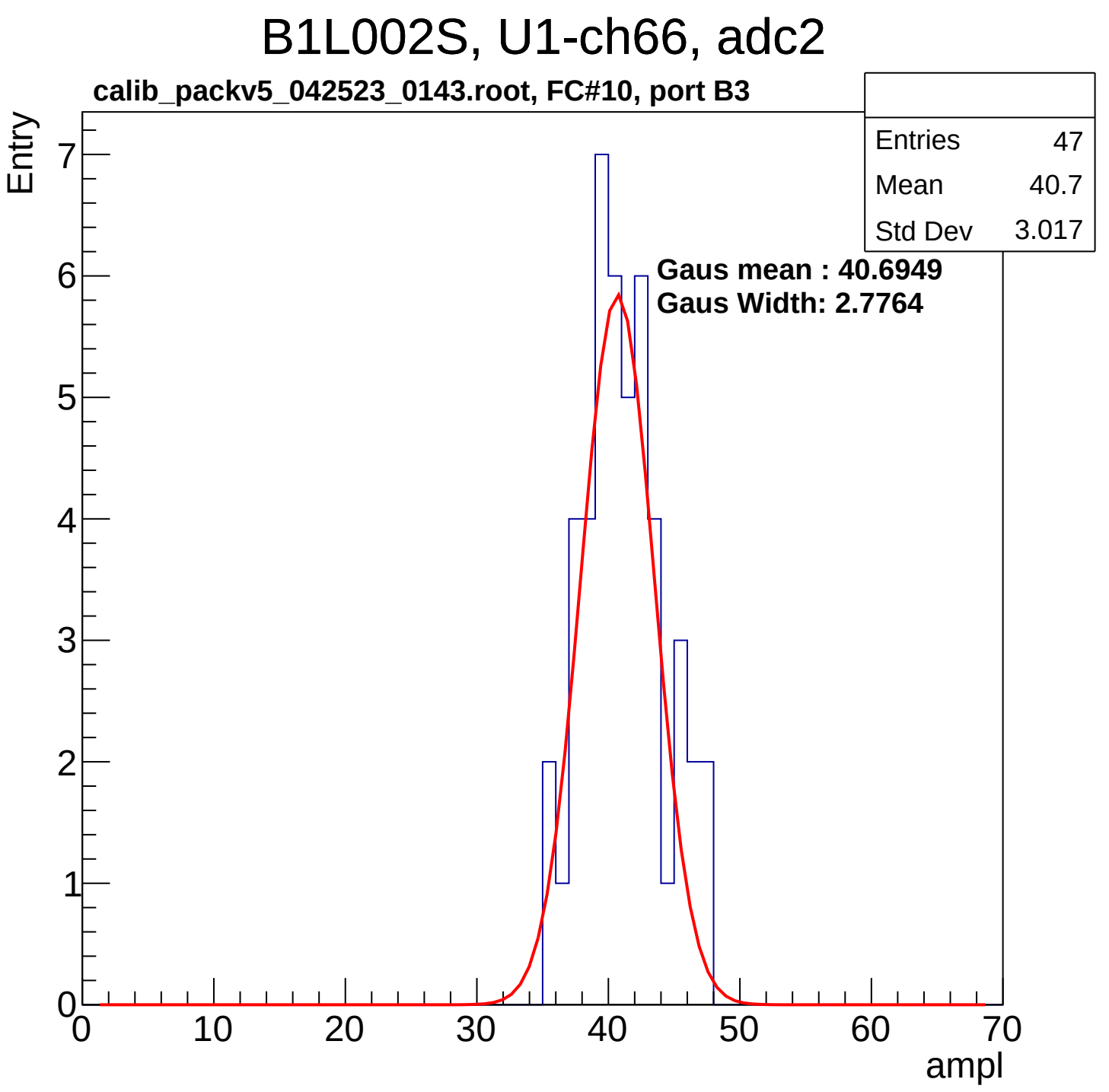
B1L002S, U1-ch66, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 40.7
Std Dev 3.017
Gaus mean : 40.6949
Gaus Width: 2.7764
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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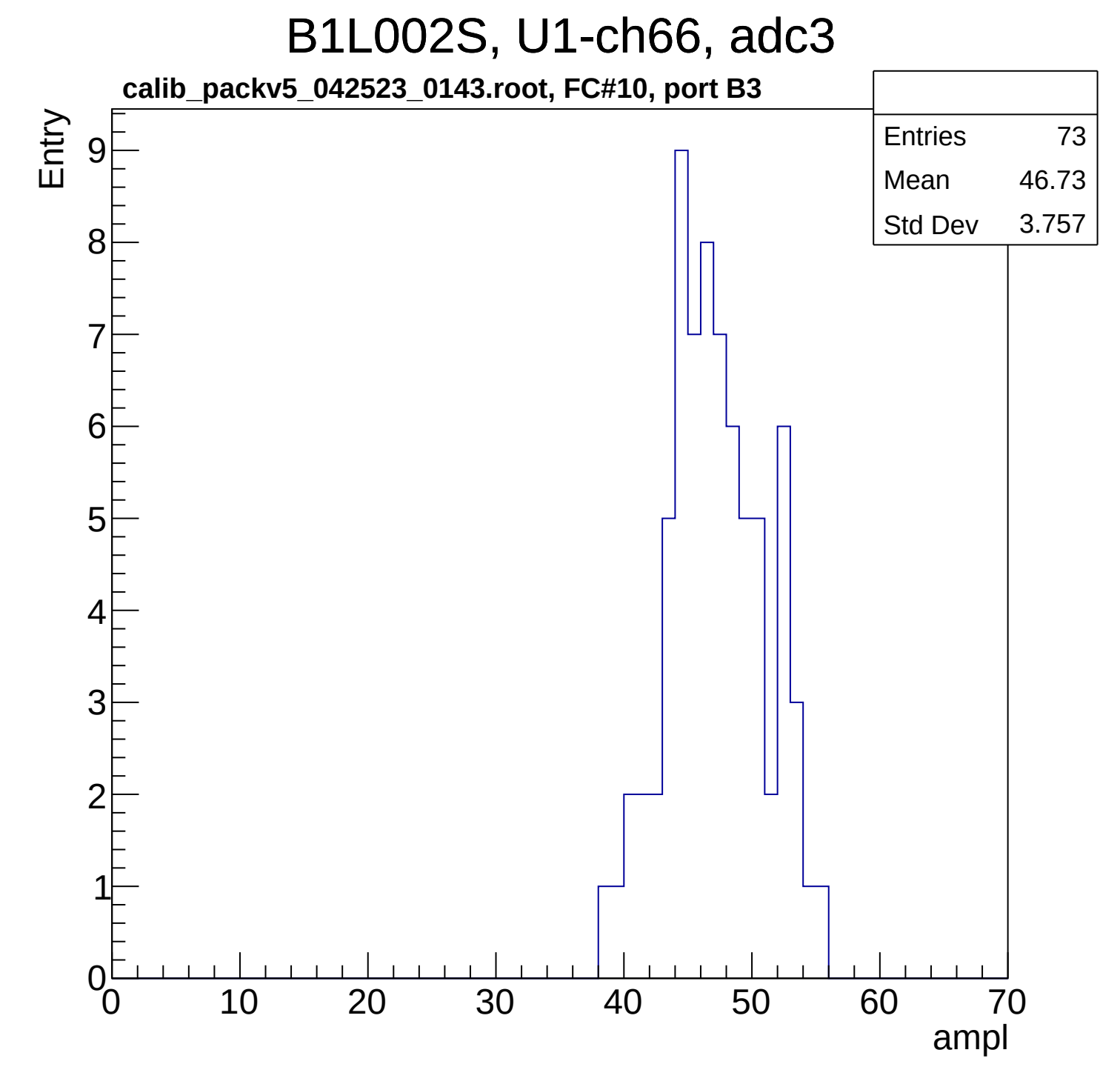
B1L002S, U1-ch66, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 46.73
Std Dev 3.757
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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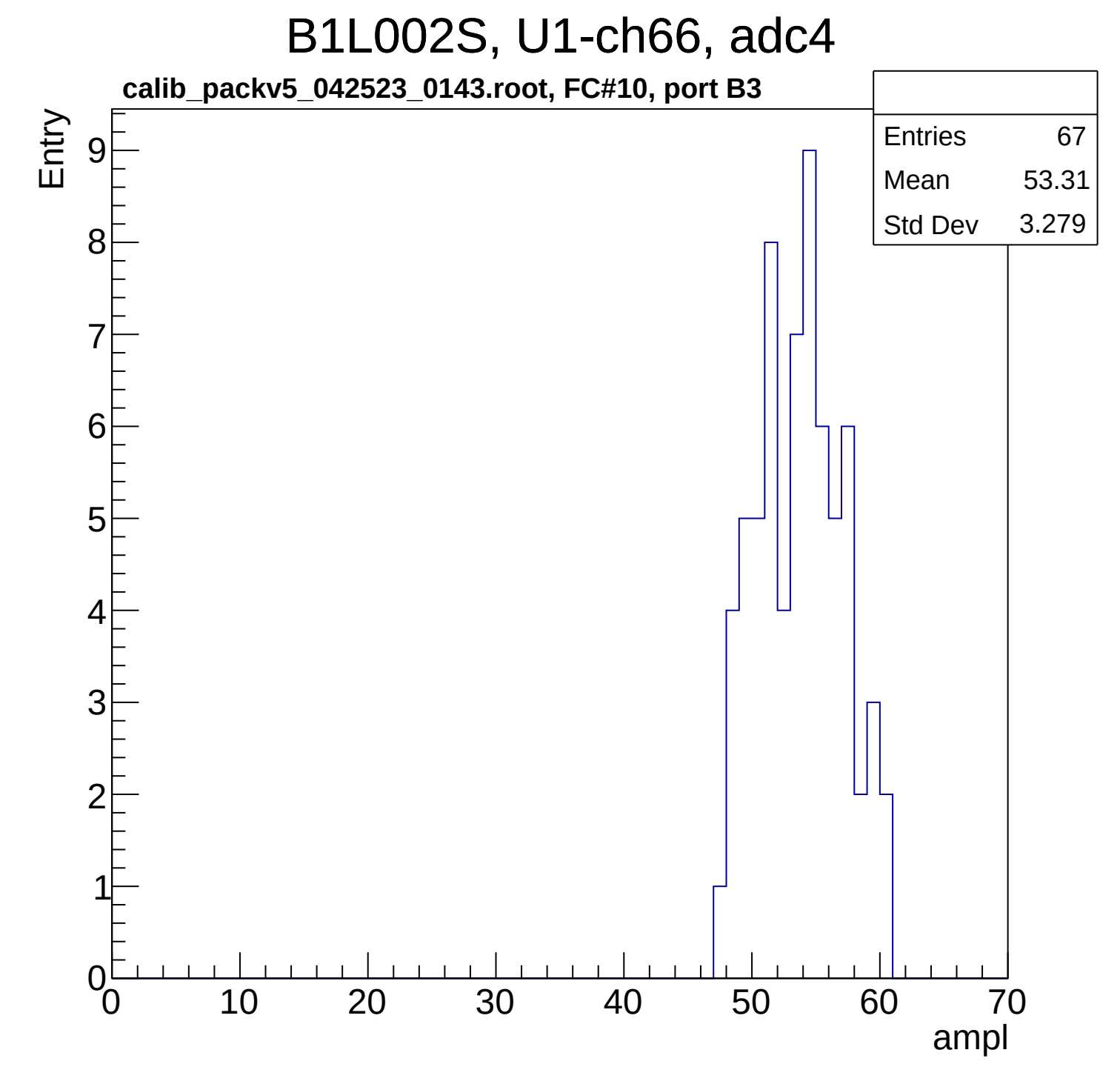
B1L002S, U1-ch66, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 53.31
Std Dev 3.279
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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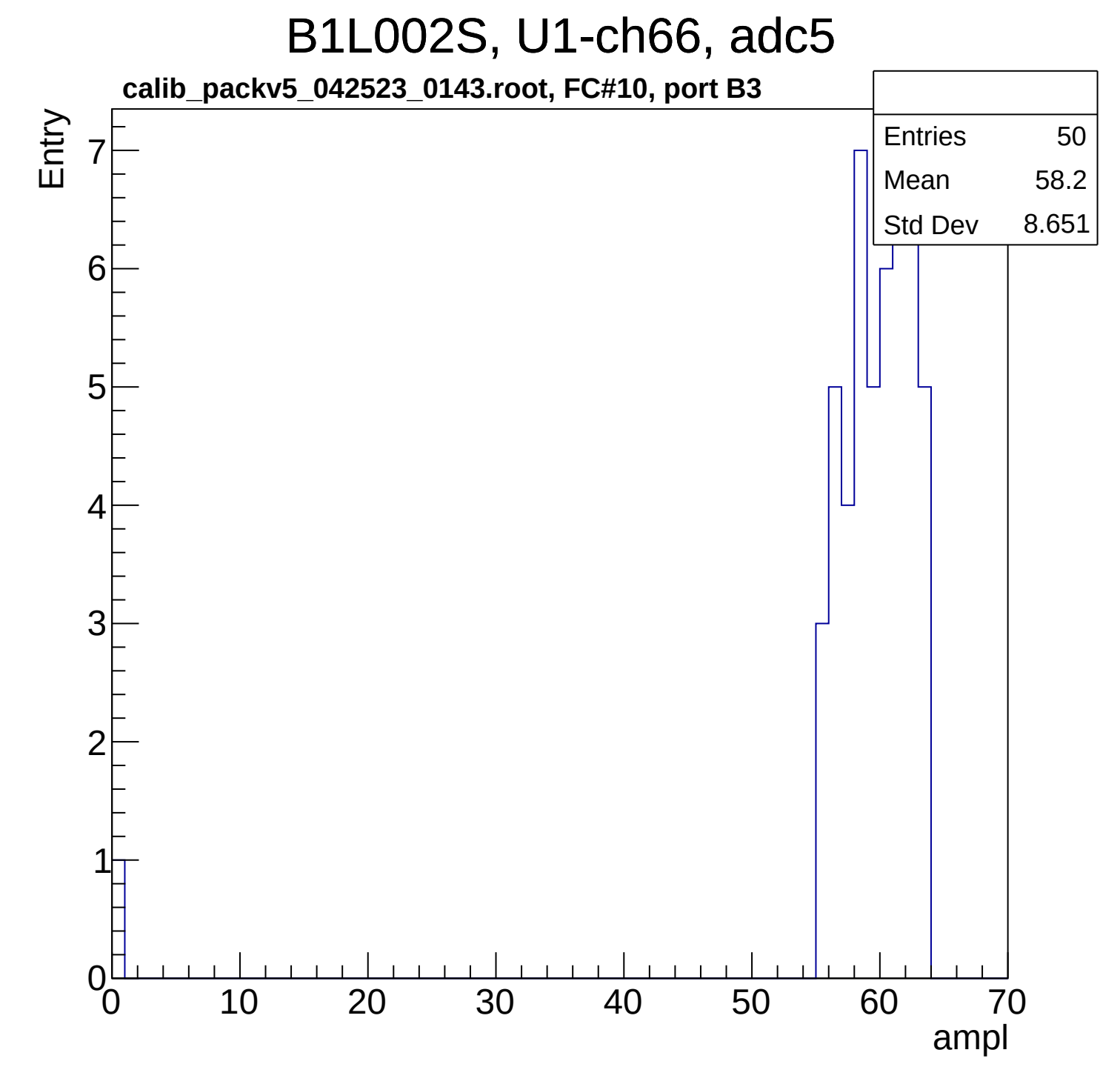
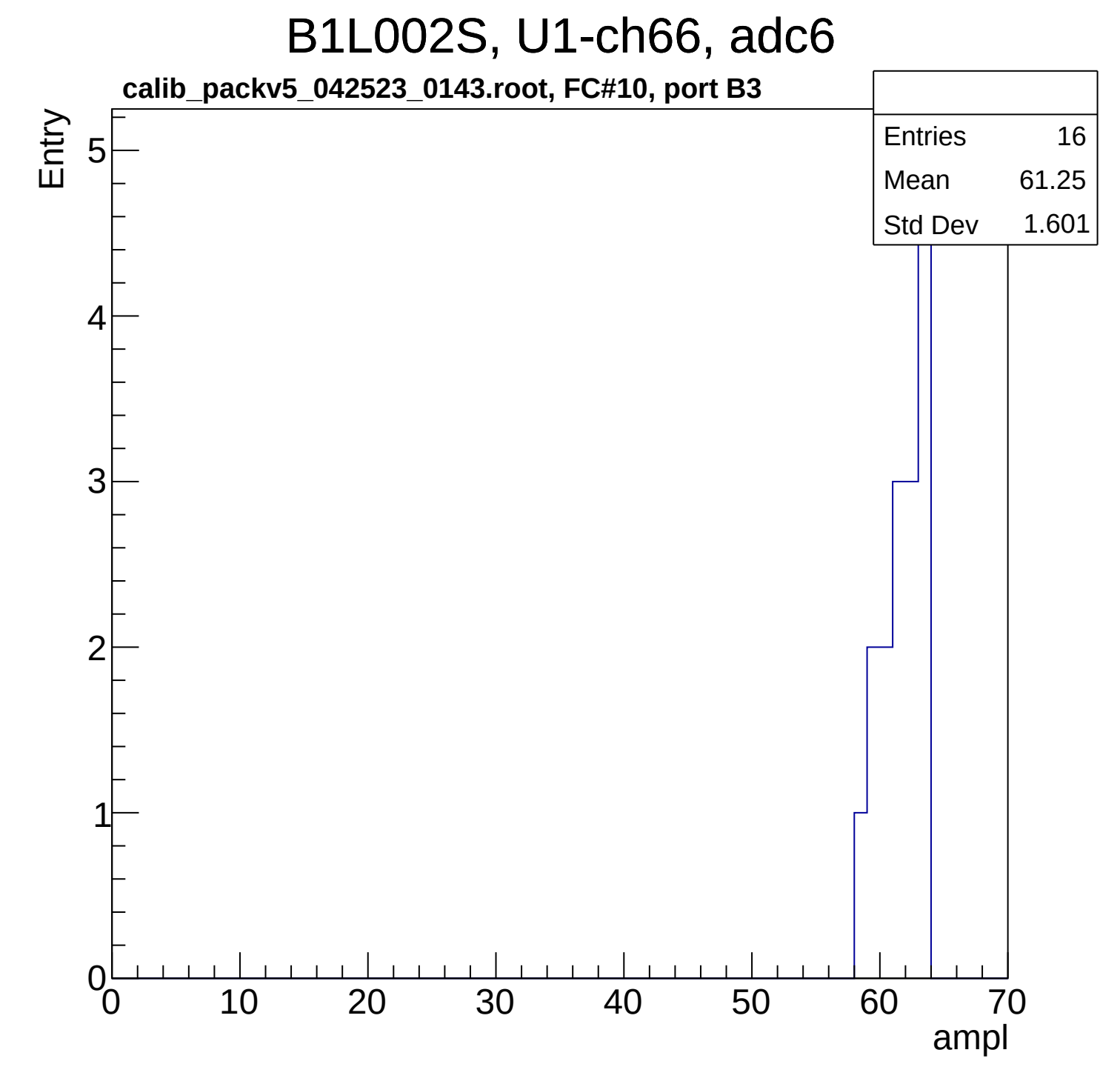
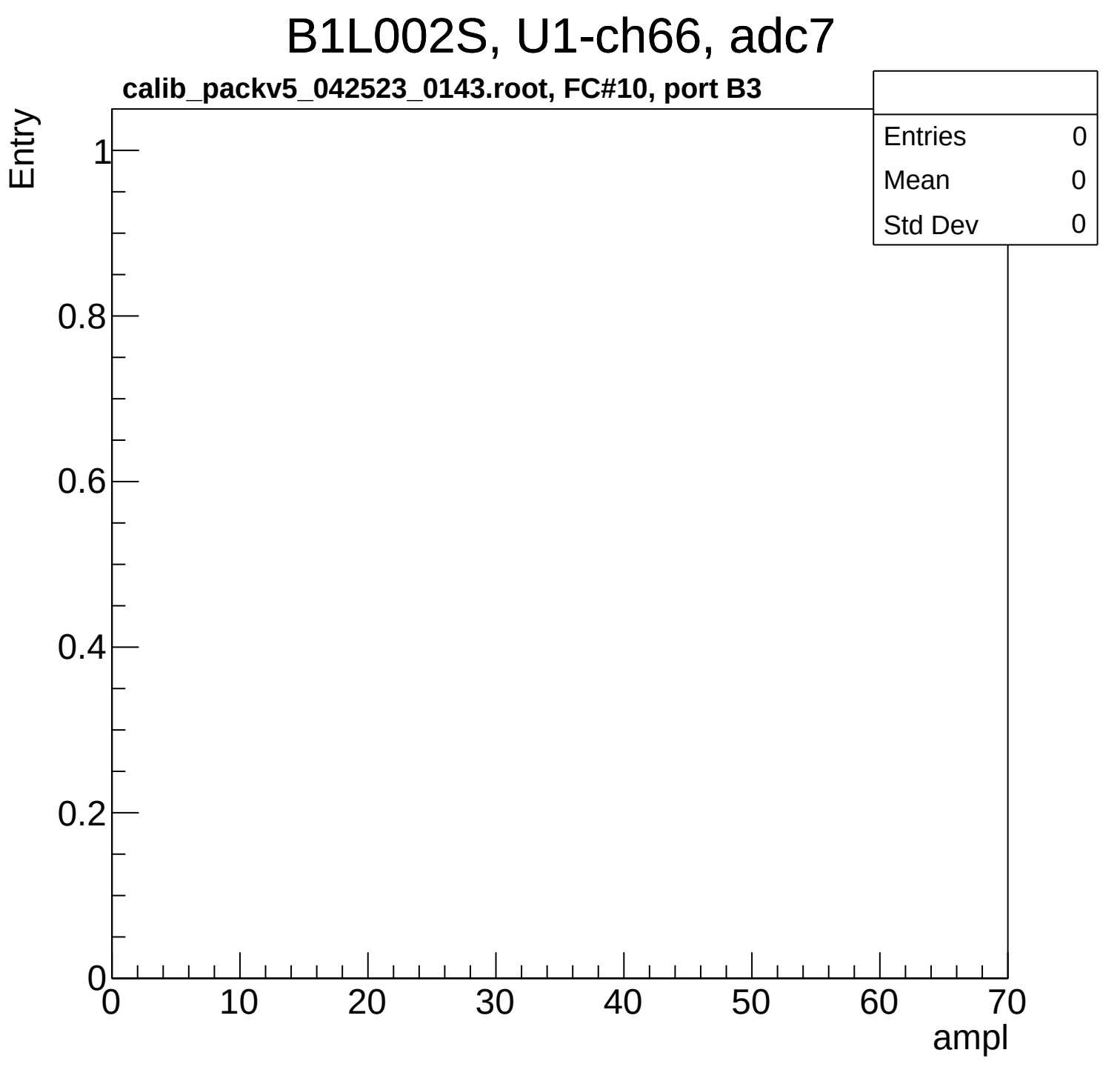

B1L002S, U1-ch66, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.2
Std Dev 8.651
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch66, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.25
Std Dev 1.601
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch66, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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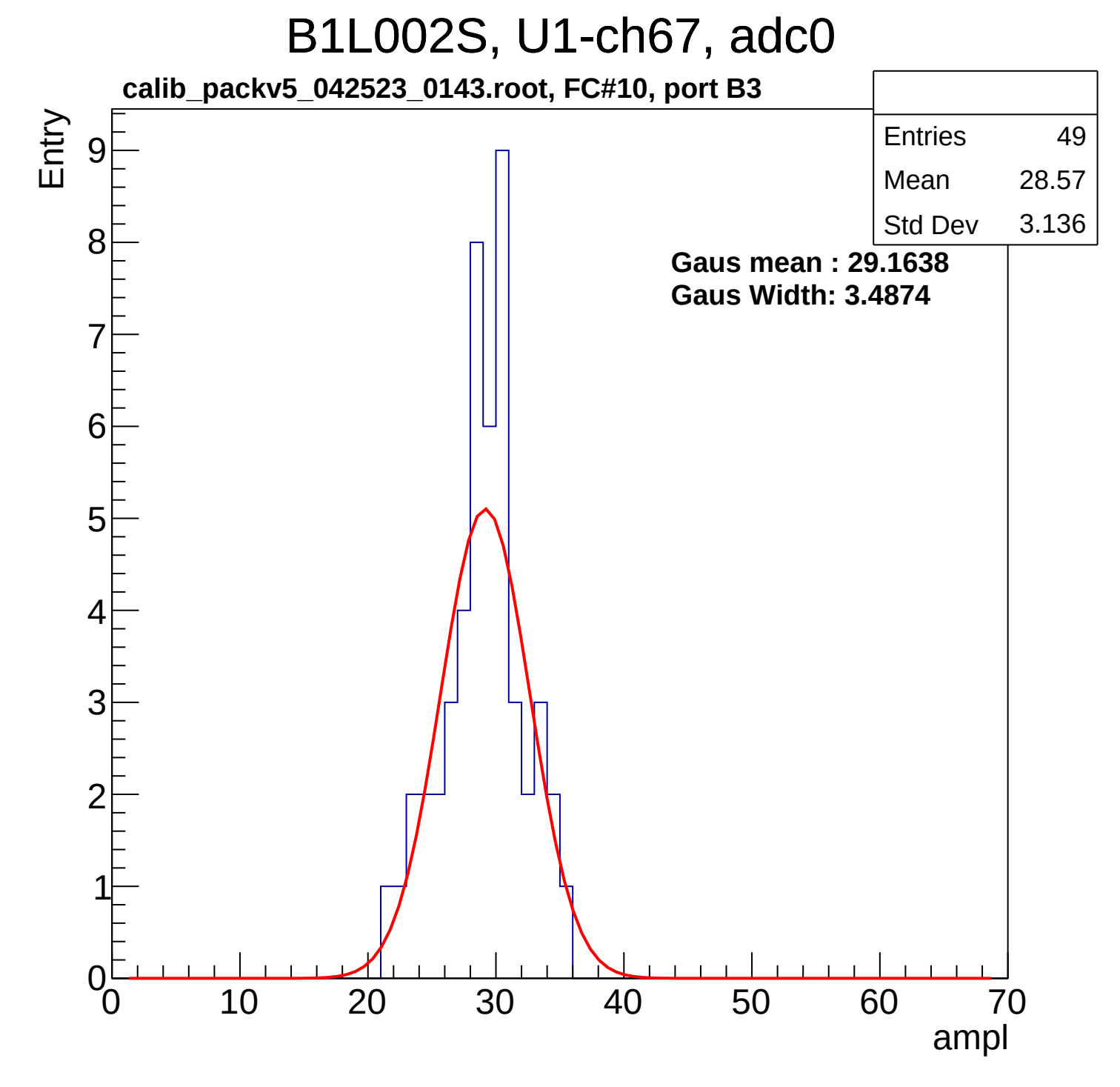
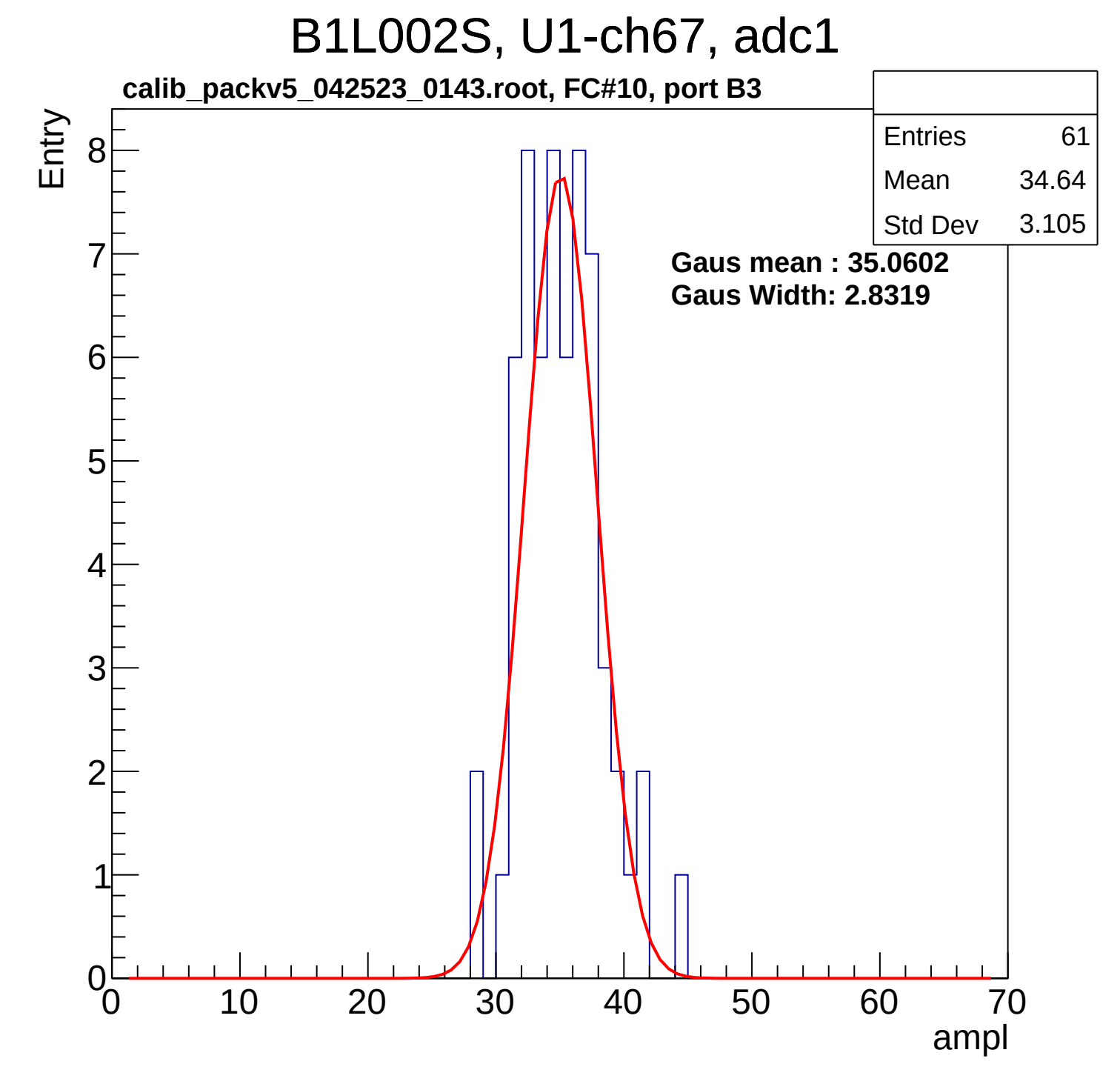
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch67, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 28.57
Std Dev 3.136
Gaus mean : 29.1638
Gaus Width: 3.4874
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch67, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 34.64
Std Dev 3.105
Gaus mean : 35.0602
Gaus Width: 2.8319
Entry
ampl
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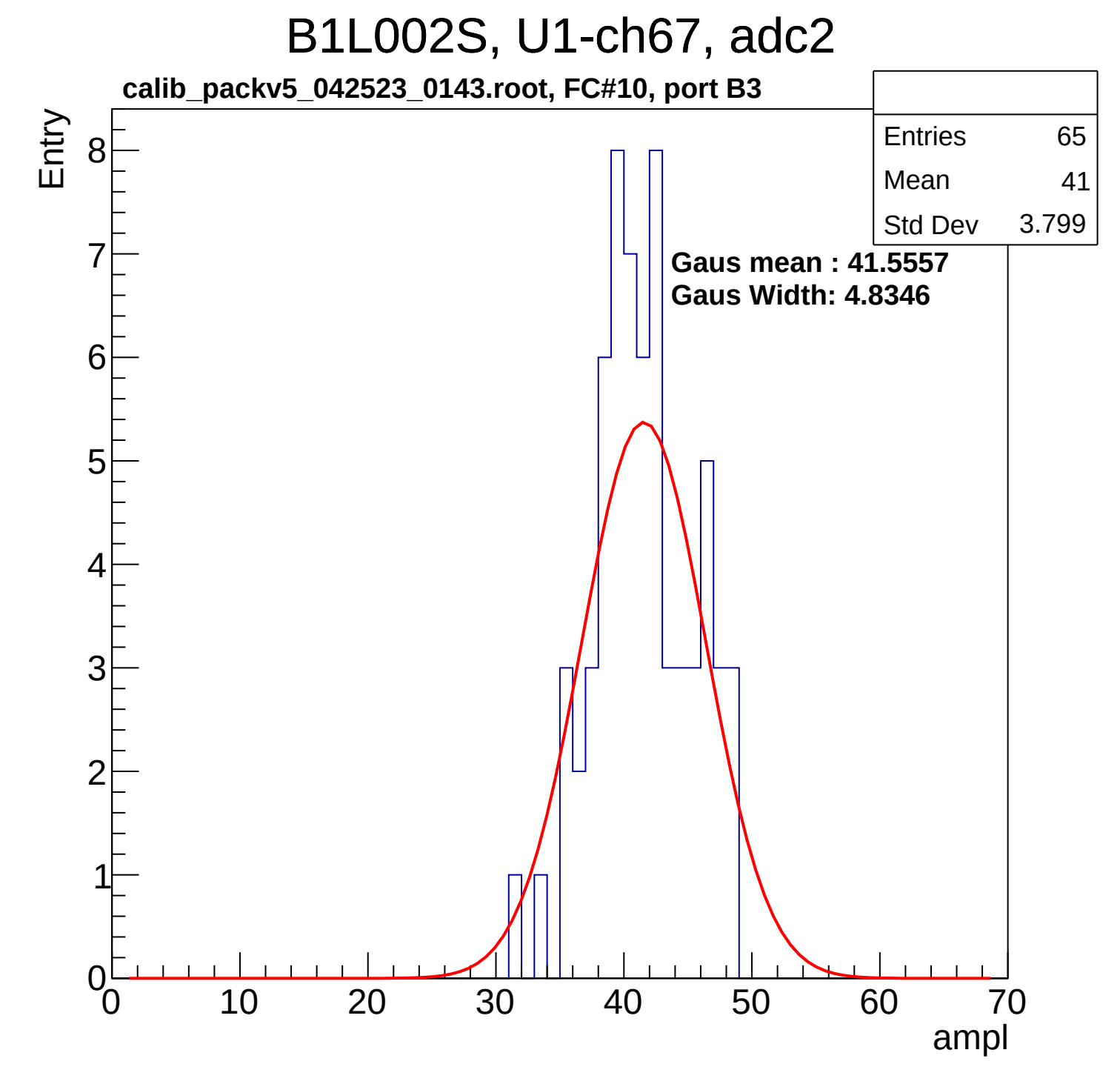
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch67, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41
Std Dev 3.799
Gaus mean : 41.5557
Gaus Width: 4.8346
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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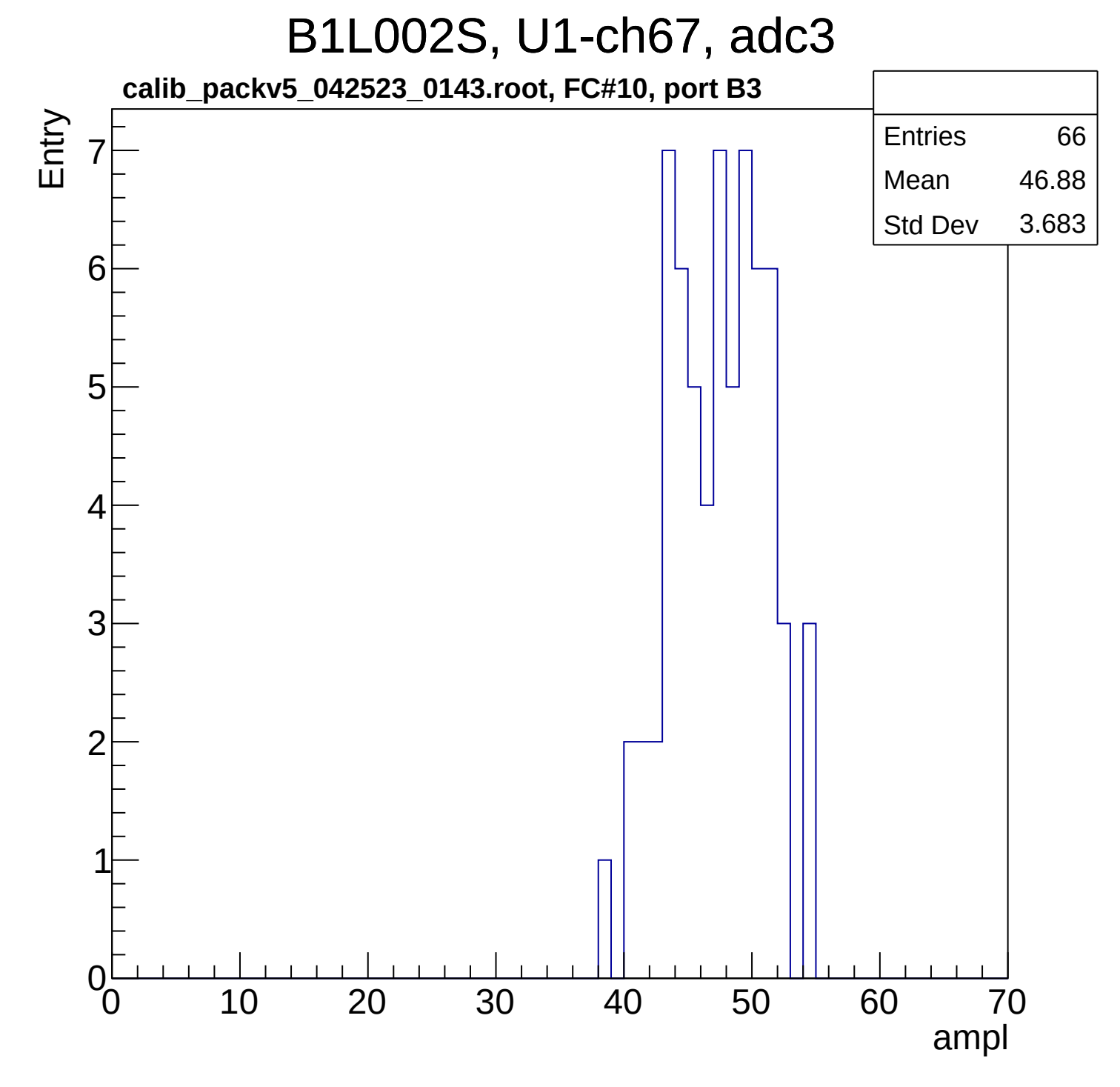
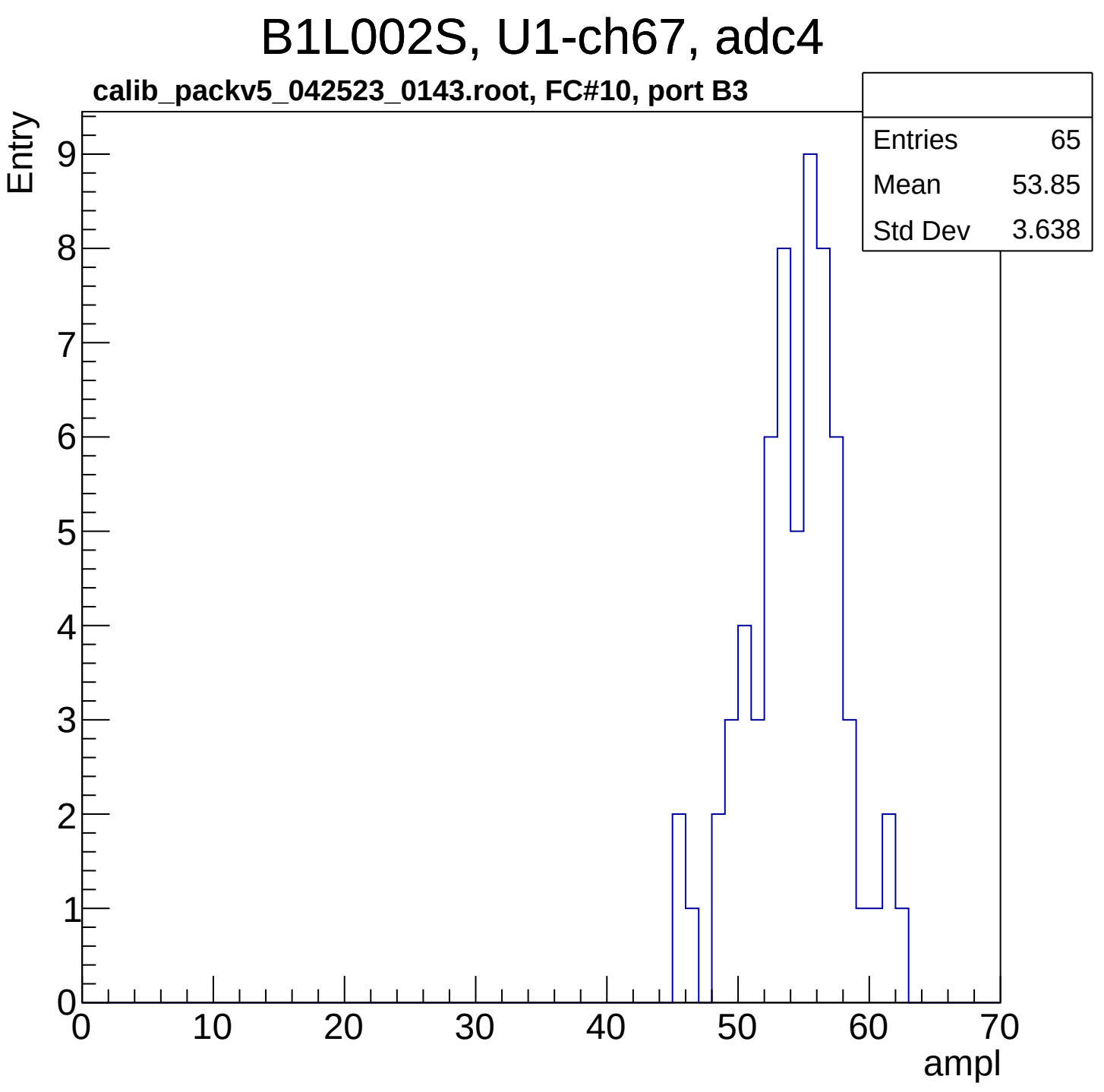

B1L002S, U1-ch67, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 46.88
Std Dev 3.683
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch67, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 53.85
Std Dev 3.638
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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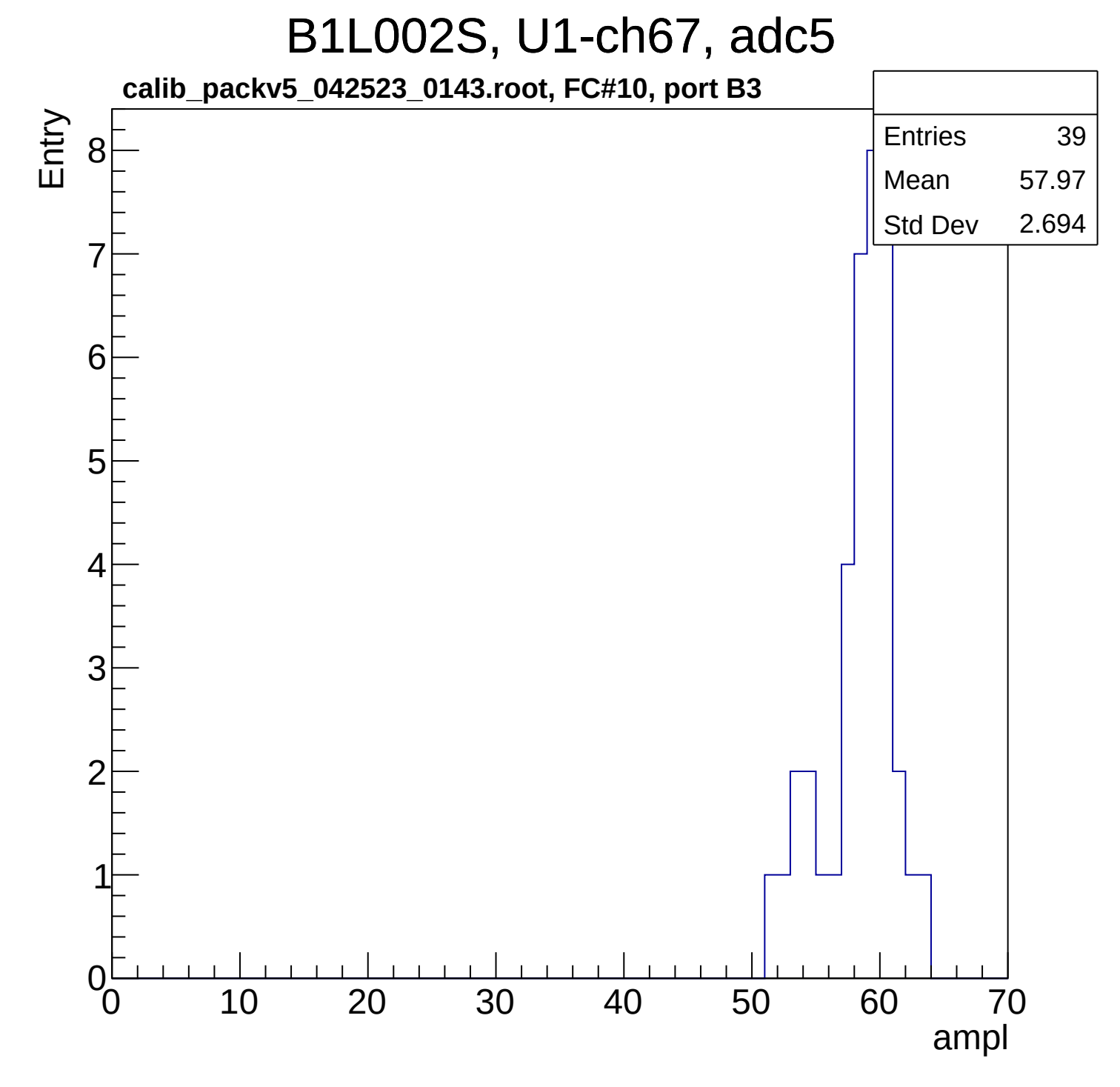
B1L002S, U1-ch67, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 57.97
Std Dev 2.694
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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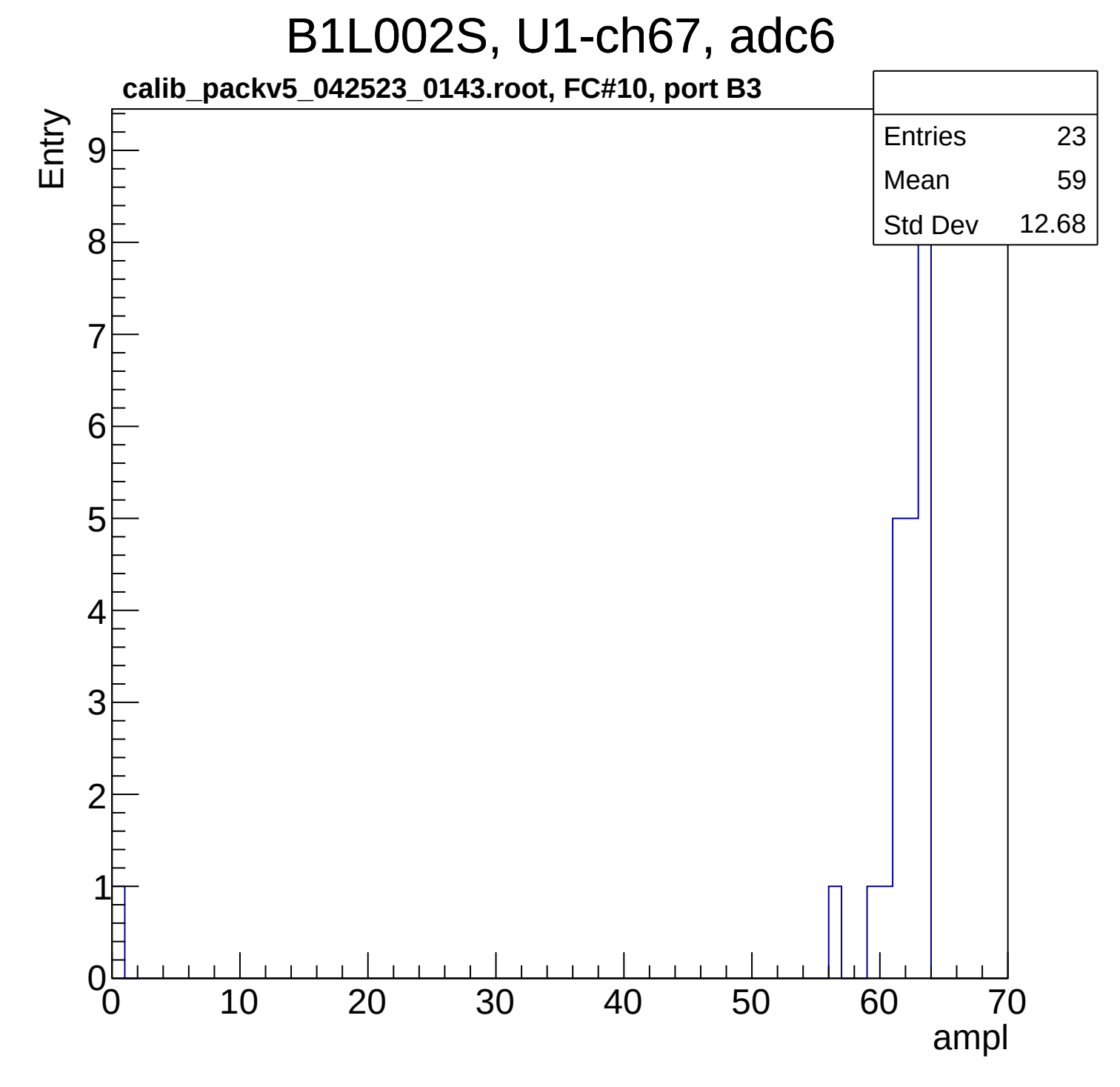
B1L002S, U1-ch67, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 59
Std Dev 12.68
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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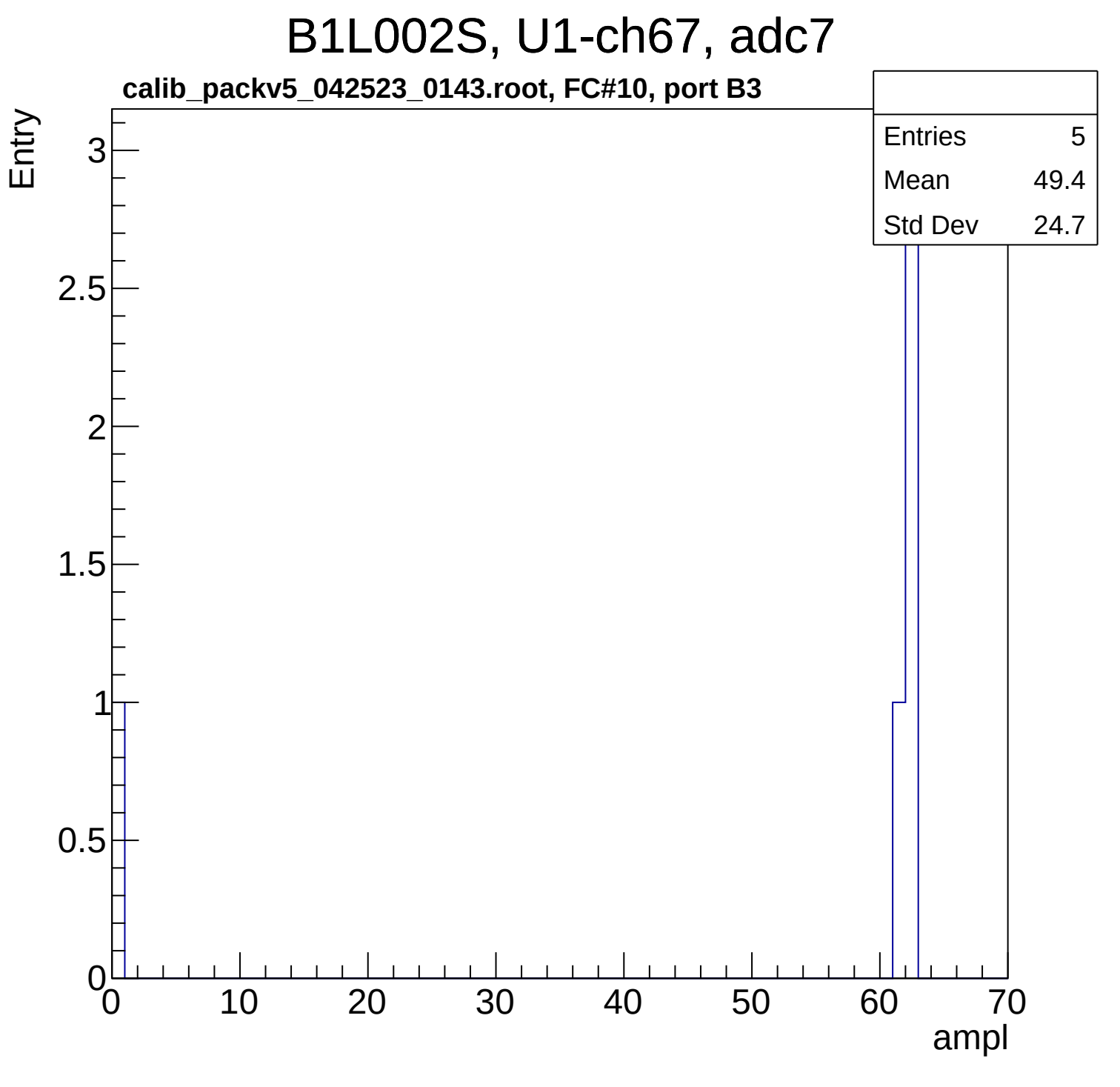
B1L002S, U1-ch67, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49.4
Std Dev 24.7
Entry
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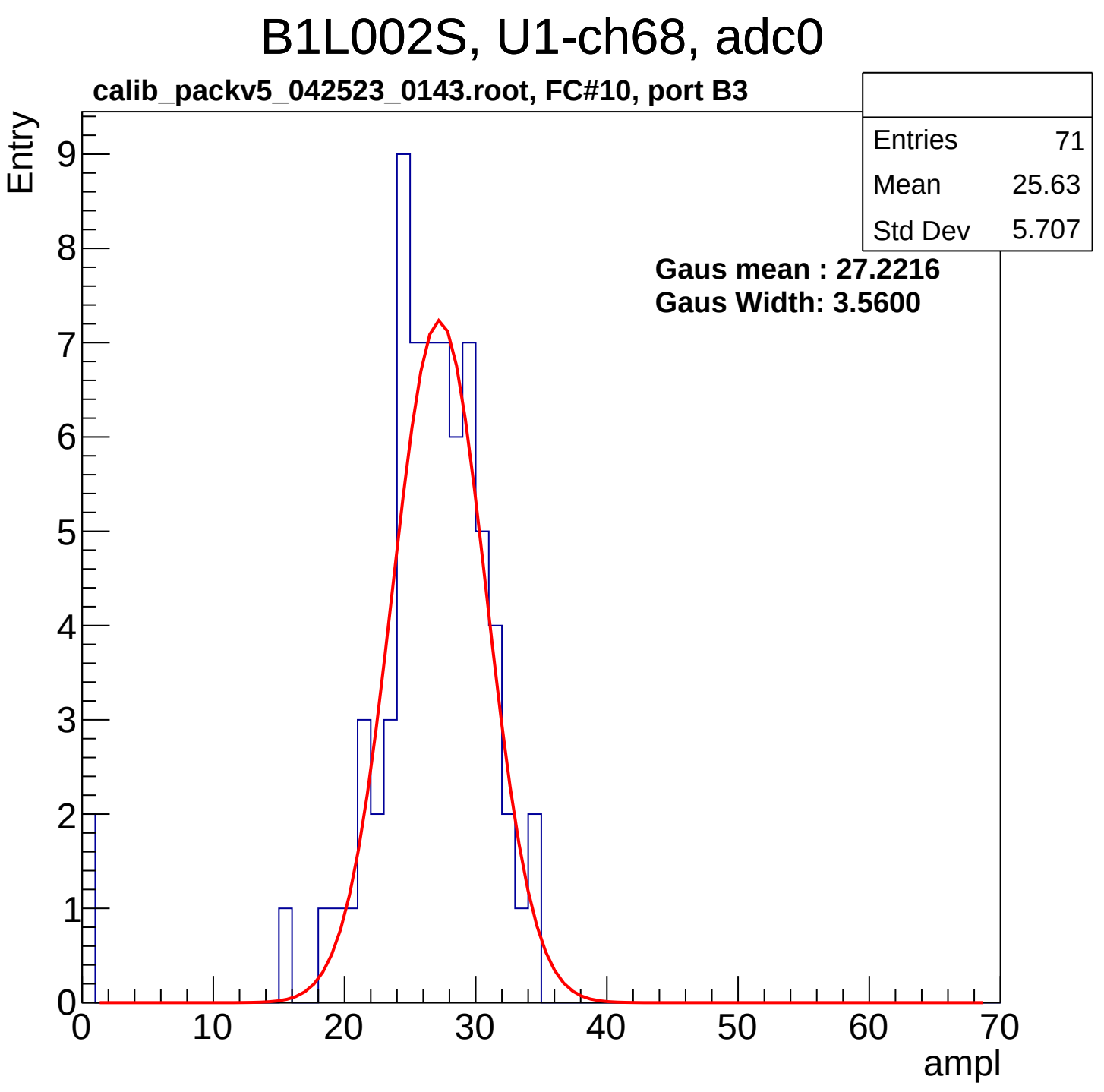
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch68, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 25.63
Std Dev 5.707
Gaus mean : 27.2216
Gaus Width: 3.5600
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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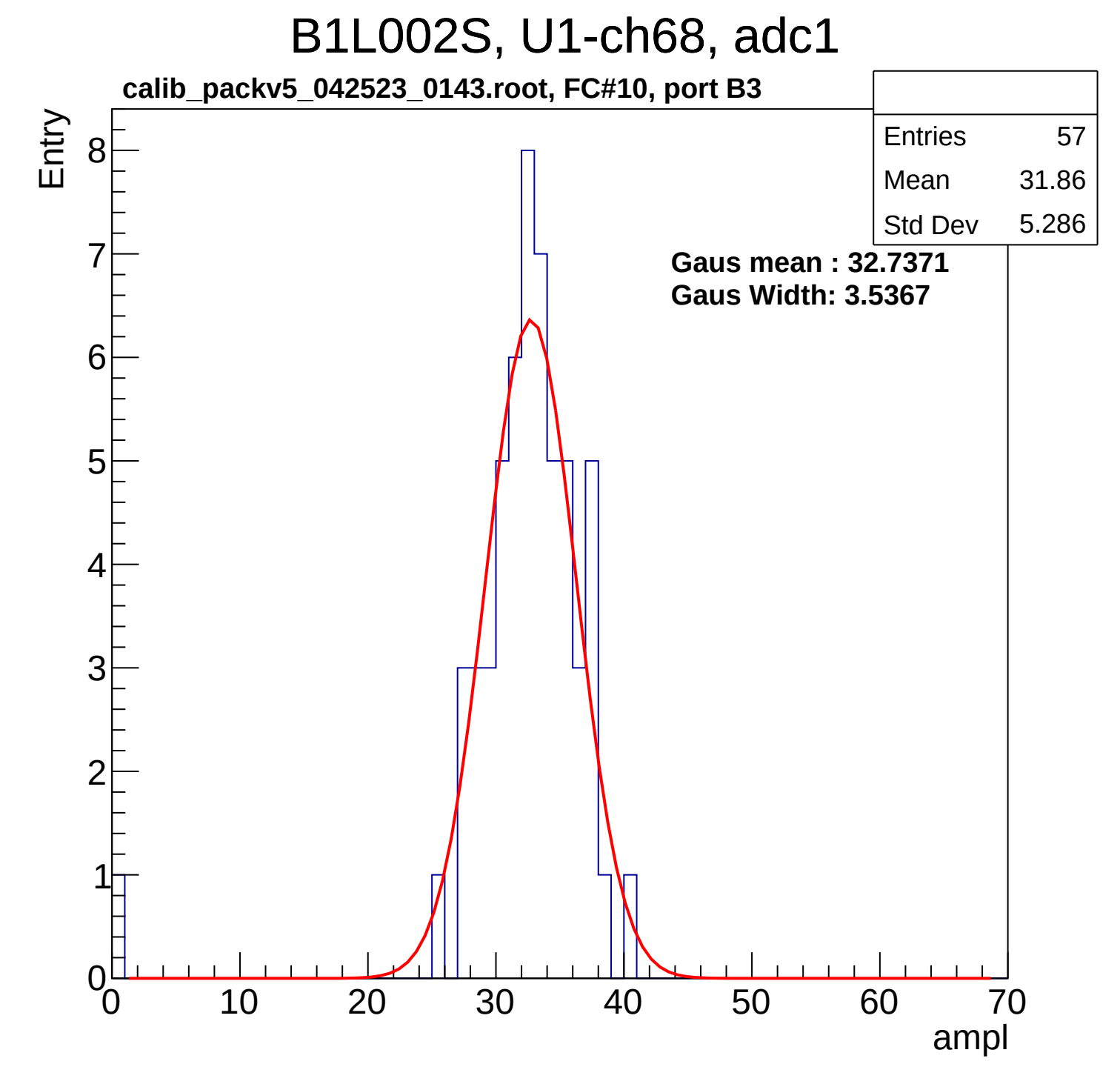
60
70


B1L002S, U1-ch68, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 31.86
Std Dev 5.286
Gaus mean : 32.7371
Gaus Width: 3.5367
Entry
ampl
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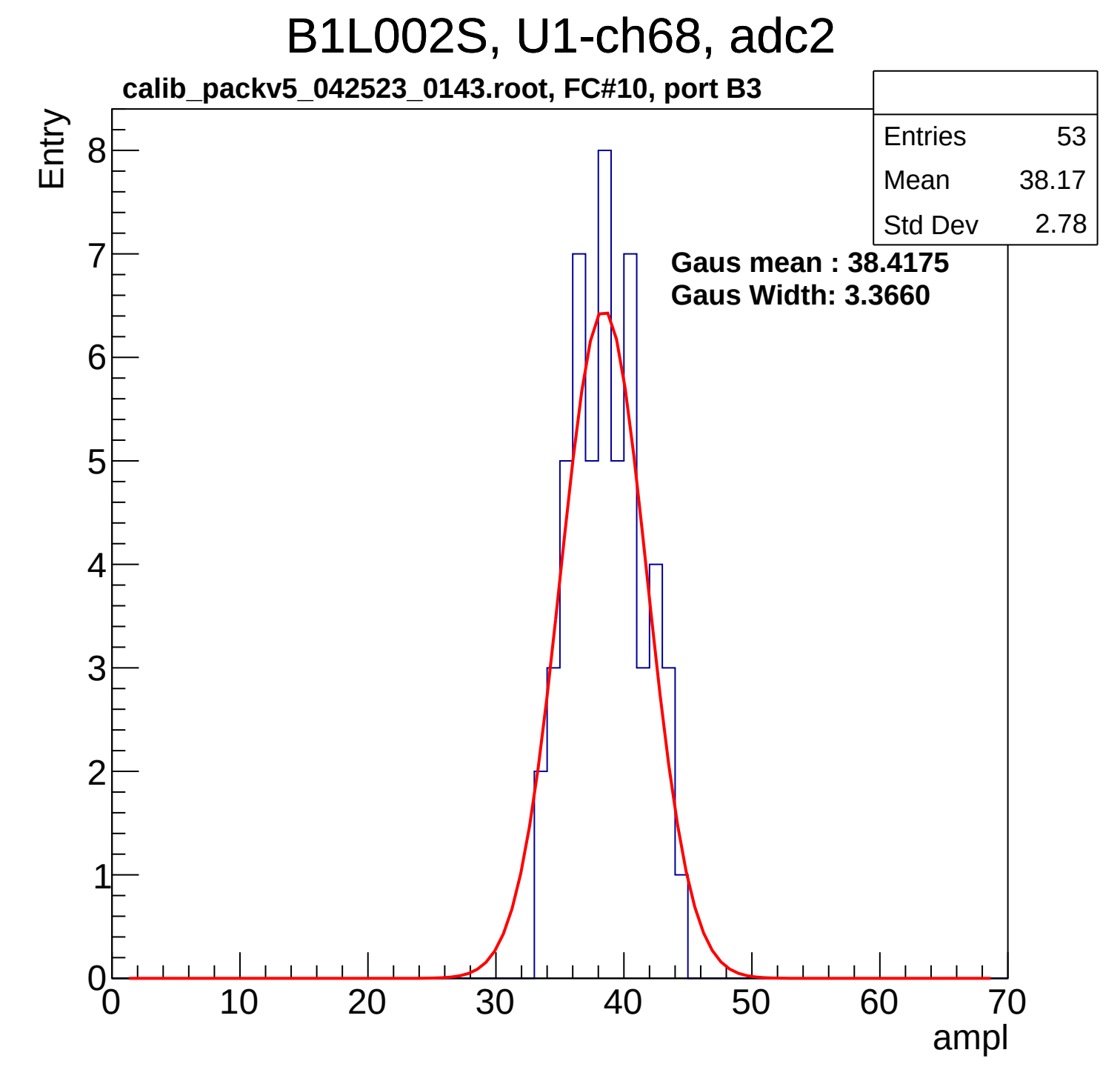
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch68, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 38.17
Std Dev 2.78
Gaus mean : 38.4175
Gaus Width: 3.3660
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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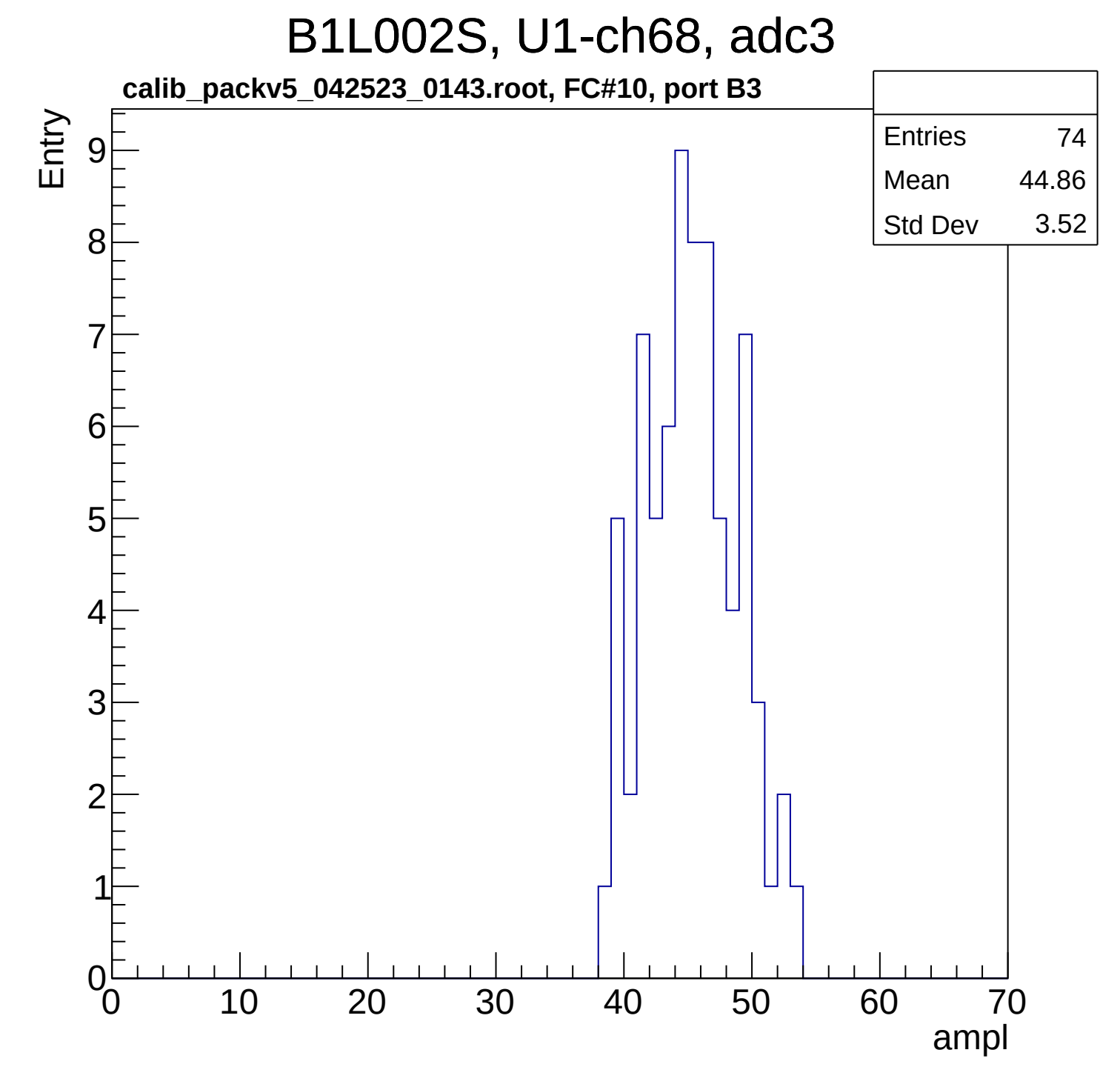
B1L002S, U1-ch68, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 44.86
Std Dev 3.52
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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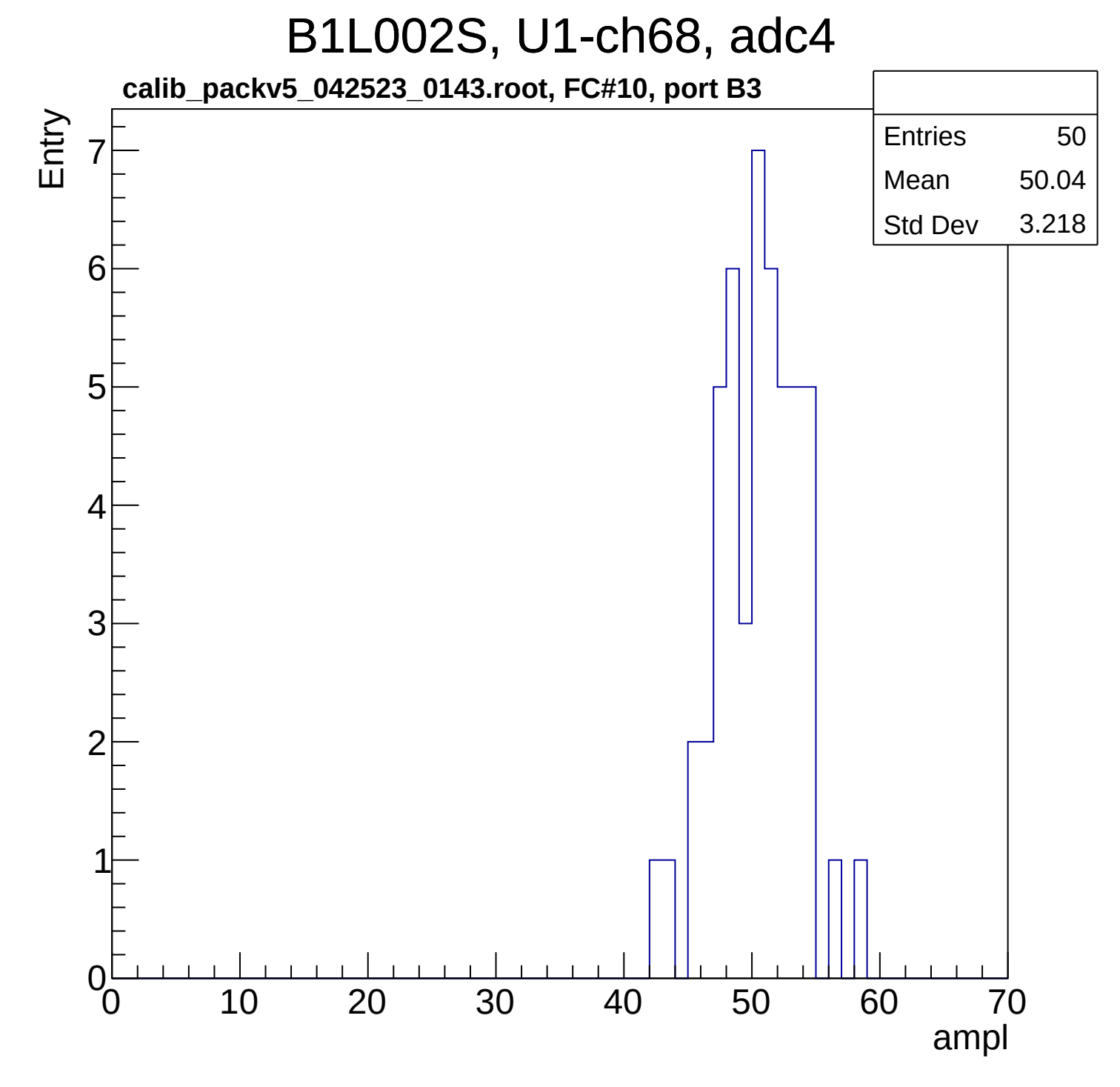
40
50
60
70


B1L002S, U1-ch68, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 50.04
Std Dev 3.218
Entry
ampl
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1
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3
4
5
6
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0
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20
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50
60
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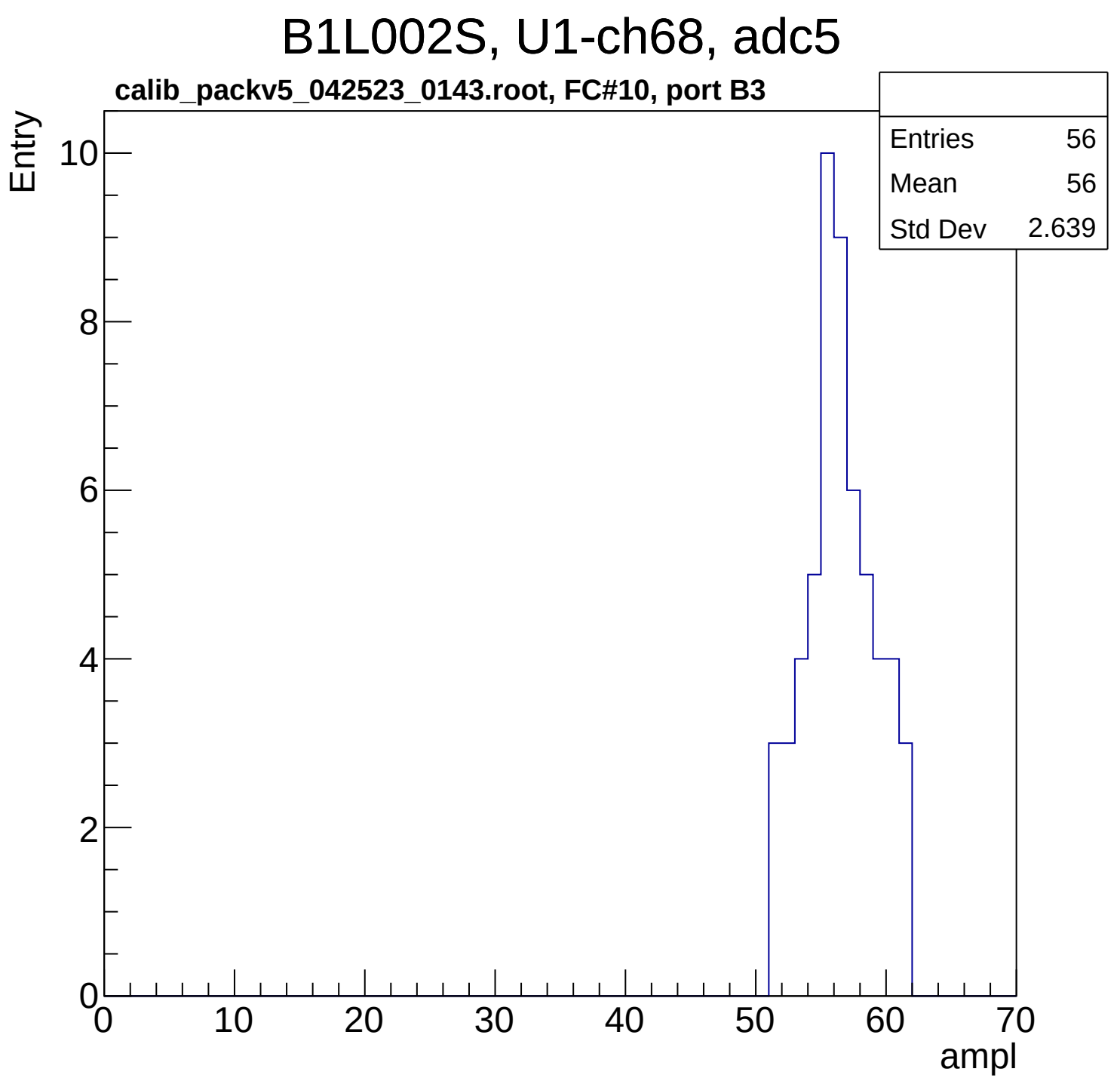
B1L002S, U1-ch68, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 56
Std Dev 2.639
Entry
ampl
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2
4
6
8
10
0
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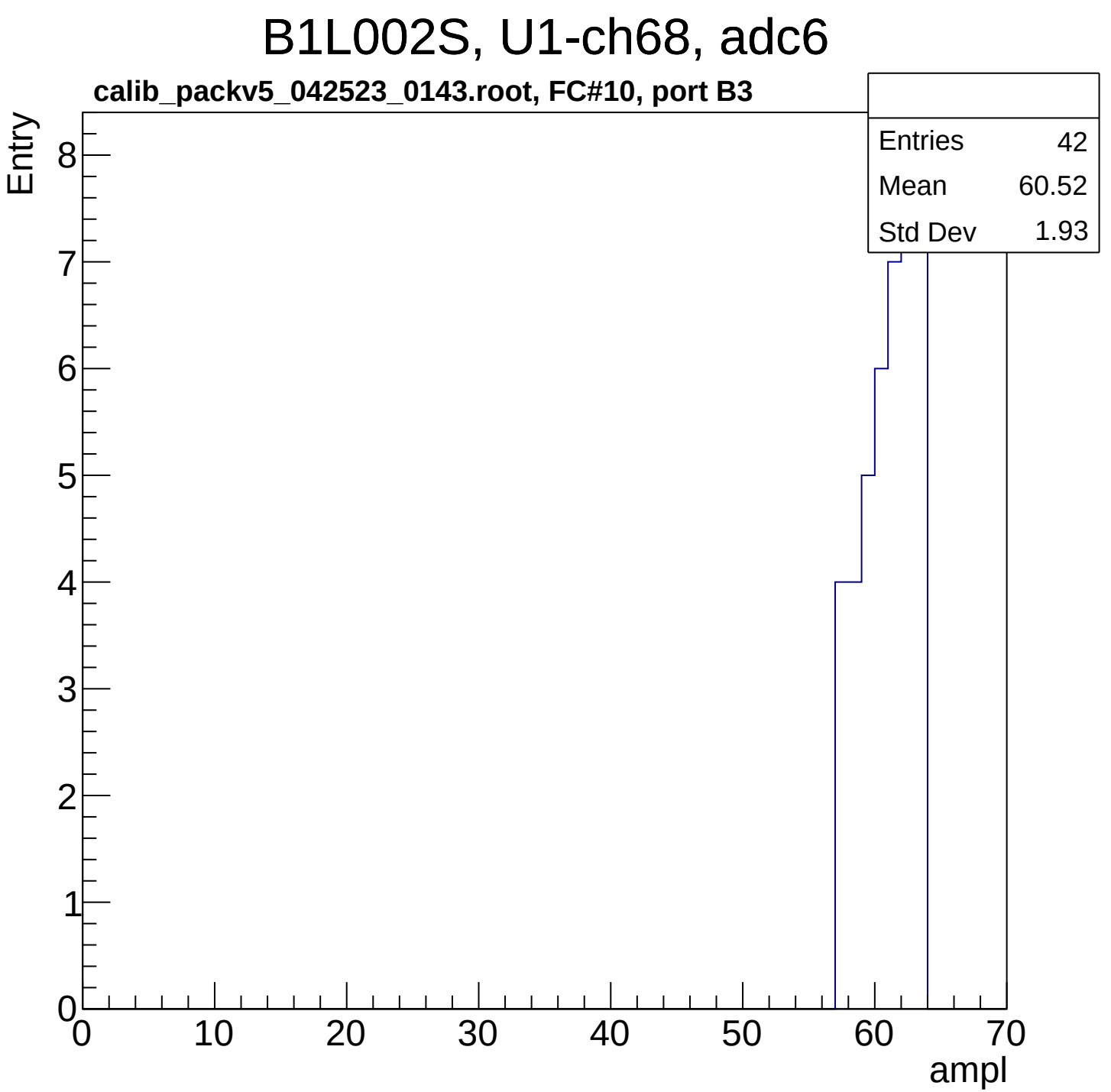
30
40
50
60
70


B1L002S, U1-ch68, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 60.52
Std Dev 1.93
Entry
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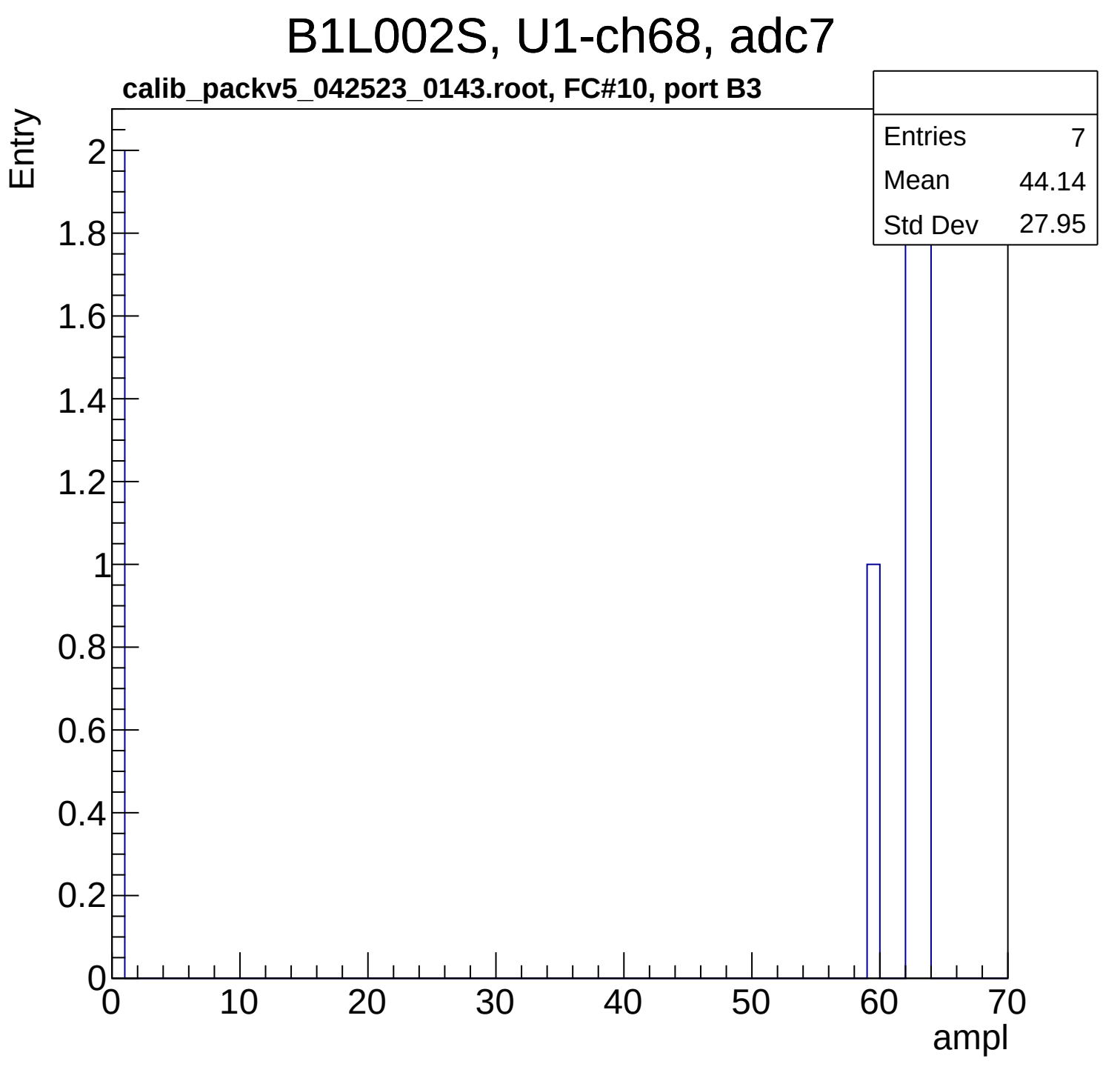
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch68, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 44.14
Std Dev 27.95
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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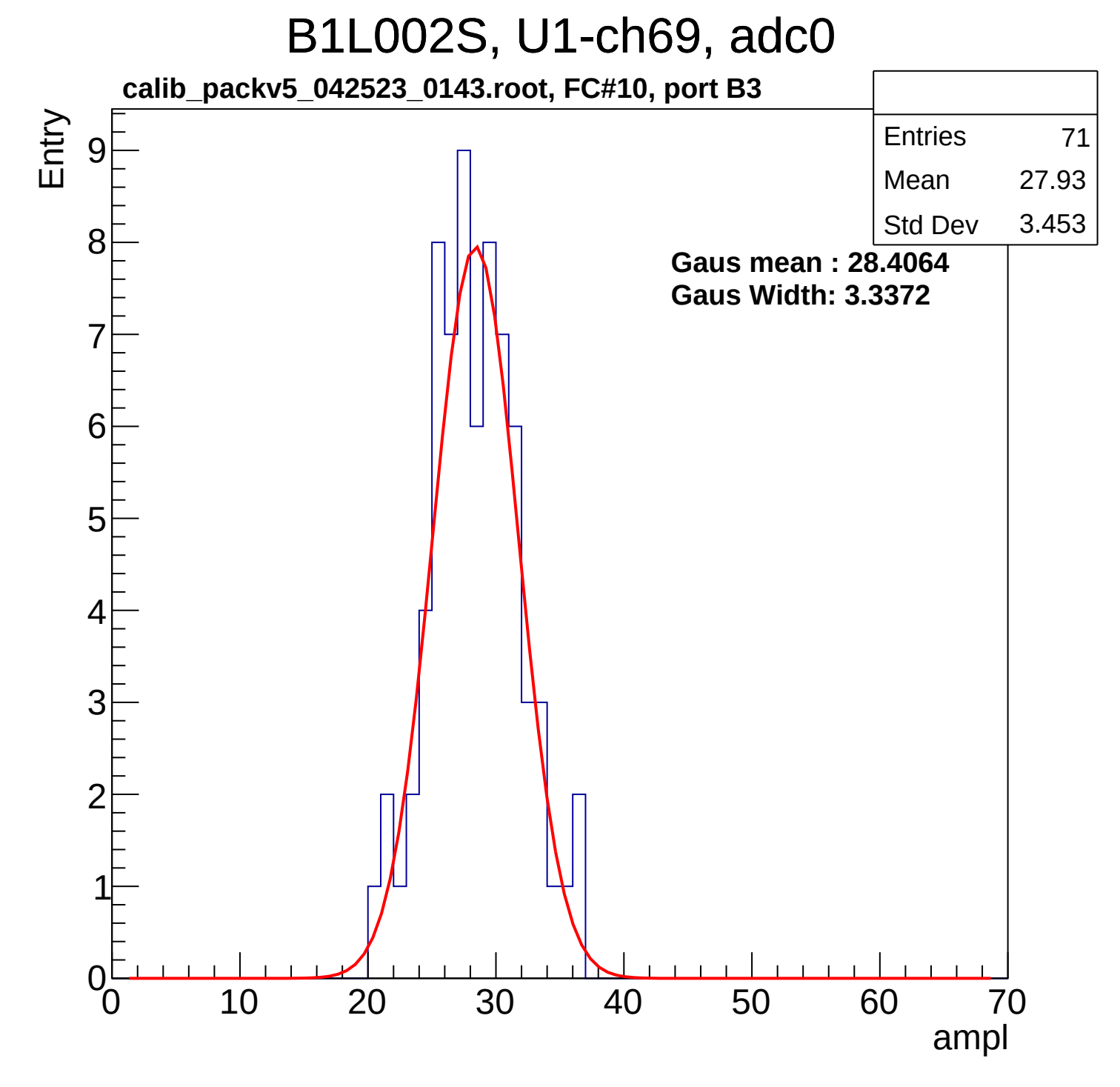
B1L002S, U1-ch69, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.93
Std Dev 3.453
Gaus mean : 28.4064
Gaus Width: 3.3372
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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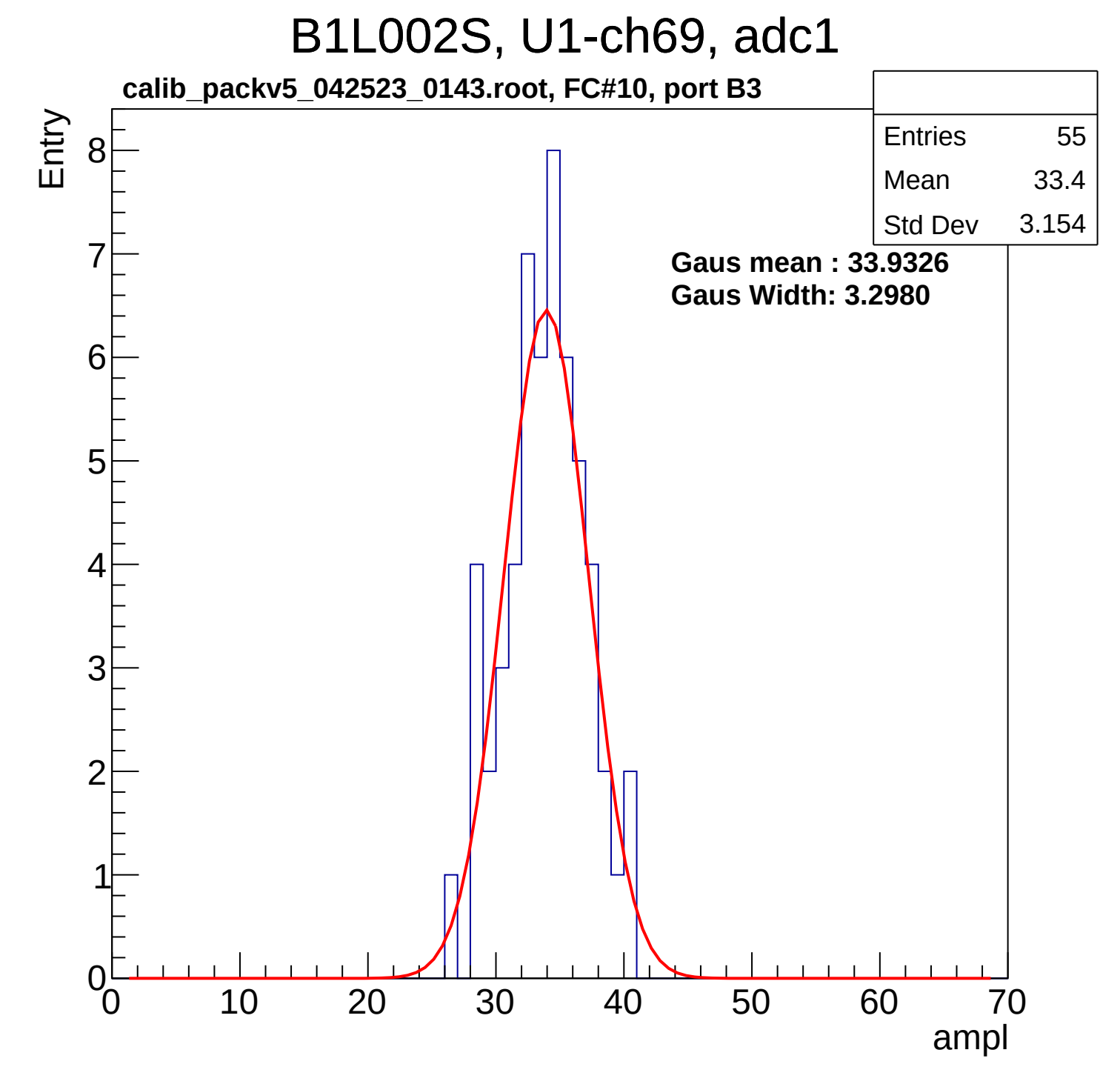
B1L002S, U1-ch69, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 33.4
Std Dev 3.154
Gaus mean : 33.9326
Gaus Width: 3.2980
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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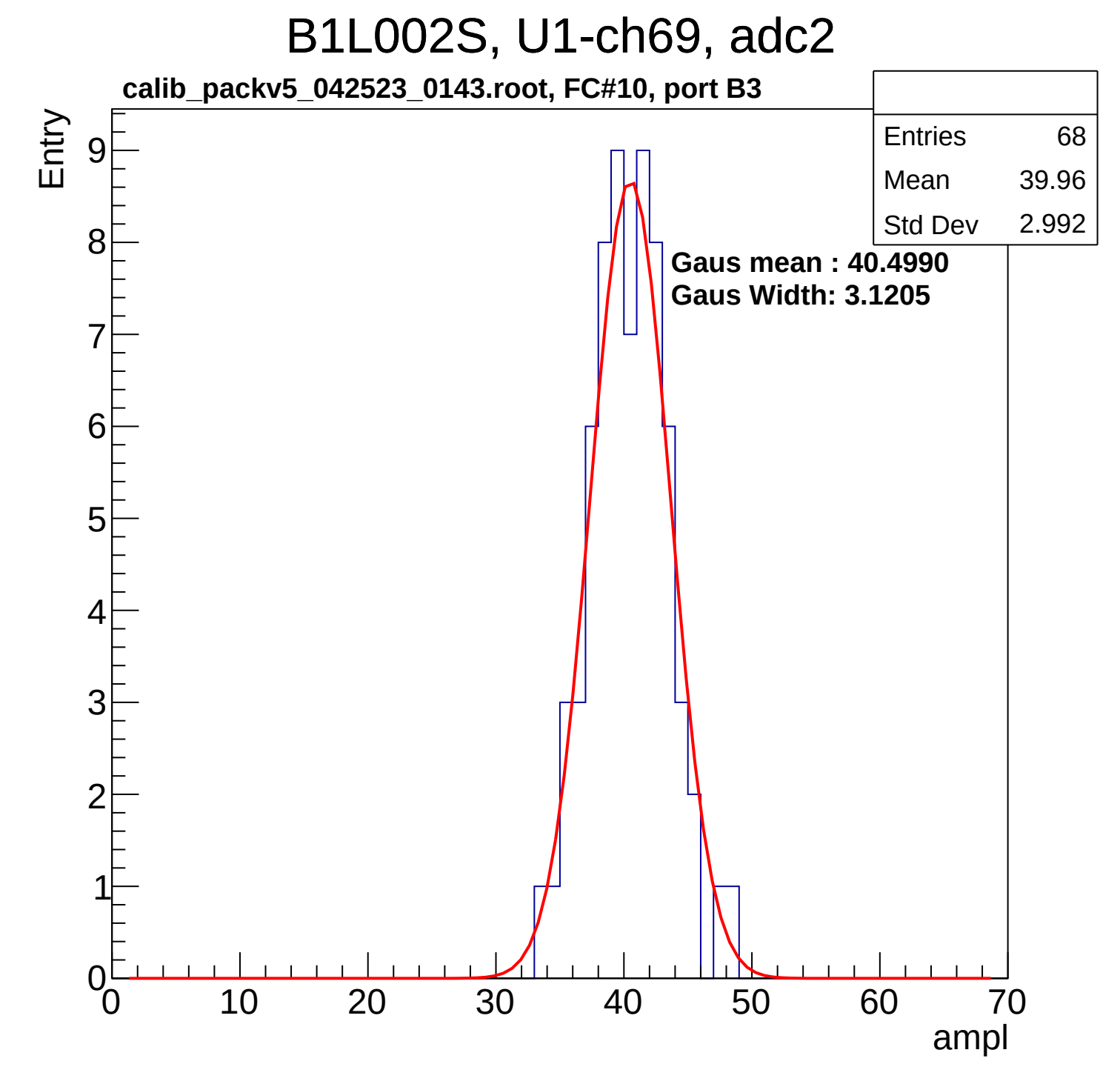
B1L002S, U1-ch69, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 39.96
Std Dev 2.992
Gaus mean : 40.4990
Gaus Width: 3.1205
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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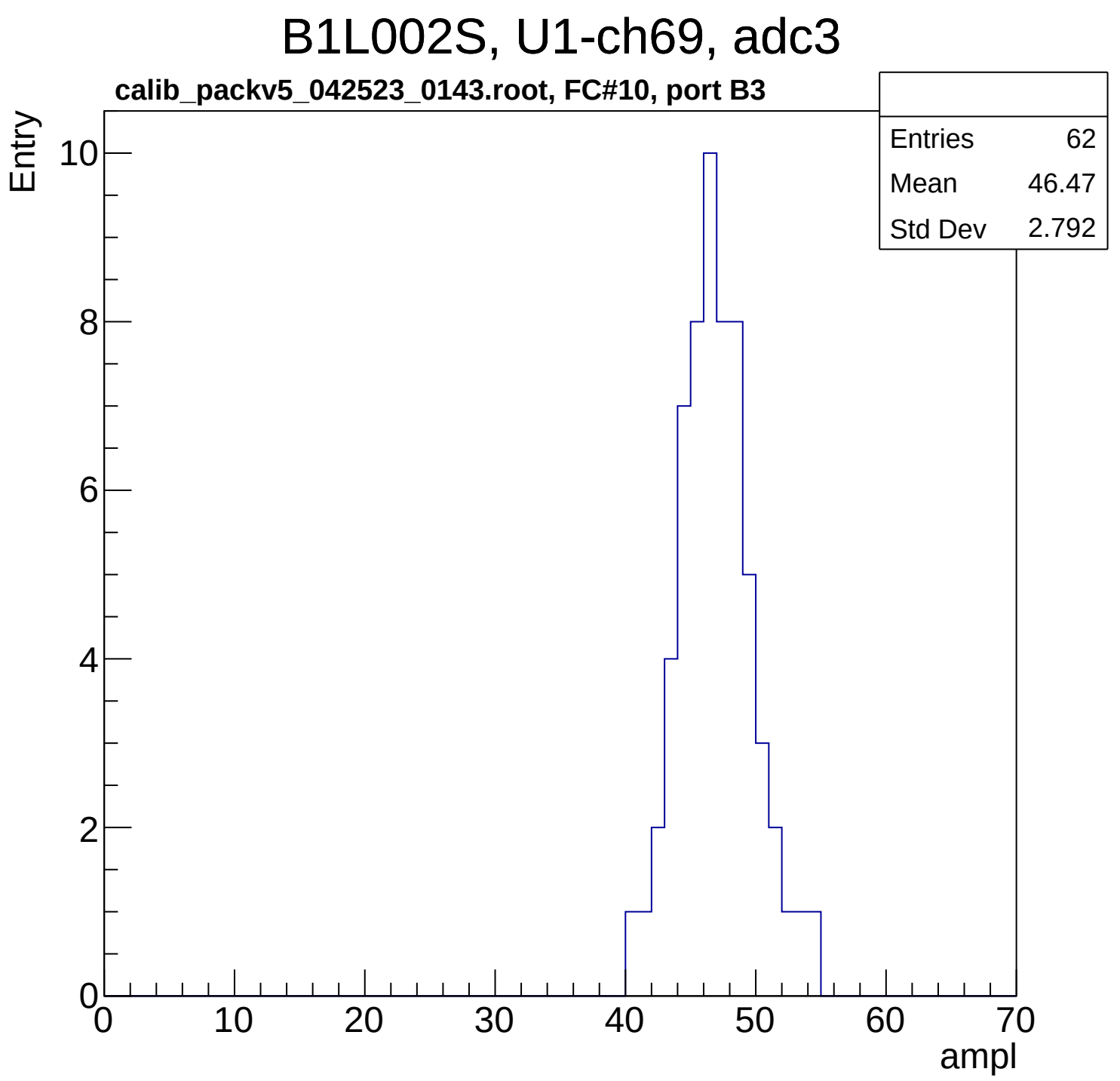
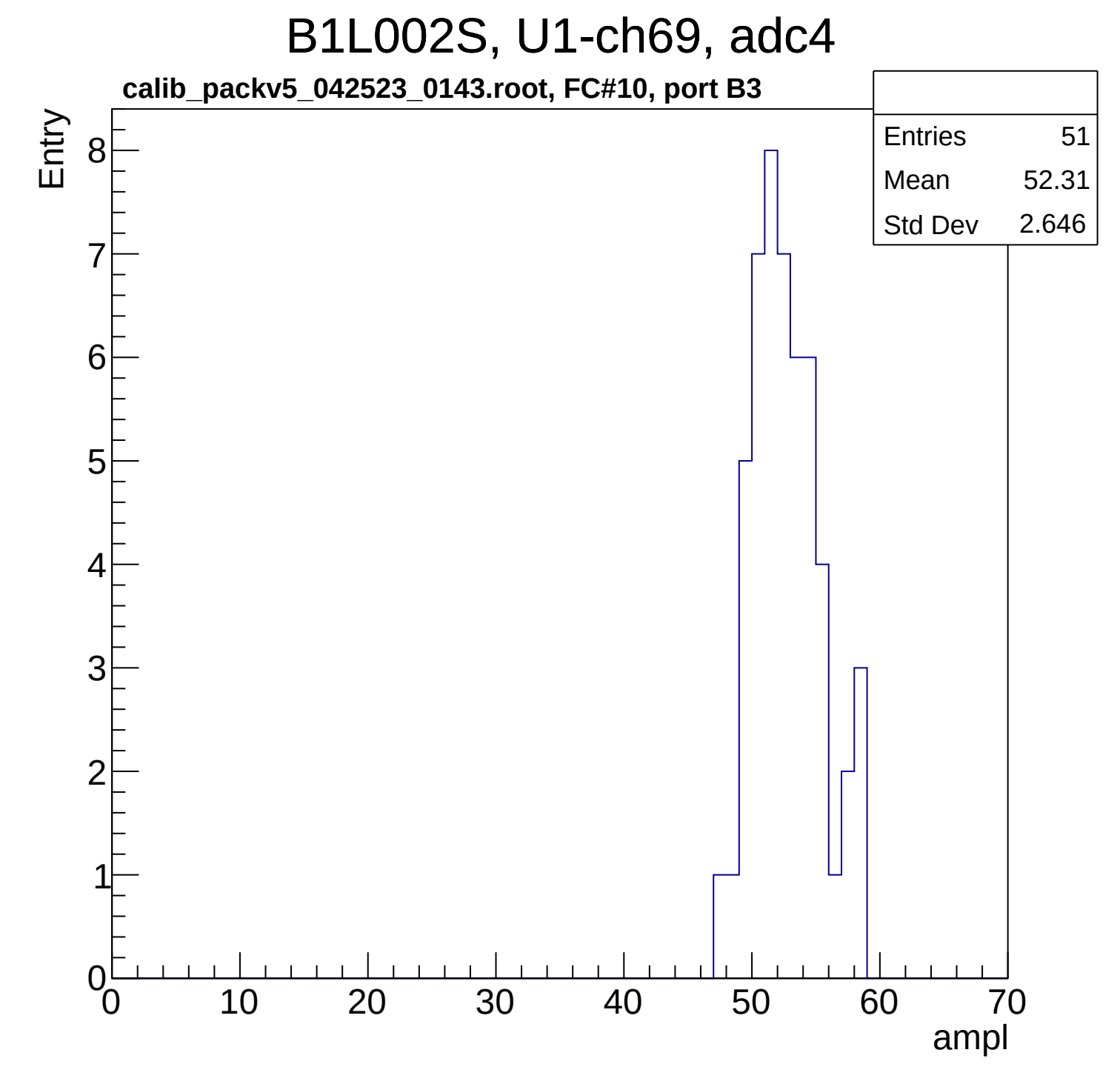

B1L002S, U1-ch69, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.47
Std Dev 2.792
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch69, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 52.31
Std Dev 2.646
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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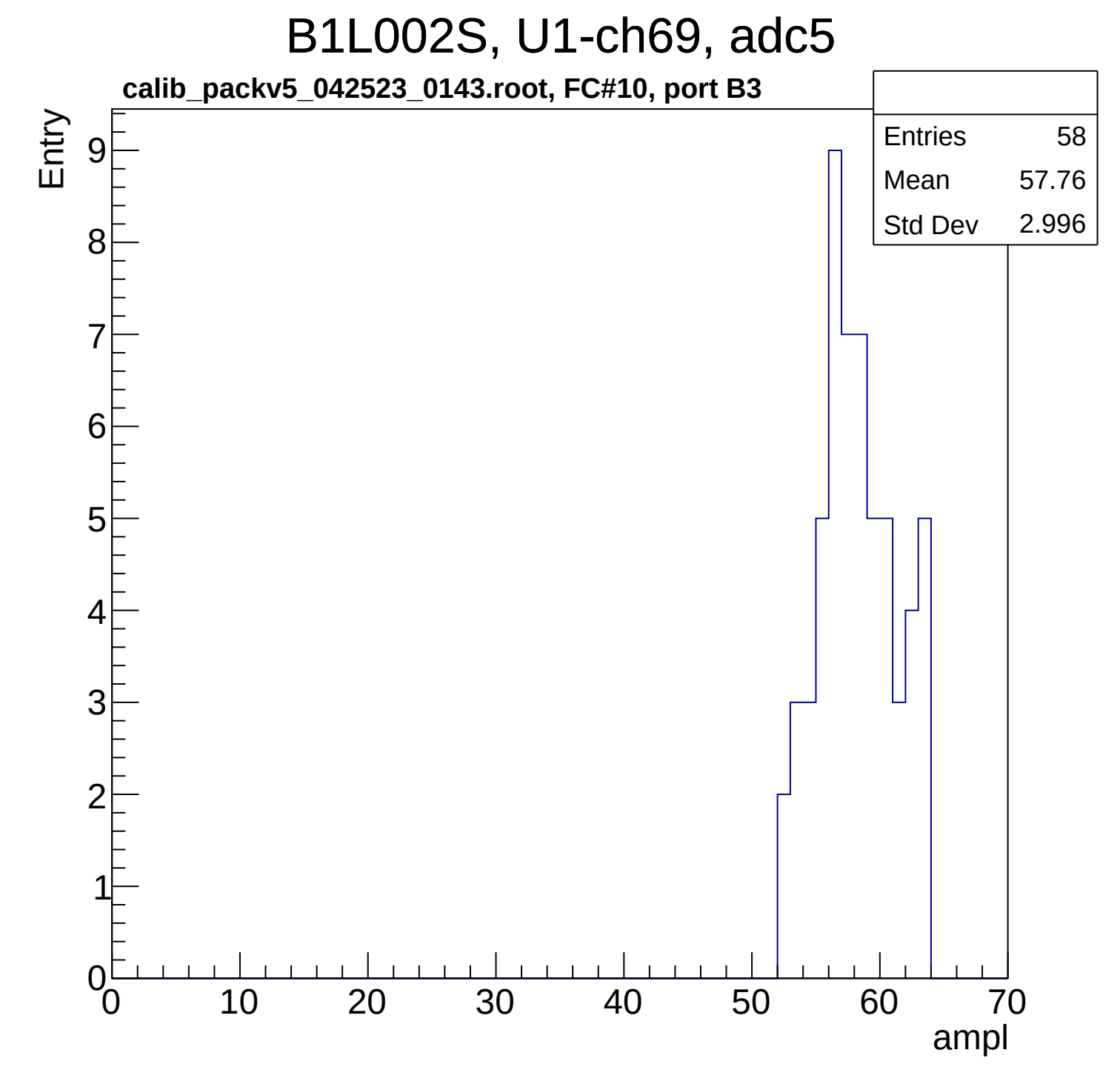
B1L002S, U1-ch69, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.76
Std Dev 2.996
Entry
ampl
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2
3
4
5
6
7
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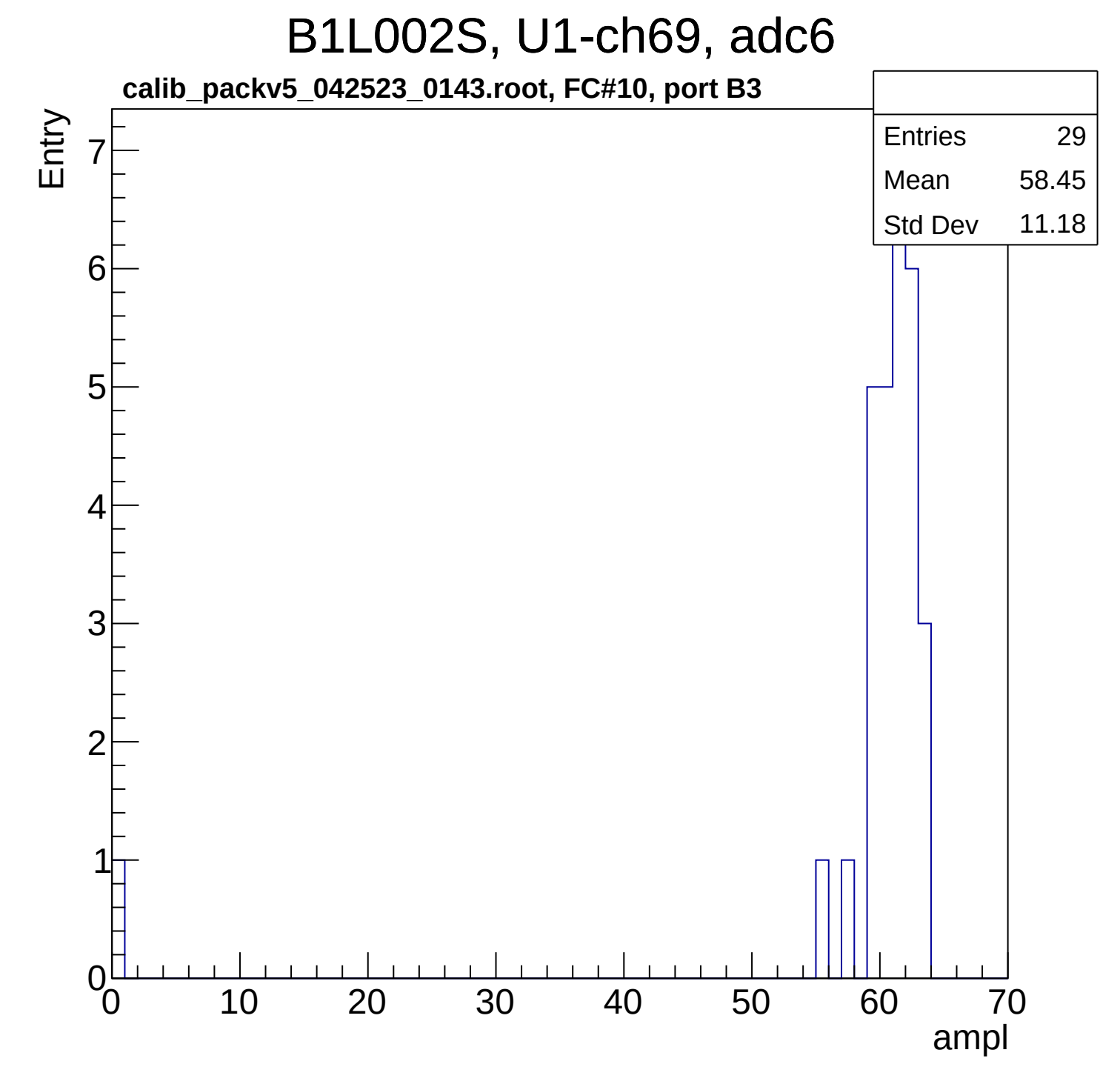
B1L002S, U1-ch69, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 58.45
Std Dev 11.18
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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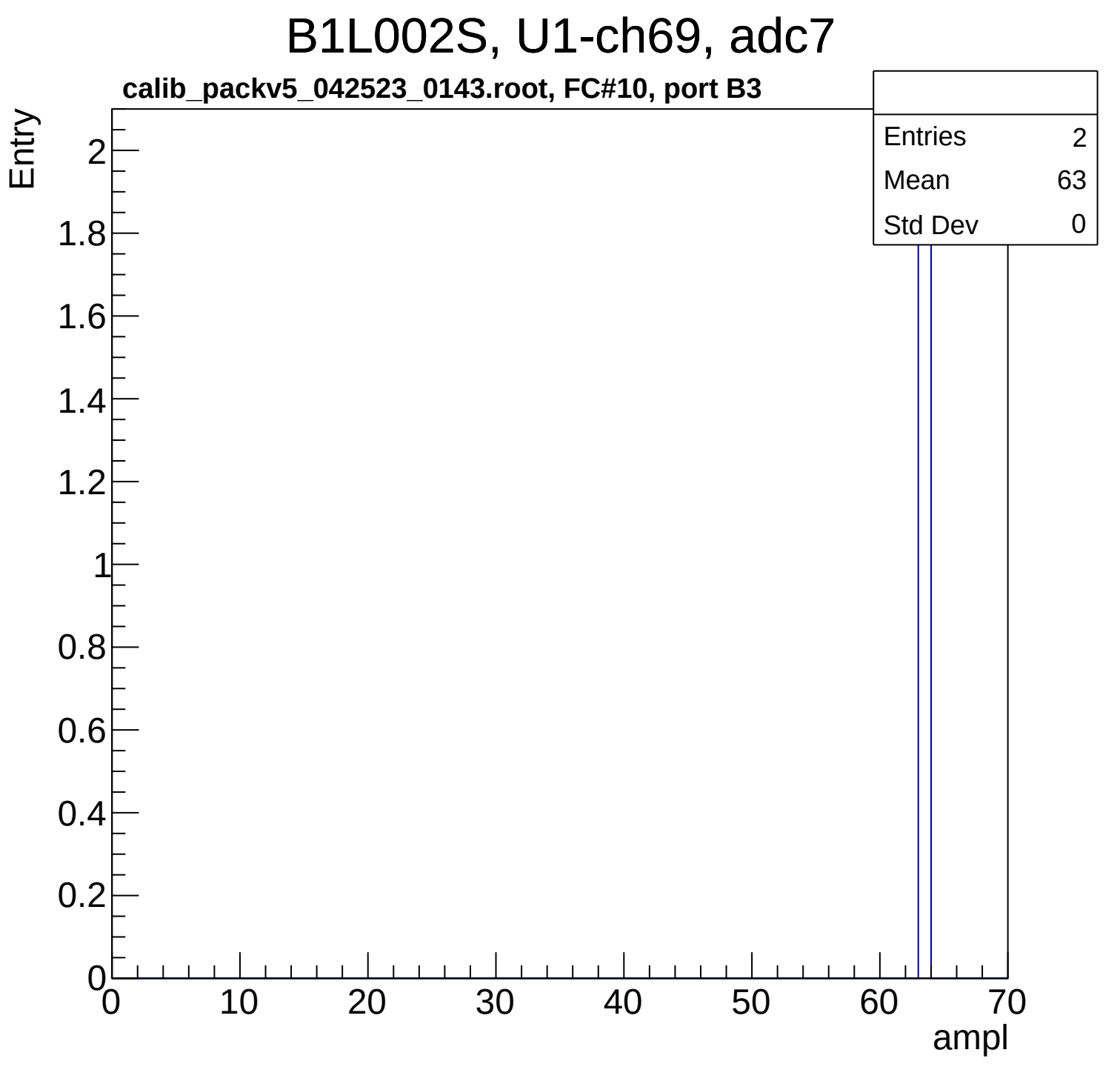
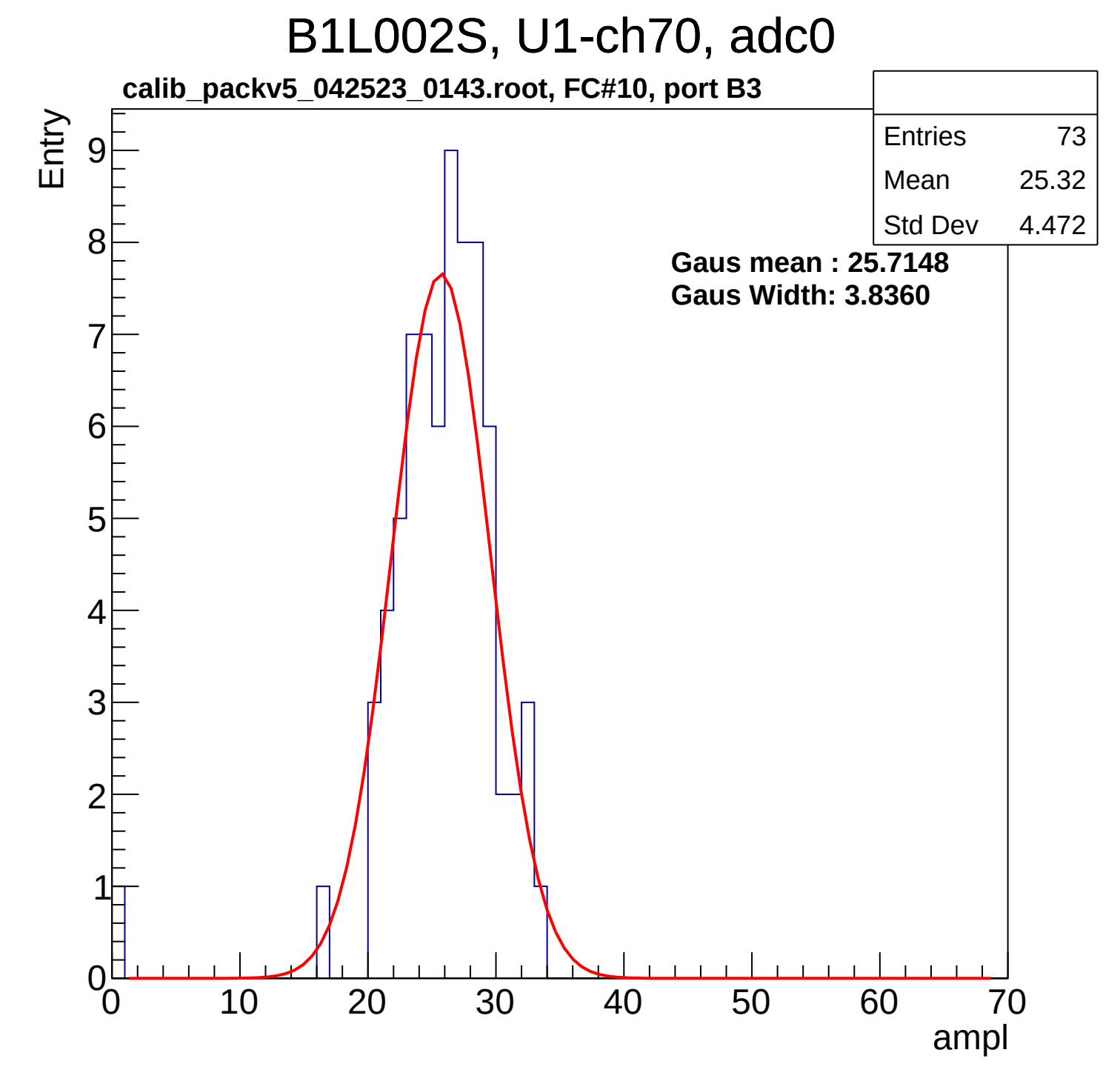

B1L002S, U1-ch69, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch70, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 25.32
Std Dev 4.472
Gaus mean : 25.7148
Gaus Width: 3.8360
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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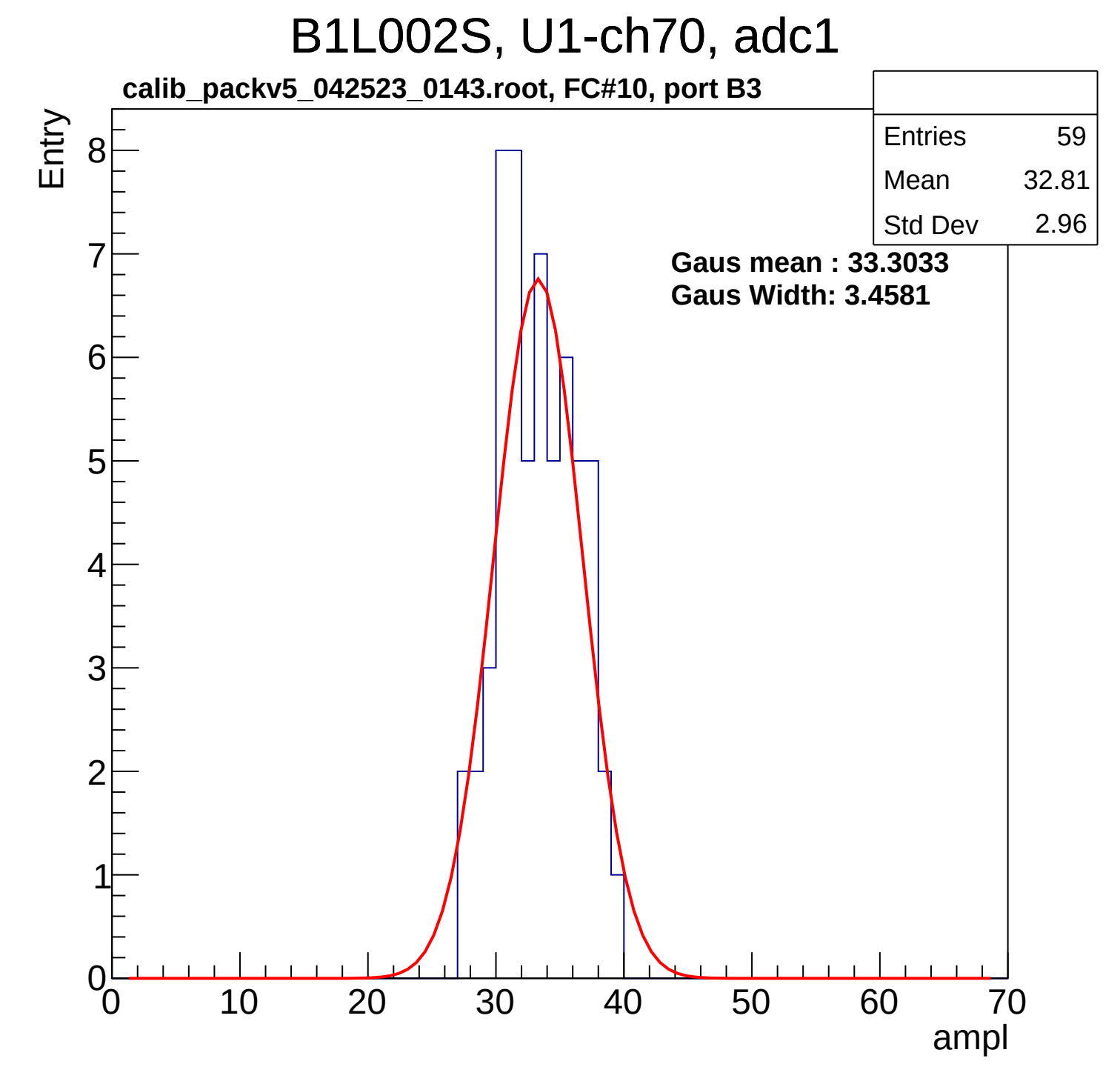
B1L002S, U1-ch70, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 32.81
Std Dev 2.96
Gaus mean : 33.3033
Gaus Width: 3.4581
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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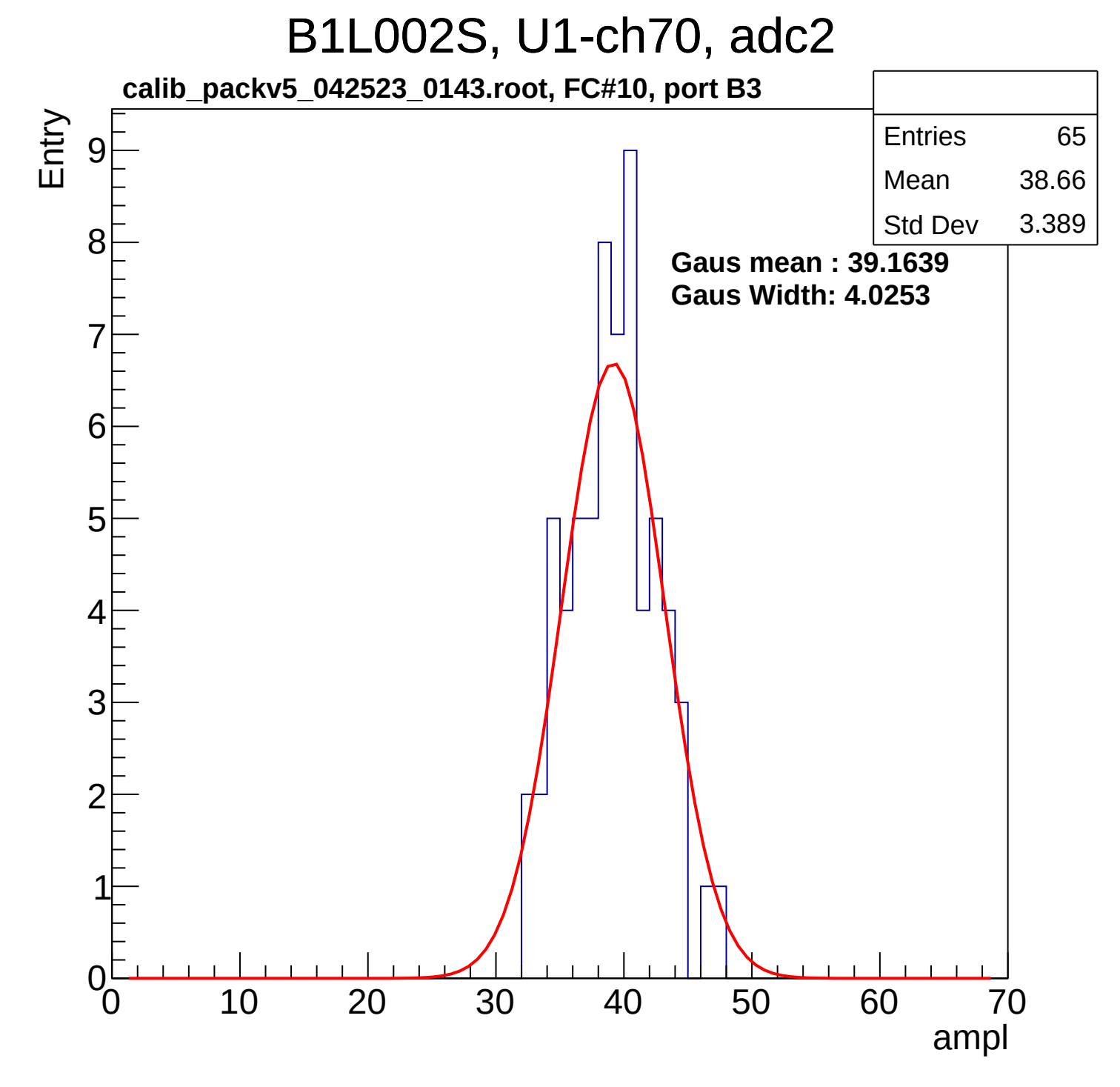
B1L002S, U1-ch70, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 38.66
Std Dev 3.389
Gaus mean : 39.1639
Gaus Width: 4.0253
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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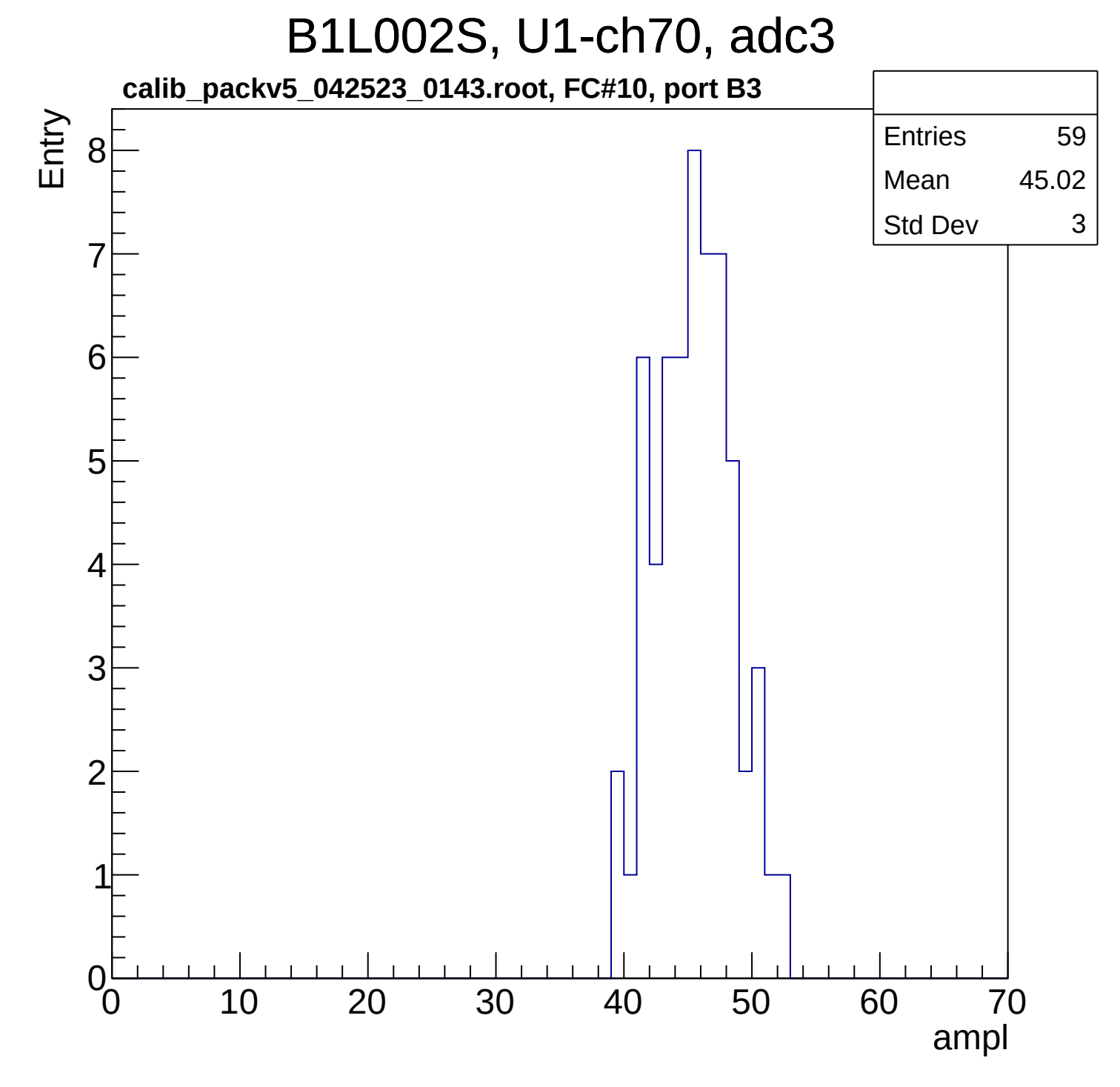
B1L002S, U1-ch70, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 45.02
Std Dev 3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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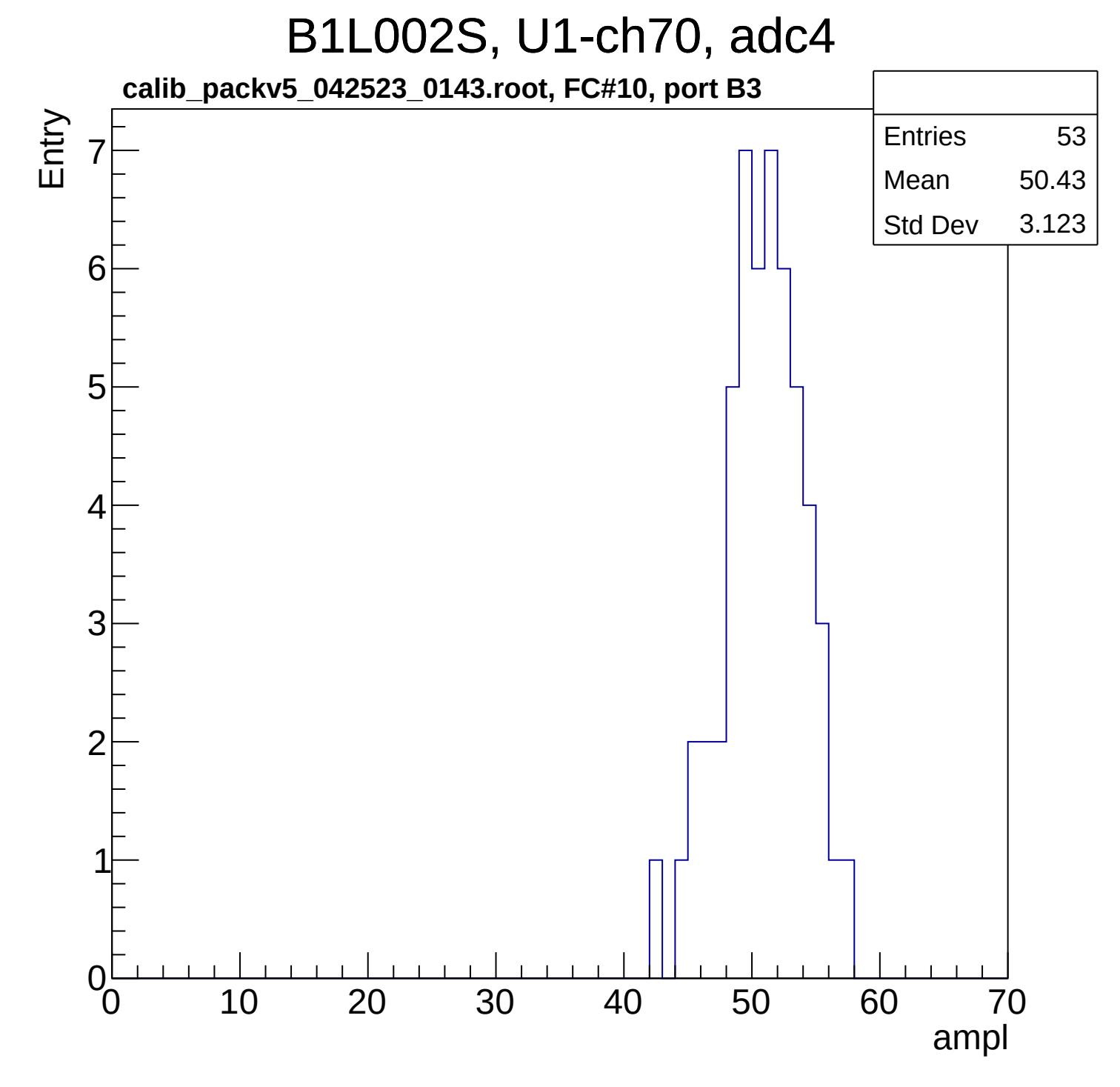
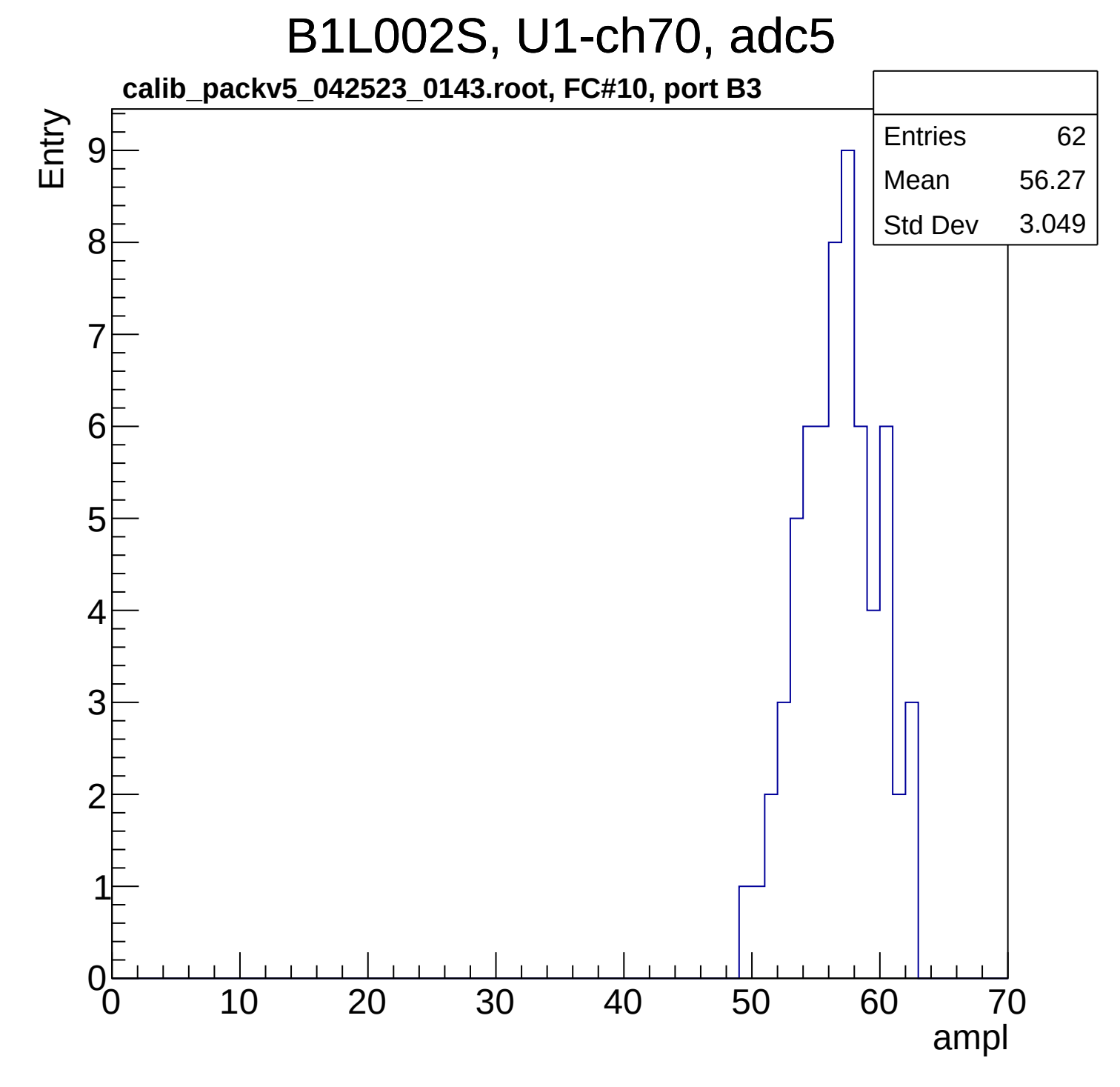
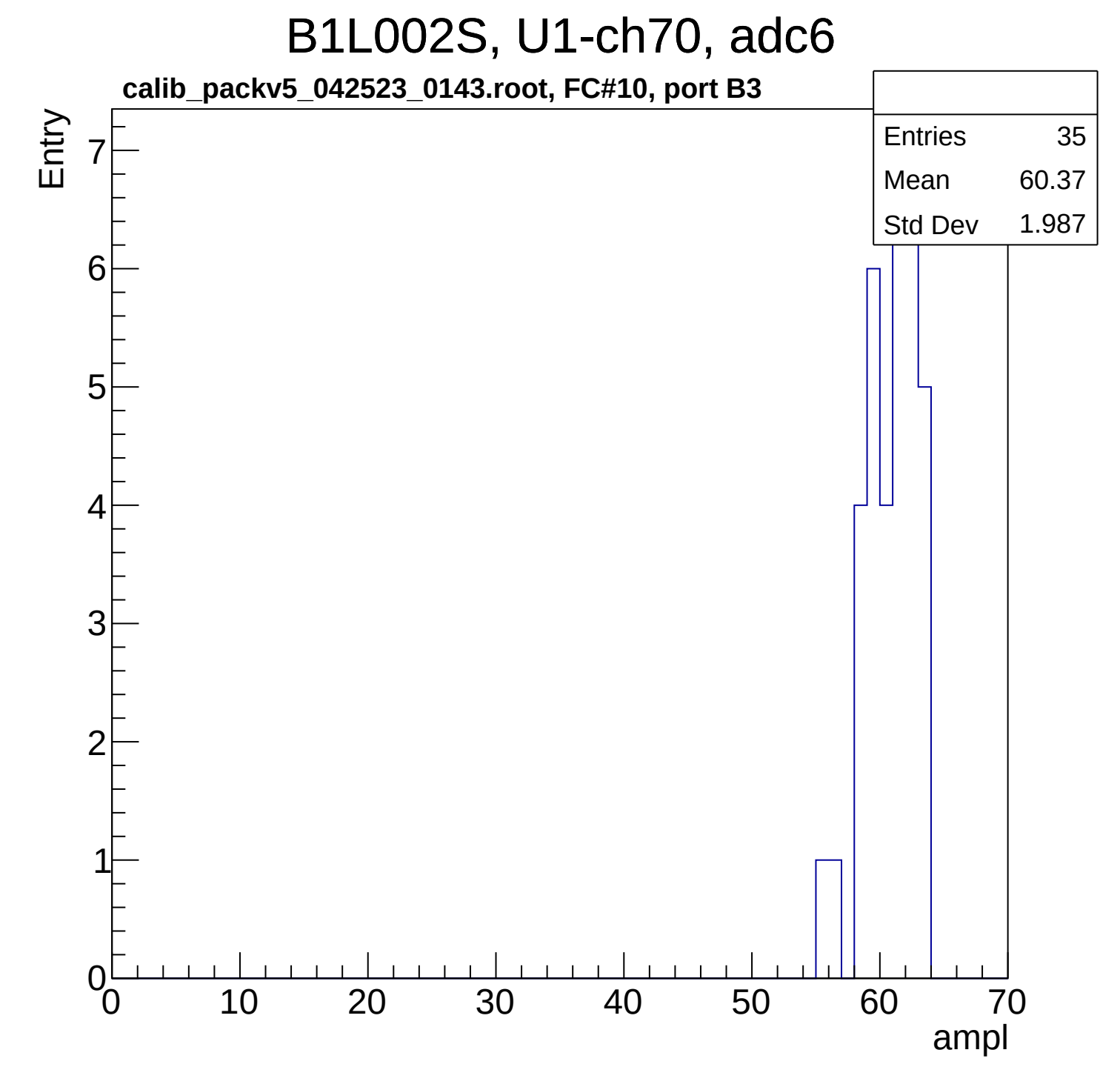
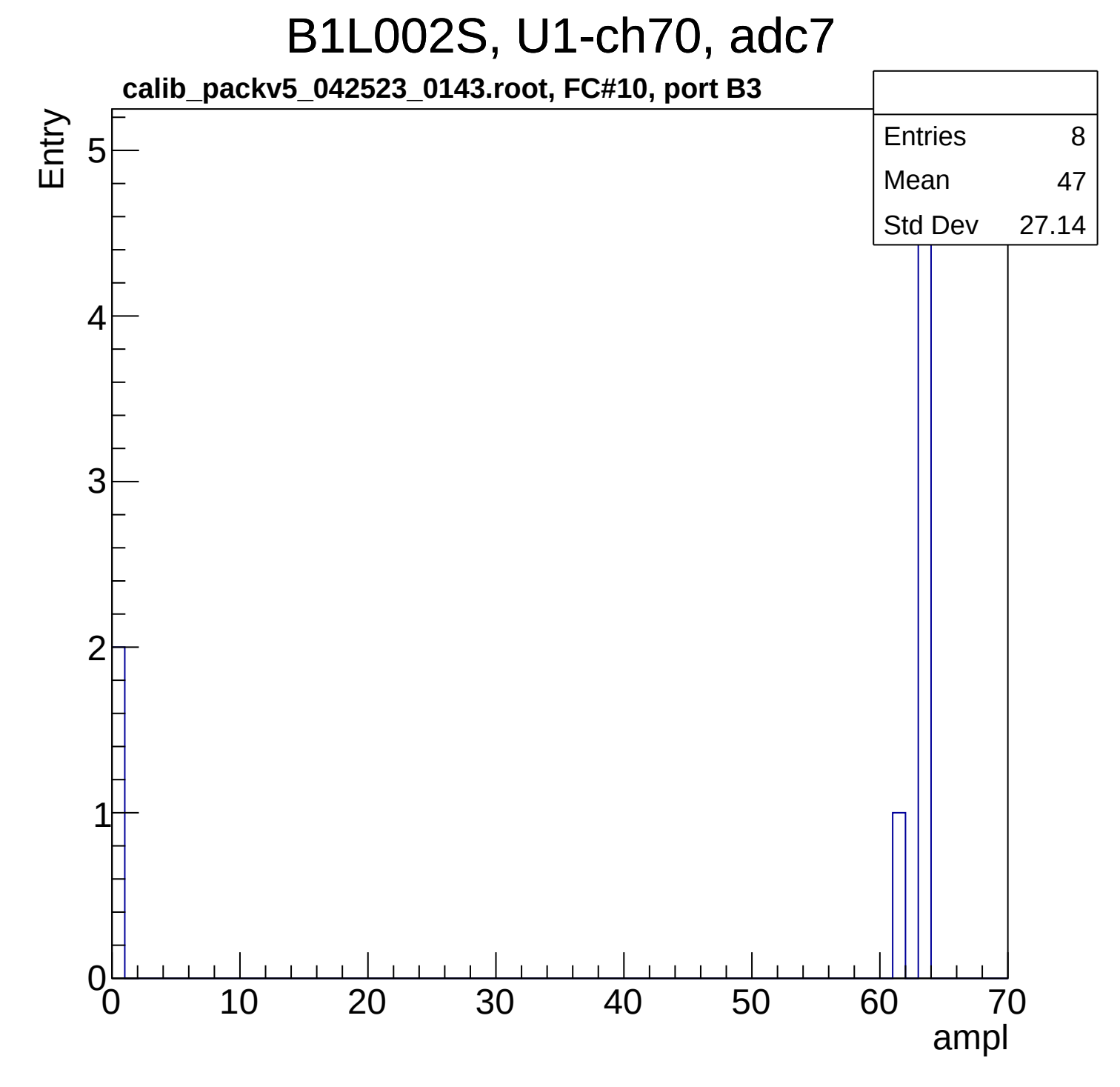
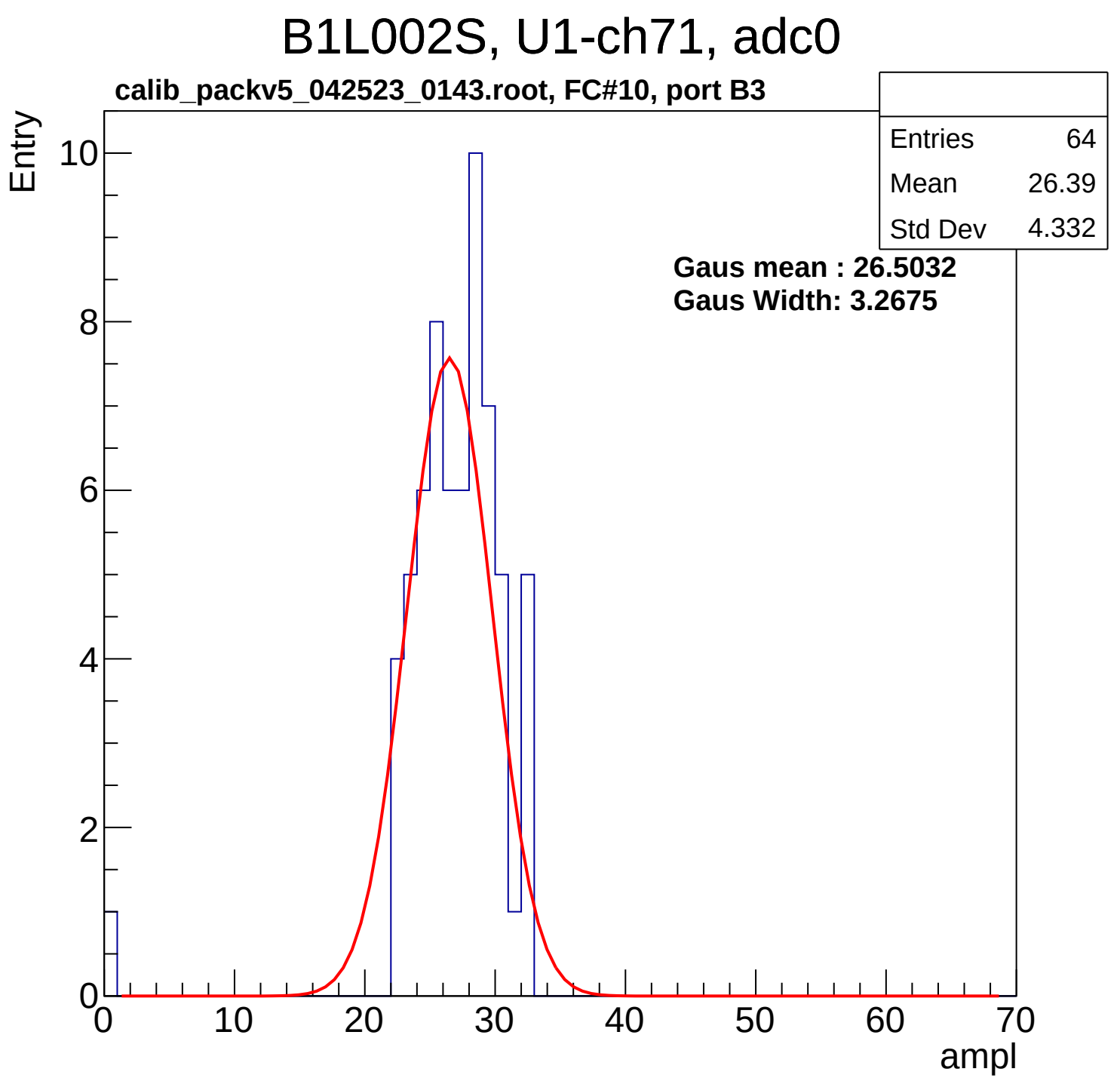
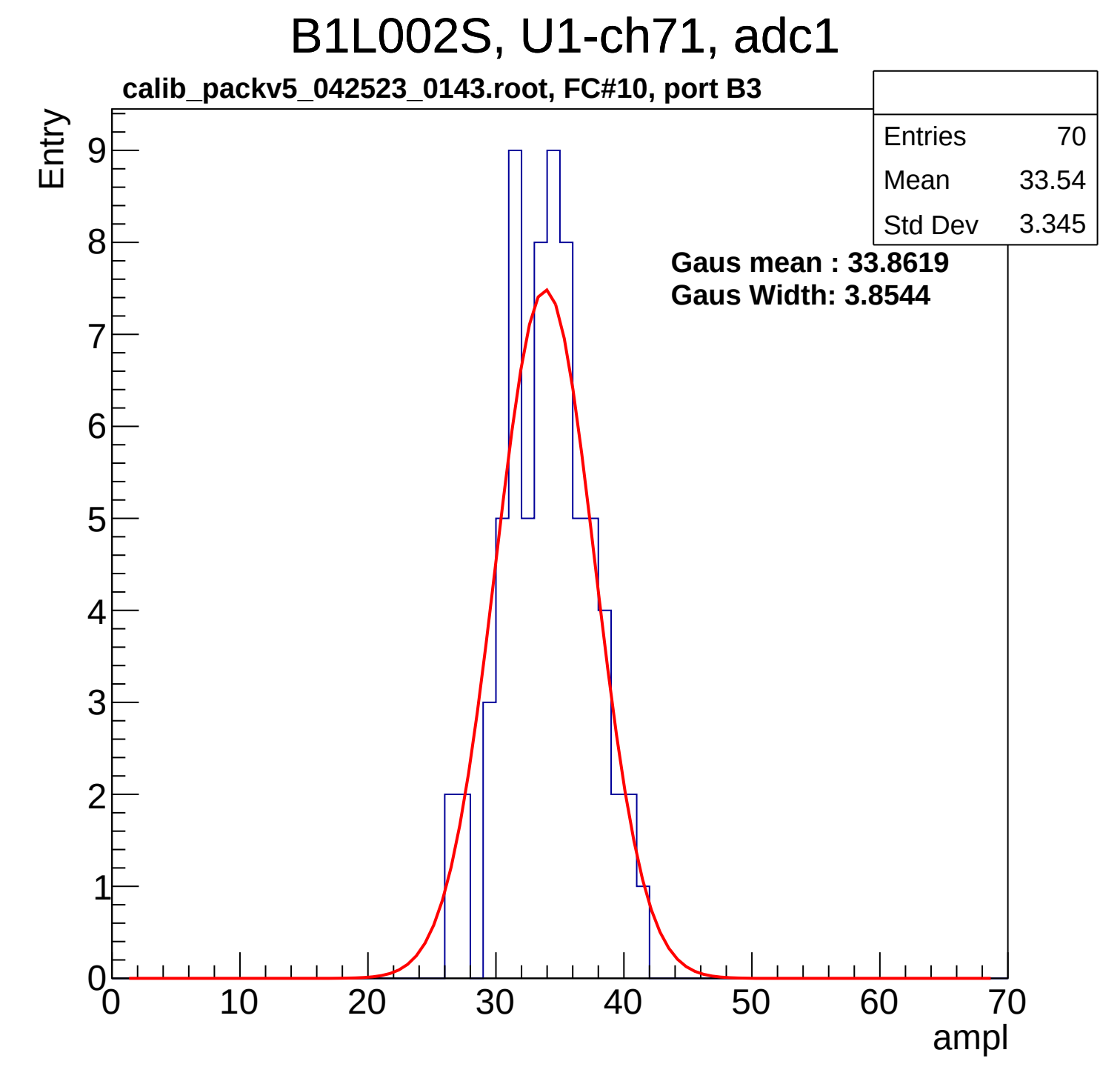

B1L002S, U1-ch70, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 50.43
Std Dev 3.123
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

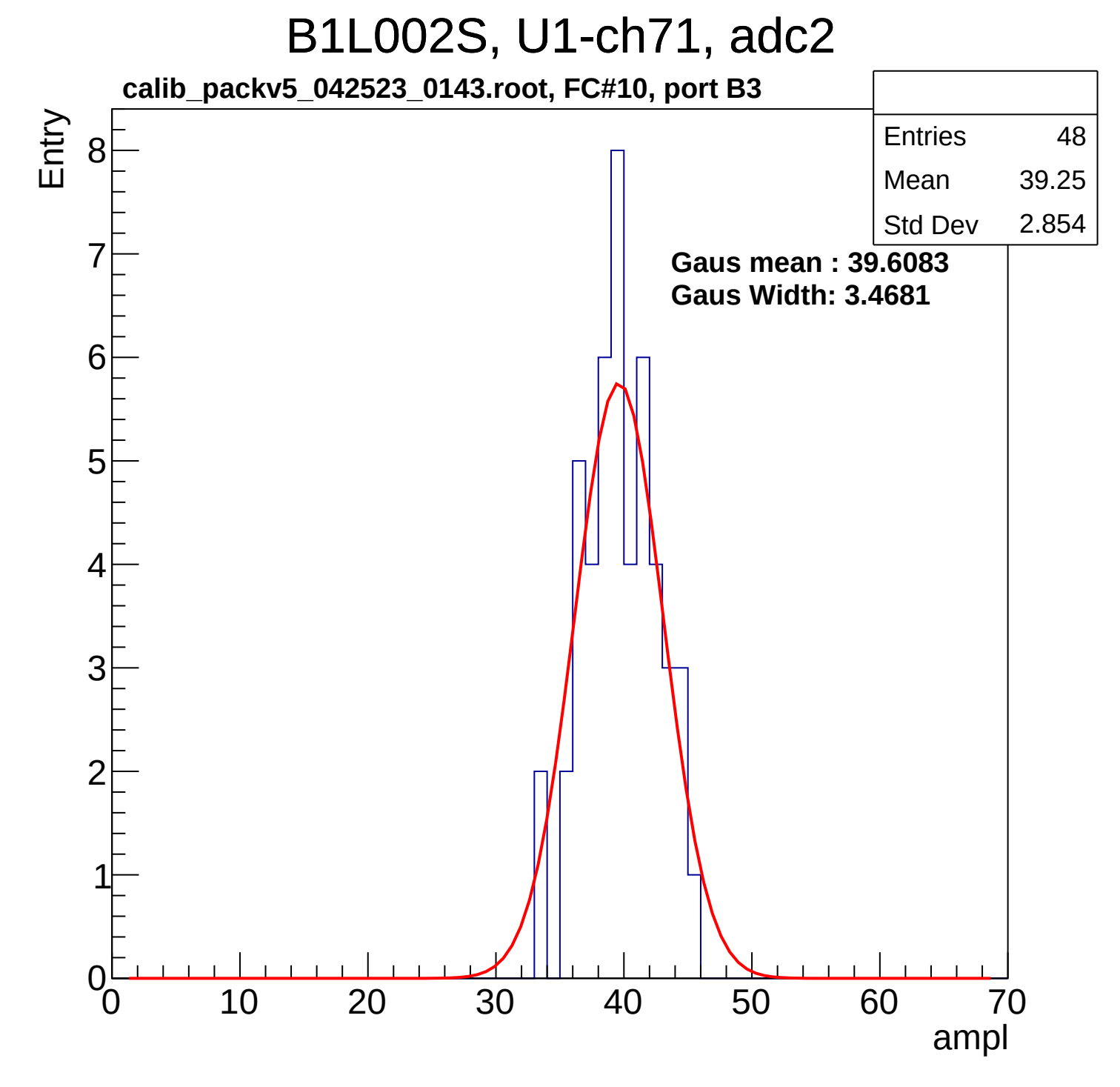

B1L002S, U1-ch70, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 56.27
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch70, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 60.37
Std Dev 1.987
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch70, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 47
Std Dev 27.14
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch71, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 26.39
Std Dev 4.332
Gaus mean : 26.5032
Gaus Width: 3.2675
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch71, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 33.54
Std Dev 3.345
Gaus mean : 33.8619
Gaus Width: 3.8544
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch71, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 39.25
Std Dev 2.854
Gaus mean : 39.6083
Gaus Width: 3.4681
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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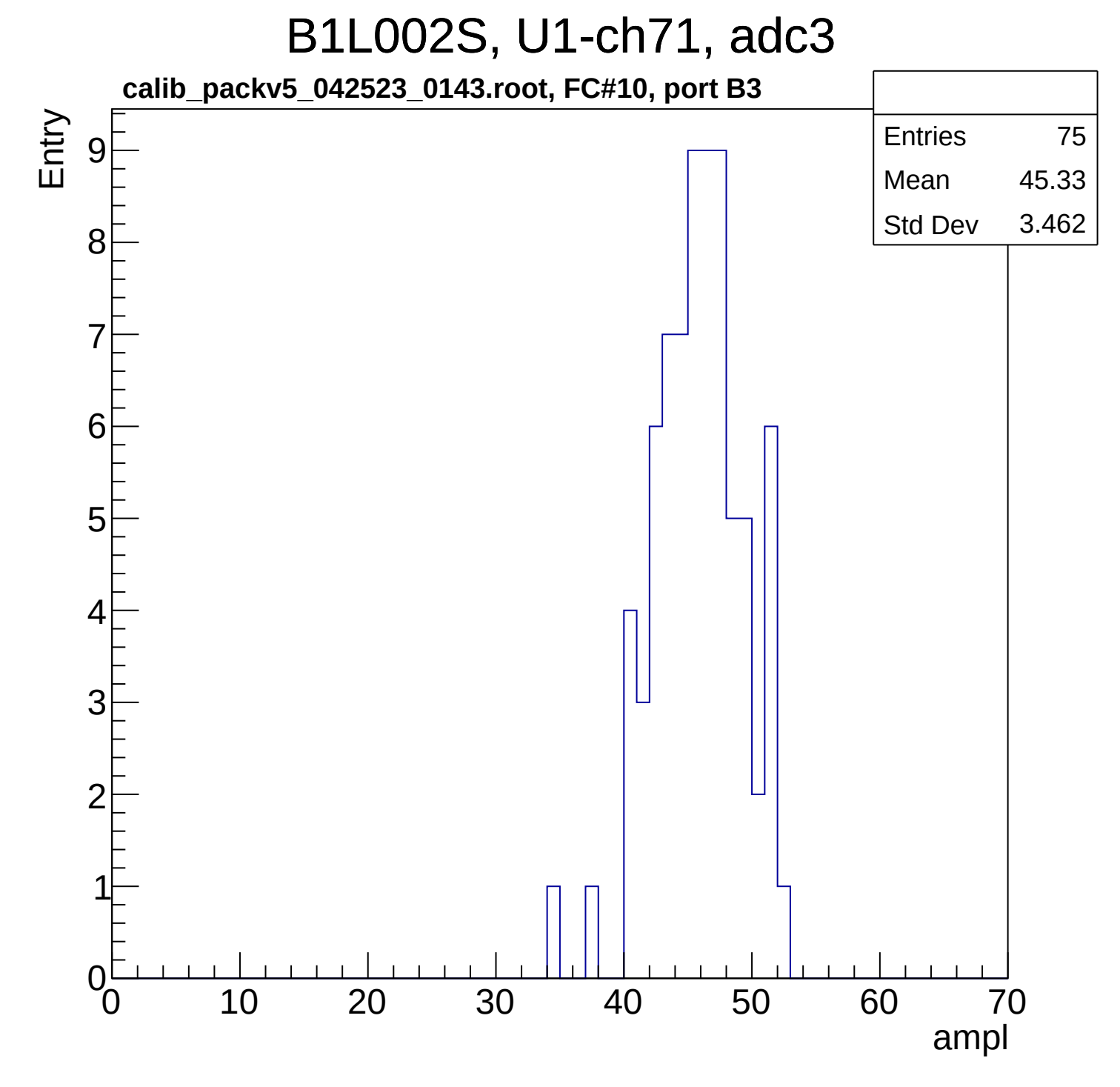
B1L002S, U1-ch71, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 45.33
Std Dev 3.462
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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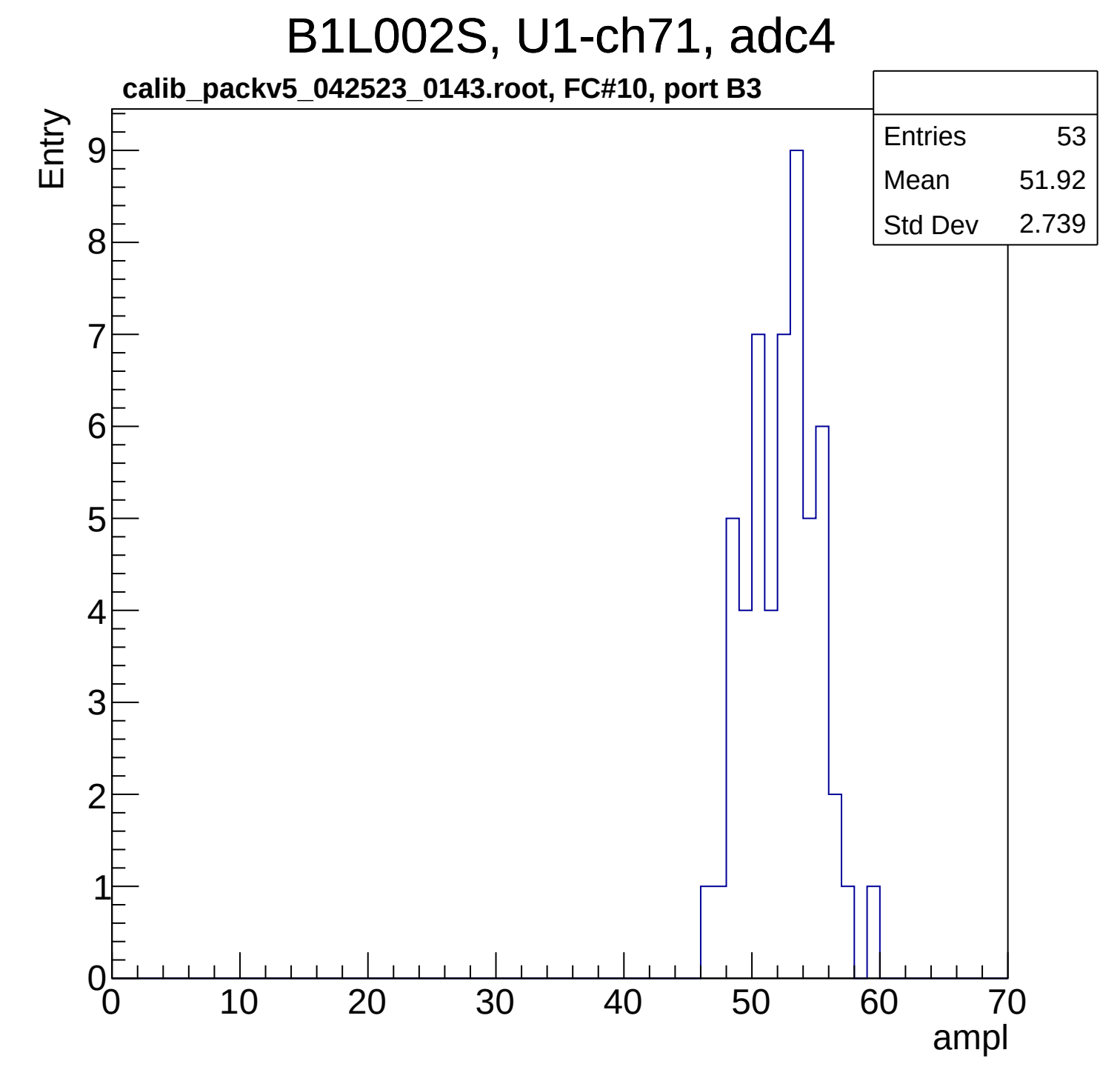
B1L002S, U1-ch71, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 51.92
Std Dev 2.739
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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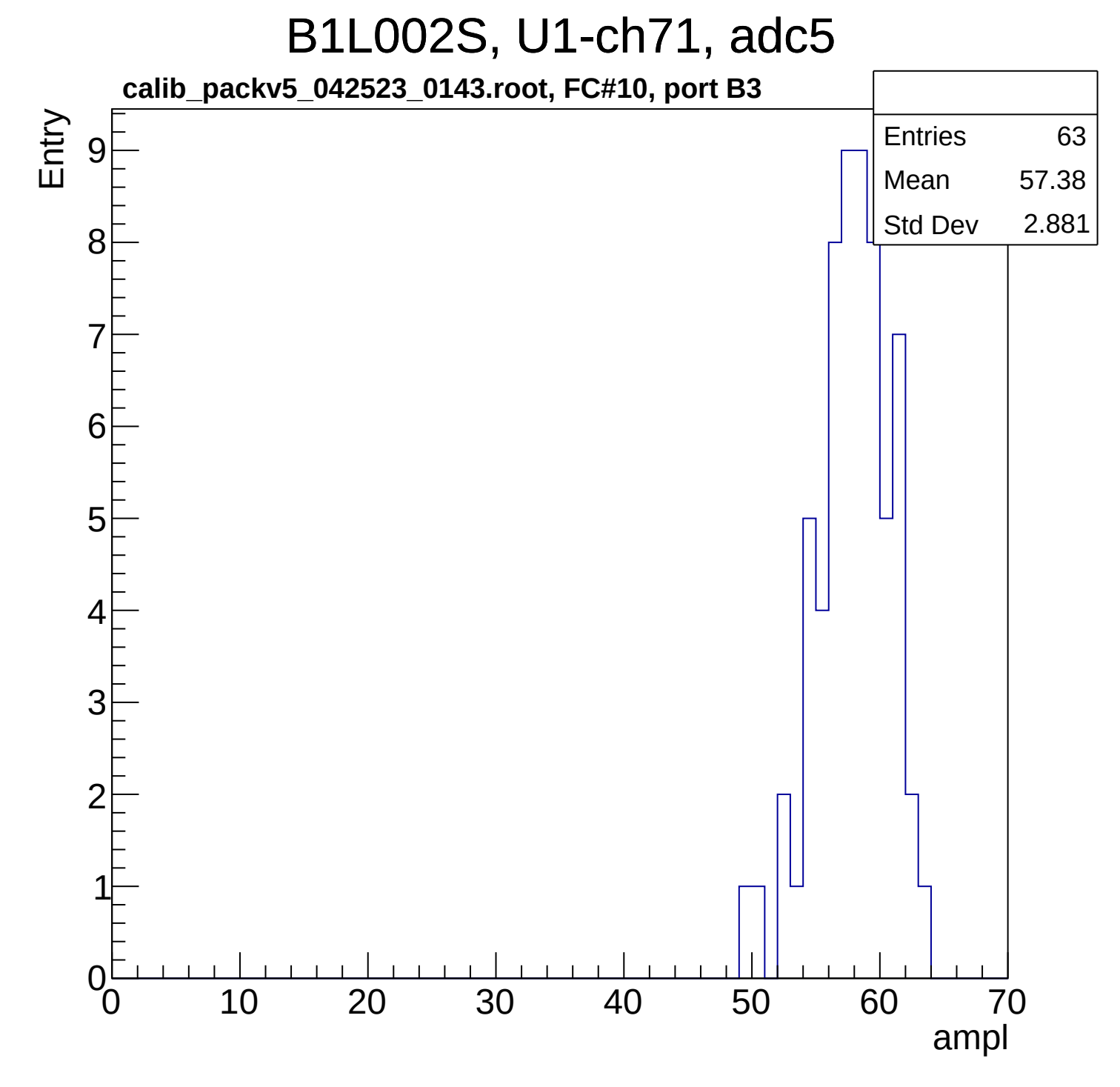
60
70


B1L002S, U1-ch71, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.38
Std Dev 2.881
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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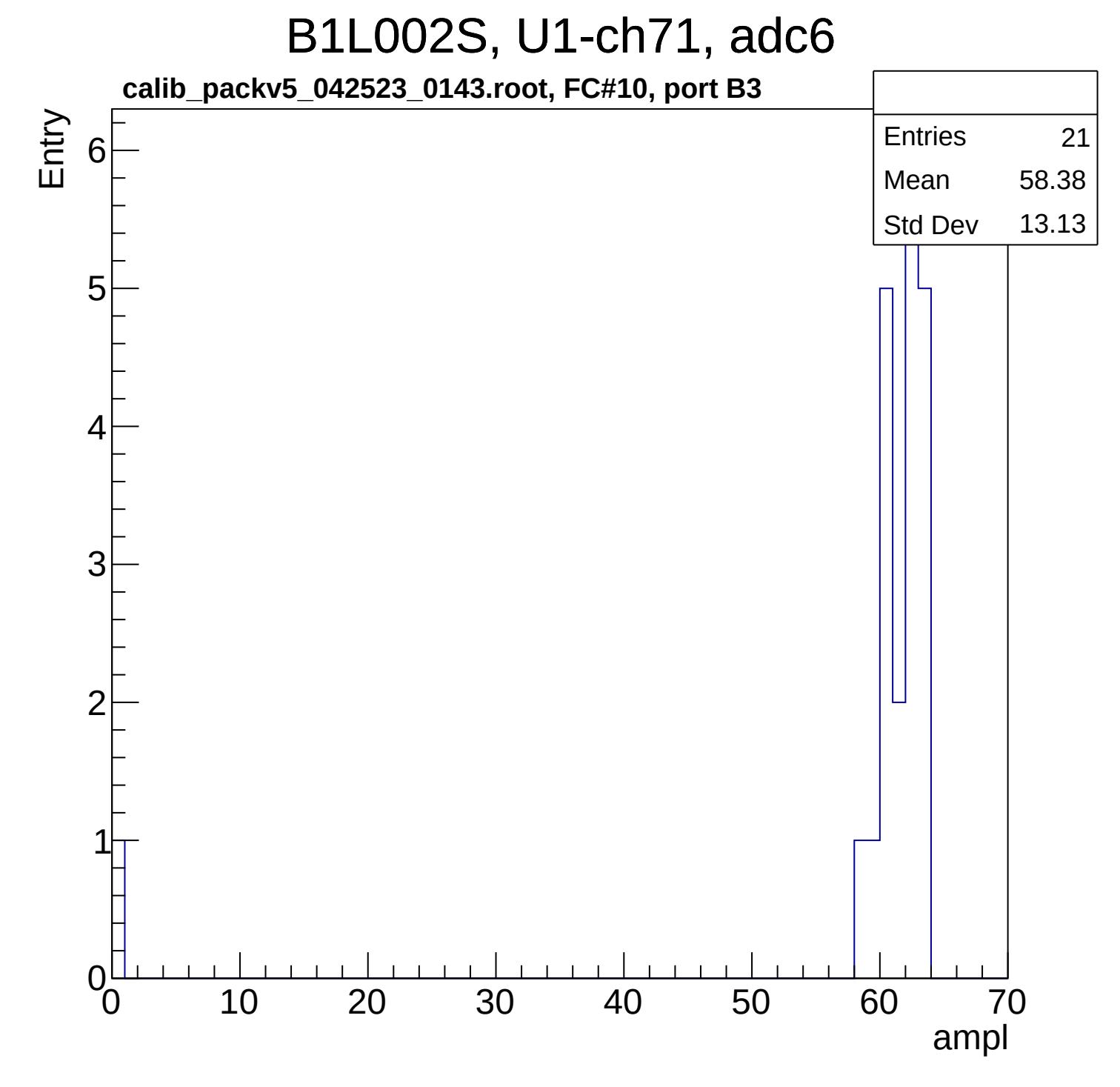
B1L002S, U1-ch71, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.38
Std Dev 13.13
Entry
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1
2
3
4
5
6
0
10
20
30
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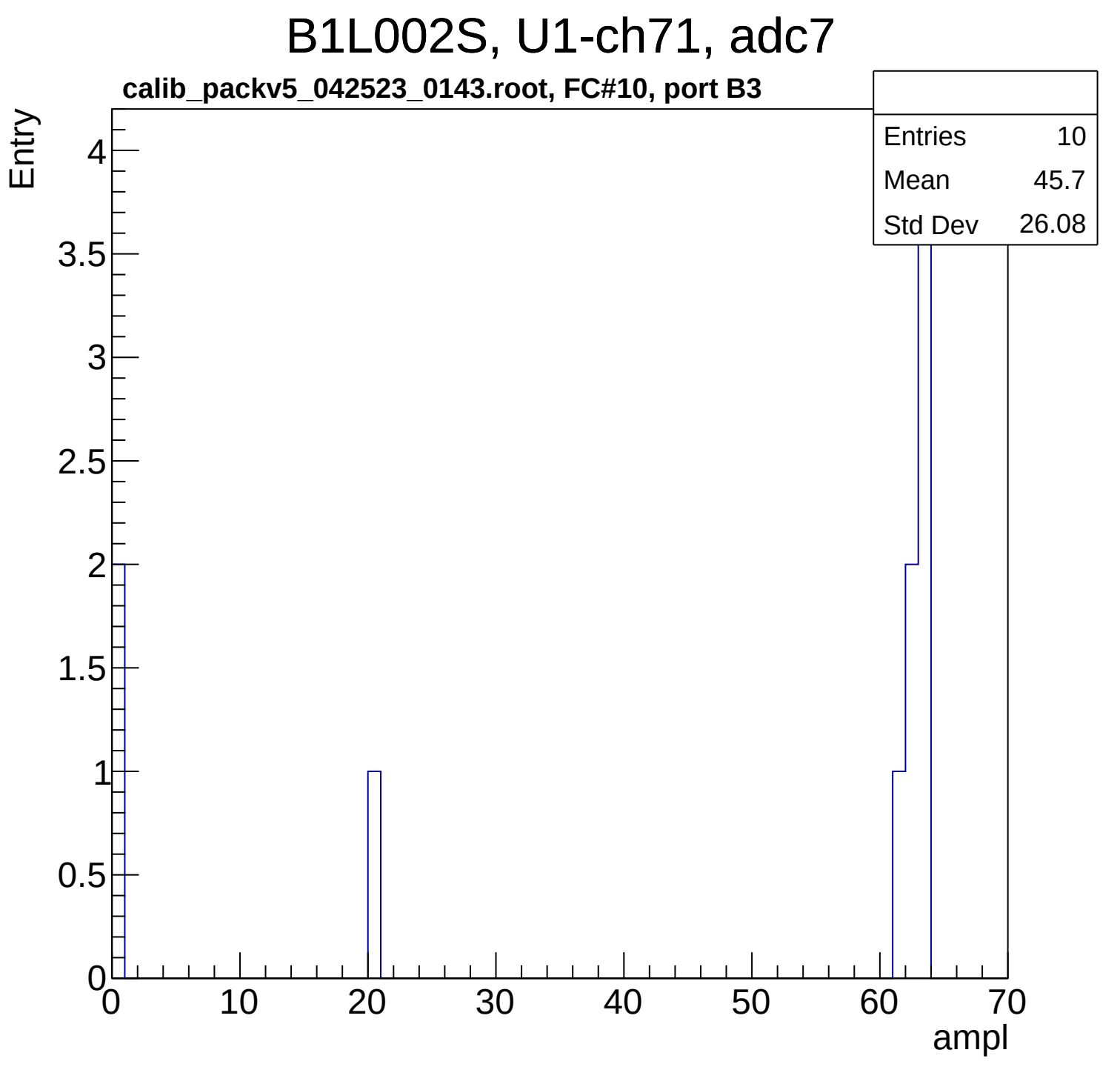
50
60
70
ampl


B1L002S, U1-ch71, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 45.7
Std Dev 26.08
Entry
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2.5
3
3.5
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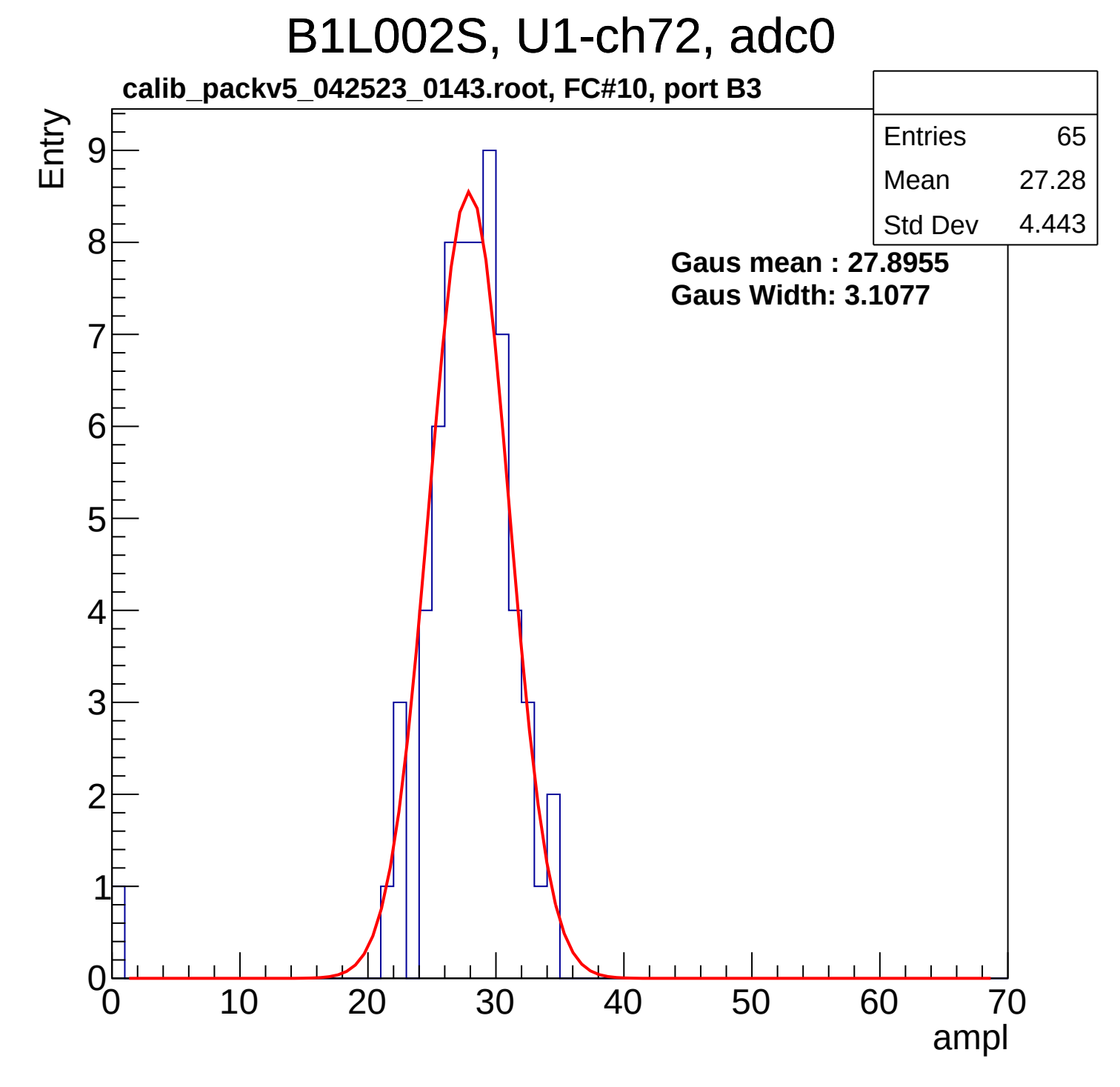
70
ampl


B1L002S, U1-ch72, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.28
Std Dev 4.443
Gaus mean : 27.8955
Gaus Width: 3.1077
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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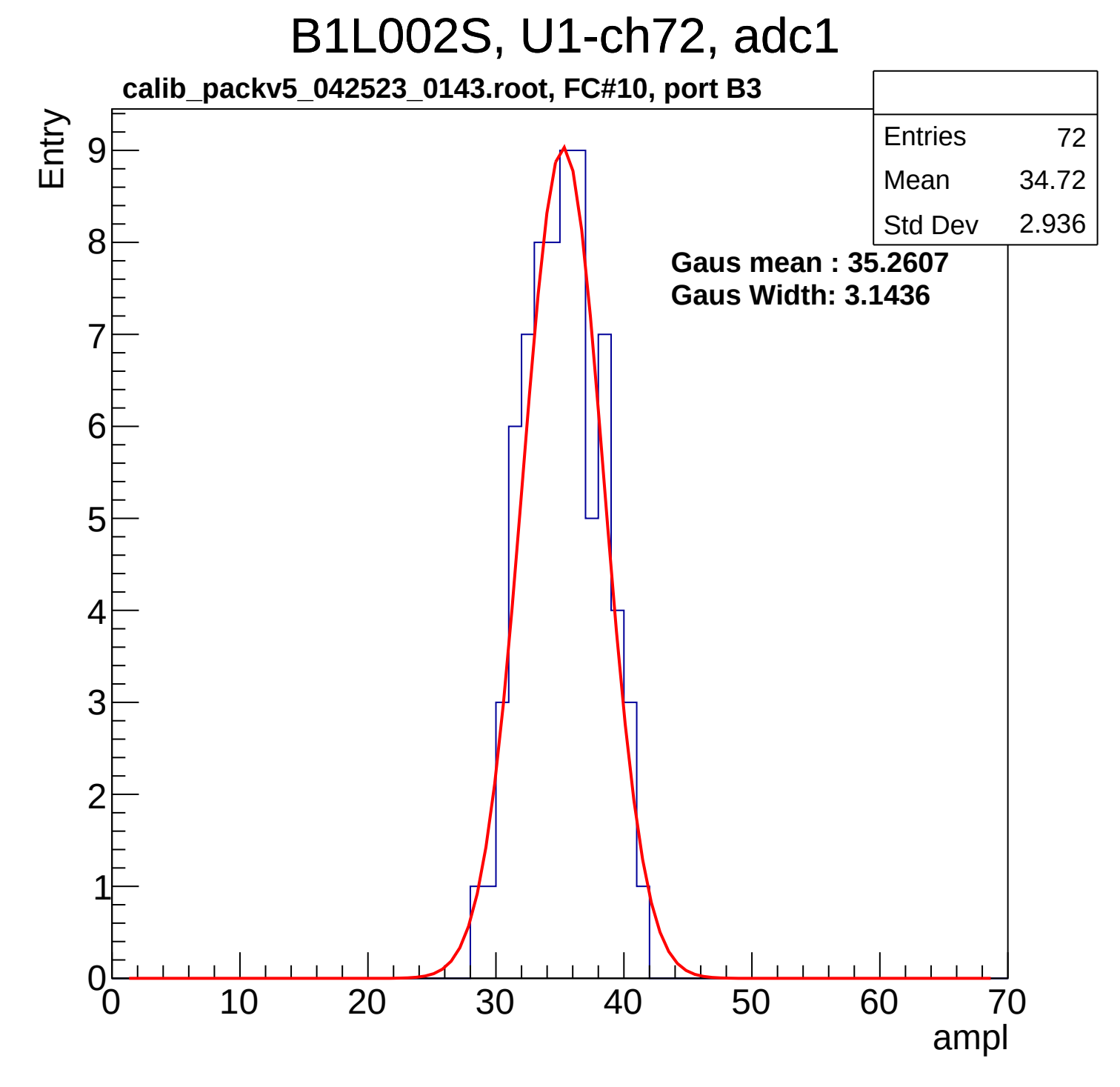
B1L002S, U1-ch72, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.72
Std Dev 2.936
Gaus mean : 35.2607
Gaus Width: 3.1436
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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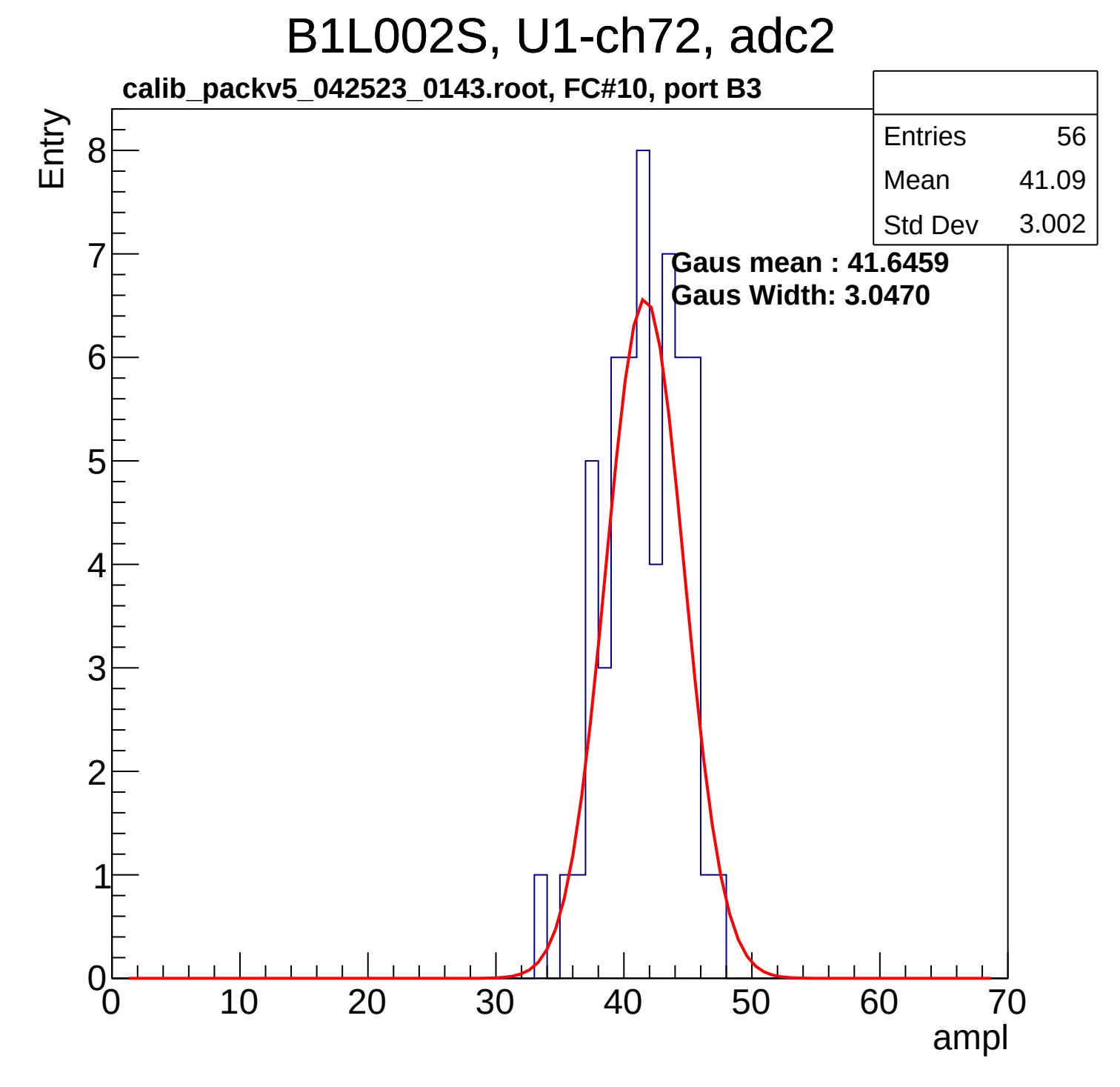
B1L002S, U1-ch72, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.09
Std Dev 3.002
Gaus mean : 41.6459
Gaus Width: 3.0470
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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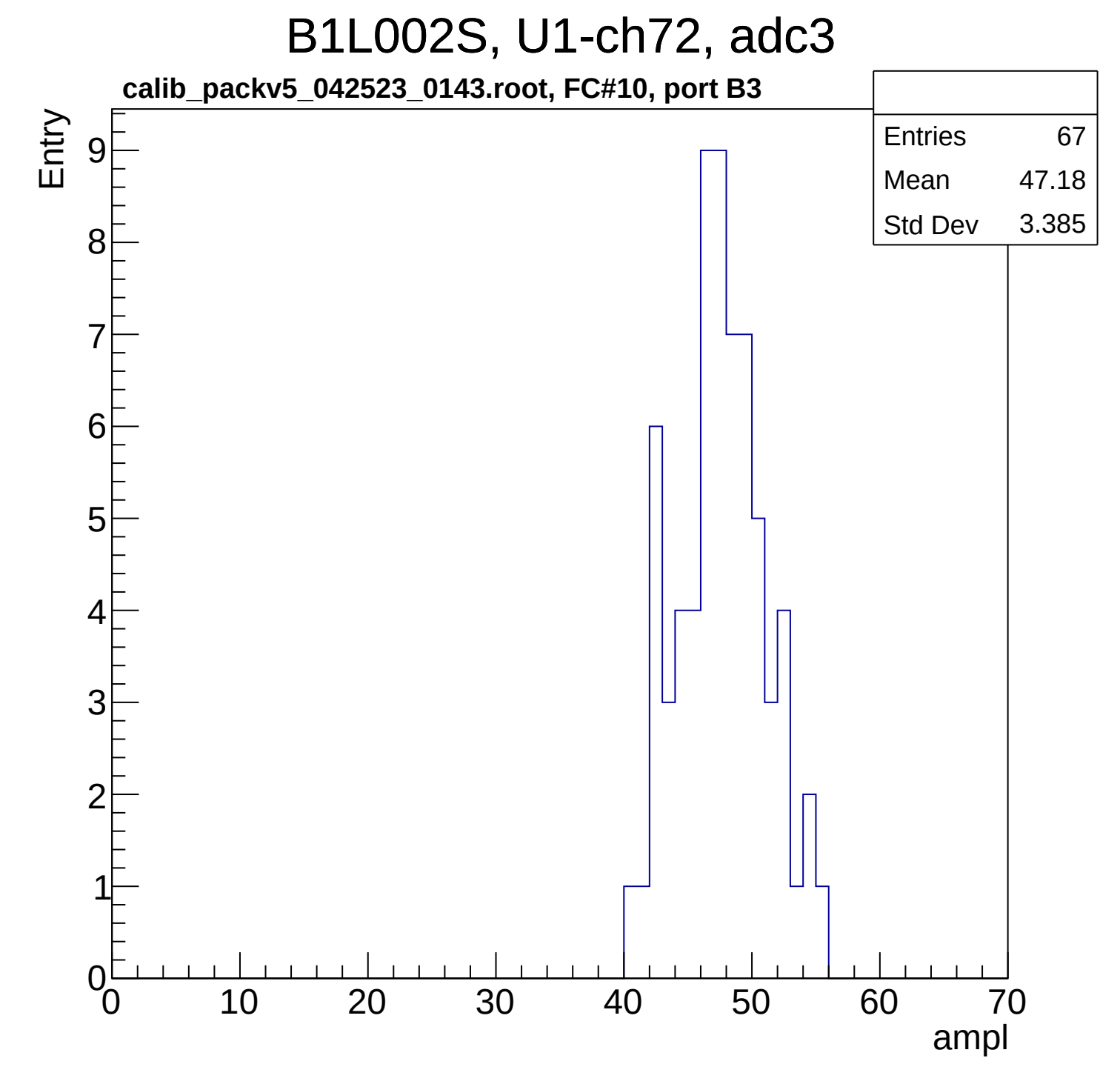
B1L002S, U1-ch72, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.18
Std Dev 3.385
Entry
ampl
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7
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0
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60
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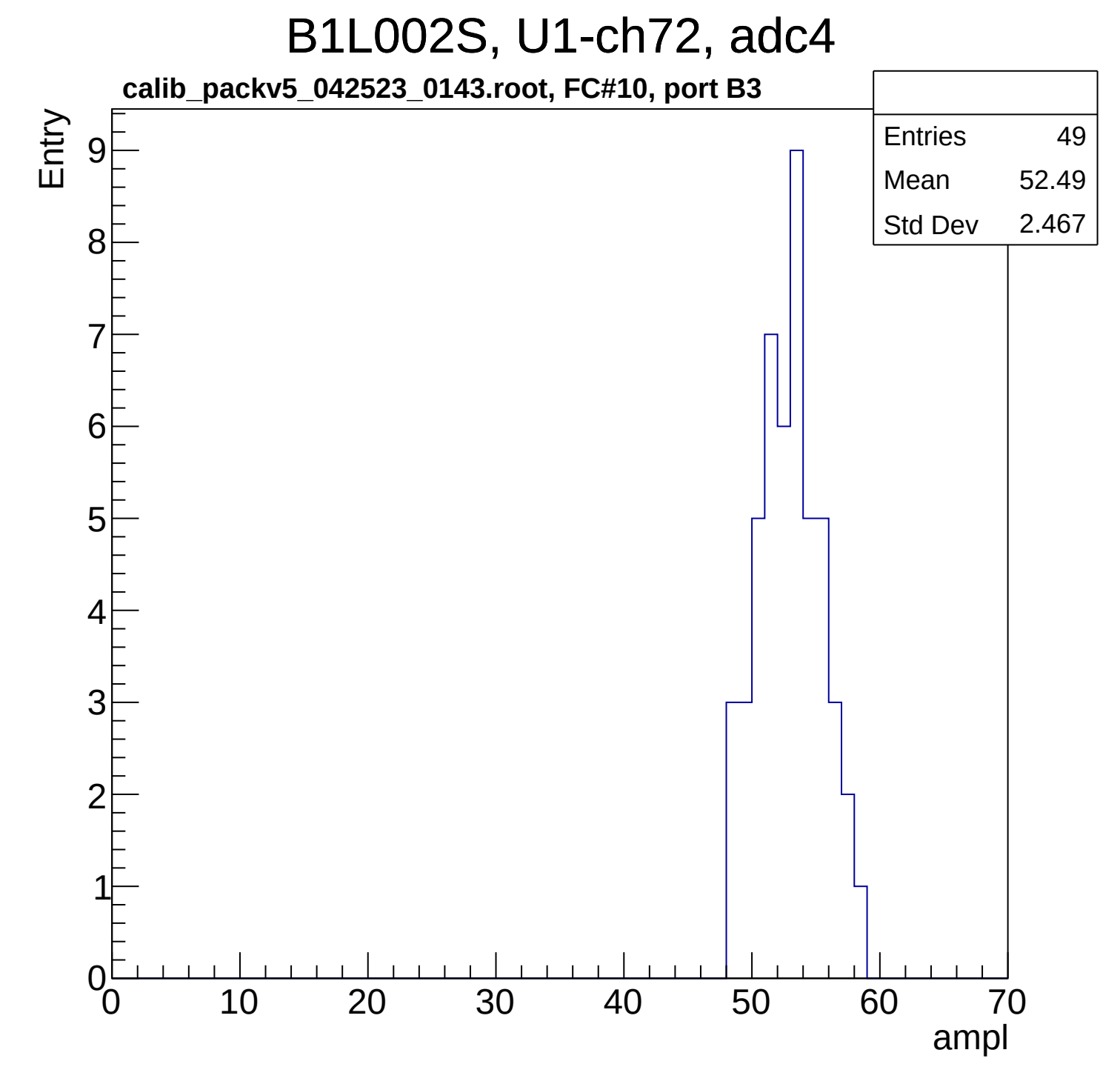
B1L002S, U1-ch72, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.49
Std Dev 2.467
Entry
ampl
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5
6
7
8
9
0
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40
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60
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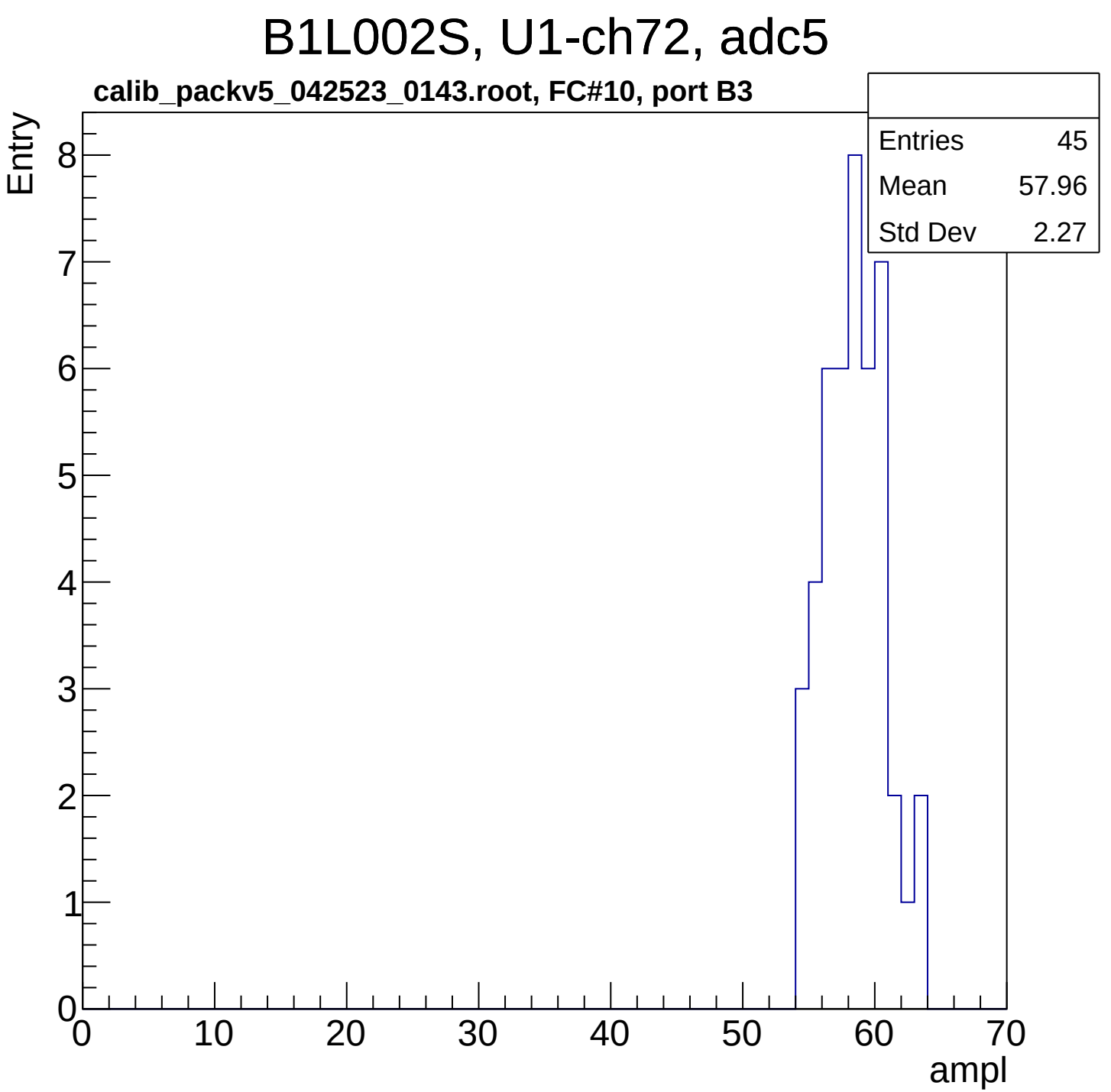
B1L002S, U1-ch72, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 57.96
Std Dev 2.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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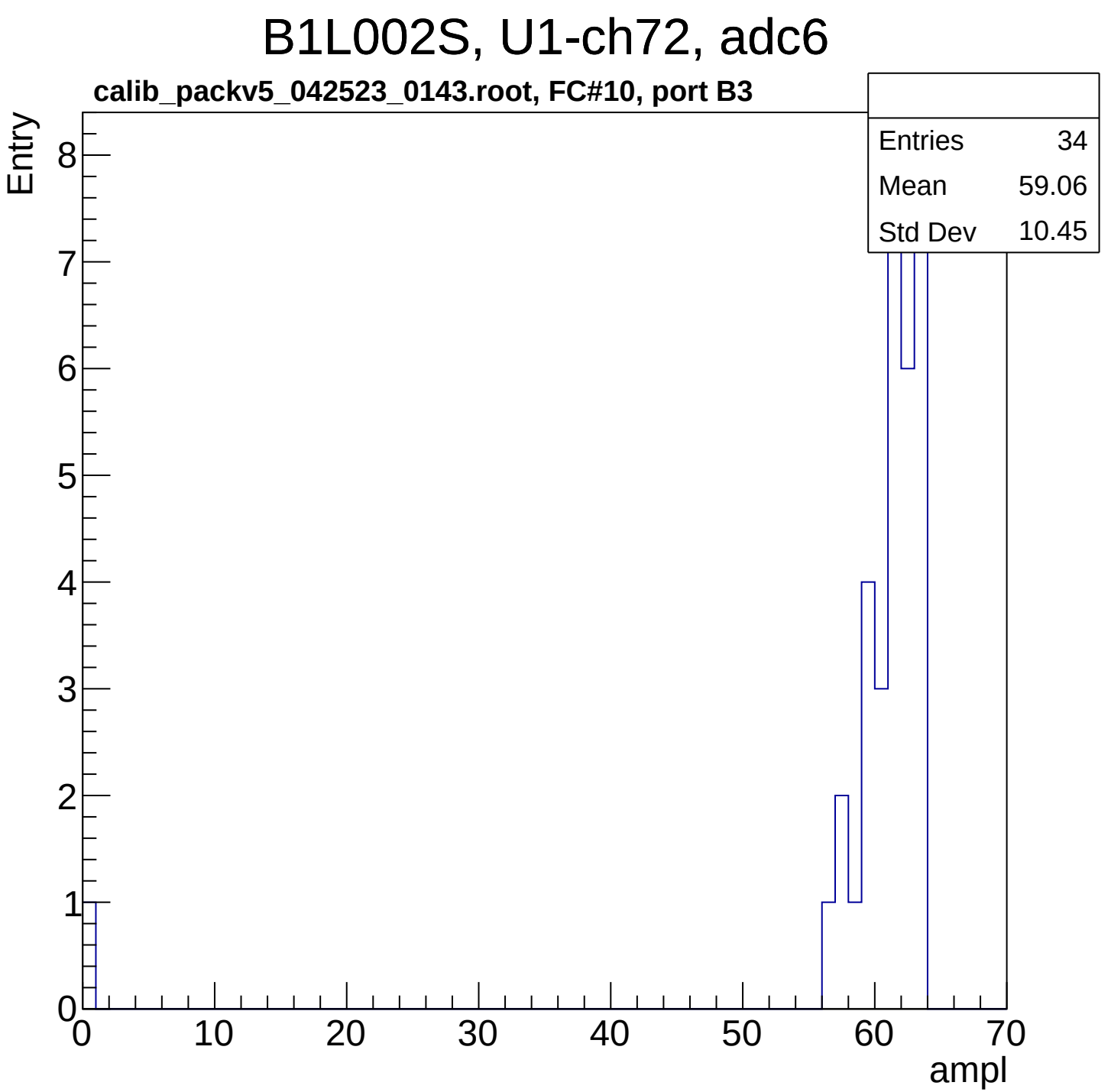
B1L002S, U1-ch72, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 59.06
Std Dev 10.45
Entry
ampl
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2
3
4
5
6
7
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0
10
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40
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60
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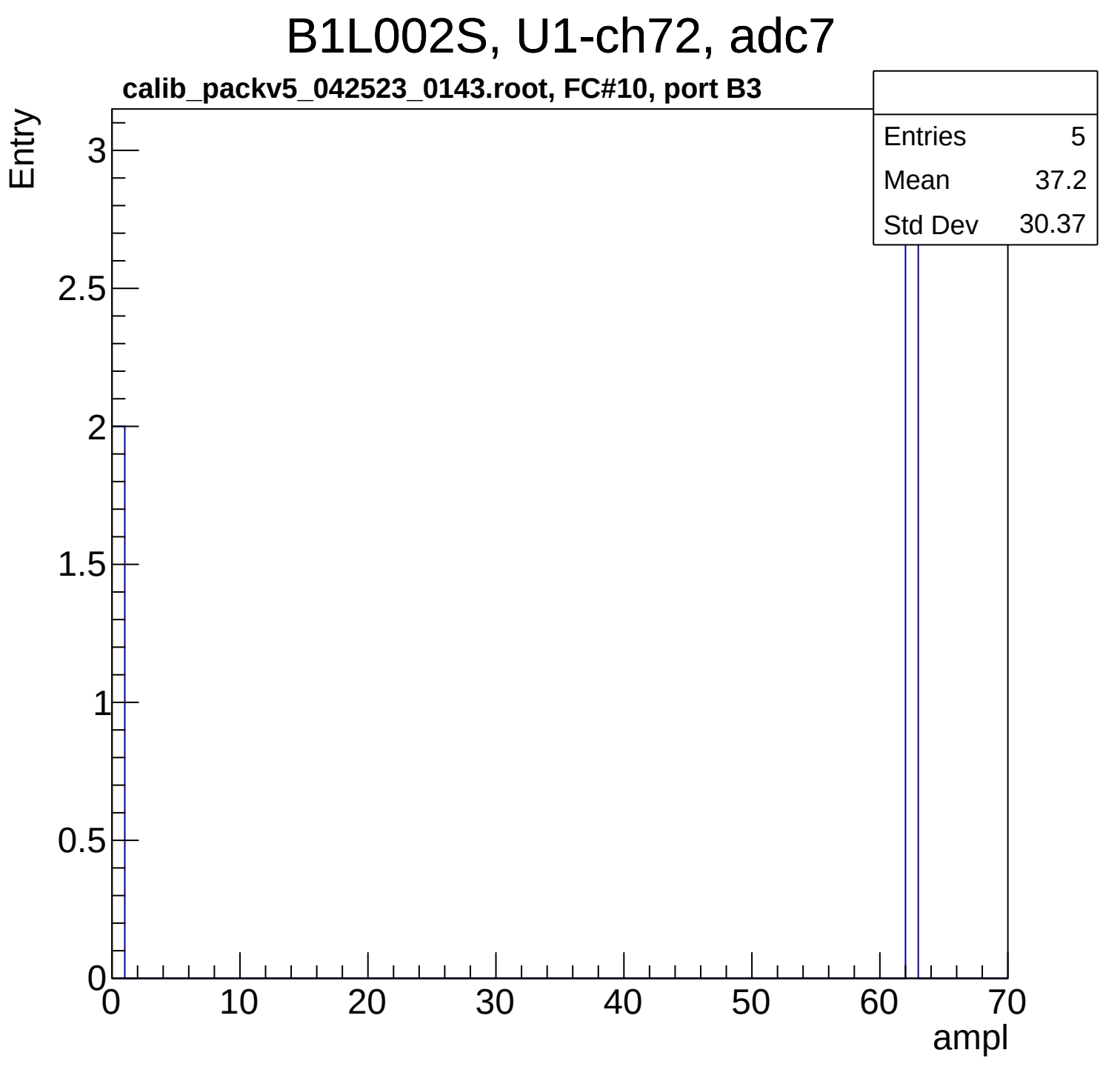
B1L002S, U1-ch72, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.2
Std Dev 30.37
Entry
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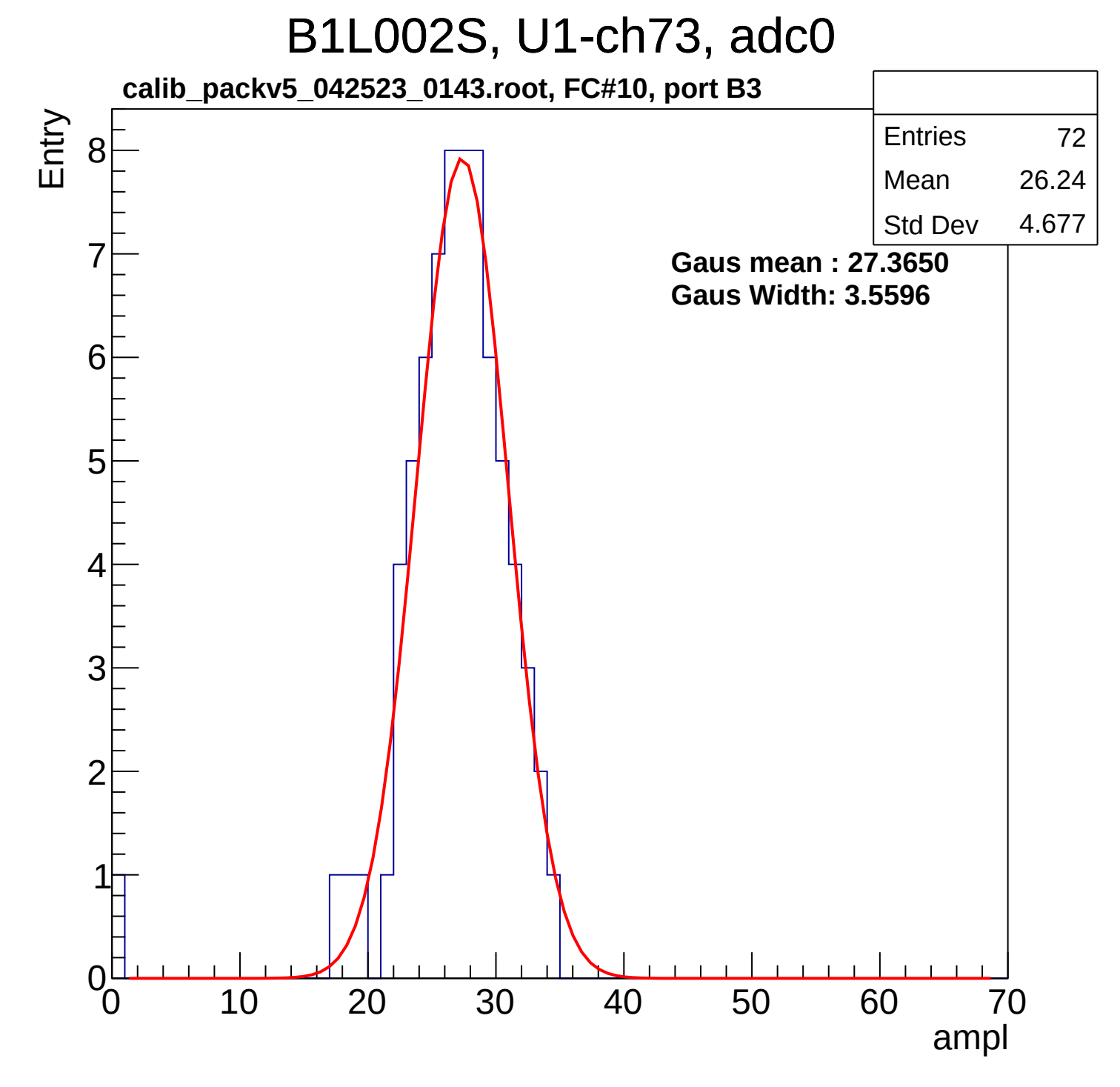
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch73, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 26.24
Std Dev 4.677
Gaus mean : 27.3650
Gaus Width: 3.5596
Entry
ampl
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2
3
4
5
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7
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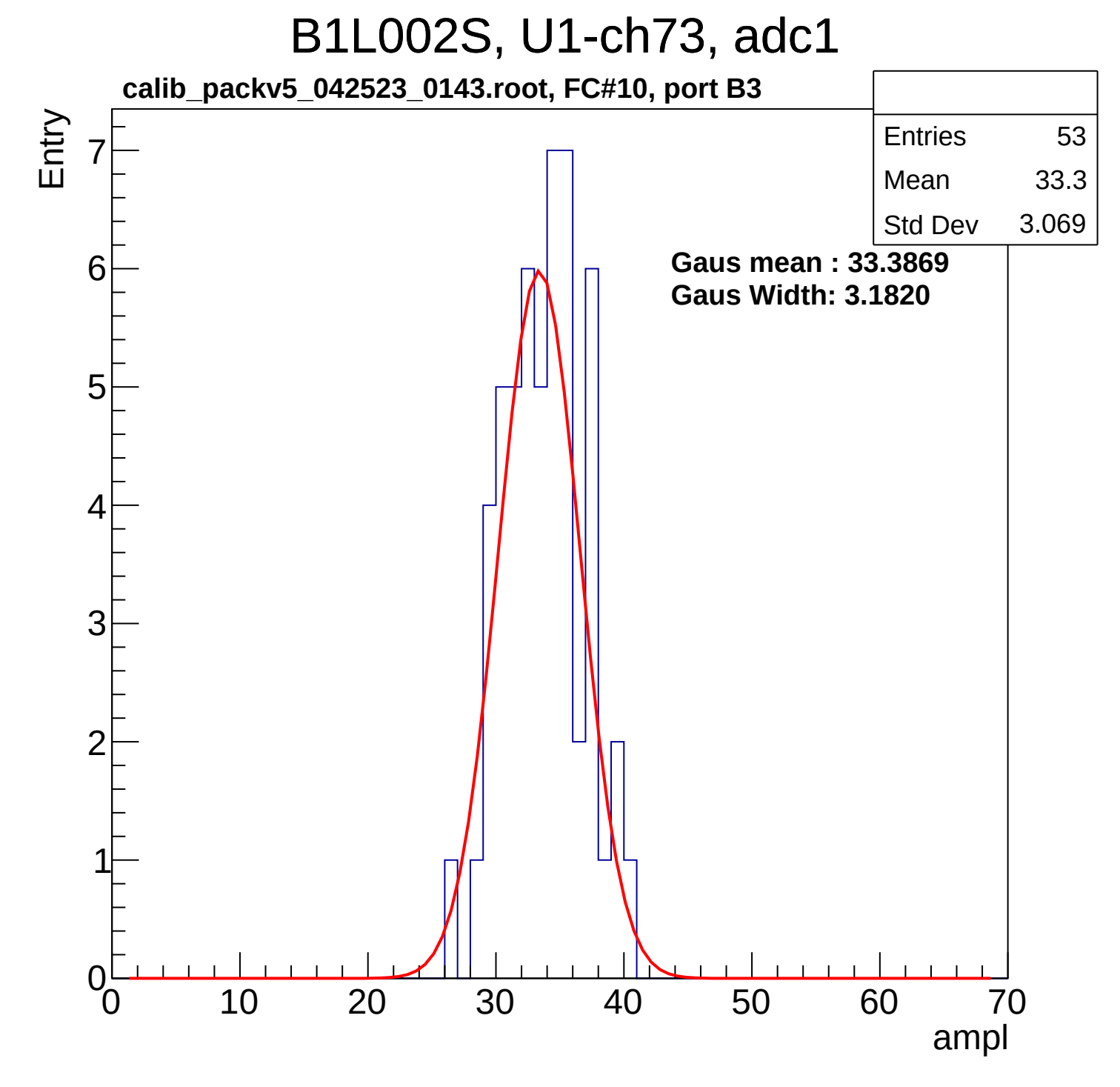
B1L002S, U1-ch73, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 33.3
Std Dev 3.069
Gaus mean : 33.3869
Gaus Width: 3.1820
Entry
ampl
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3
4
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6
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0
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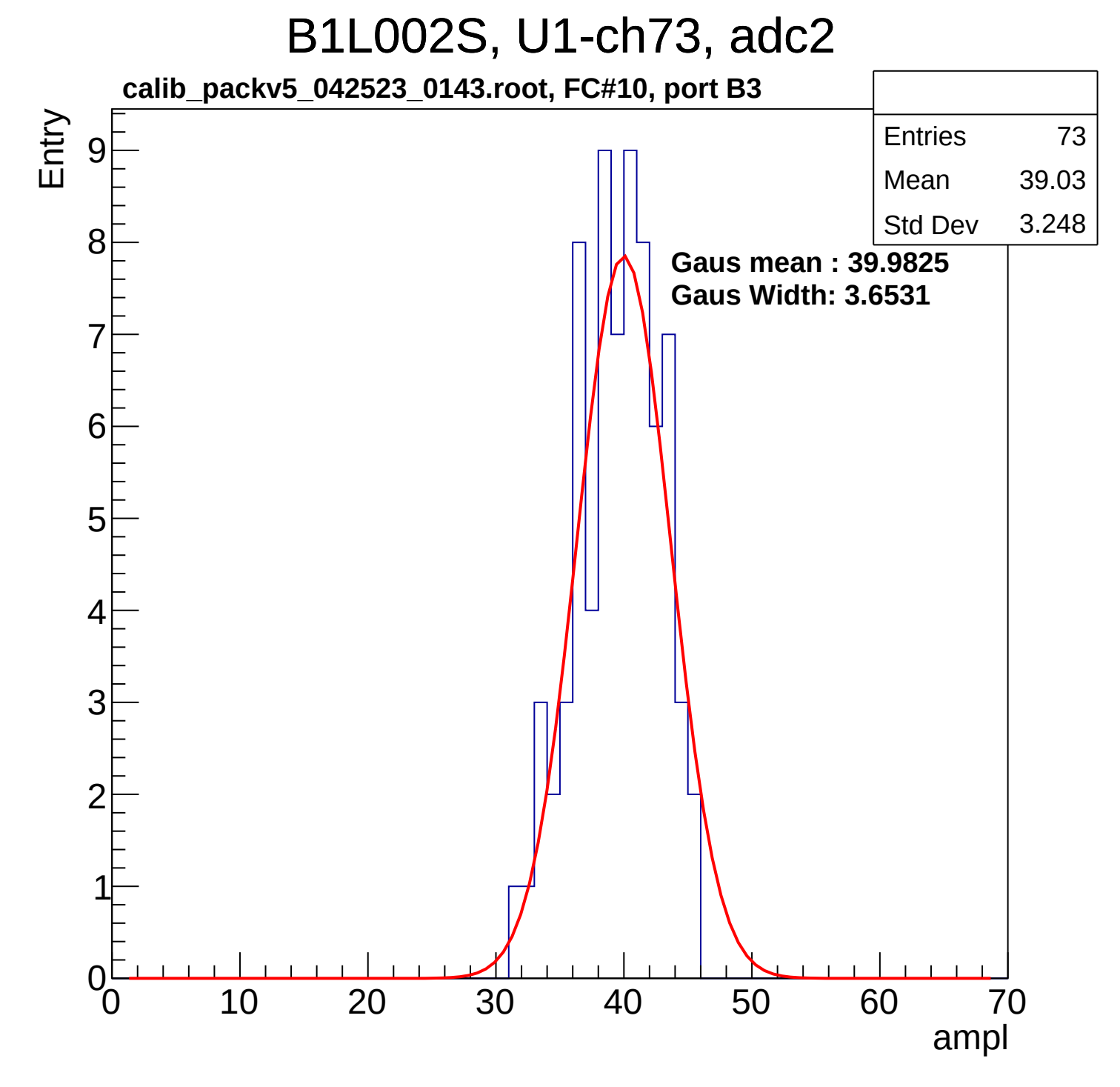
B1L002S, U1-ch73, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 39.03
Std Dev 3.248
Gaus mean : 39.9825
Gaus Width: 3.6531
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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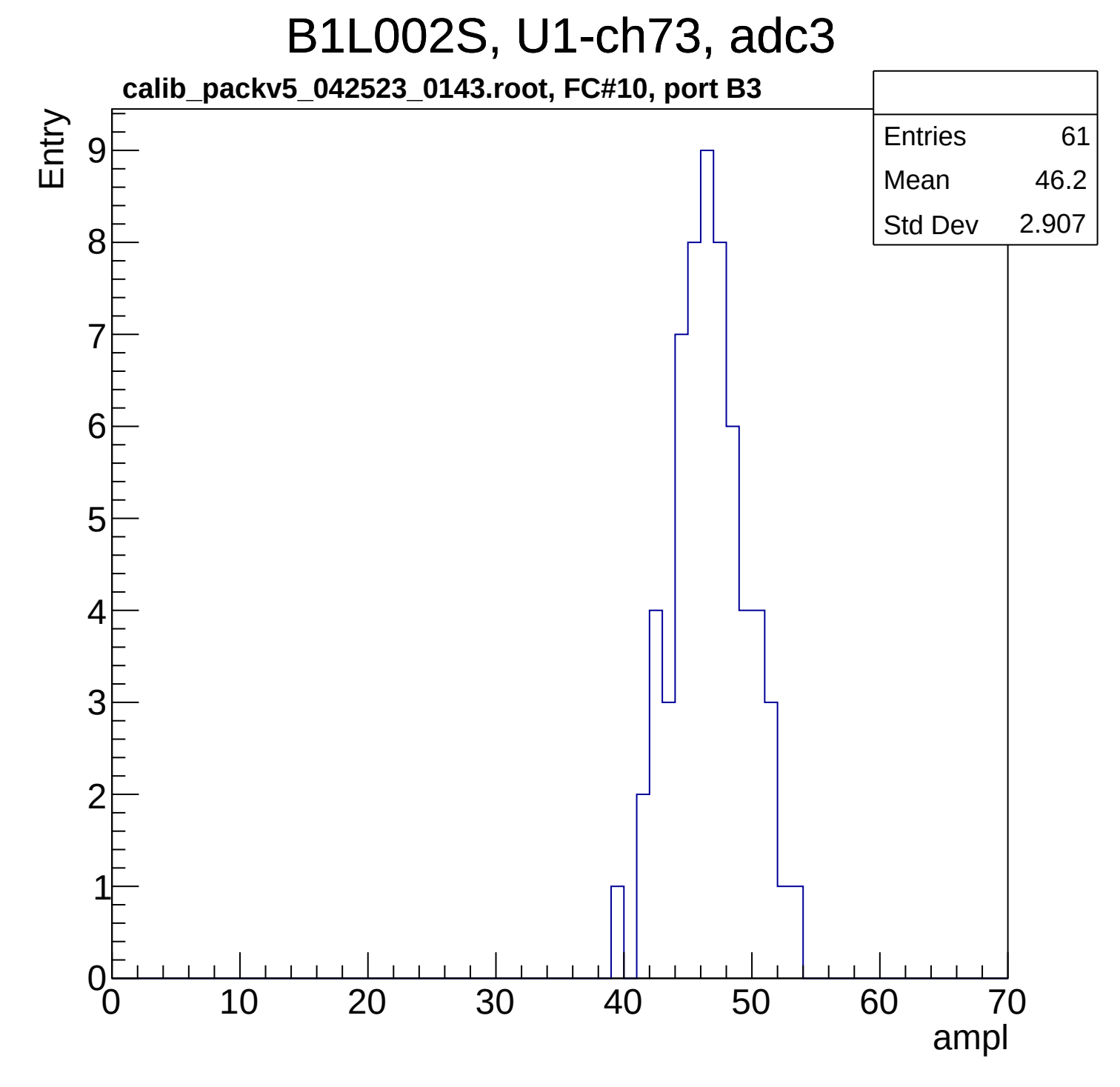
B1L002S, U1-ch73, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.2
Std Dev 2.907
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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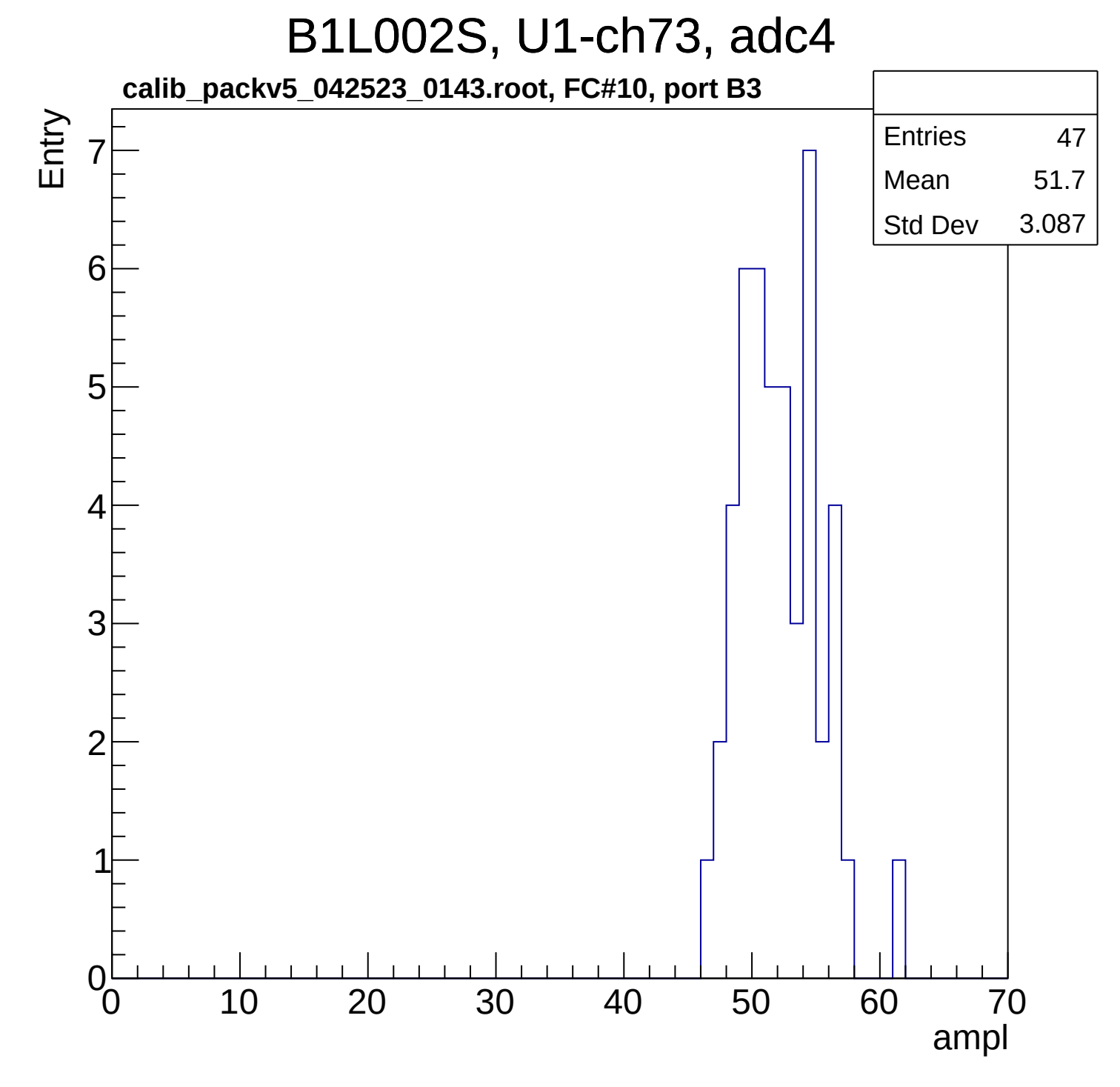
B1L002S, U1-ch73, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 51.7
Std Dev 3.087
Entry
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1
2
3
4
5
6
7
0
10
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30
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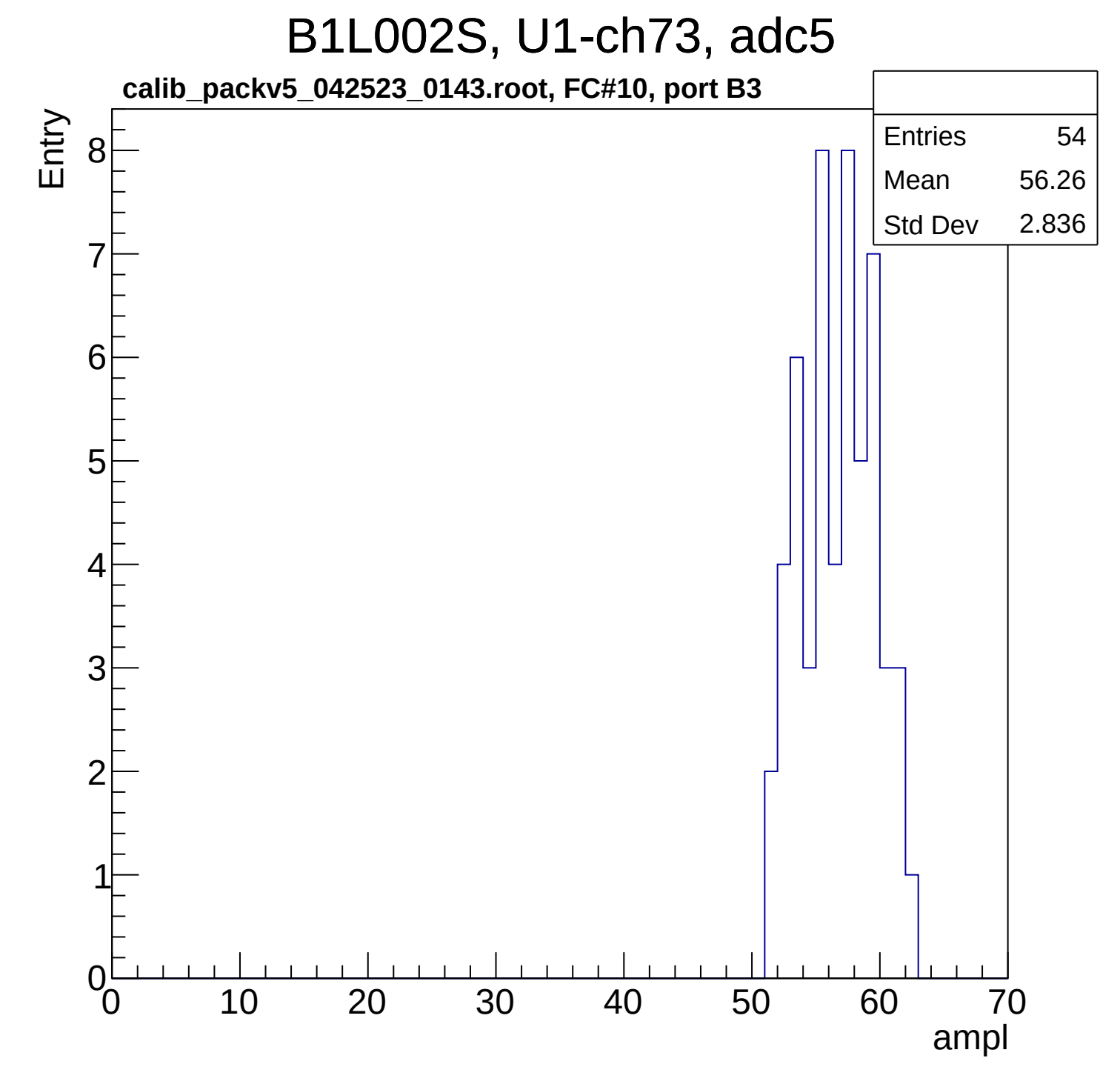
50
60
70
ampl


B1L002S, U1-ch73, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 56.26
Std Dev 2.836
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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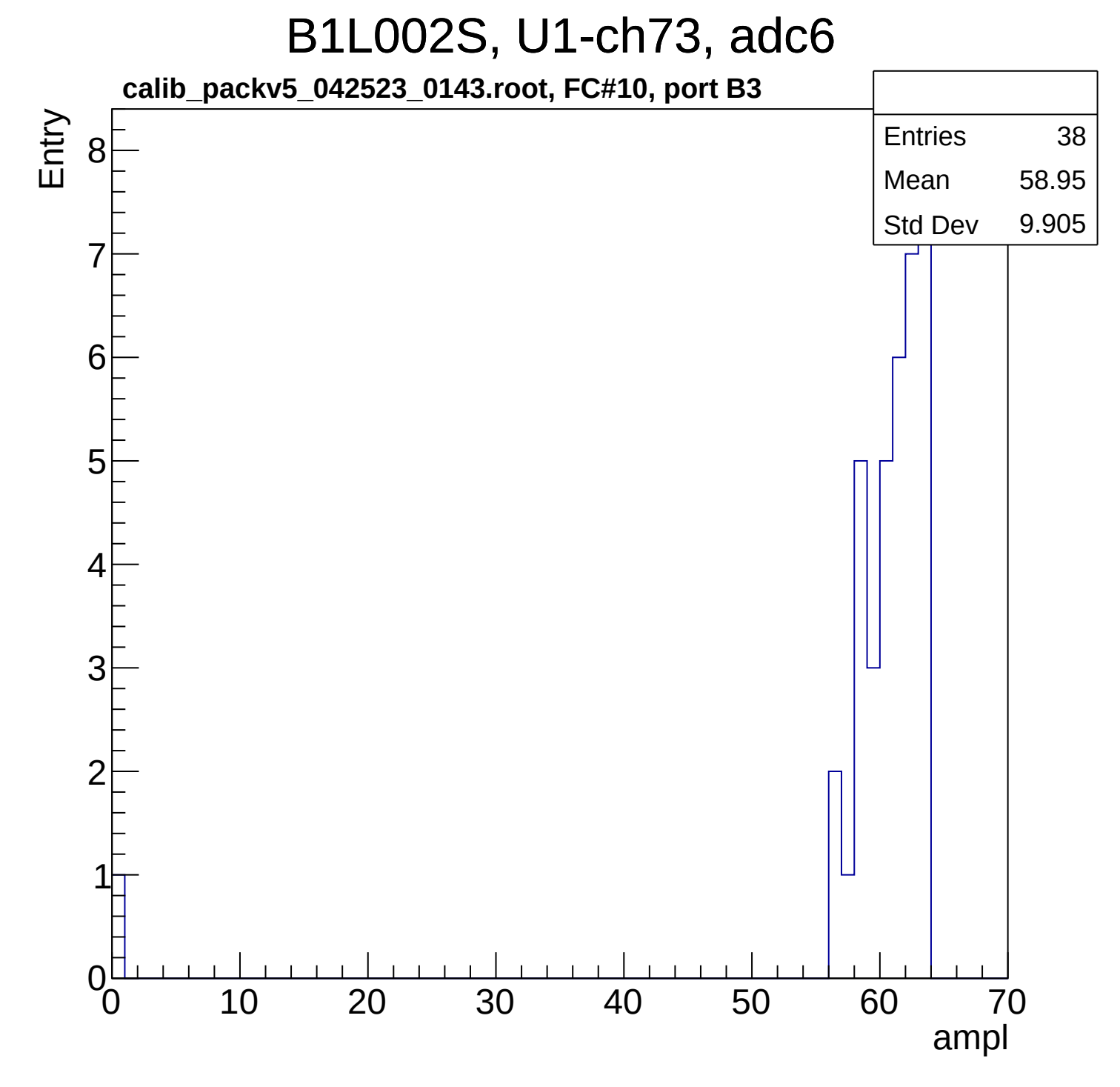
B1L002S, U1-ch73, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 58.95
Std Dev 9.905
Entry
0
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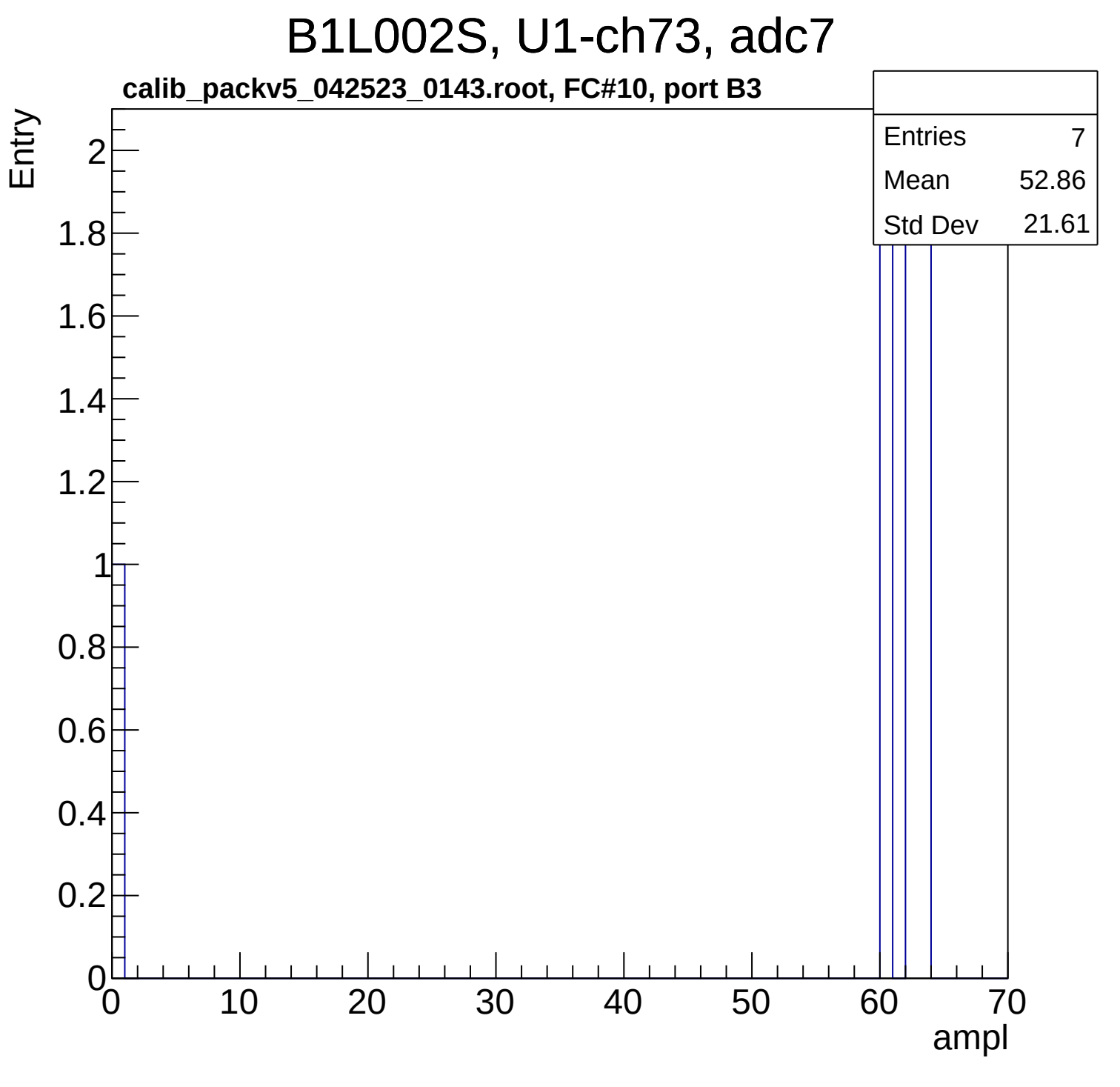
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch73, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 52.86
Std Dev 21.61
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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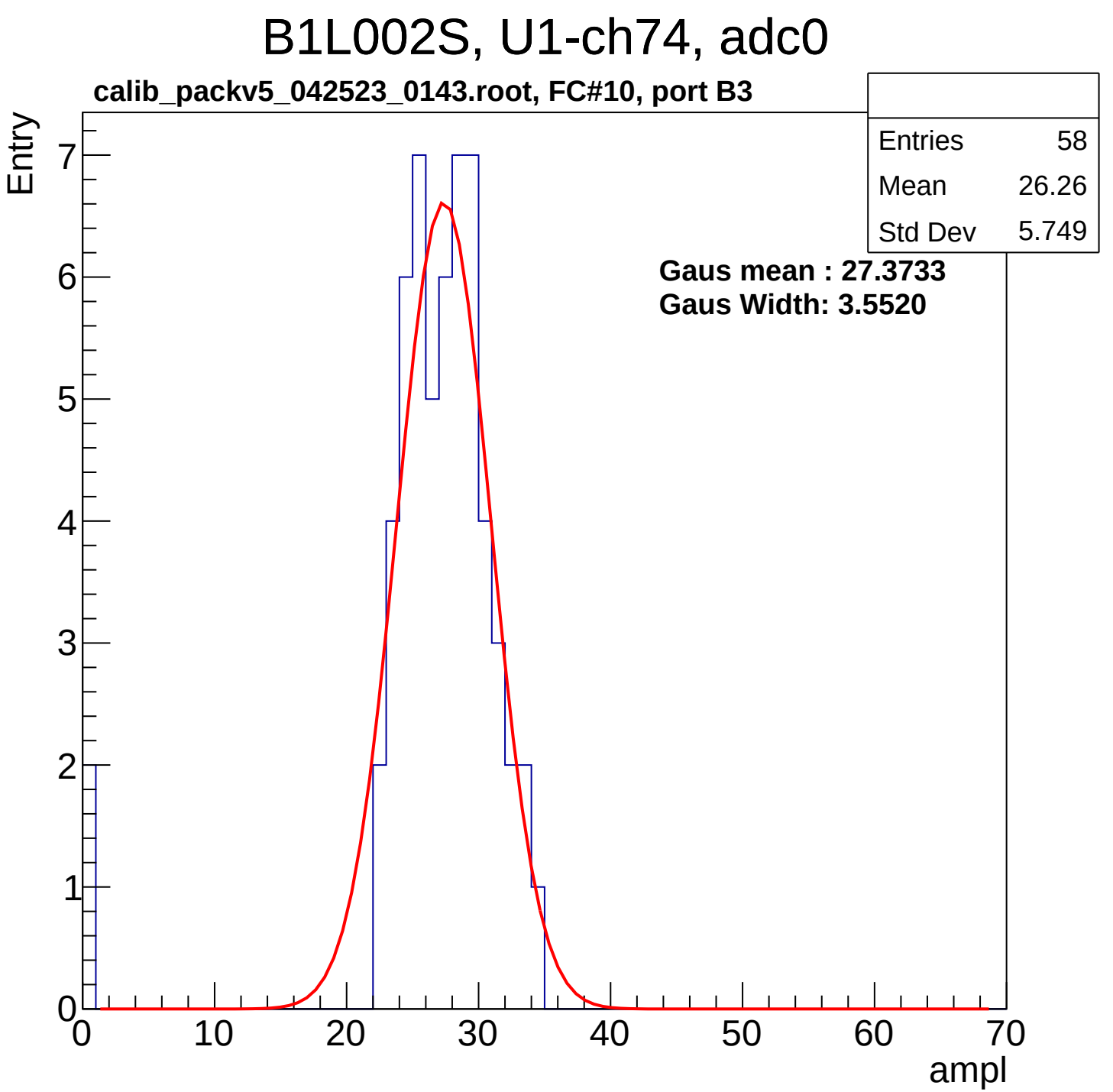
B1L002S, U1-ch74, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 26.26
Std Dev 5.749
Gaus mean : 27.3733
Gaus Width: 3.5520
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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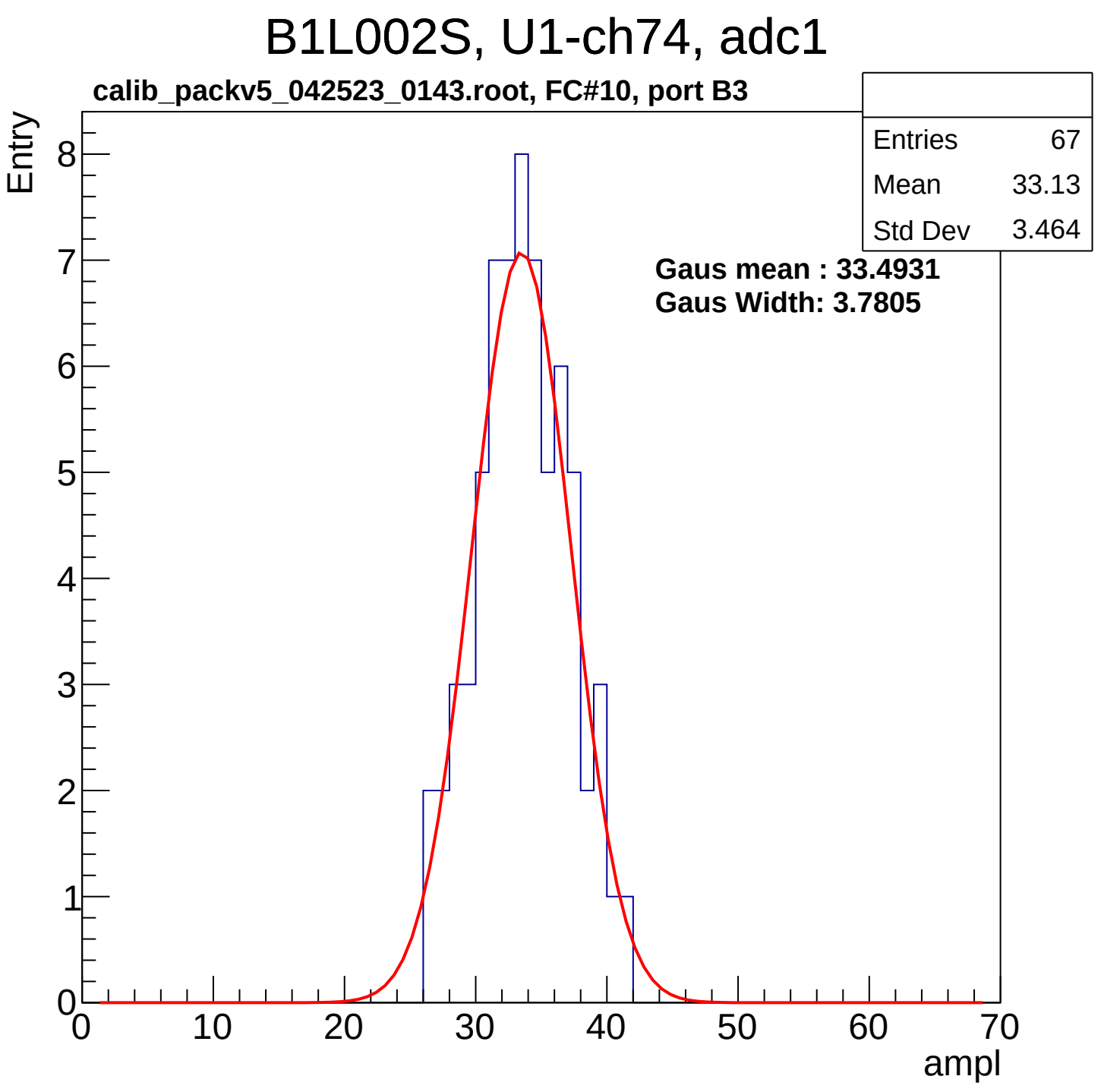
B1L002S, U1-ch74, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.13
Std Dev 3.464
Gaus mean : 33.4931
Gaus Width: 3.7805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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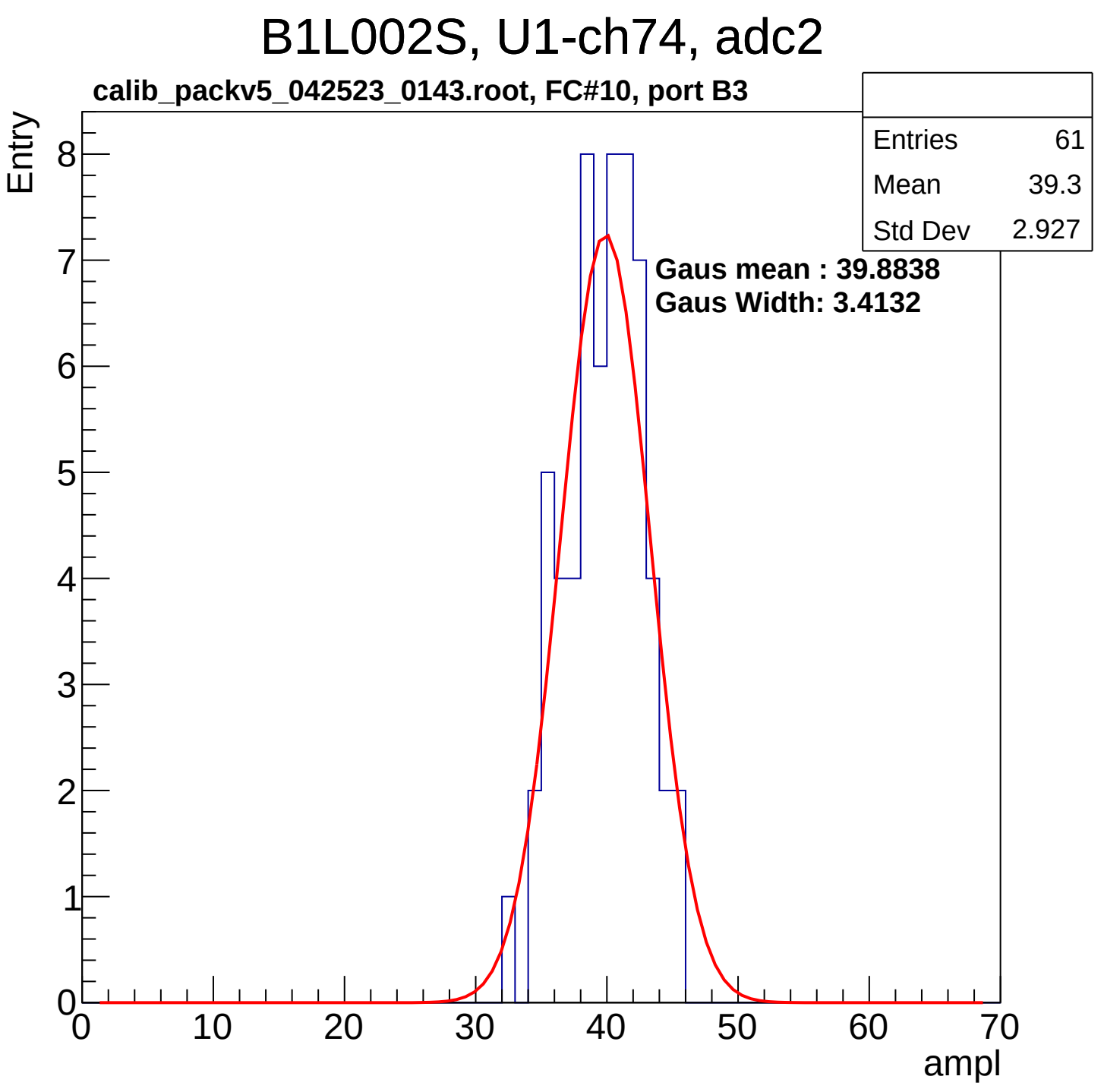
B1L002S, U1-ch74, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 39.3
Std Dev 2.927
Gaus mean : 39.8838
Gaus Width: 3.4132
Entry
ampl
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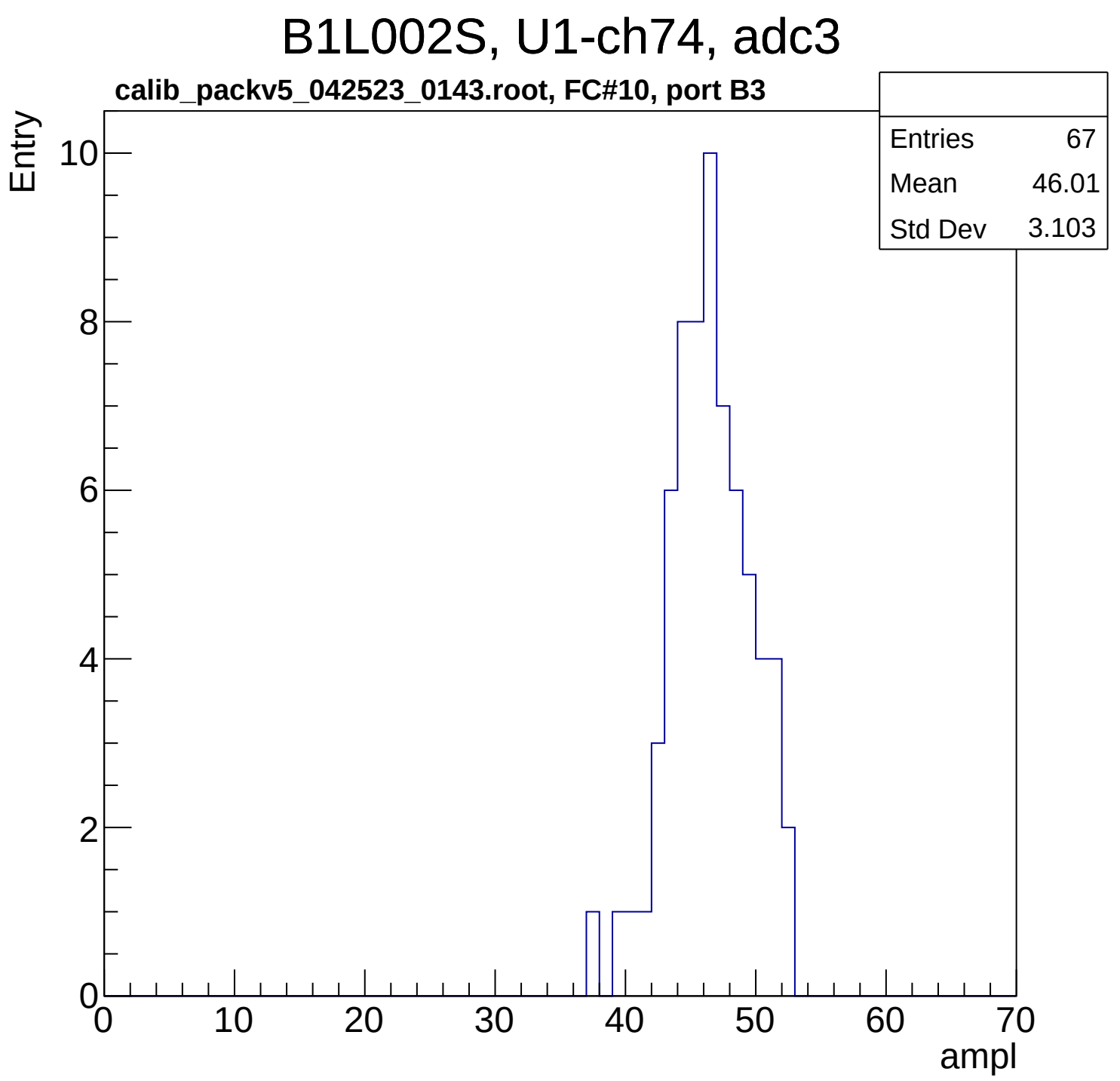
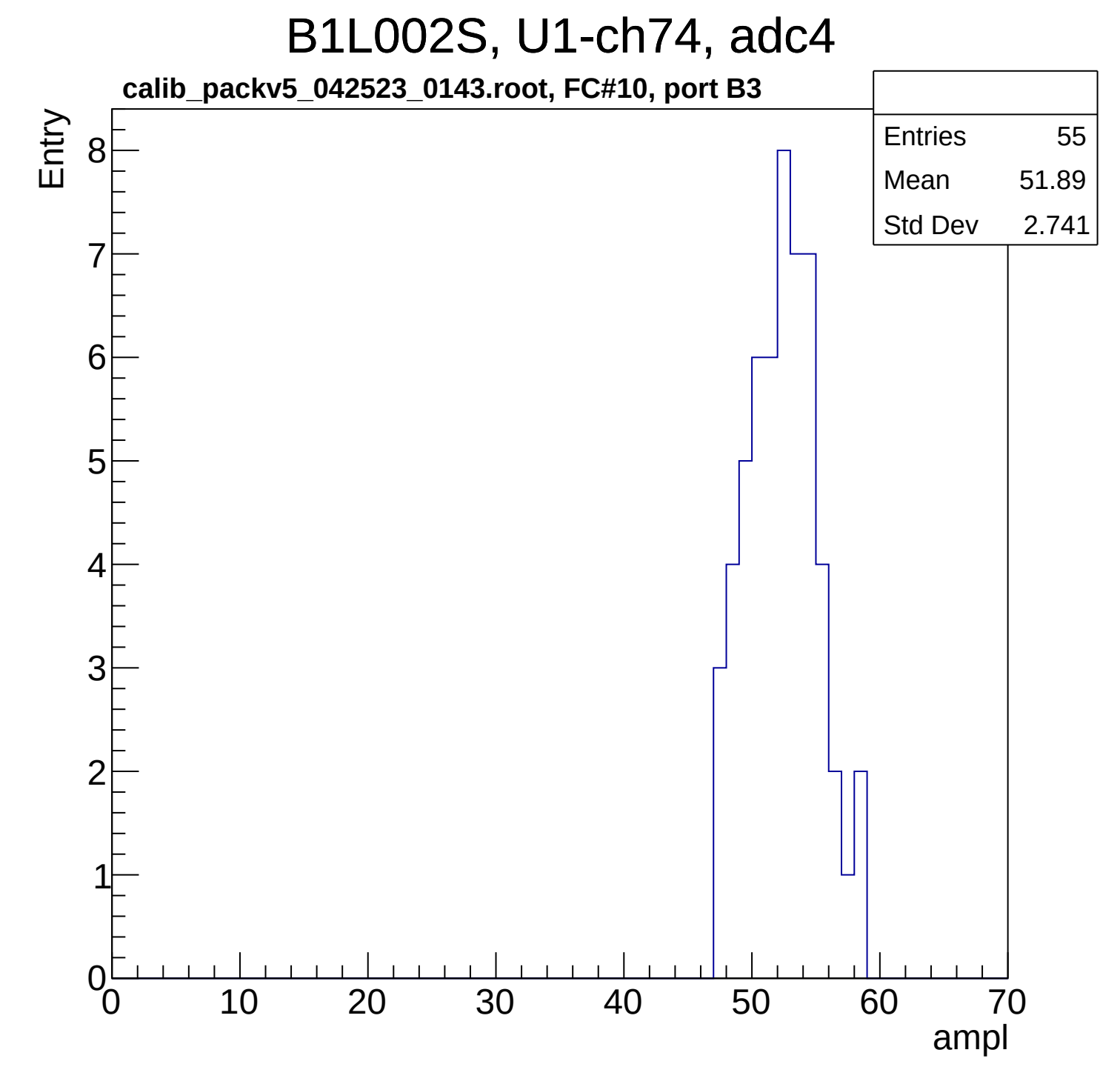
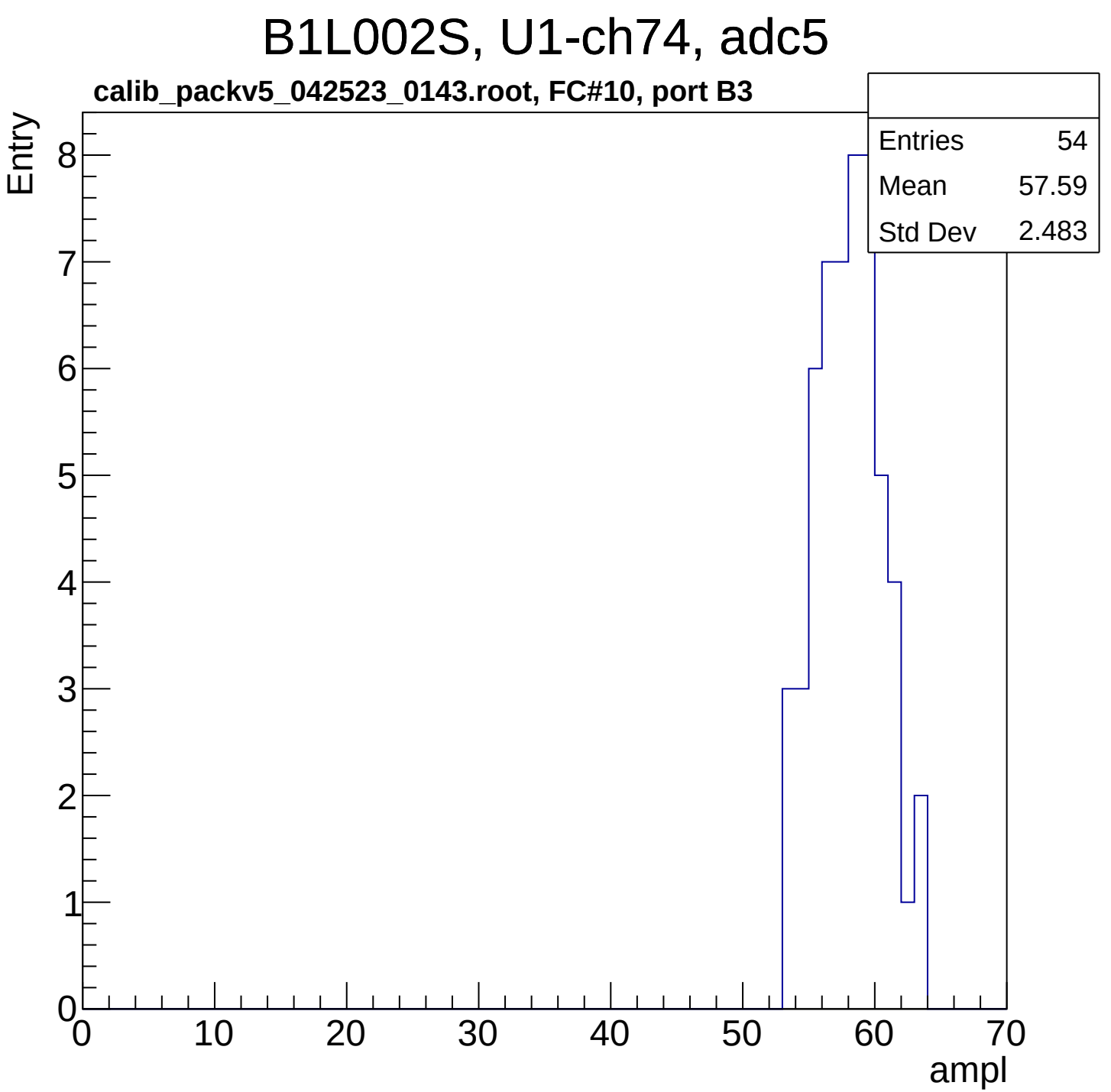
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch74, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.01
Std Dev 3.103
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch74, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 51.89
Std Dev 2.741
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch74, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.59
Std Dev 2.483
Entry
ampl
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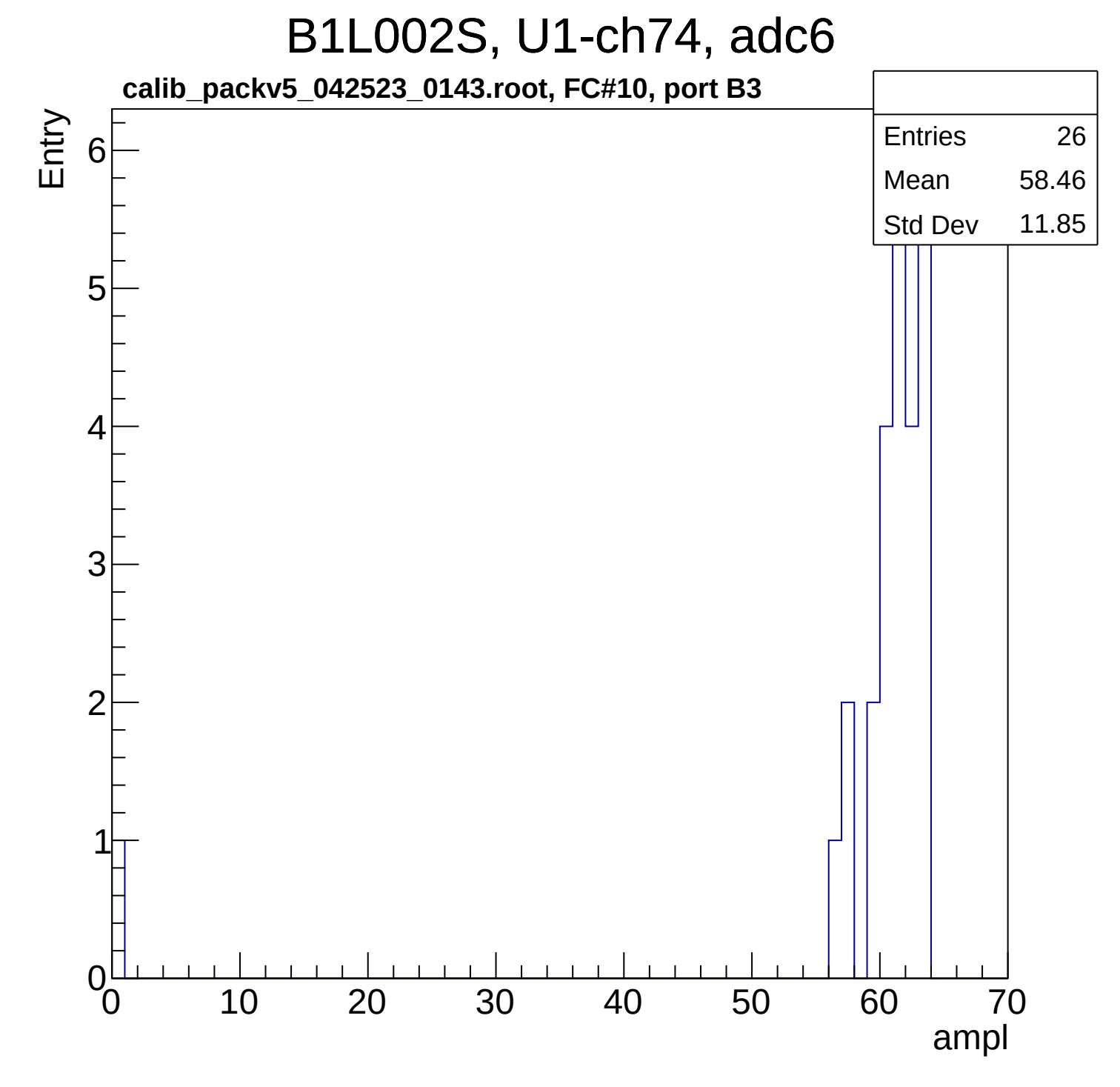
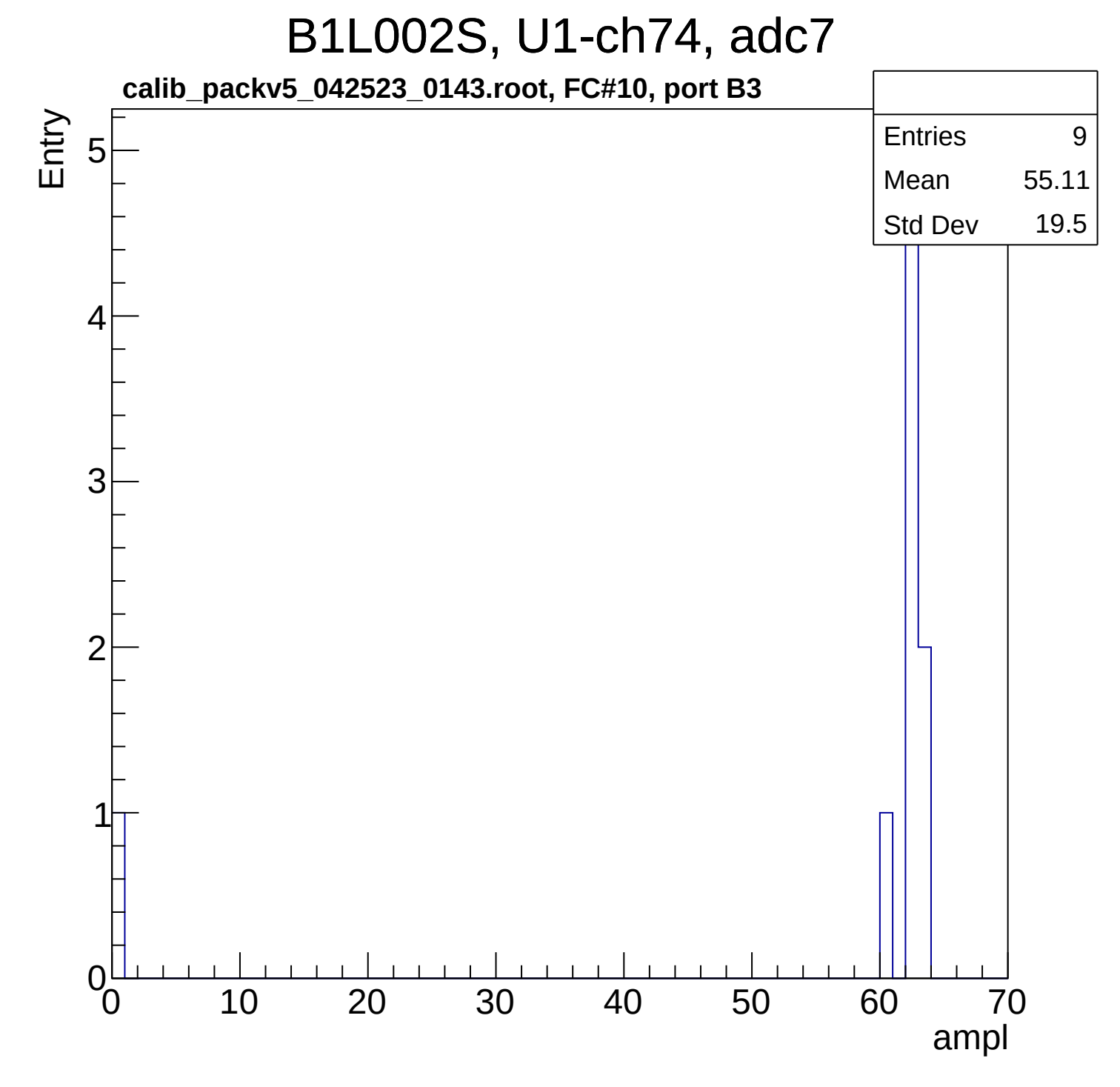
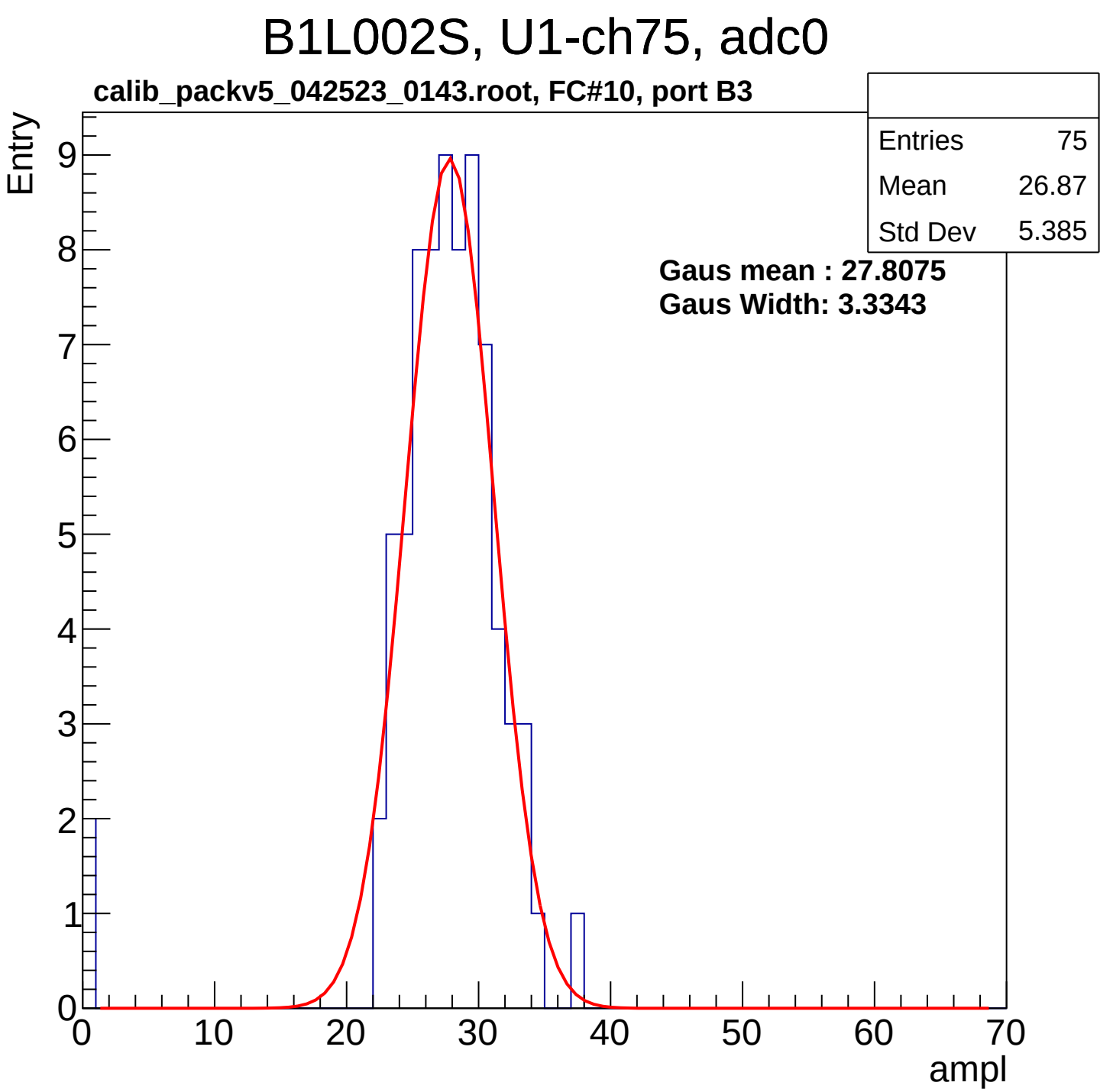
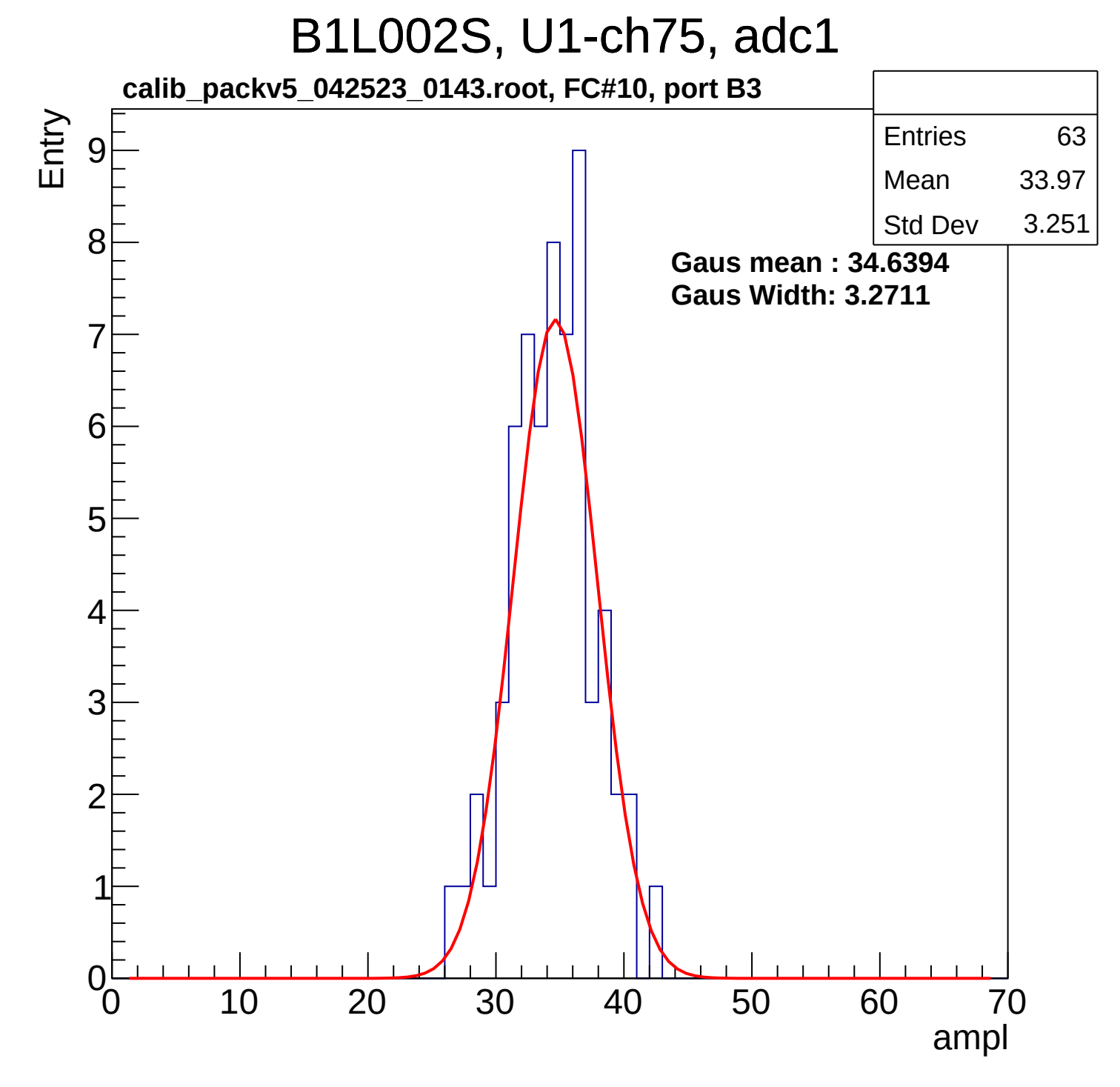
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch74, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.46
Std Dev 11.85
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch74, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.11
Std Dev 19.5
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch75, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 26.87
Std Dev 5.385
Gaus mean : 27.8075
Gaus Width: 3.3343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch75, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.97
Std Dev 3.251
Gaus mean : 34.6394
Gaus Width: 3.2711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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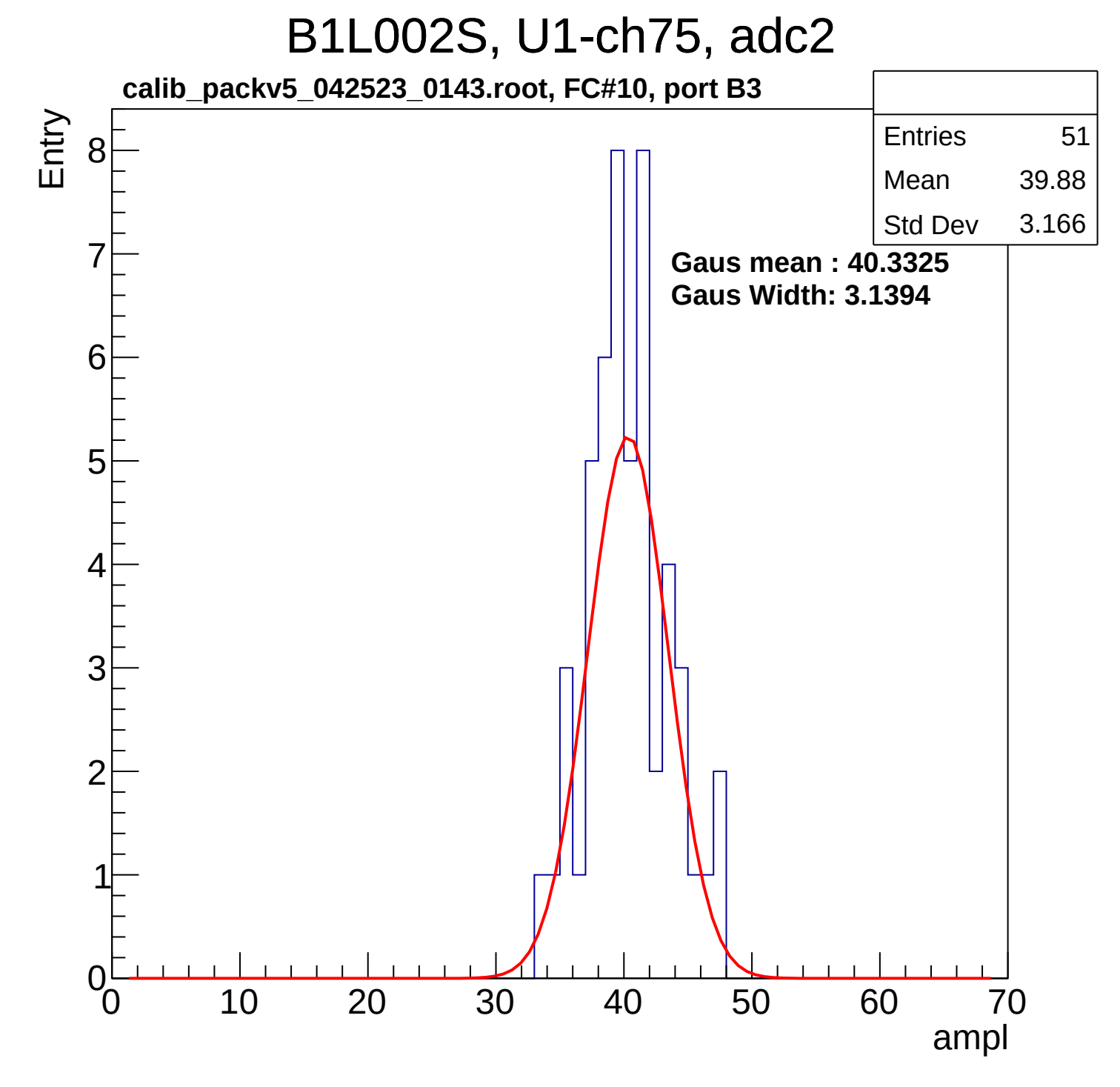
B1L002S, U1-ch75, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 39.88
Std Dev 3.166
Gaus mean : 40.3325
Gaus Width: 3.1394
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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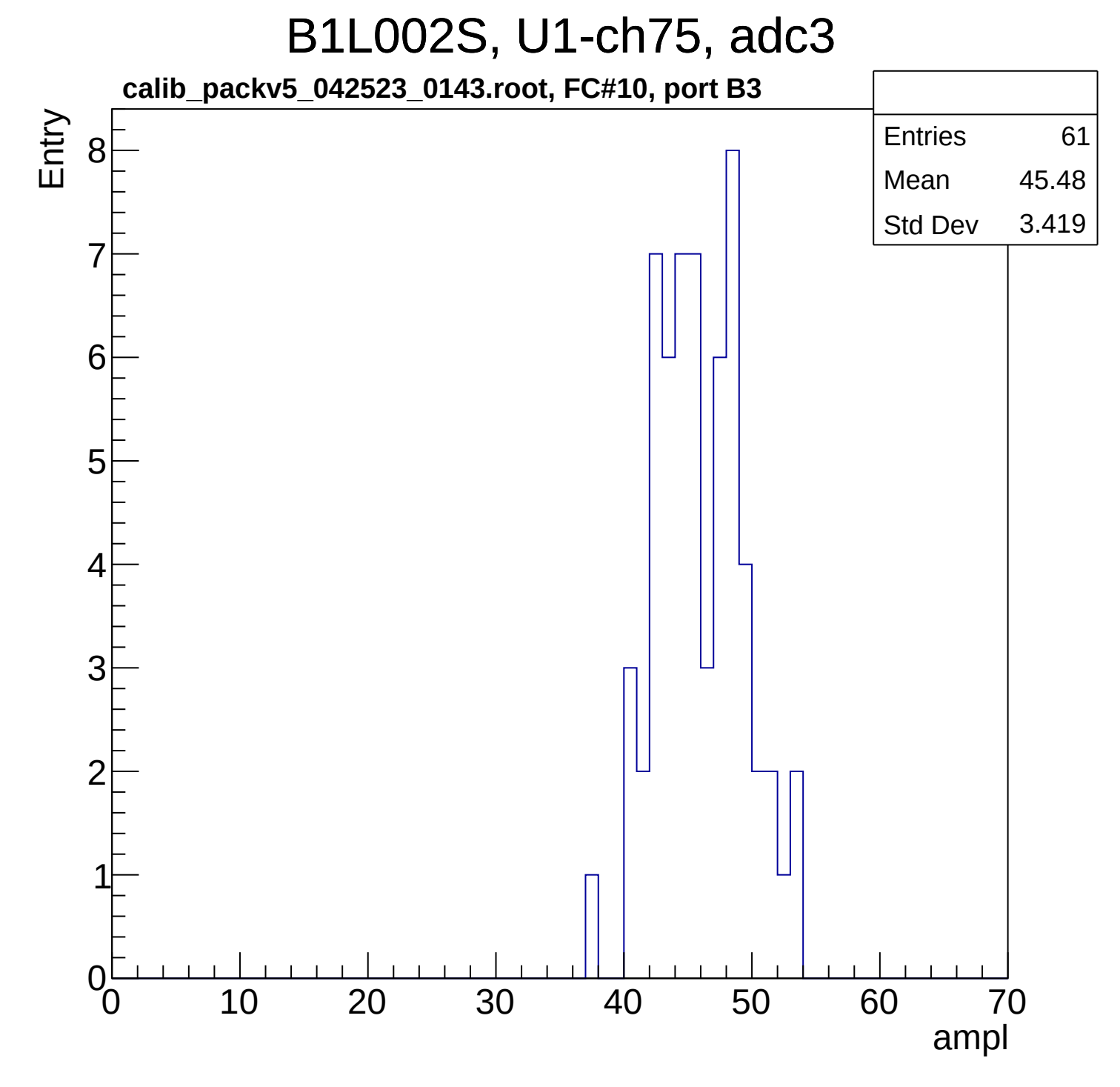
B1L002S, U1-ch75, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 45.48
Std Dev 3.419
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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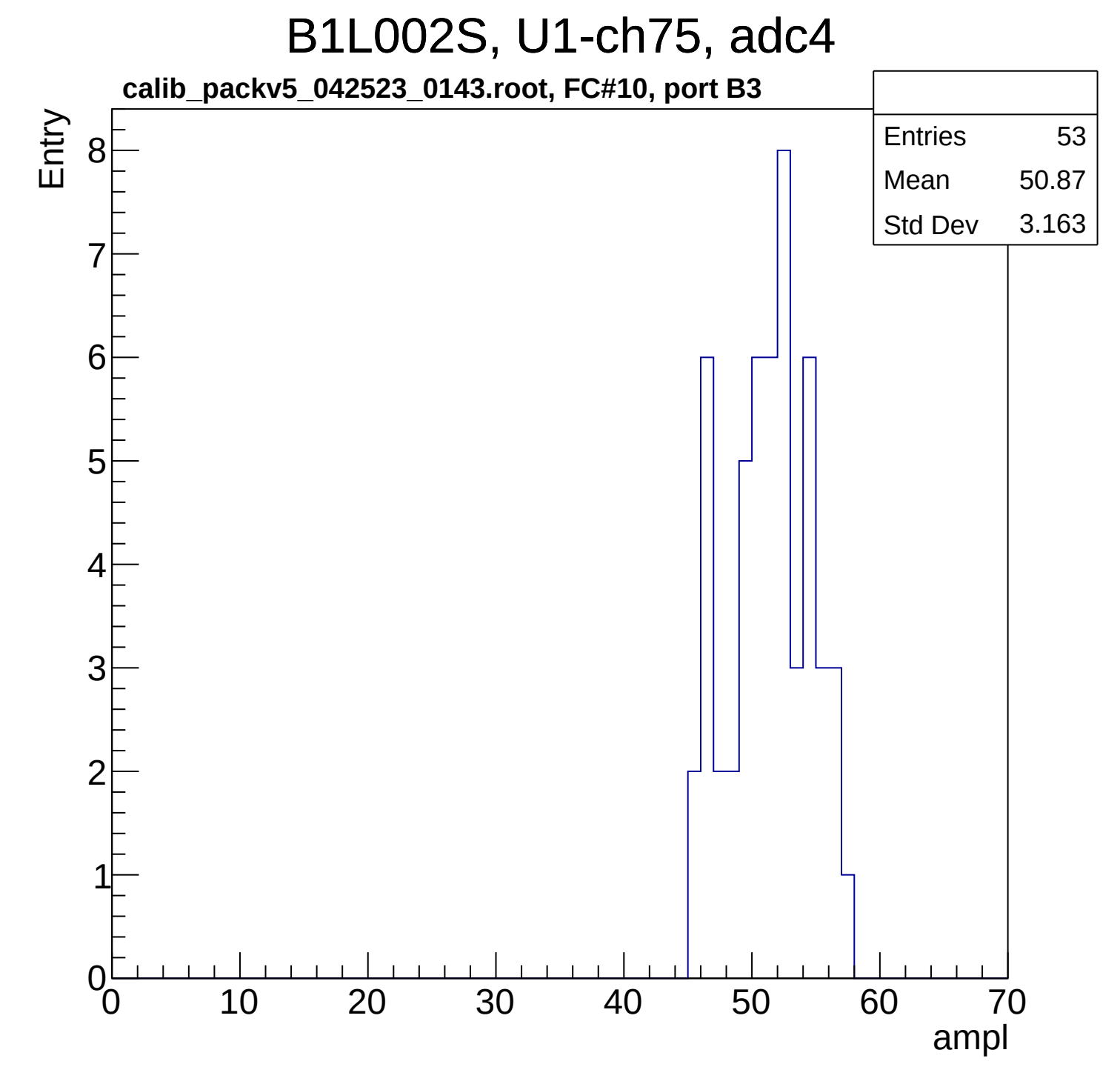
B1L002S, U1-ch75, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 50.87
Std Dev 3.163
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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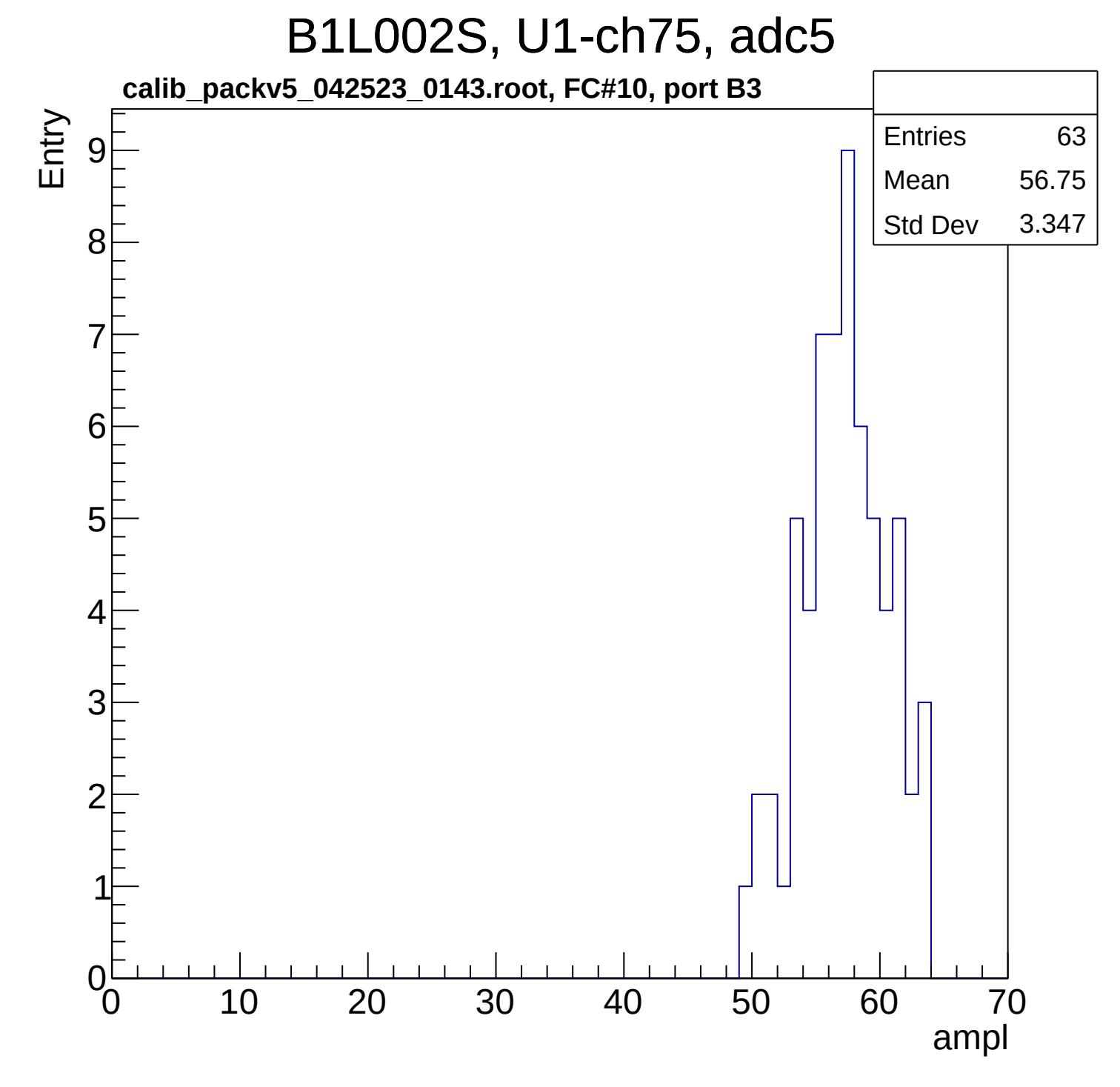
B1L002S, U1-ch75, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 56.75
Std Dev 3.347
Entry
ampl
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3
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6
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0
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60
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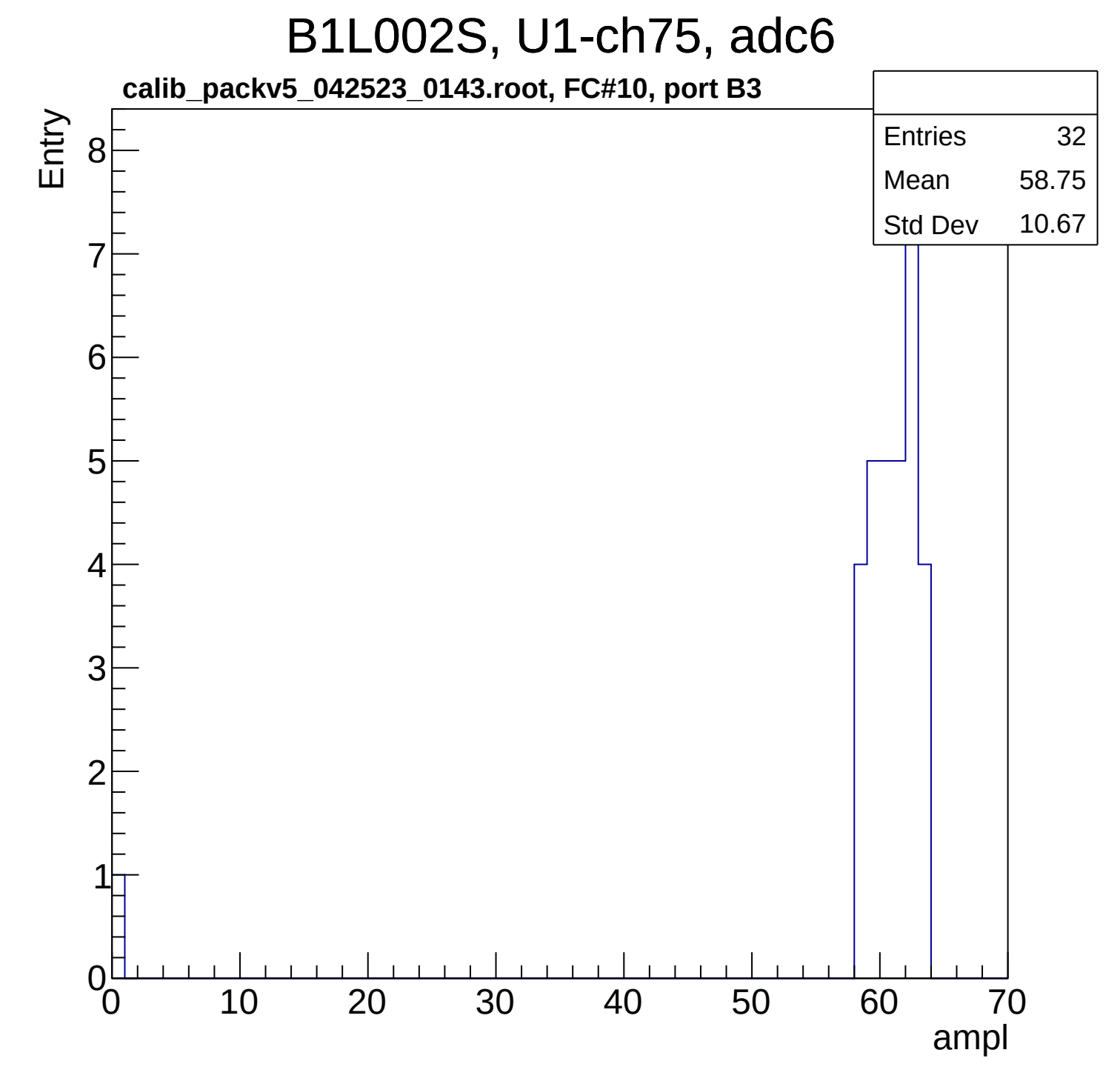
B1L002S, U1-ch75, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 58.75
Std Dev 10.67
Entry
ampl
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1
2
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5
6
7
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0
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20
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60
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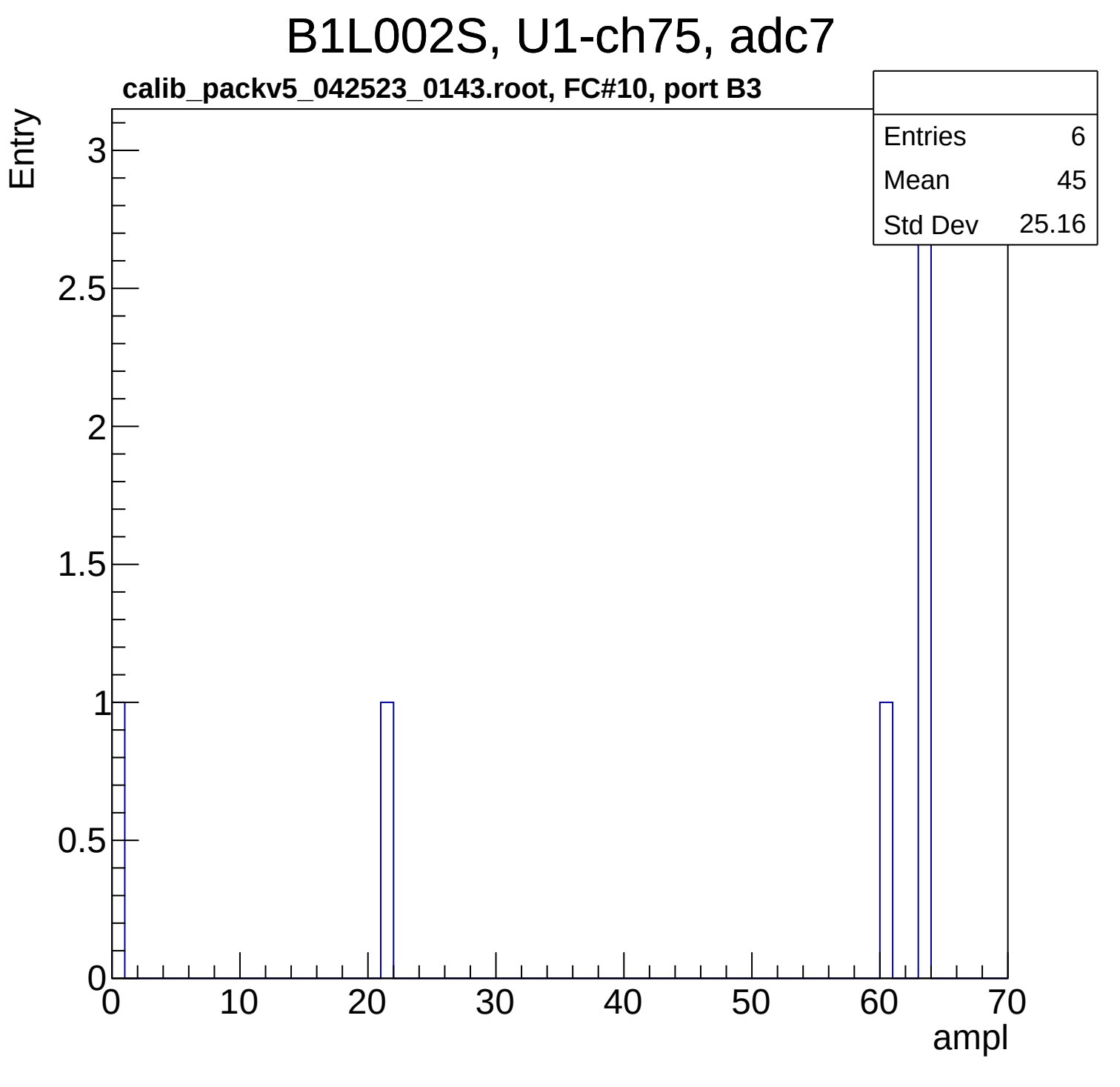
B1L002S, U1-ch75, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 45
Std Dev 25.16
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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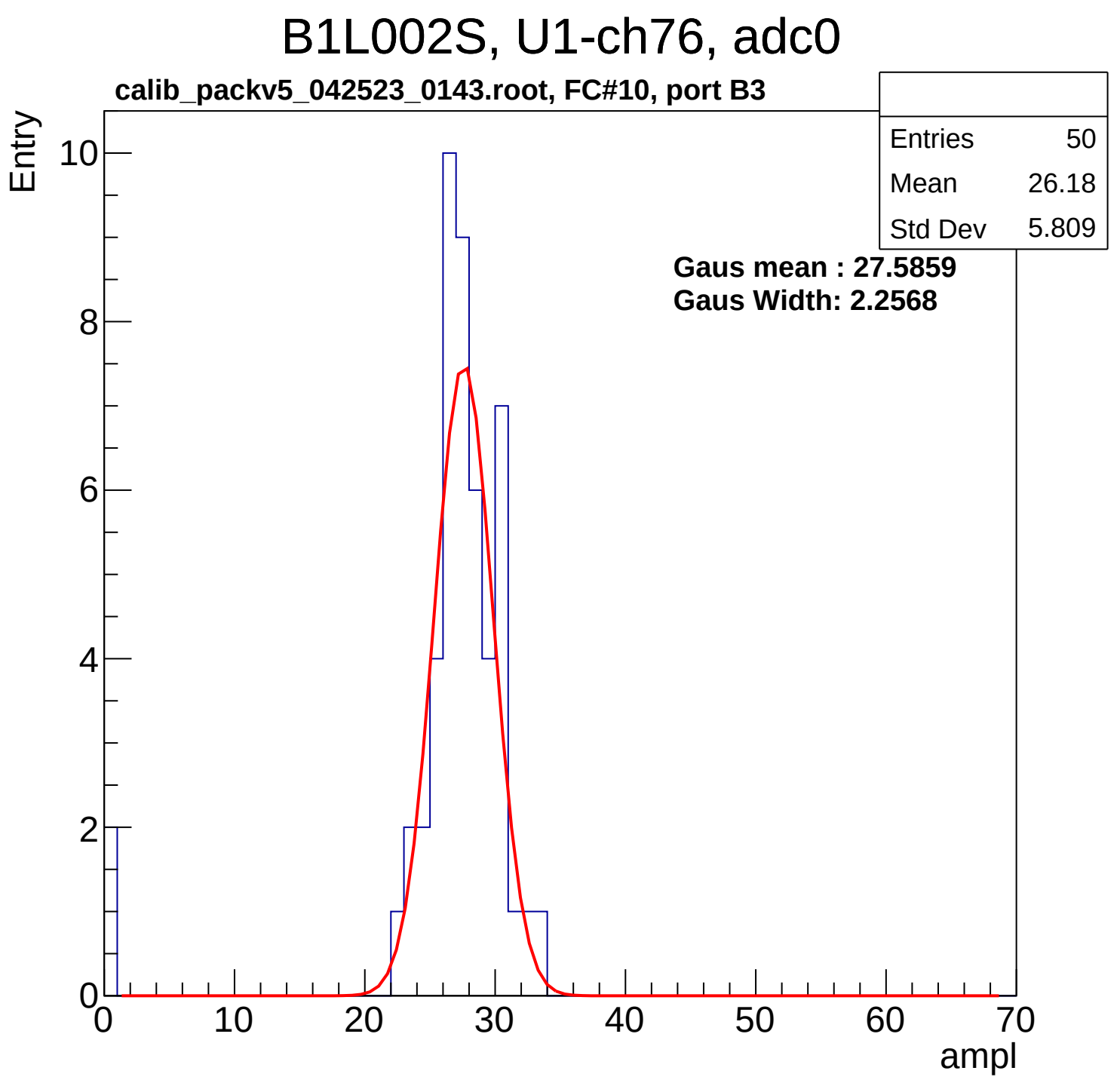
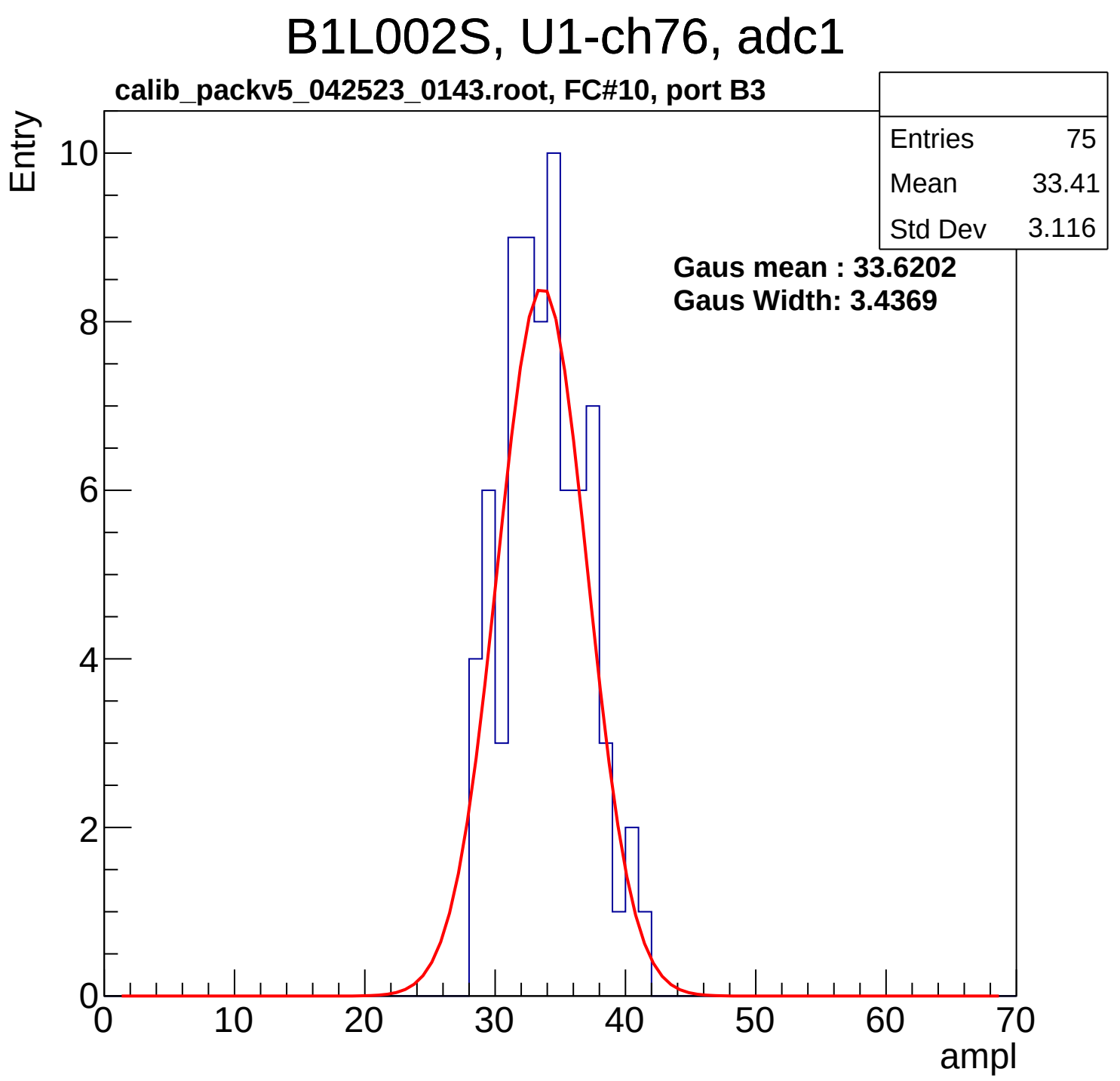
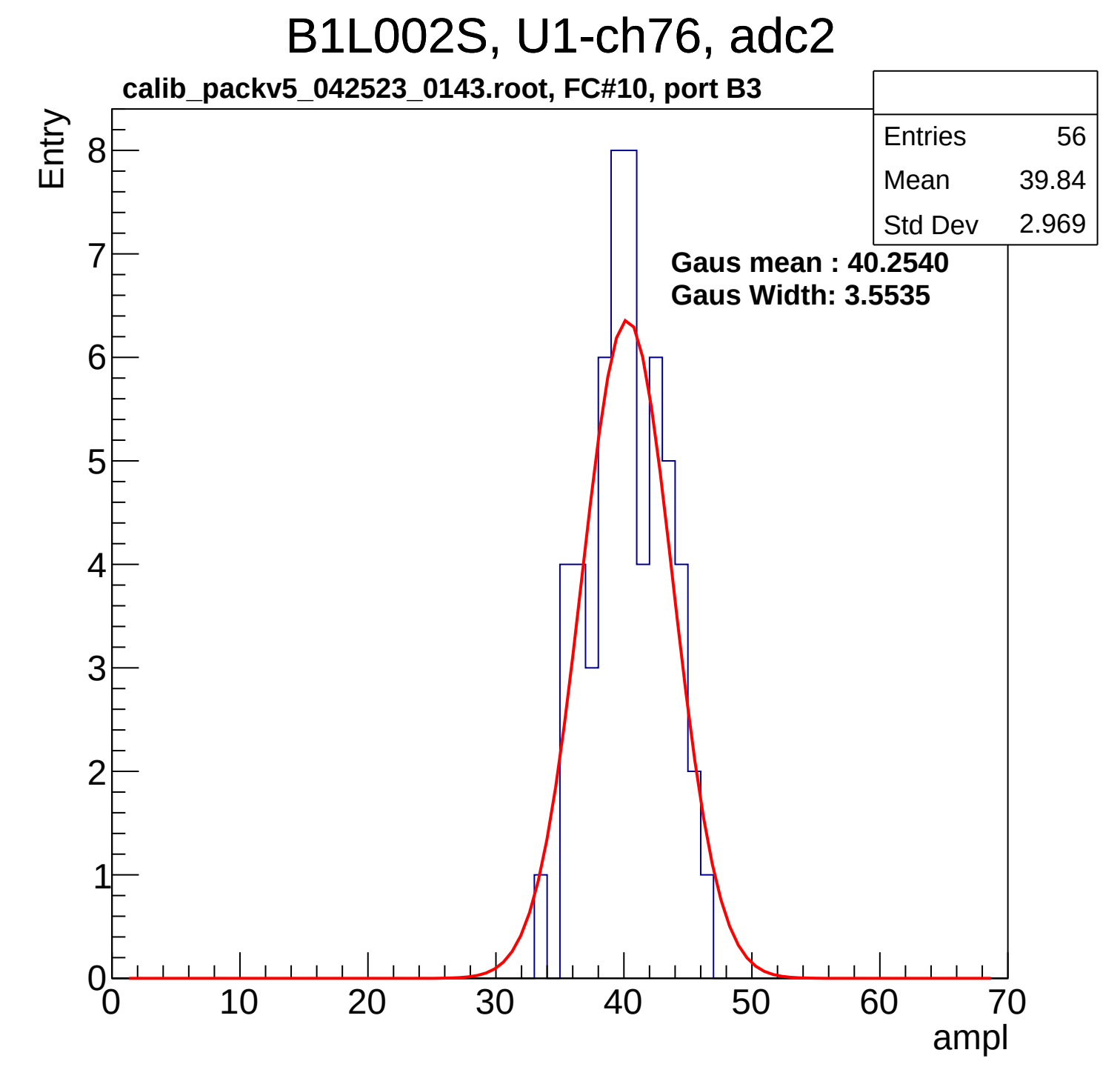
40
50
60
70


B1L002S, U1-ch76, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 26.18
Std Dev 5.809
Gaus mean : 27.5859
Gaus Width: 2.2568
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch76, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 33.41
Std Dev 3.116
Gaus mean : 33.6202
Gaus Width: 3.4369
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch76, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 39.84
Std Dev 2.969
Gaus mean : 40.2540
Gaus Width: 3.5535
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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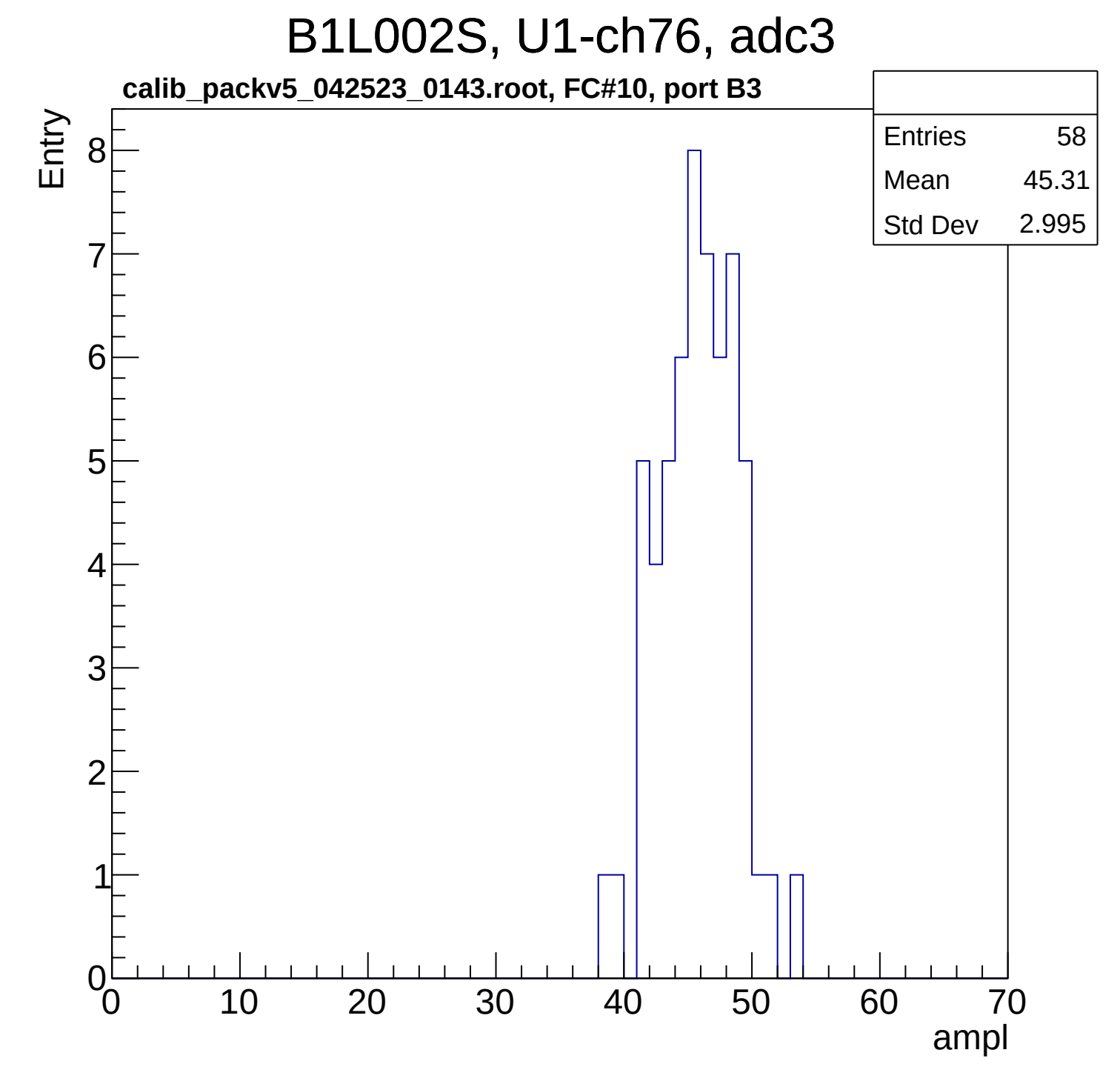
B1L002S, U1-ch76, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 45.31
Std Dev 2.995
Entry
ampl
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3
4
5
6
7
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0
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40
50
60
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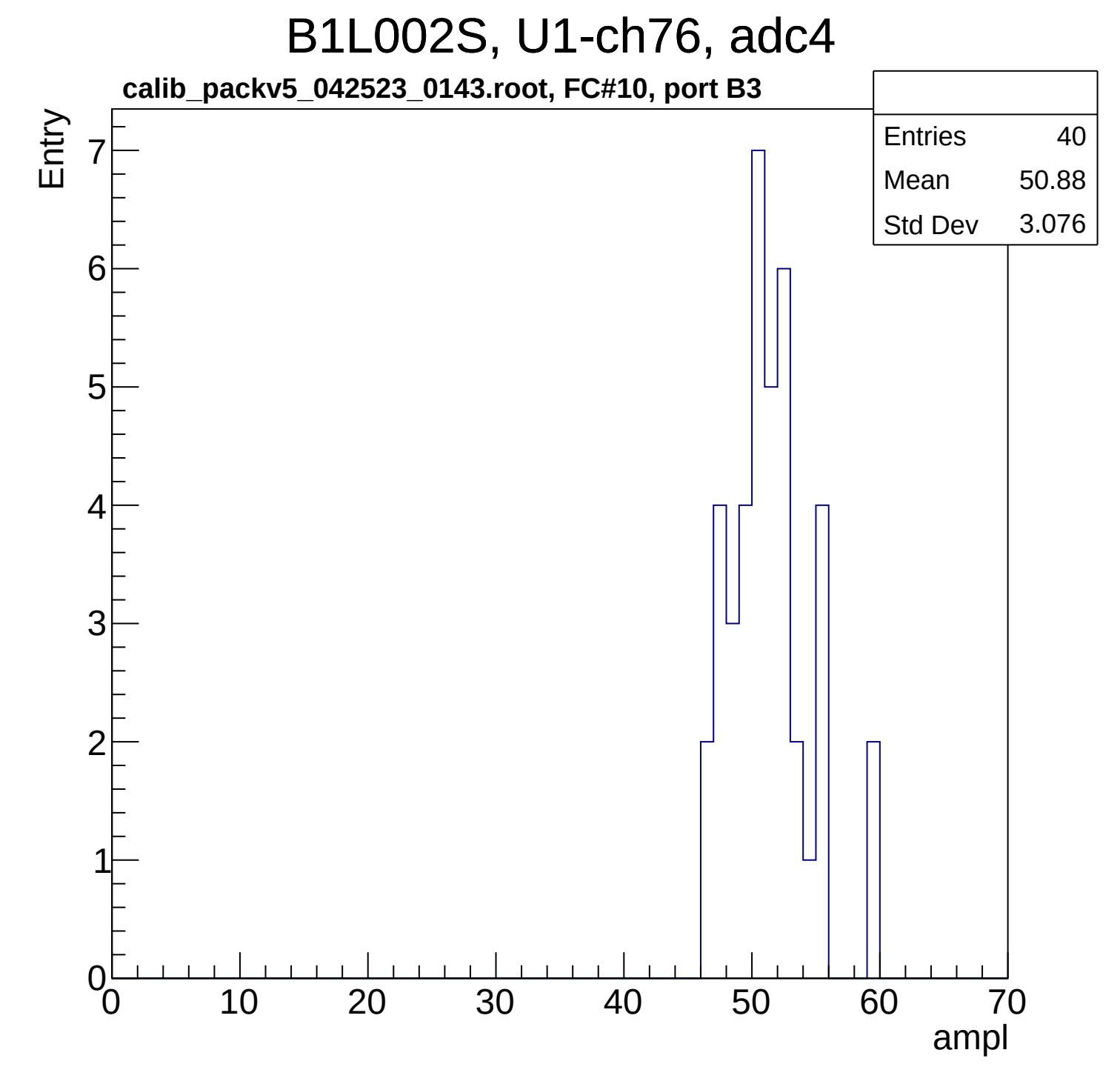
B1L002S, U1-ch76, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 50.88
Std Dev 3.076
Entry
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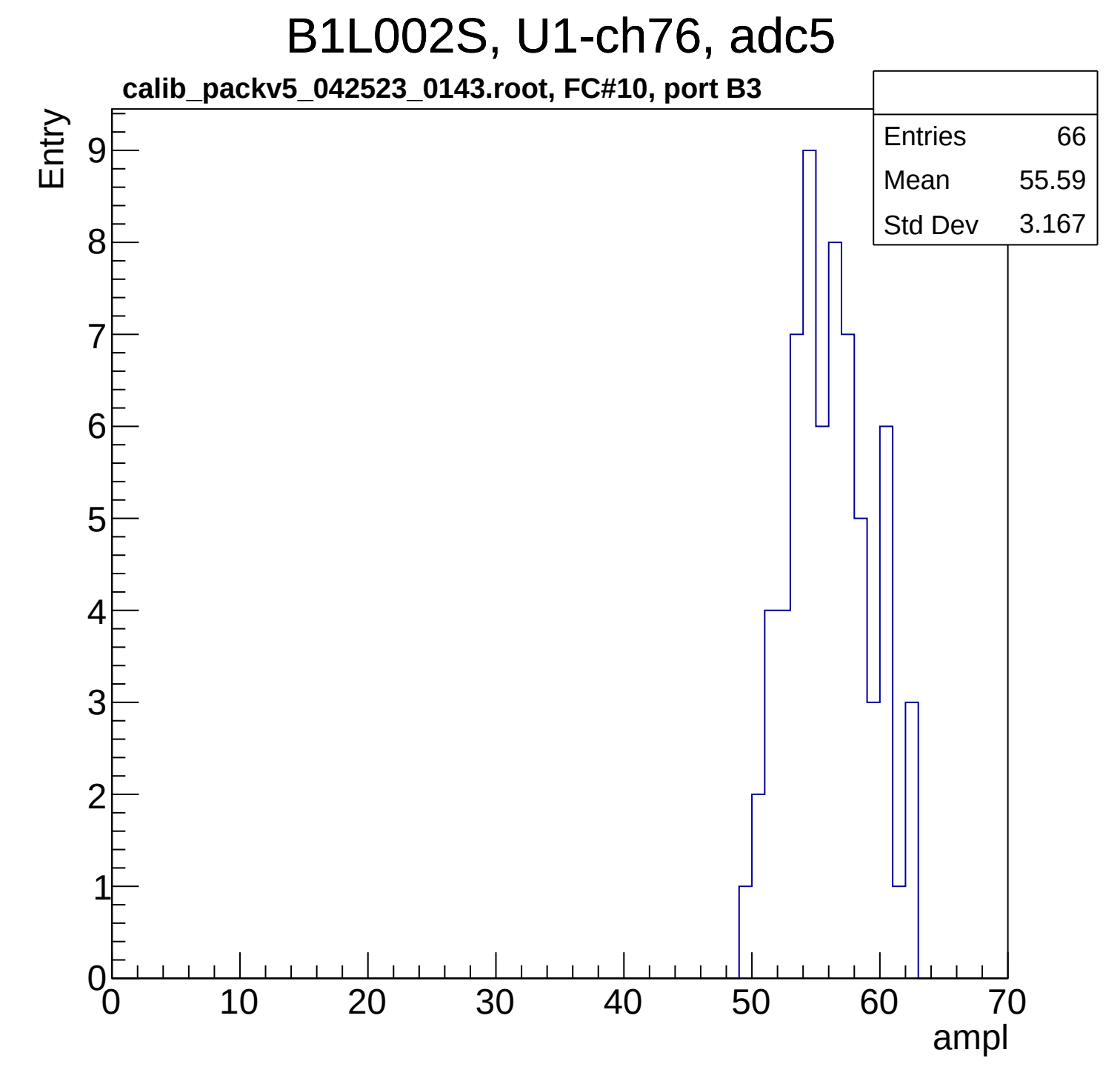
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch76, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 55.59
Std Dev 3.167
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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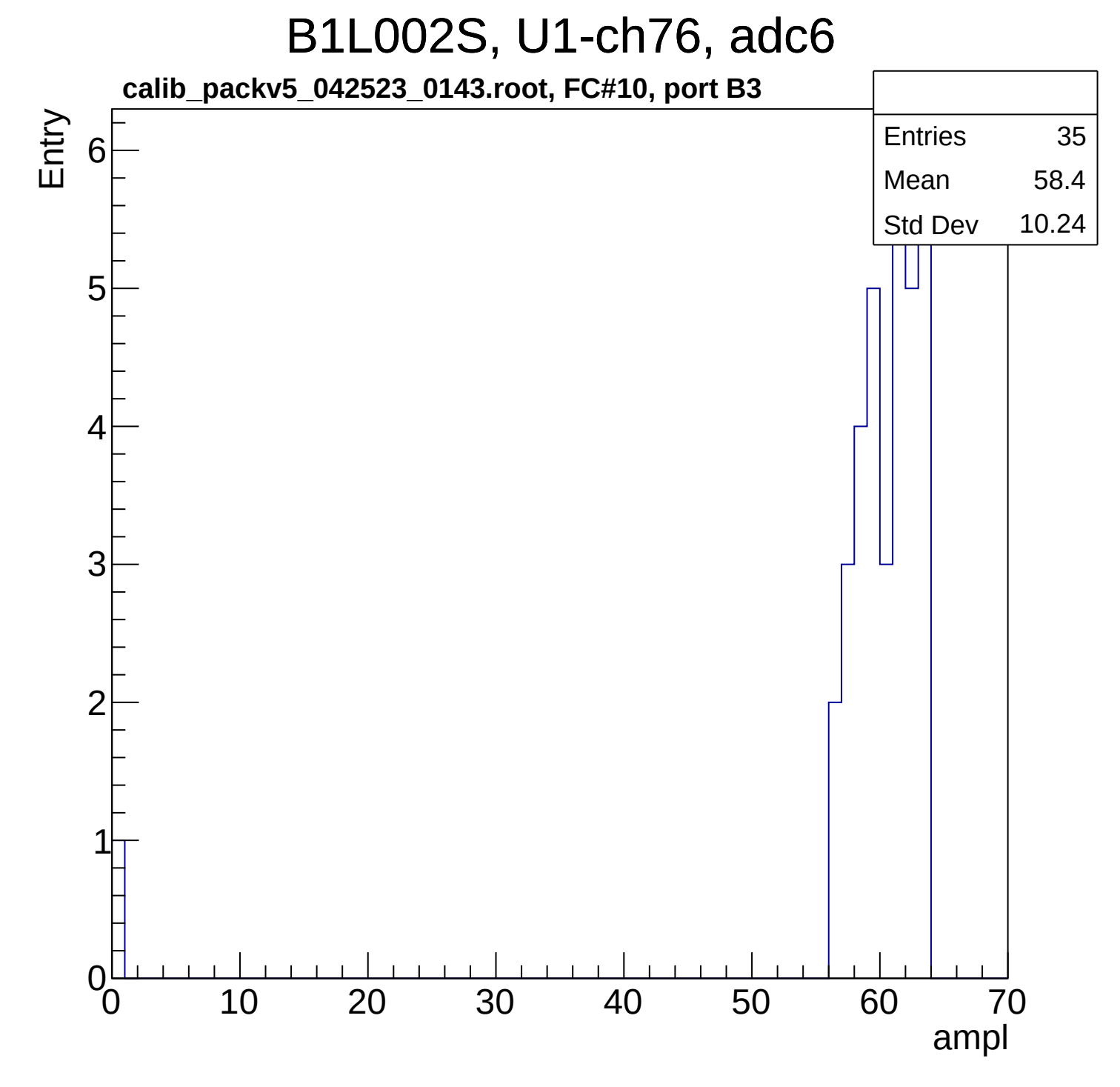
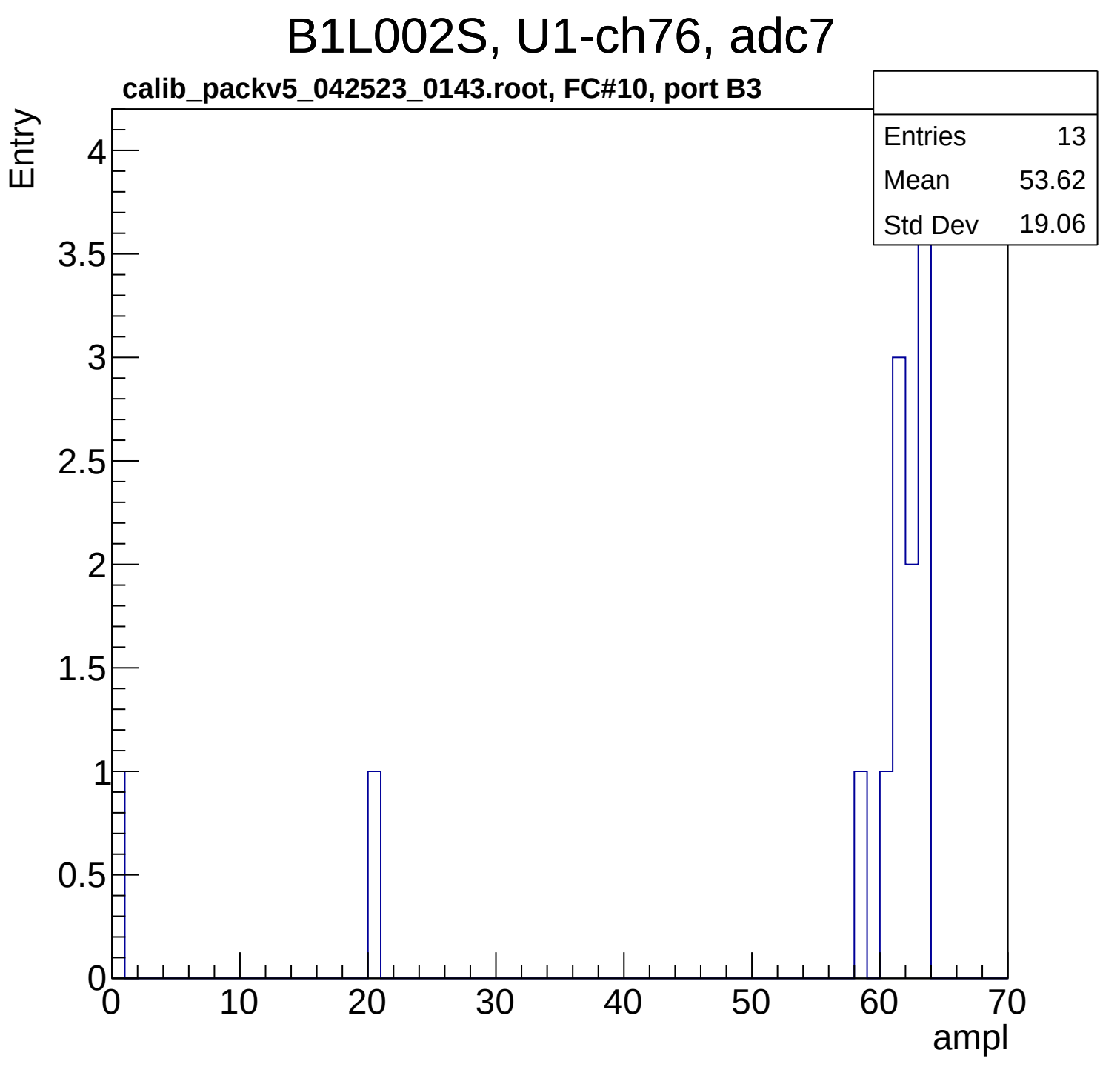

B1L002S, U1-ch76, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 58.4
Std Dev 10.24
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch76, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 53.62
Std Dev 19.06
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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40
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60
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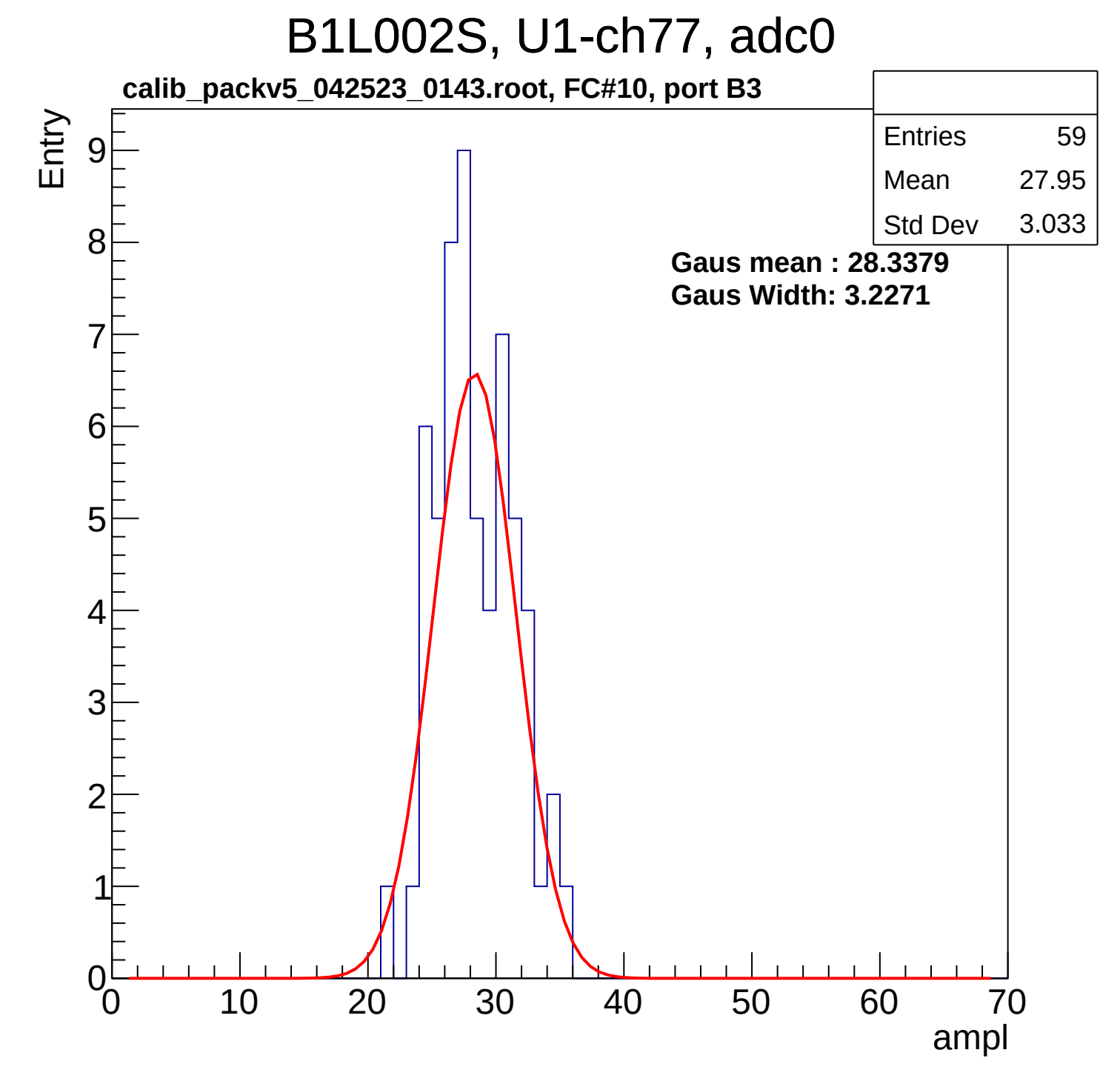
B1L002S, U1-ch77, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.95
Std Dev 3.033
Gaus mean : 28.3379
Gaus Width: 3.2271
Entry
ampl
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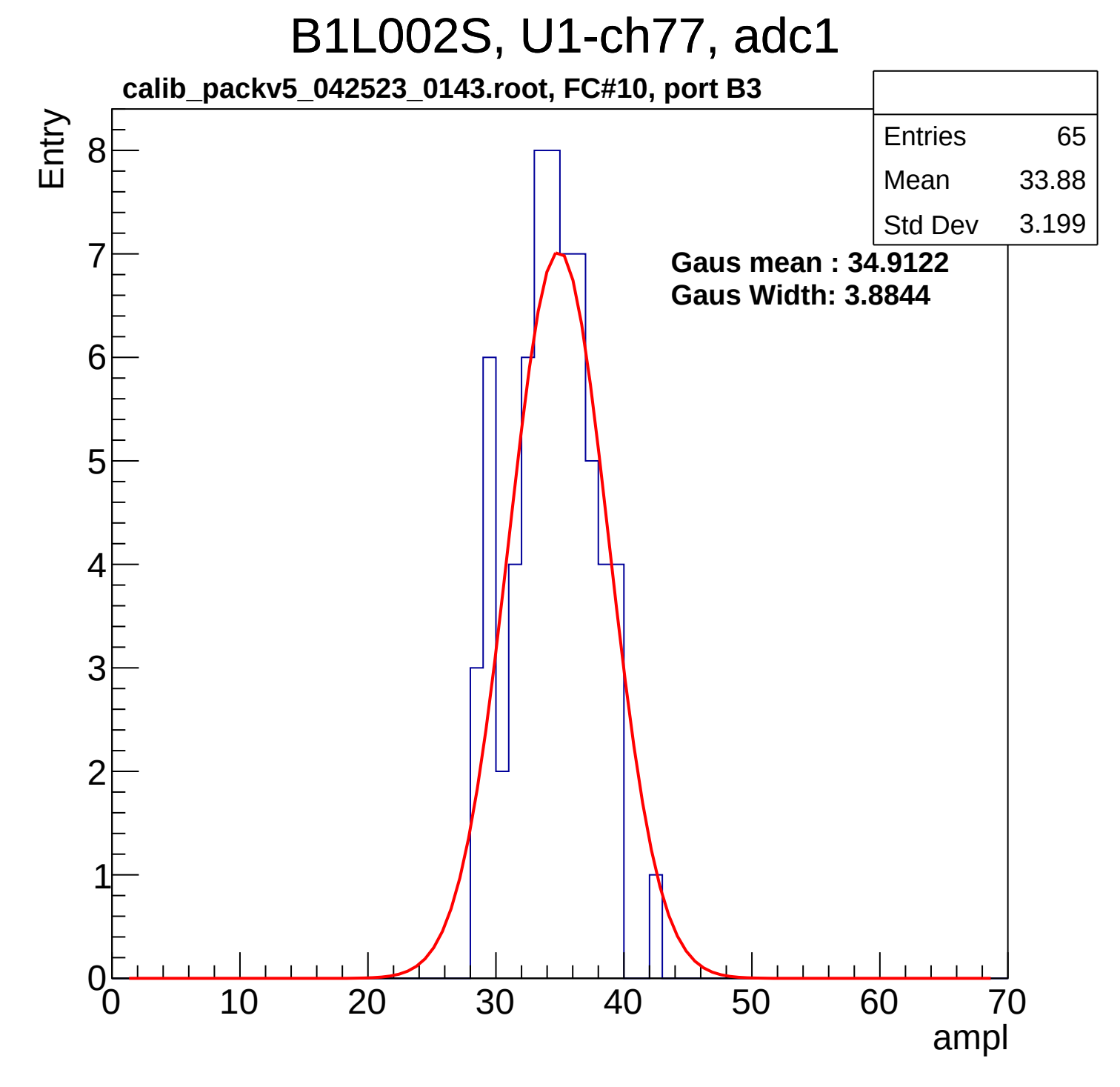
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch77, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.88
Std Dev 3.199
Gaus mean : 34.9122
Gaus Width: 3.8844
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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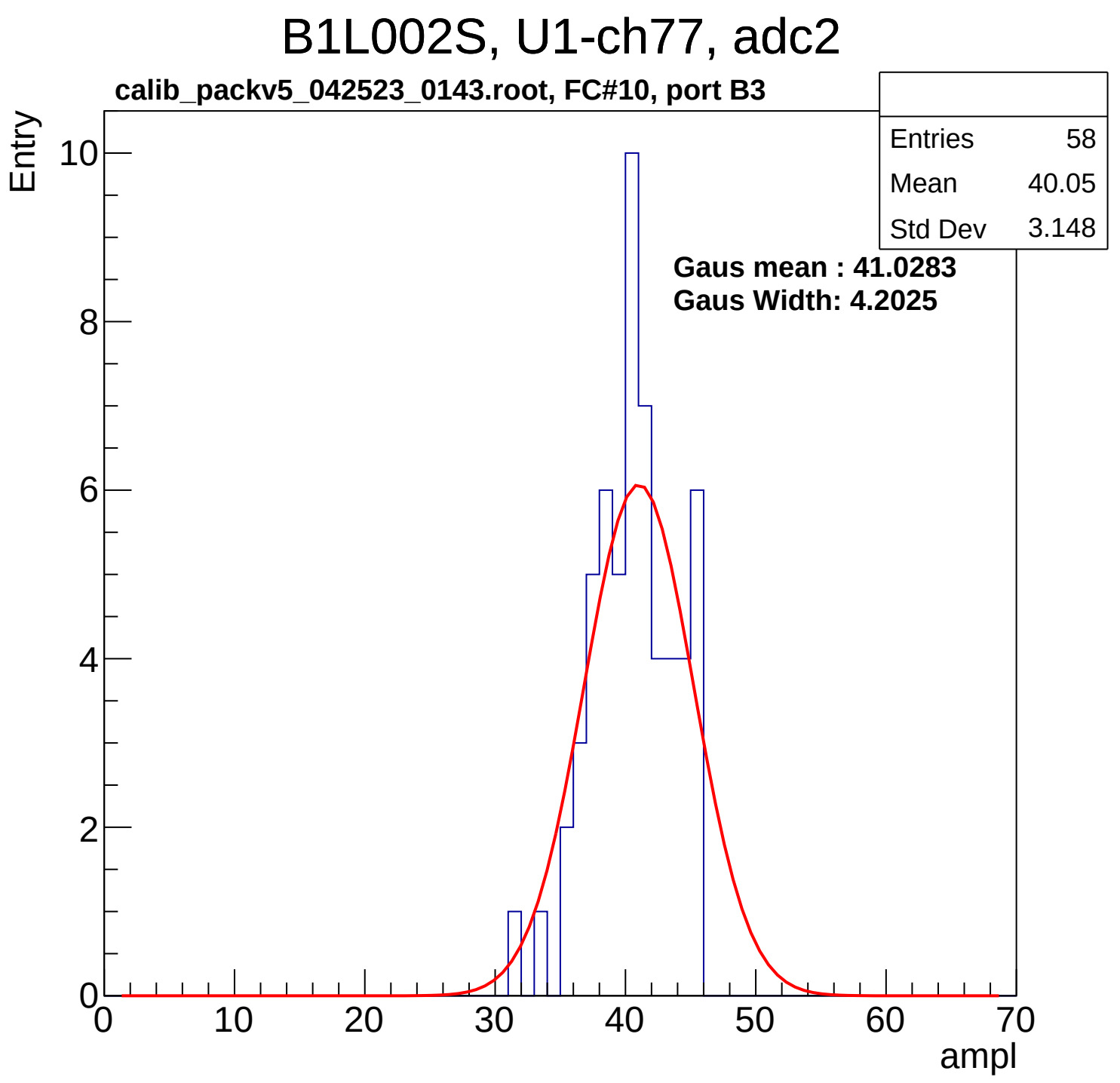
50
60
70


B1L002S, U1-ch77, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.05
Std Dev 3.148
Gaus mean : 41.0283
Gaus Width: 4.2025
Entry
ampl
0
2
4
6
8
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0
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40
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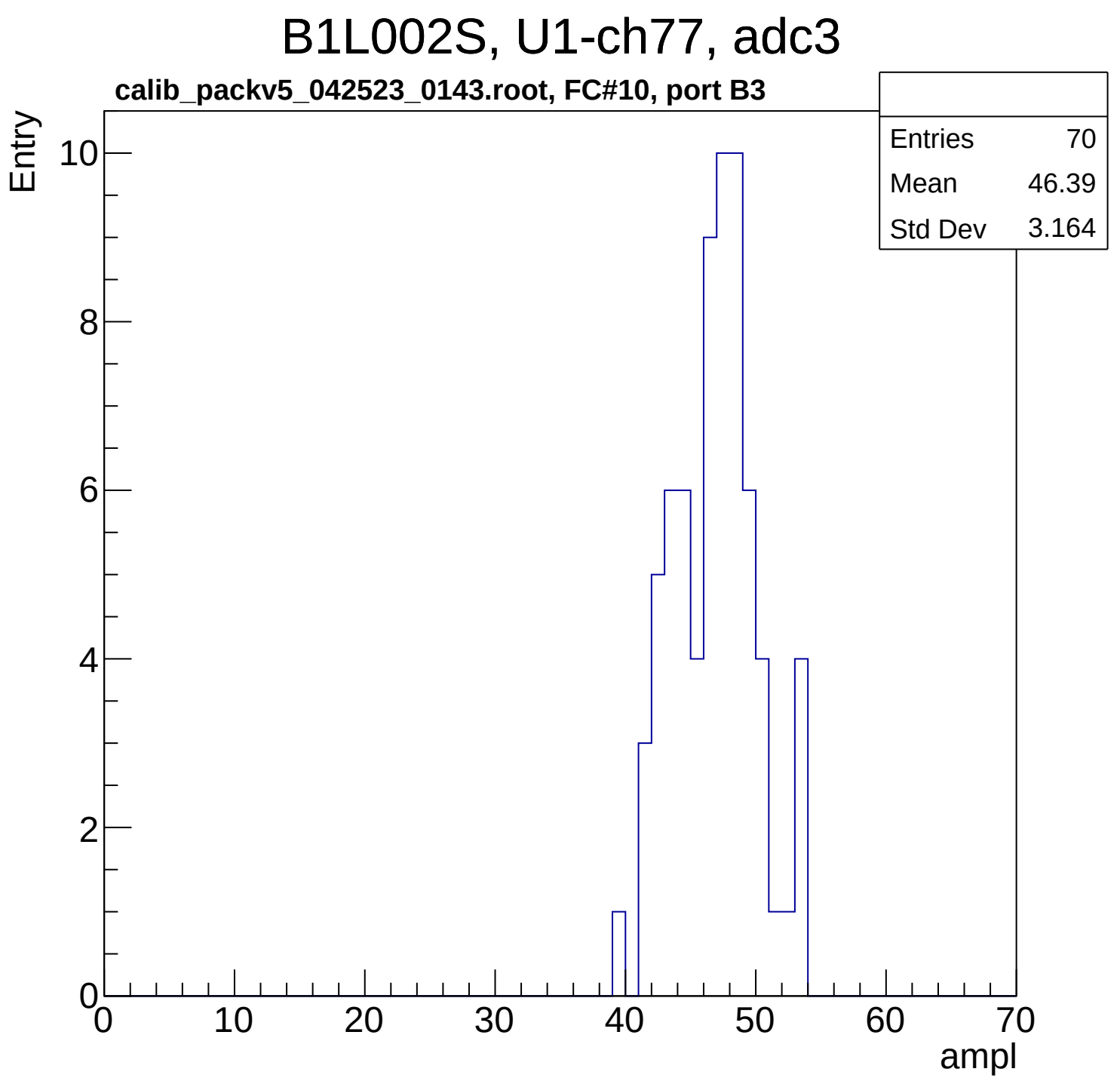
B1L002S, U1-ch77, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.39
Std Dev 3.164
Entry
0
2
4
6
8
10
0
10
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30
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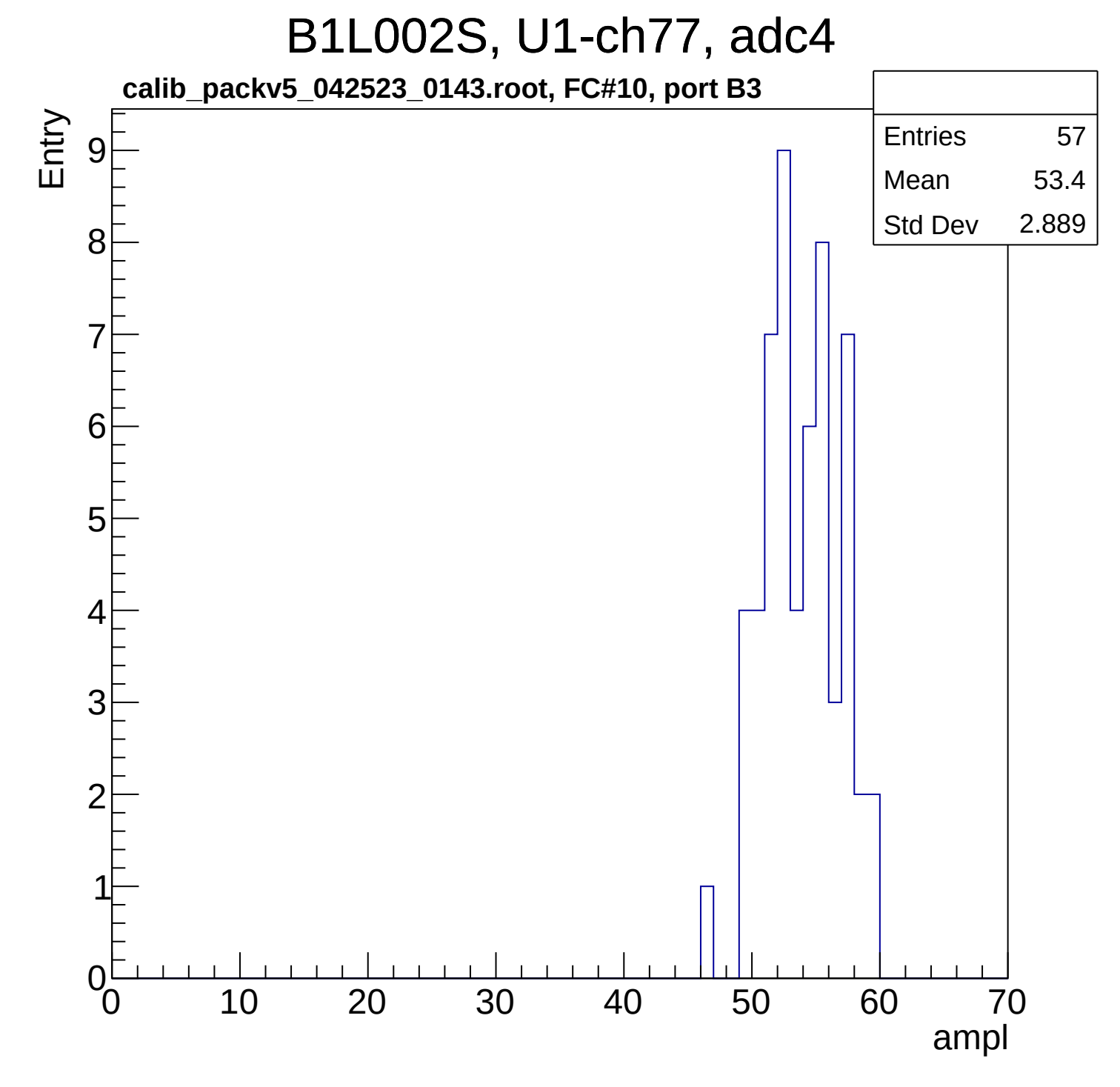
50
60
70
ampl


B1L002S, U1-ch77, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.4
Std Dev 2.889
Entry
ampl
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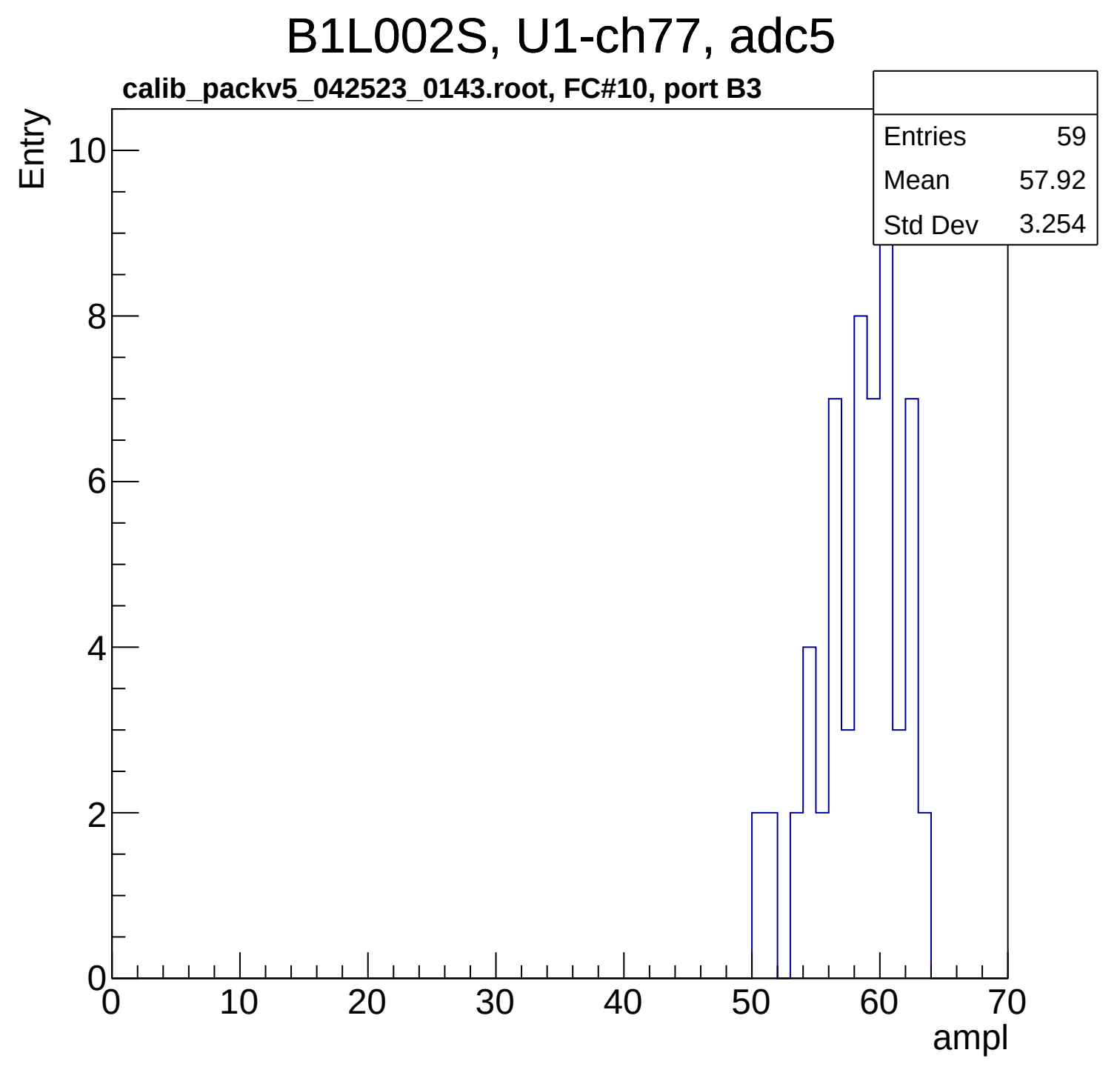
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch77, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.92
Std Dev 3.254
Entry
ampl
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2
4
6
8
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0
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20
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40
50
60
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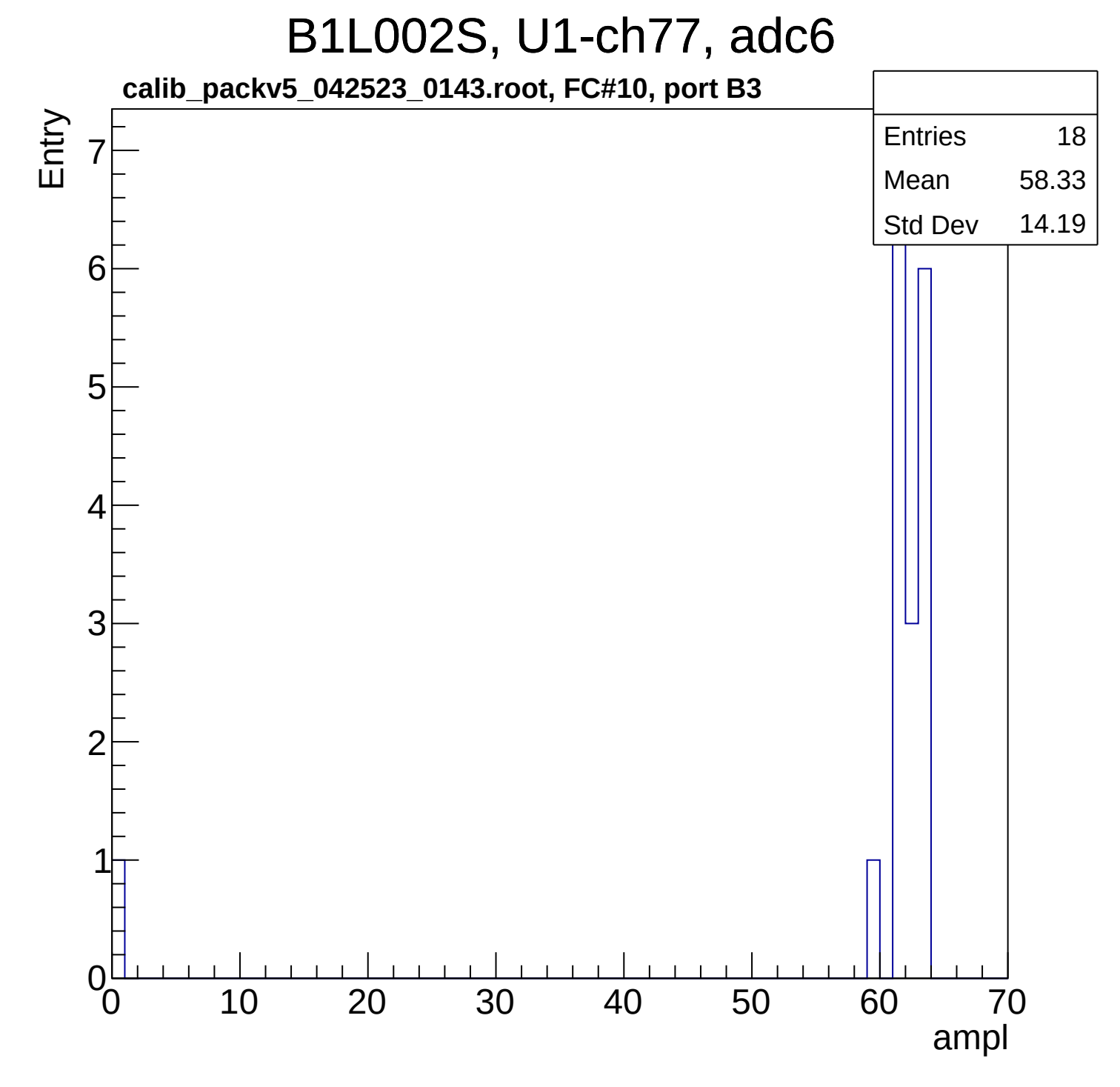
B1L002S, U1-ch77, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.33
Std Dev 14.19
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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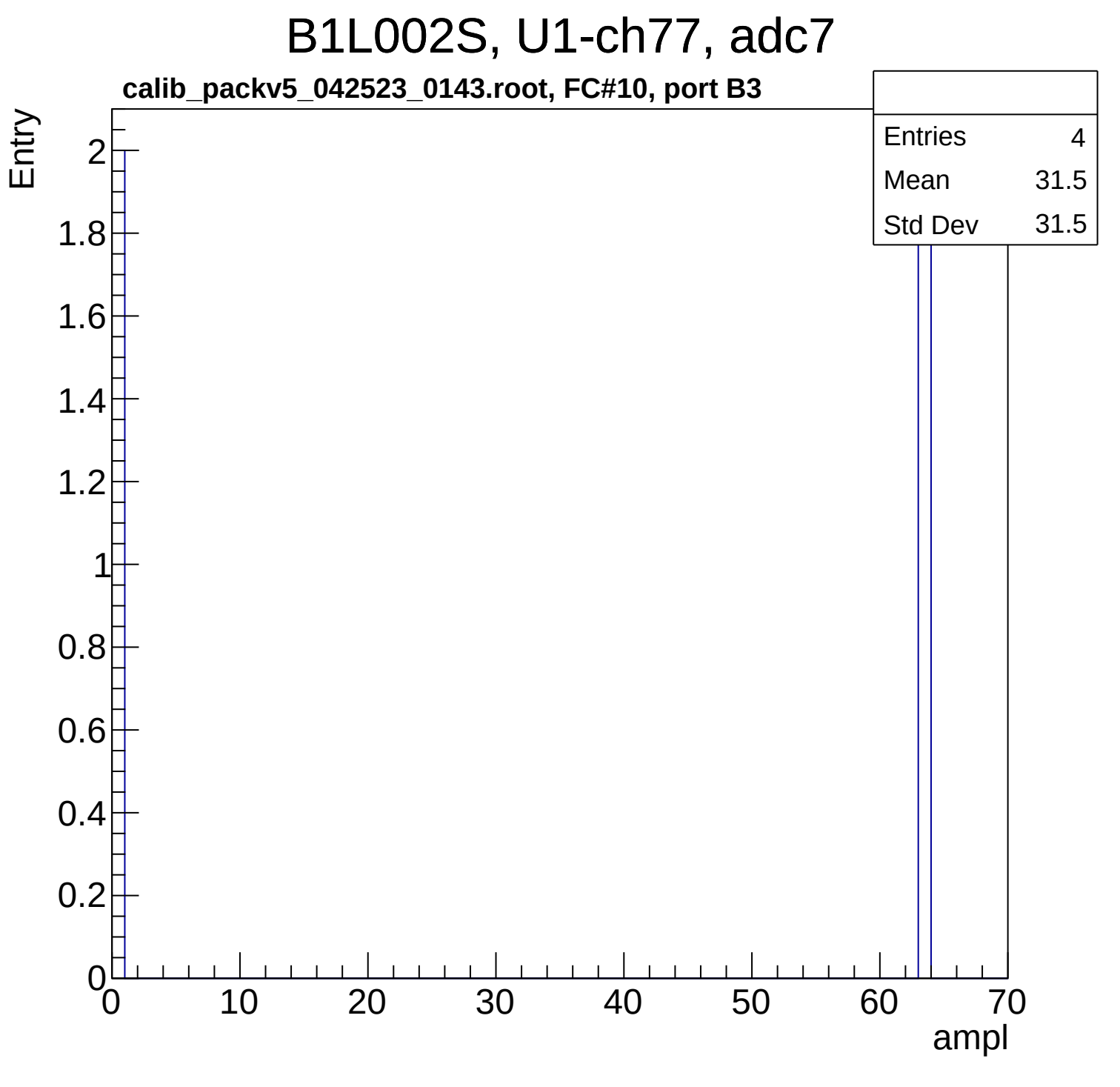
B1L002S, U1-ch77, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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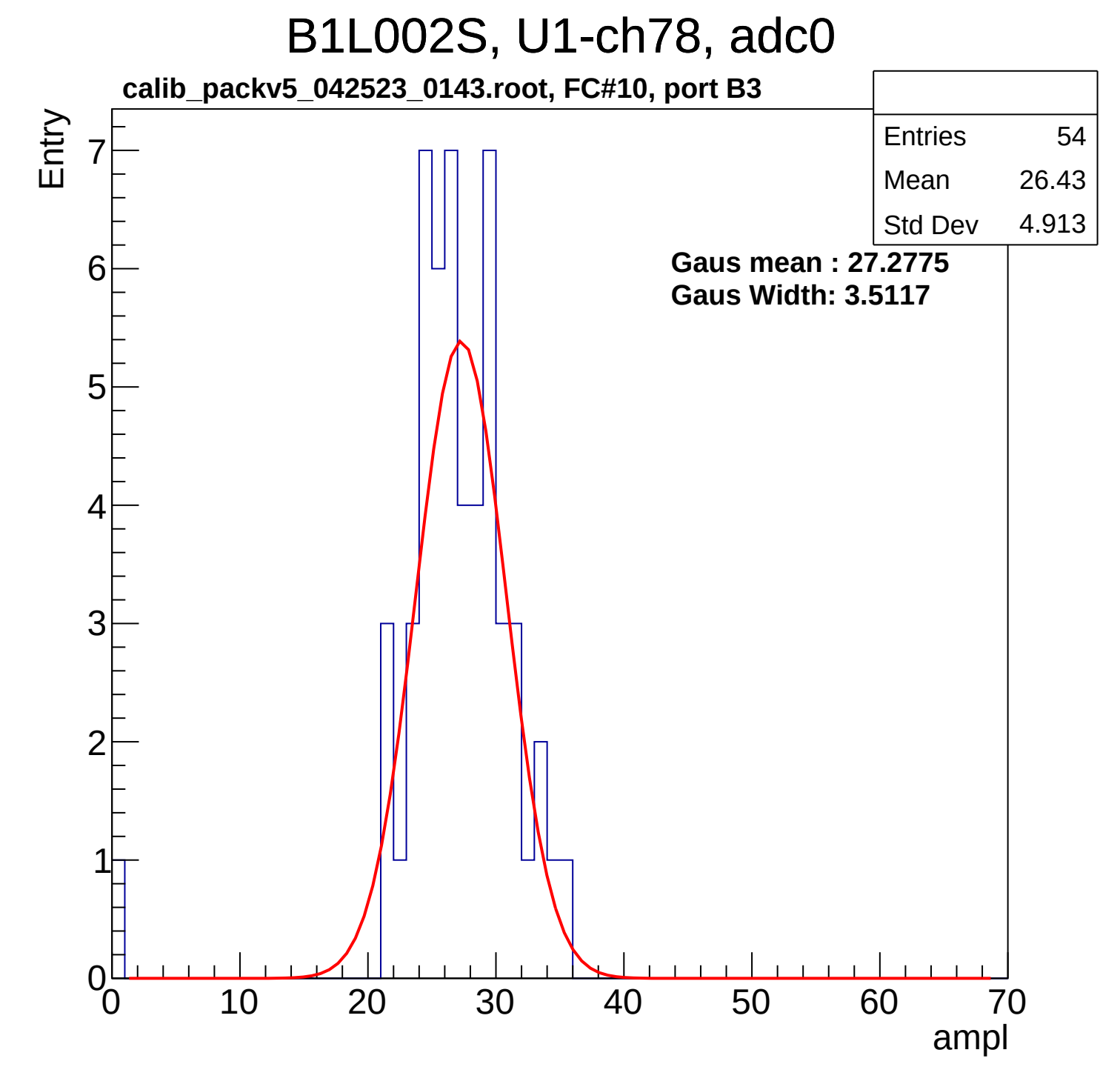
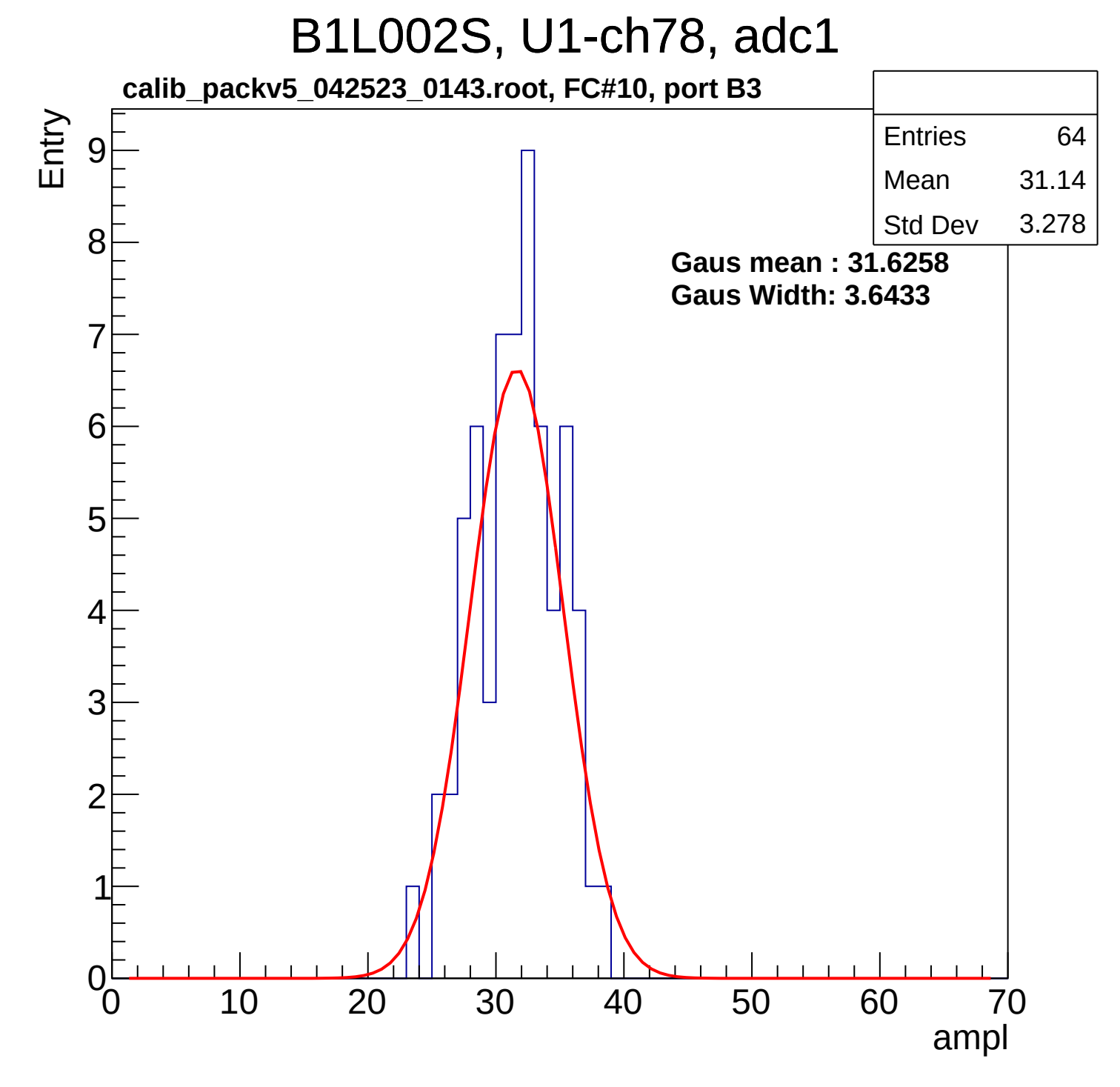

B1L002S, U1-ch78, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 26.43
Std Dev 4.913
Gaus mean : 27.2775
Gaus Width: 3.5117
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch78, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 31.14
Std Dev 3.278
Gaus mean : 31.6258
Gaus Width: 3.6433
Entry
ampl
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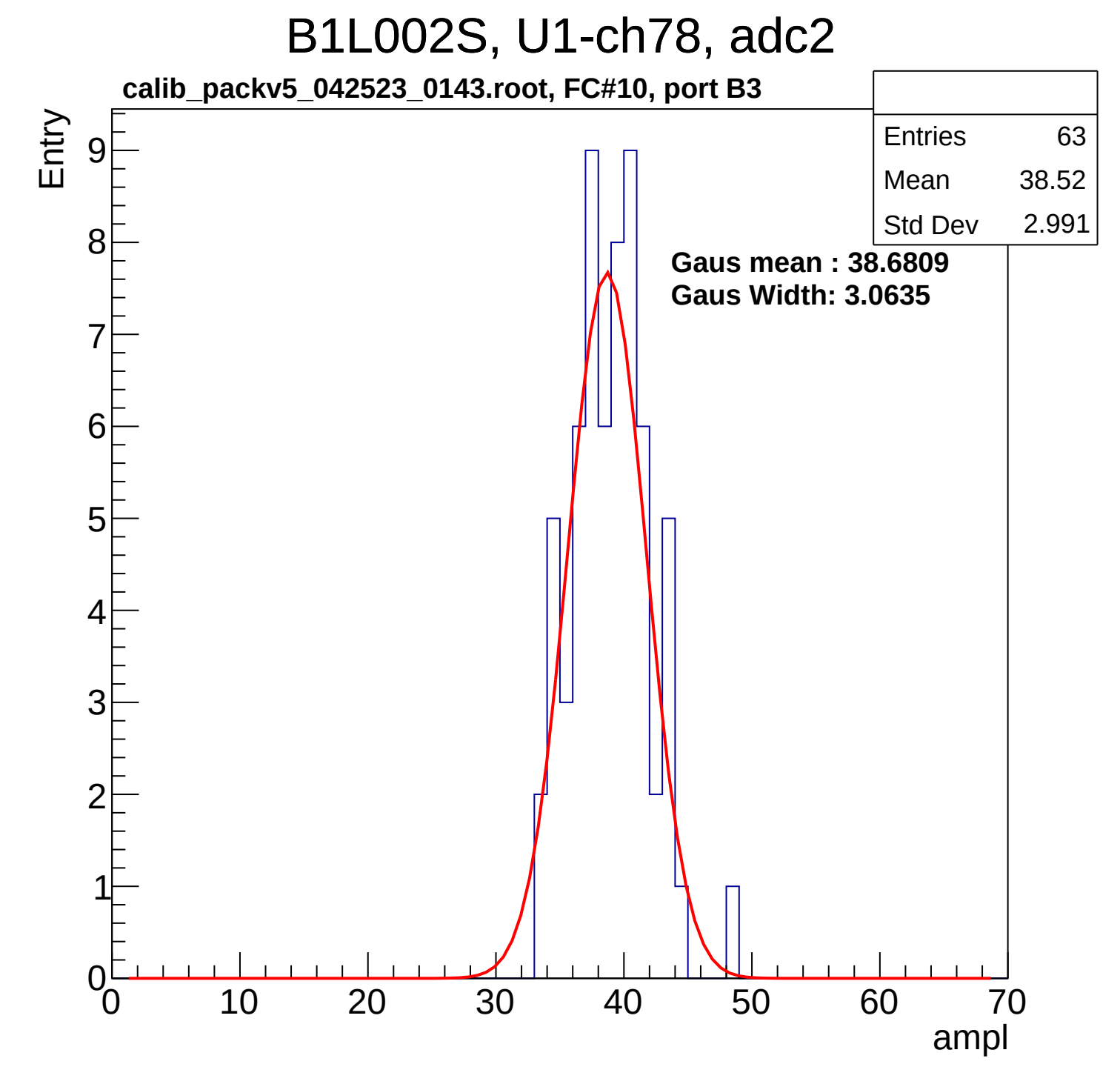
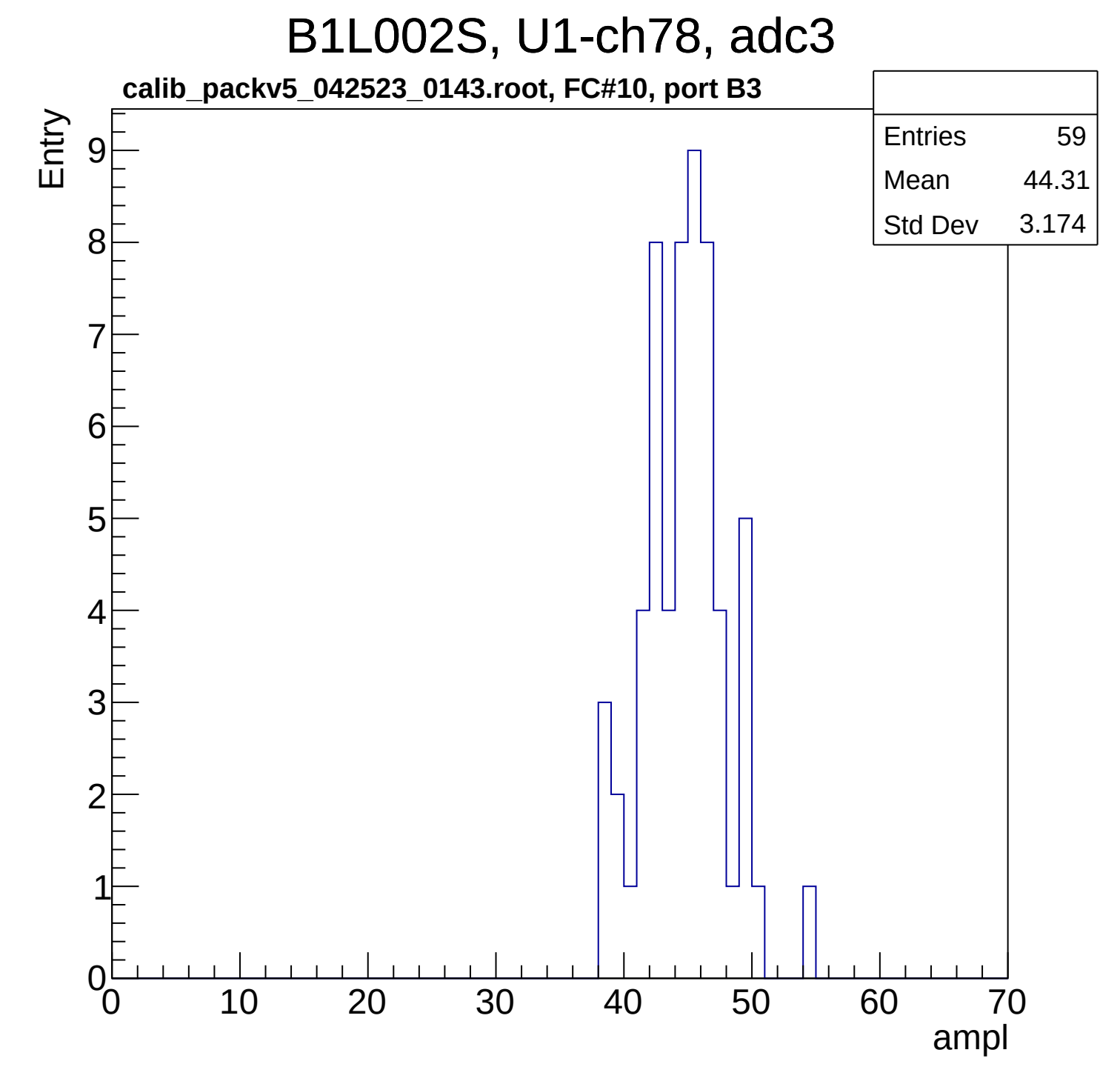
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch78, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 38.52
Std Dev 2.991
Gaus mean : 38.6809
Gaus Width: 3.0635
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch78, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 44.31
Std Dev 3.174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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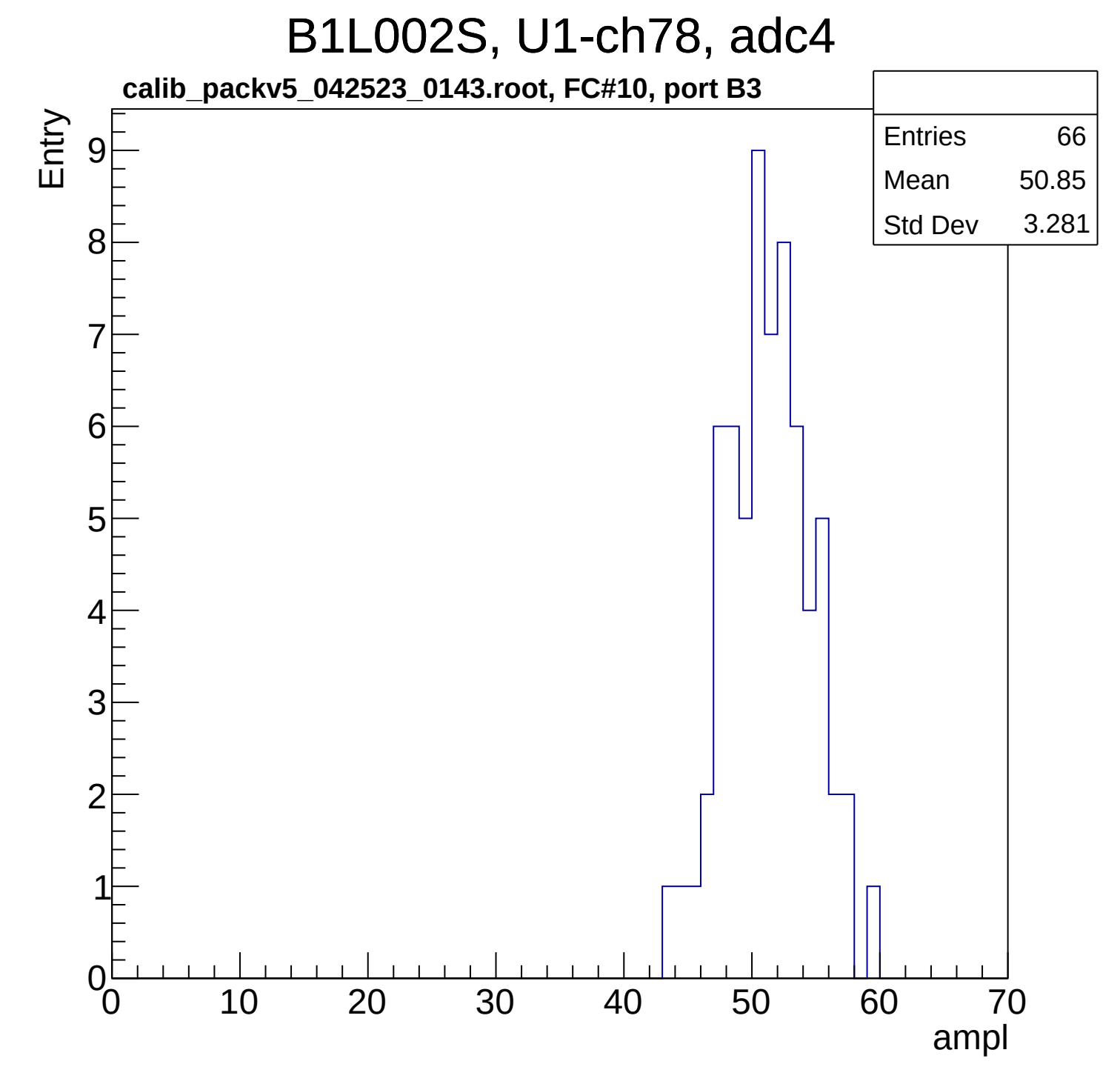
B1L002S, U1-ch78, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 50.85
Std Dev 3.281
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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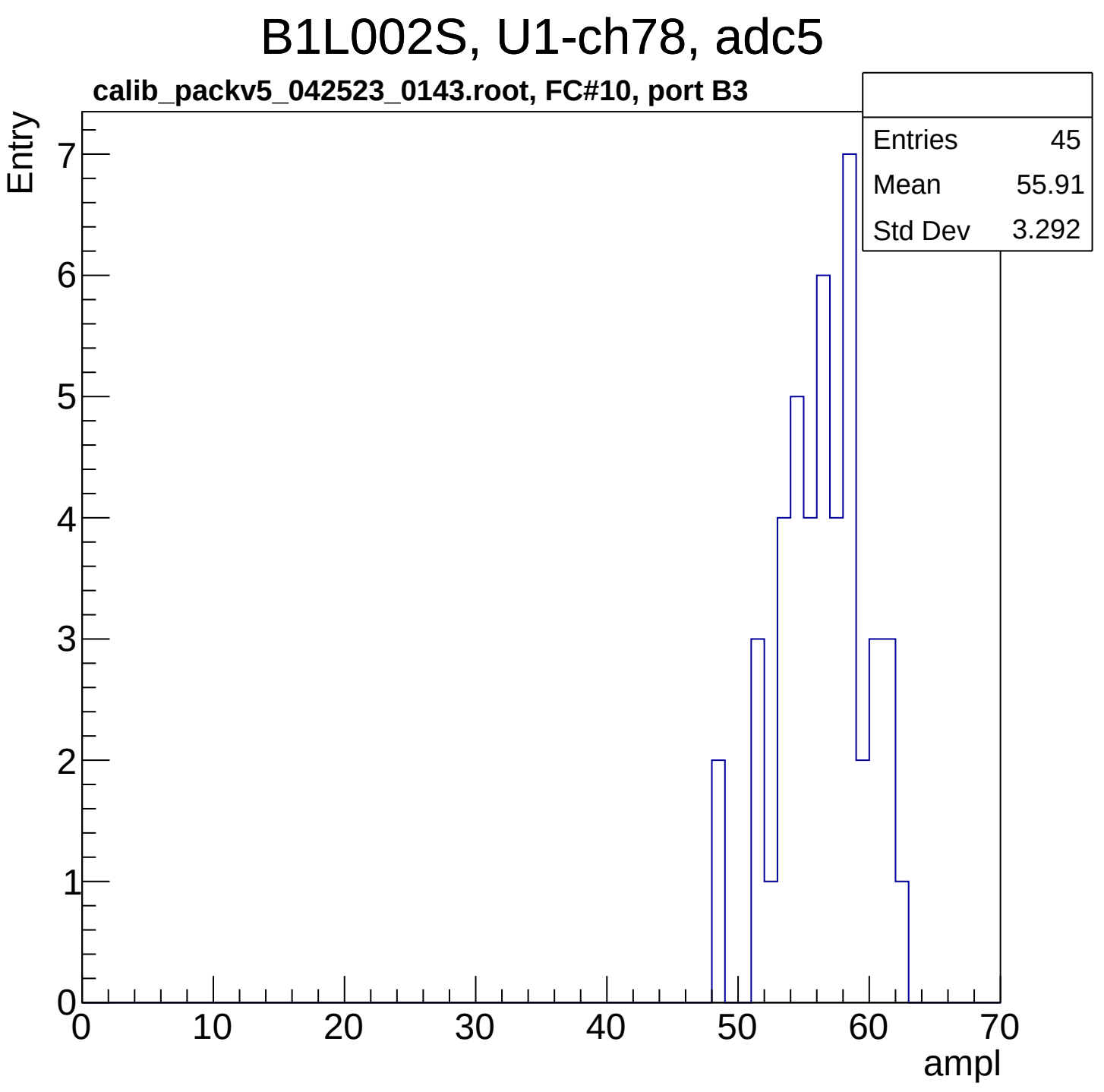
B1L002S, U1-ch78, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 55.91
Std Dev 3.292
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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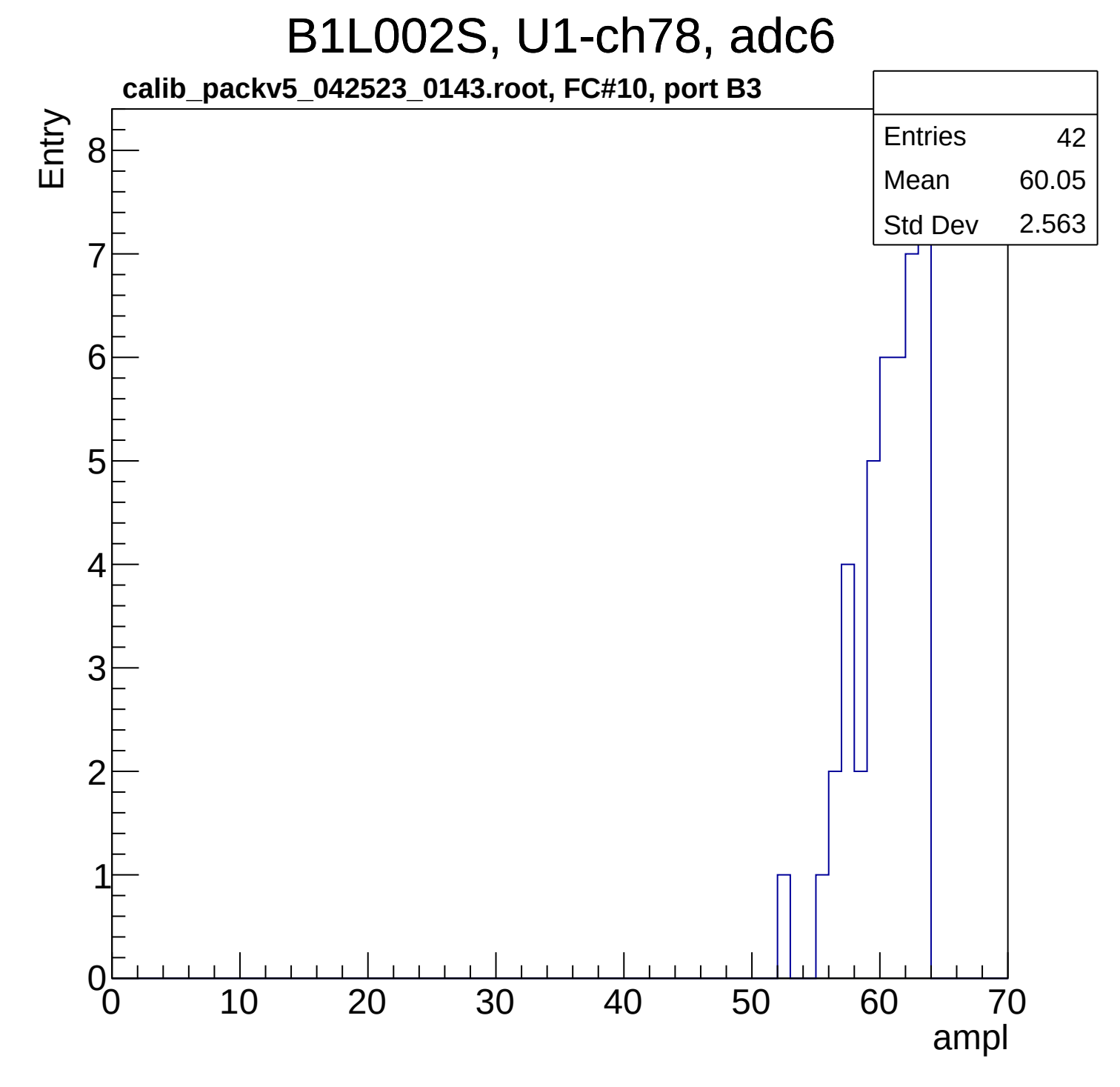
B1L002S, U1-ch78, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 60.05
Std Dev 2.563
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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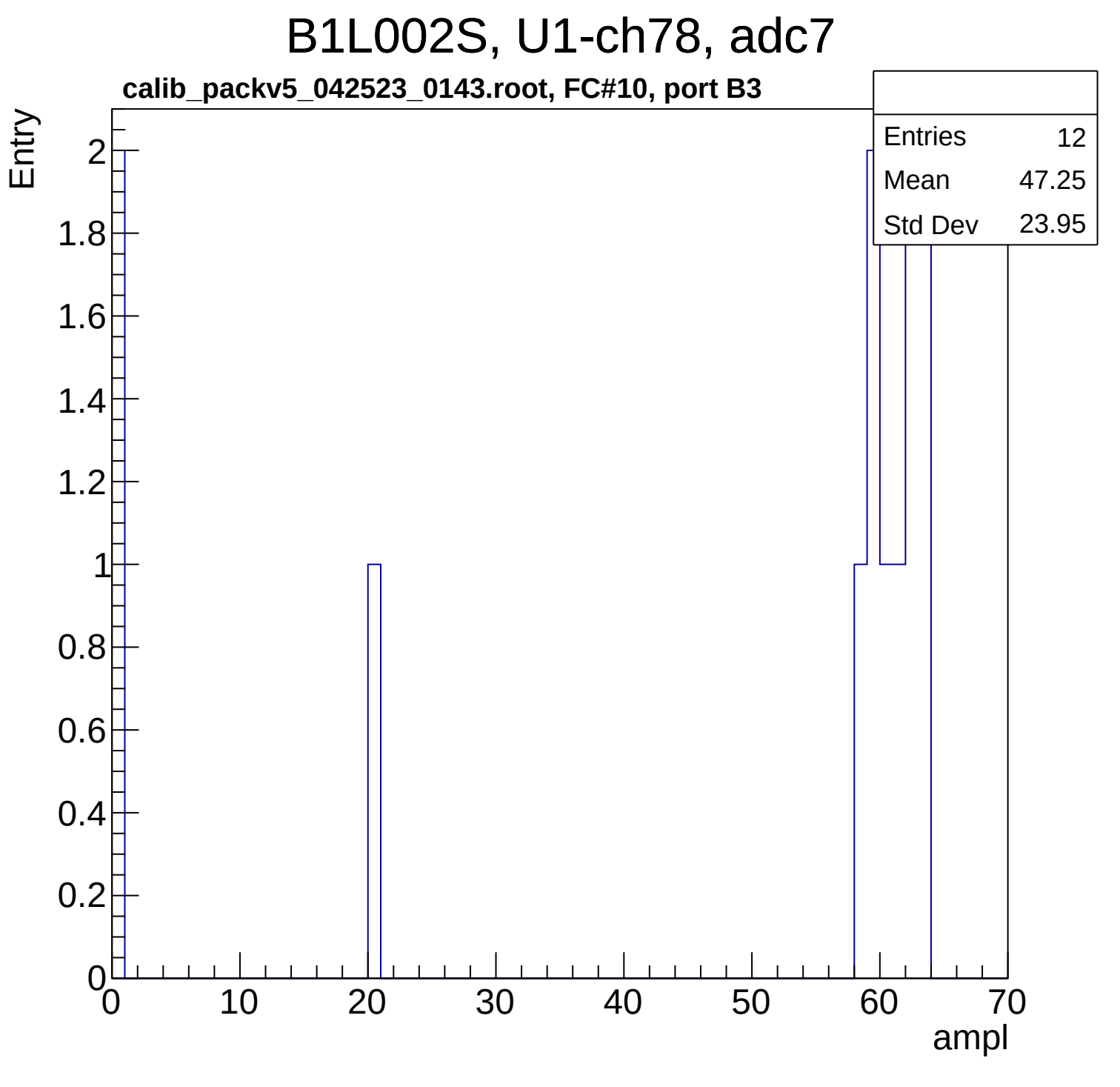
B1L002S, U1-ch78, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 47.25
Std Dev 23.95
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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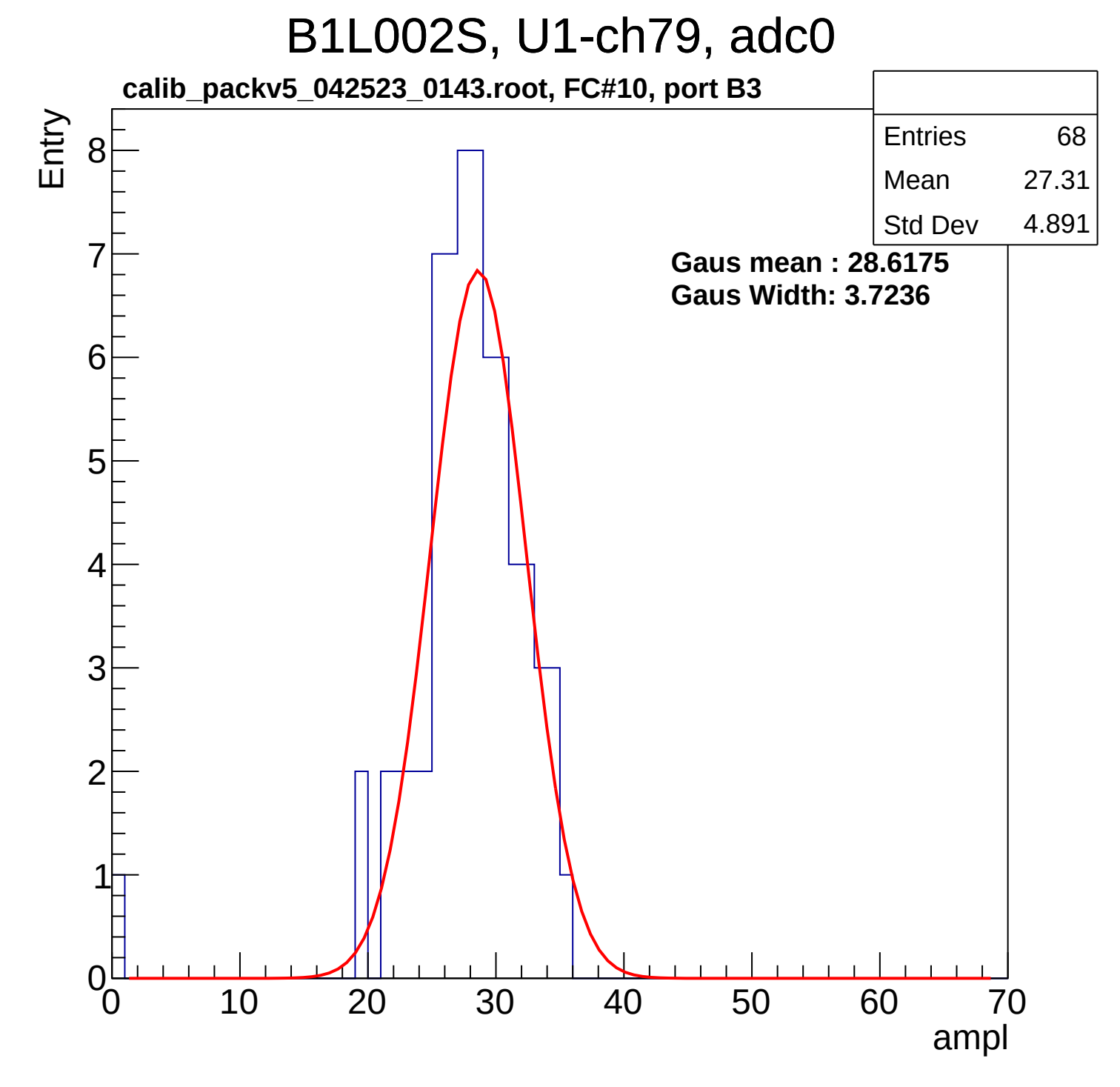
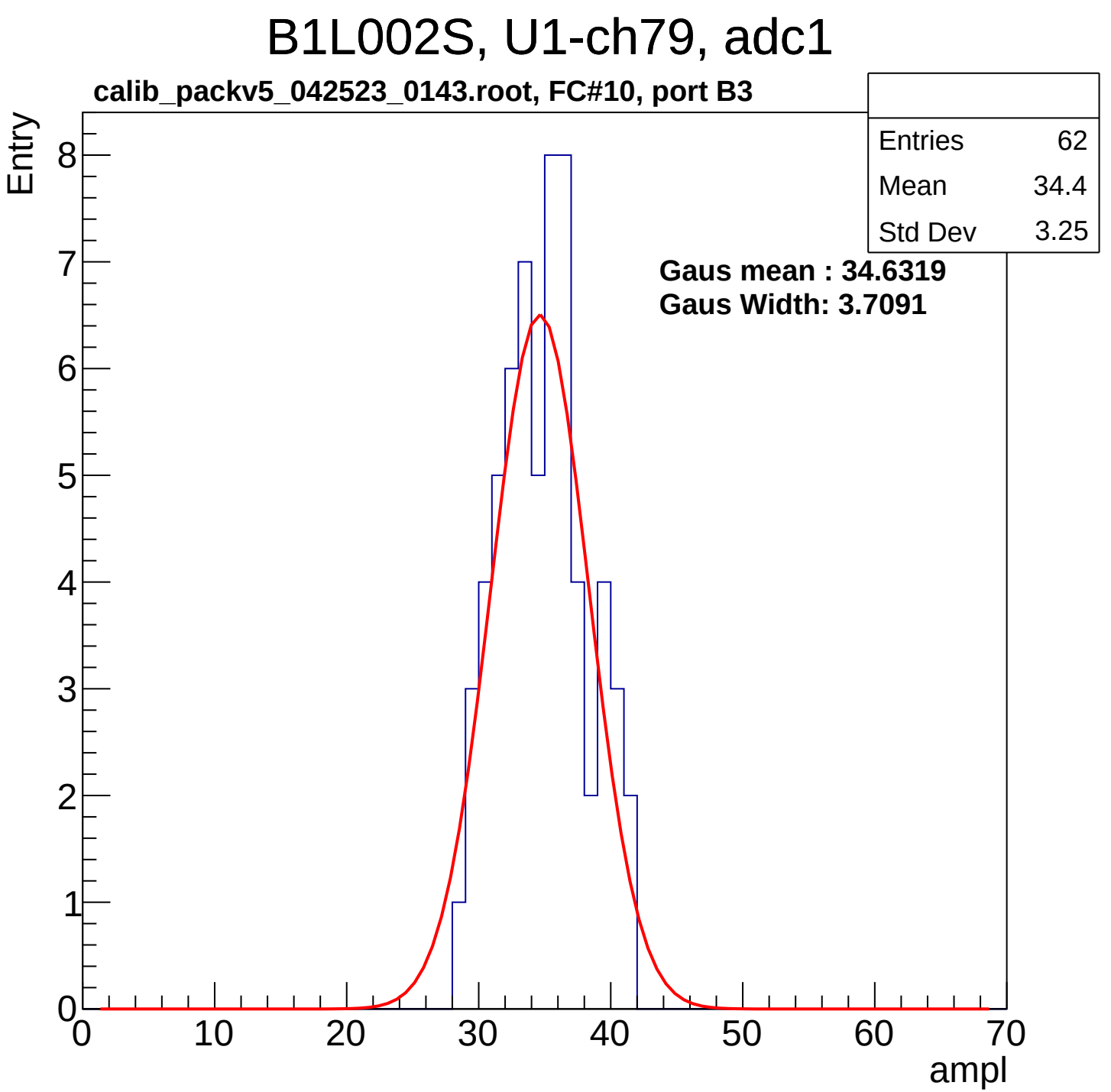
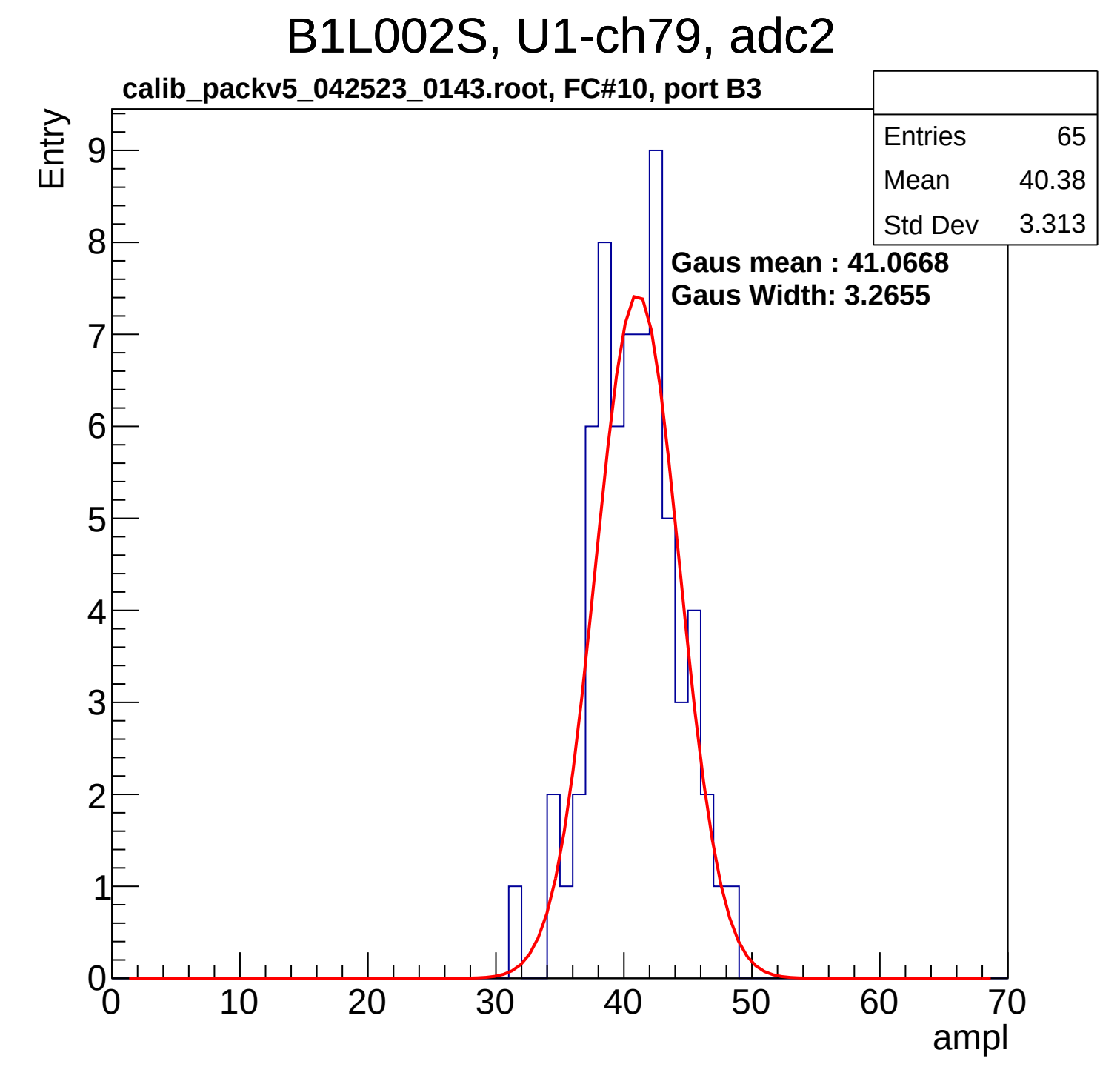
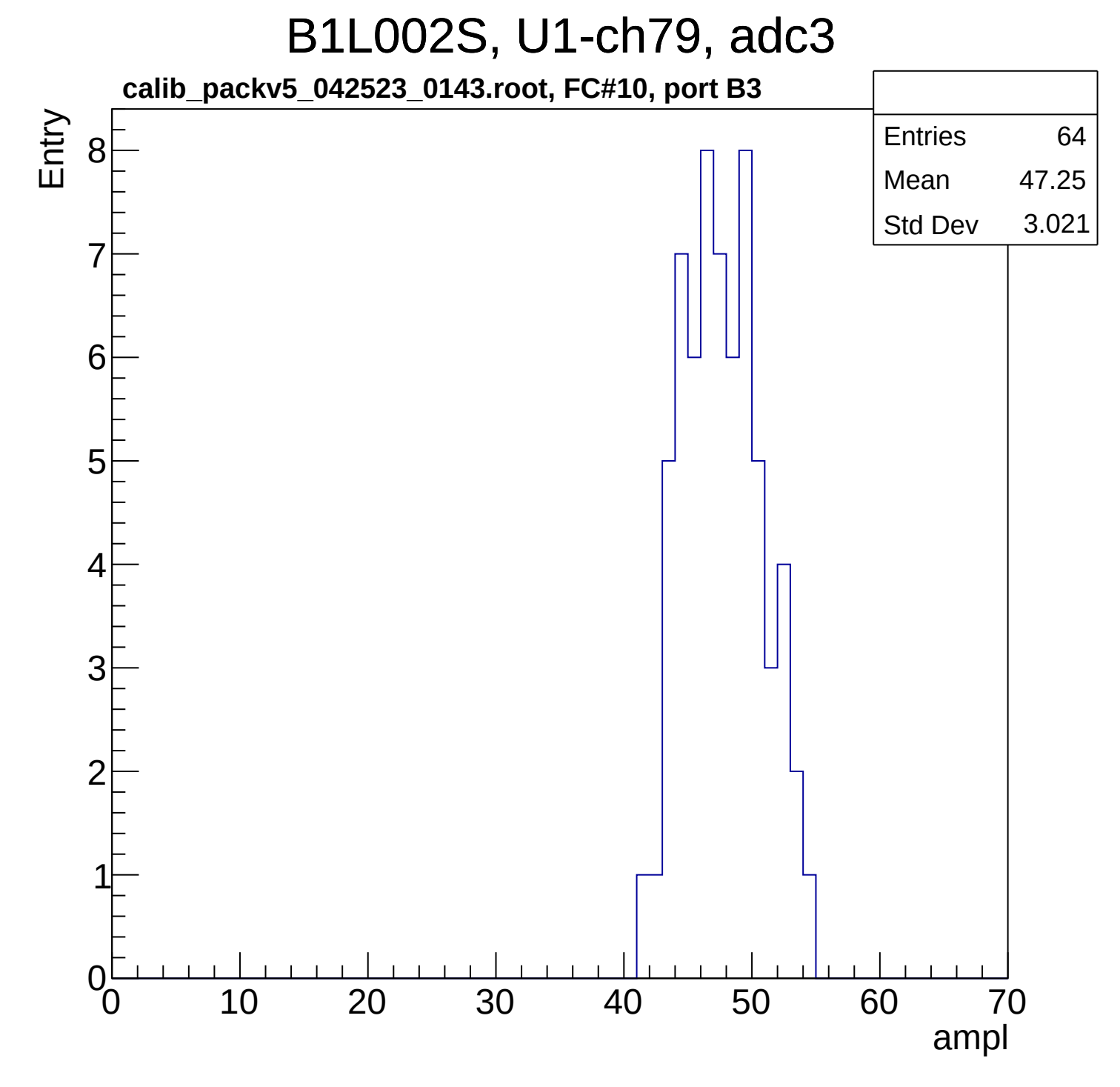

B1L002S, U1-ch79, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.31
Std Dev 4.891
Gaus mean : 28.6175
Gaus Width: 3.7236
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch79, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 34.4
Std Dev 3.25
Gaus mean : 34.6319
Gaus Width: 3.7091
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch79, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.38
Std Dev 3.313
Gaus mean : 41.0668
Gaus Width: 3.2655
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch79, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.25
Std Dev 3.021
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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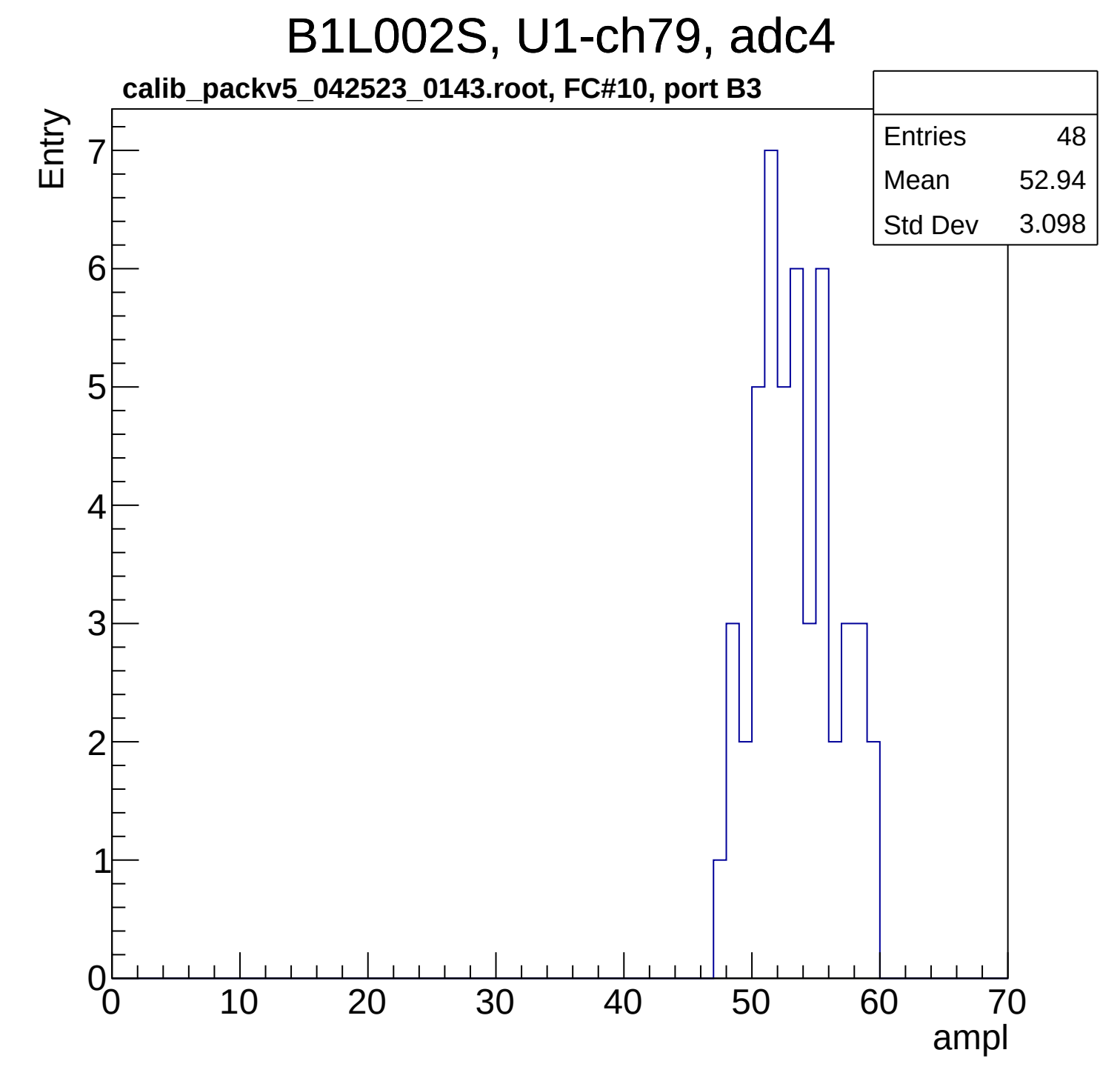
40
50
60
70


B1L002S, U1-ch79, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.94
Std Dev 3.098
Entry
ampl
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1
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3
4
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6
7
0
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20
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40
50
60
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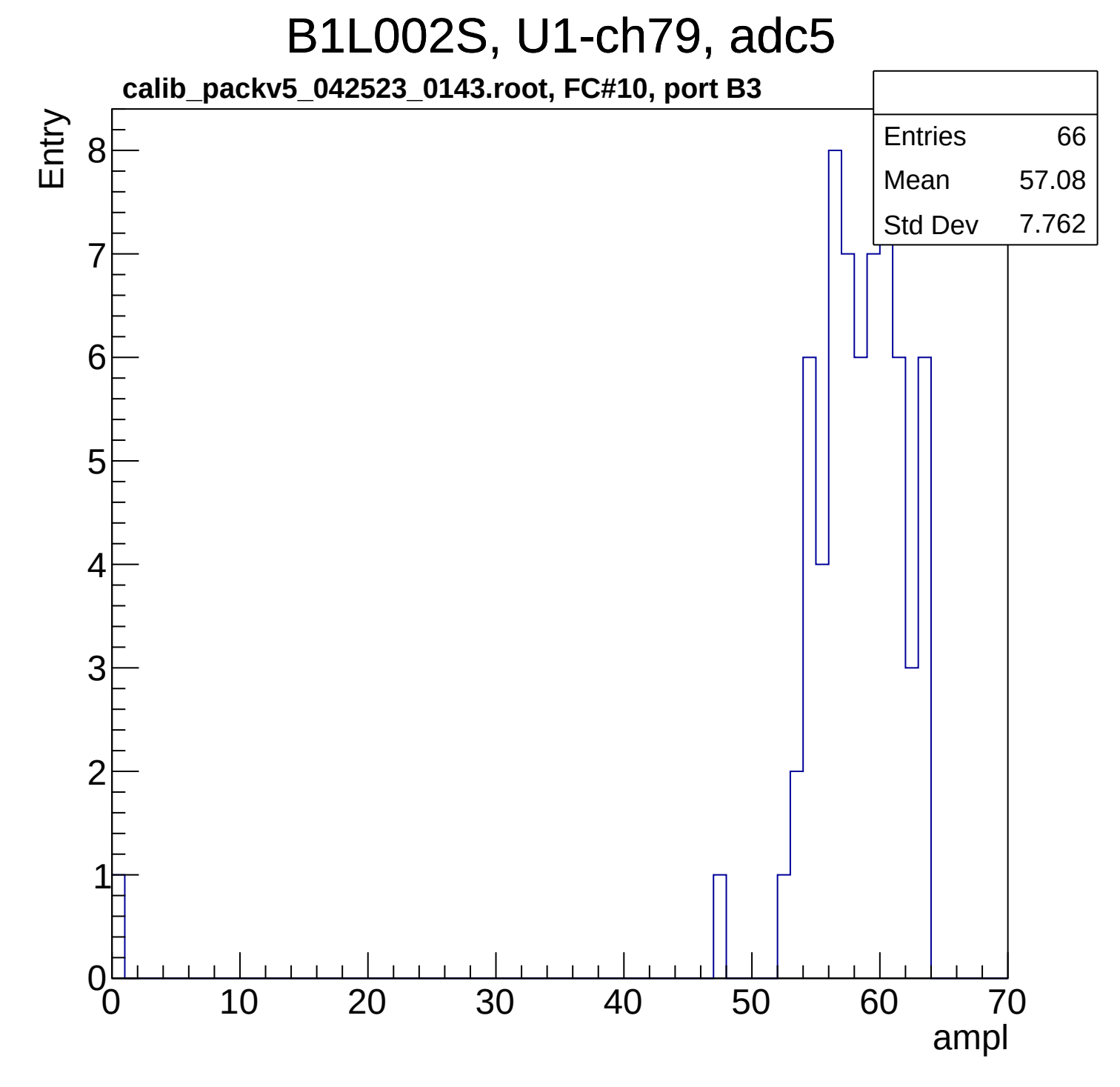
B1L002S, U1-ch79, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.08
Std Dev 7.762
Entry
ampl
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7
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0
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40
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60
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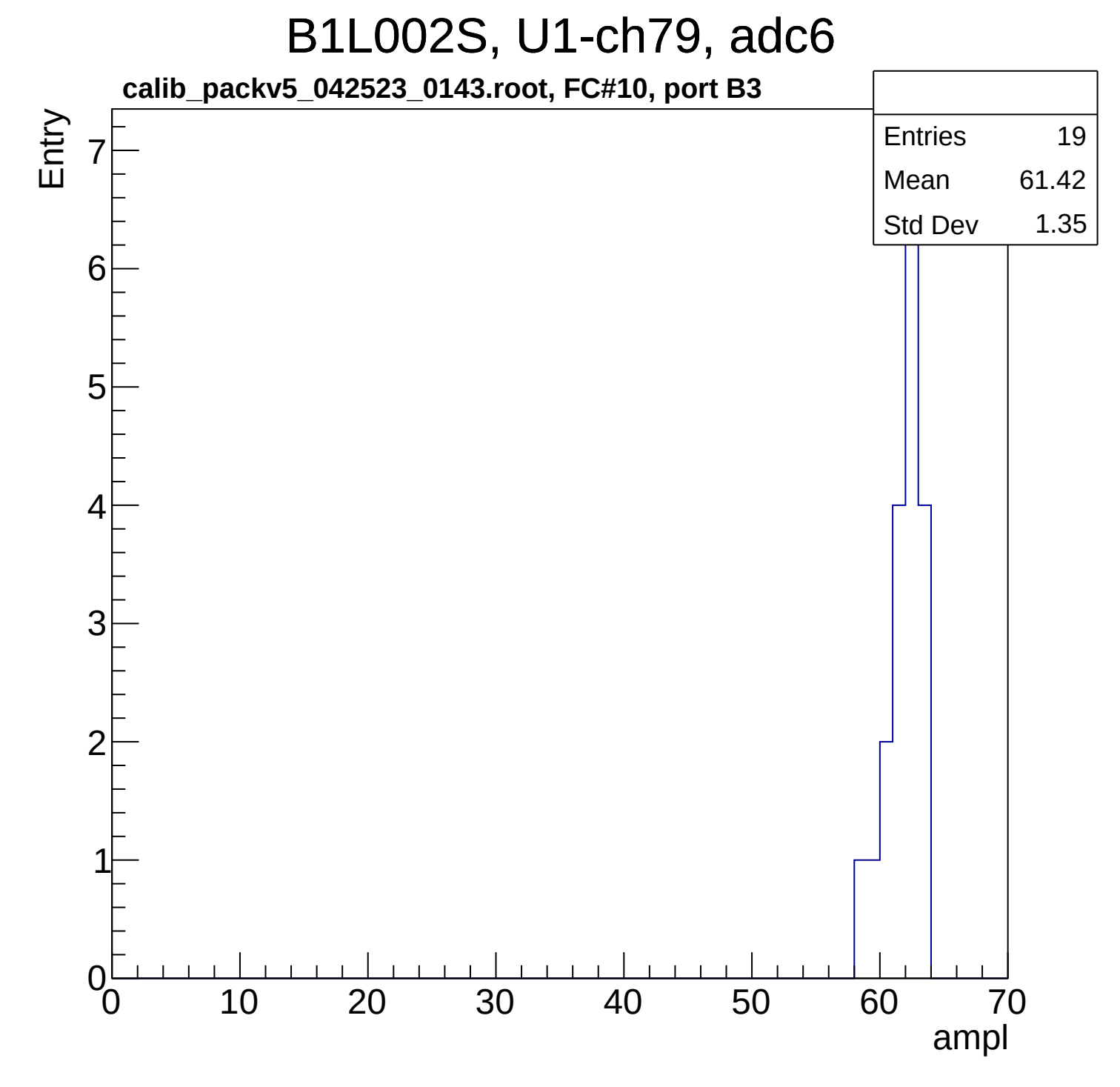
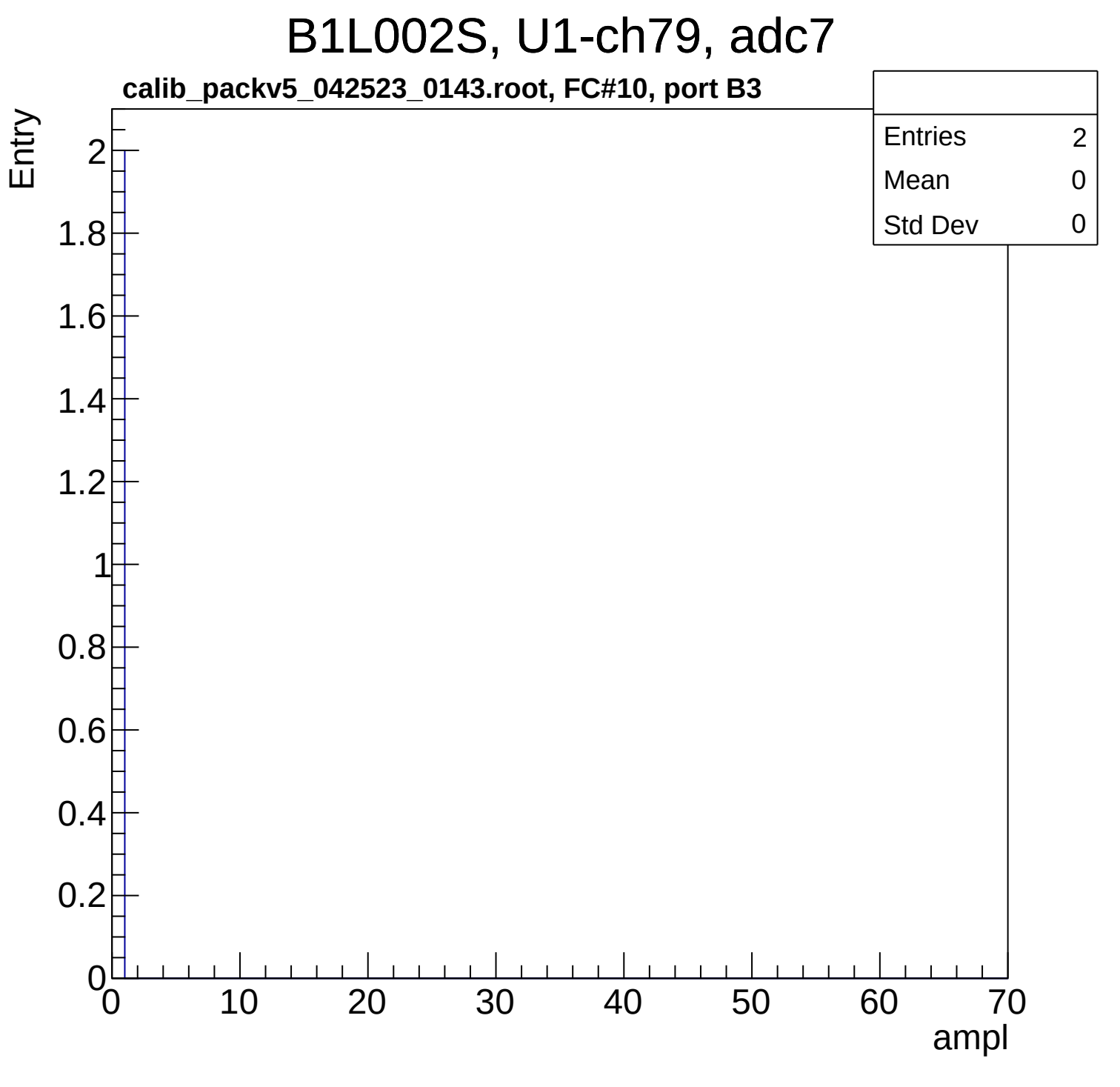

B1L002S, U1-ch79, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.42
Std Dev 1.35
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch79, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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20
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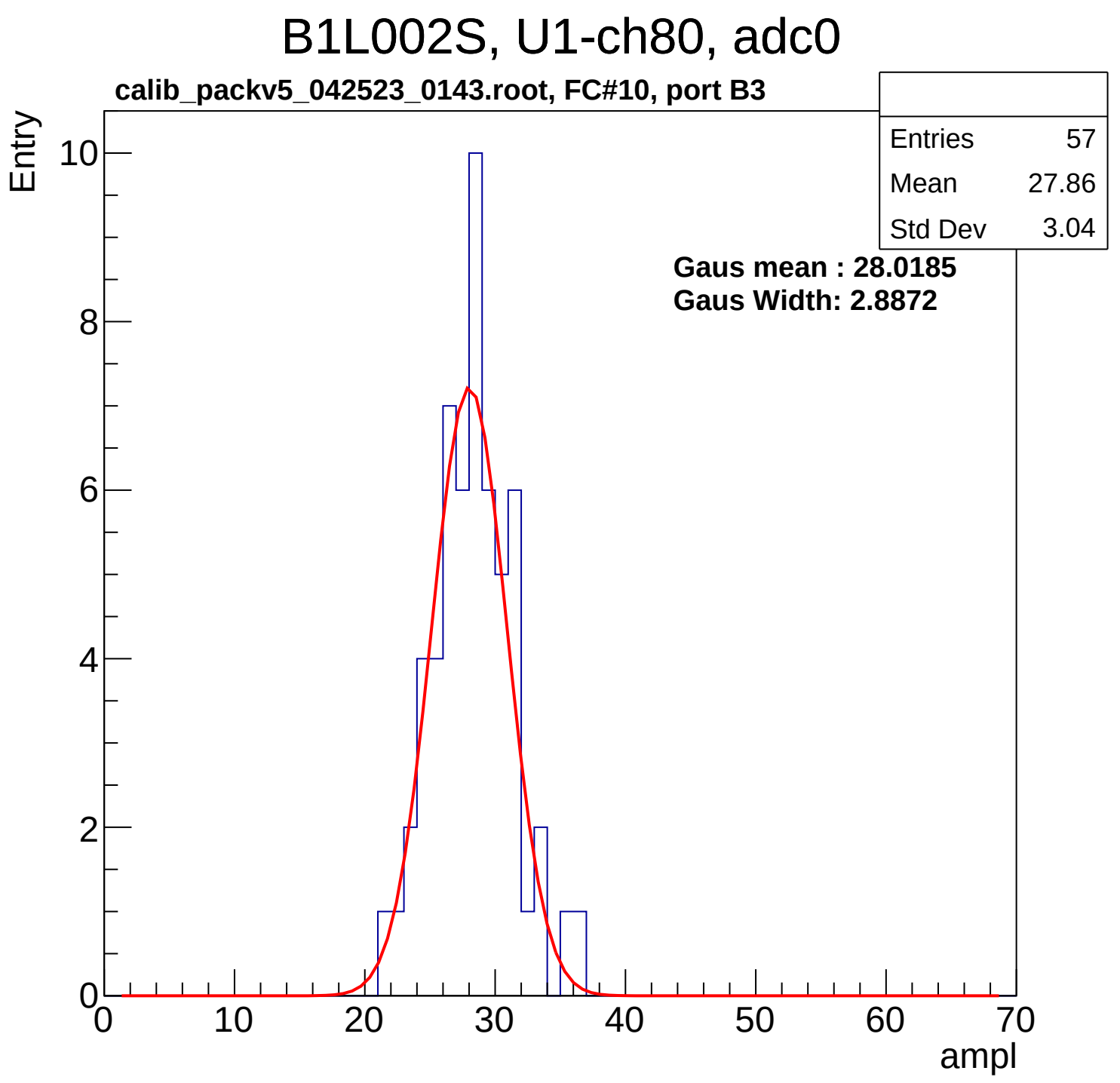
40
50
60
70


B1L002S, U1-ch80, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.86
Std Dev 3.04
Gaus mean : 28.0185
Gaus Width: 2.8872
Entry
ampl
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2
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6
8
10
0
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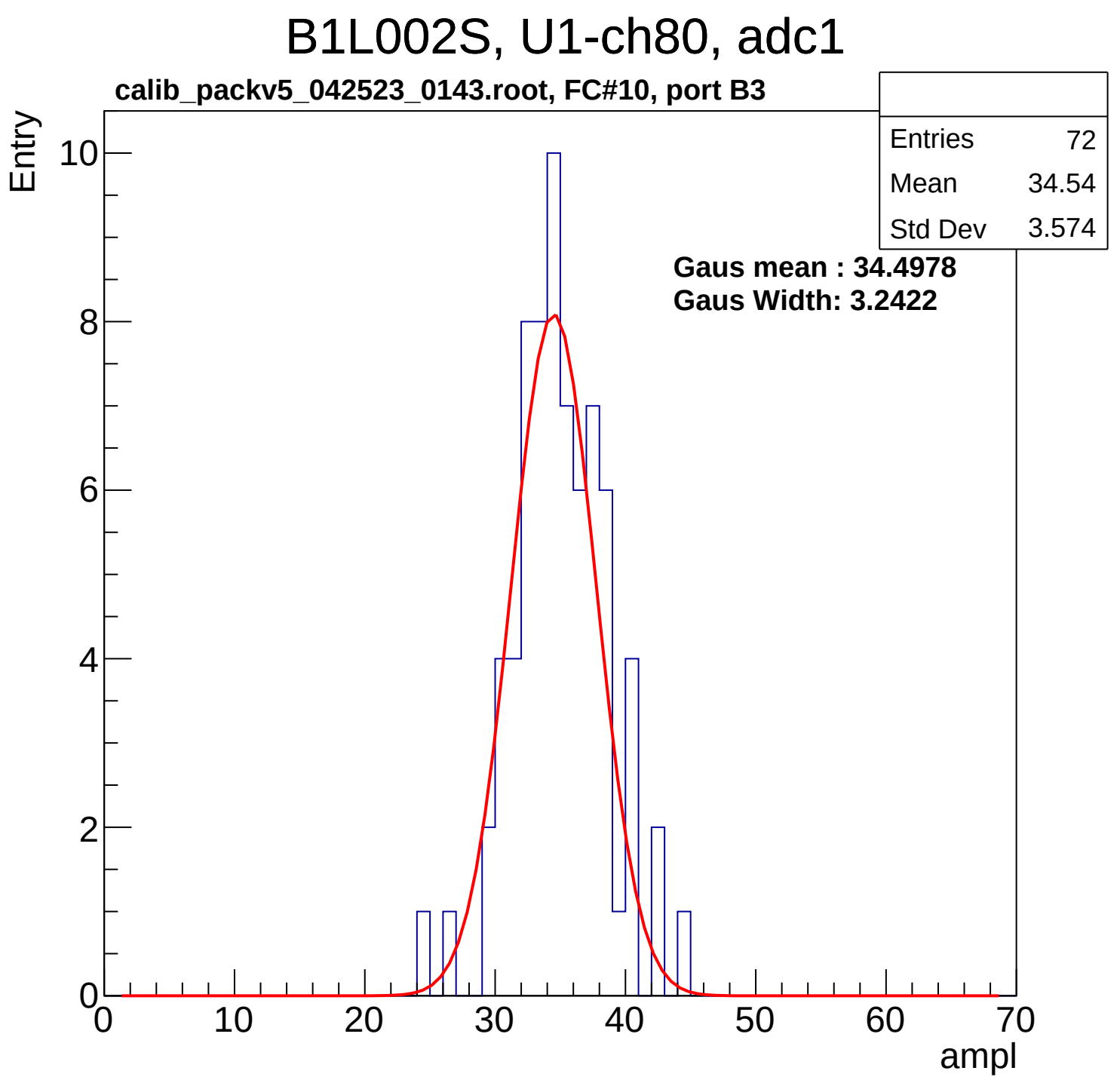
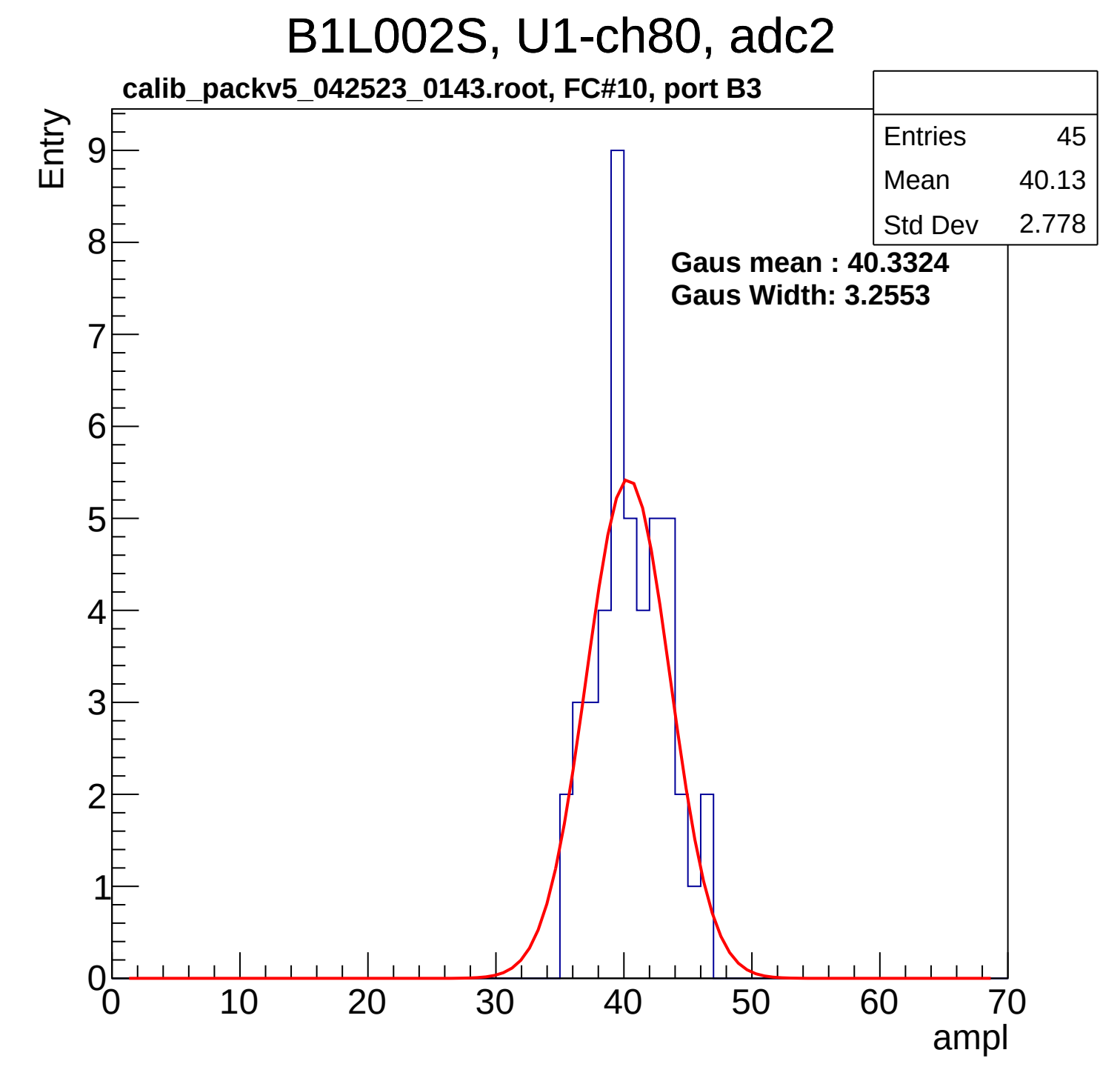

B1L002S, U1-ch80, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.54
Std Dev 3.574
Gaus mean : 34.4978
Gaus Width: 3.2422
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch80, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 40.13
Std Dev 2.778
Gaus mean : 40.3324
Gaus Width: 3.2553
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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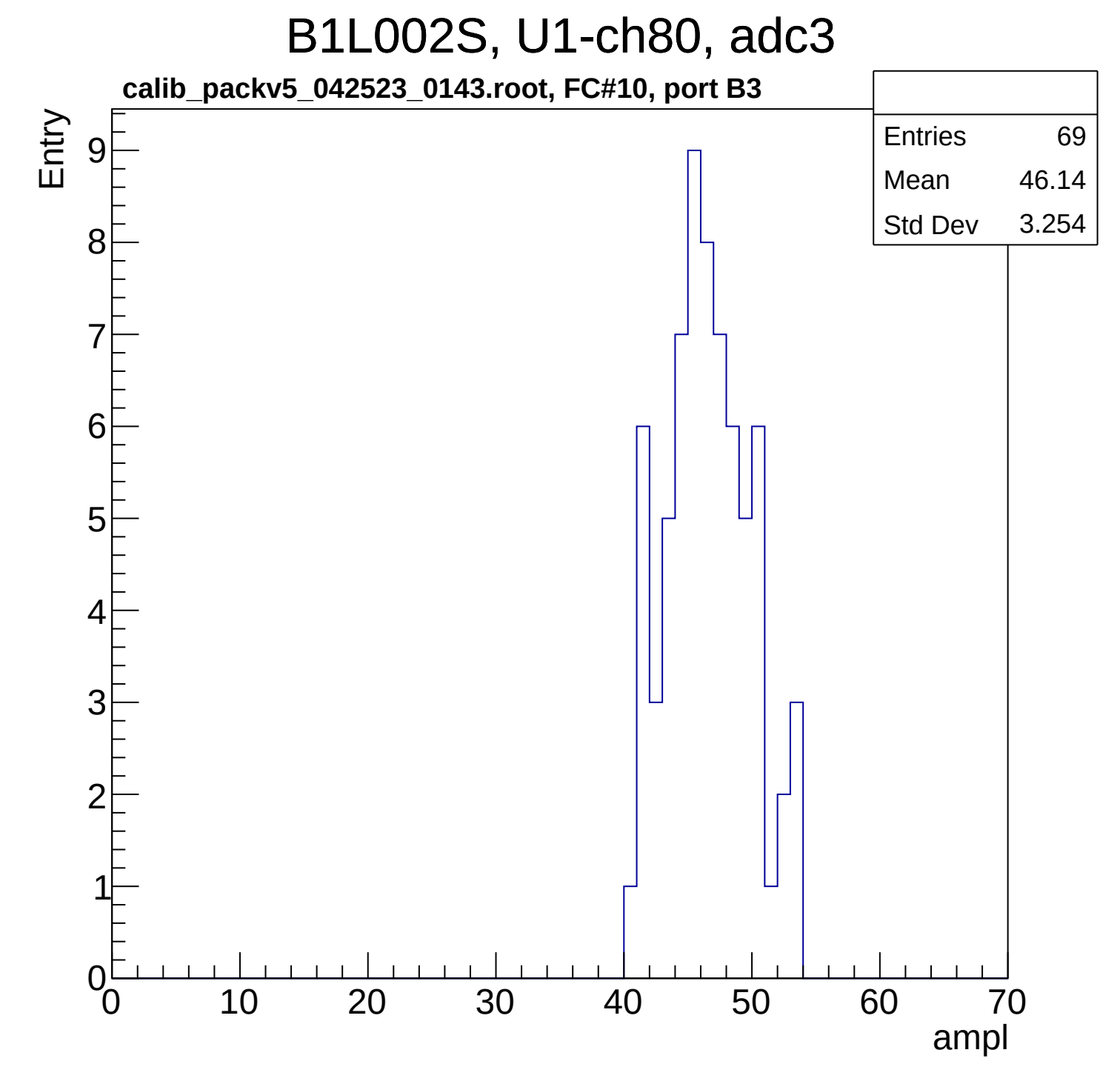
B1L002S, U1-ch80, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.14
Std Dev 3.254
Entry
ampl
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3
4
5
6
7
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0
10
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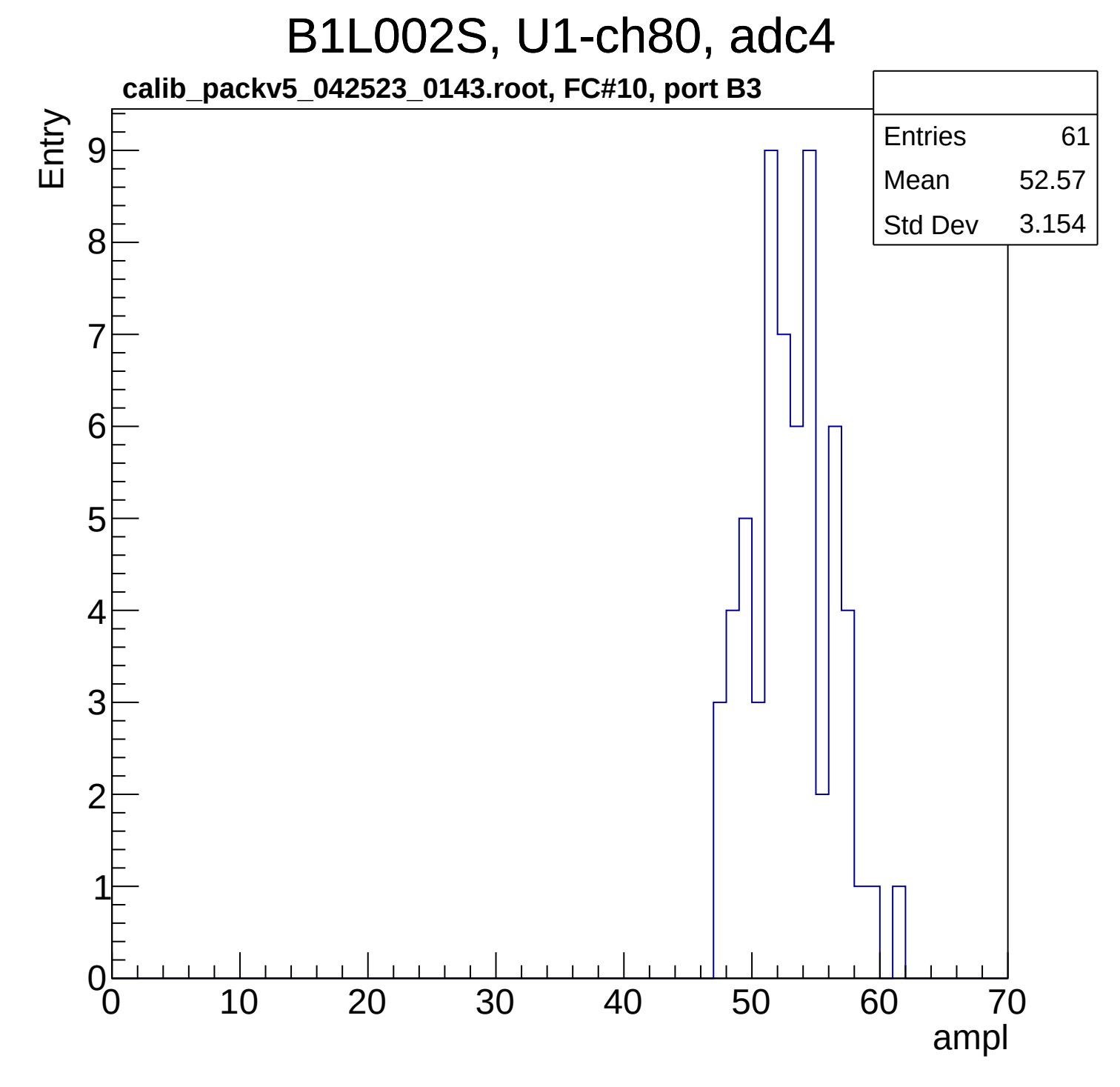
B1L002S, U1-ch80, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 52.57
Std Dev 3.154
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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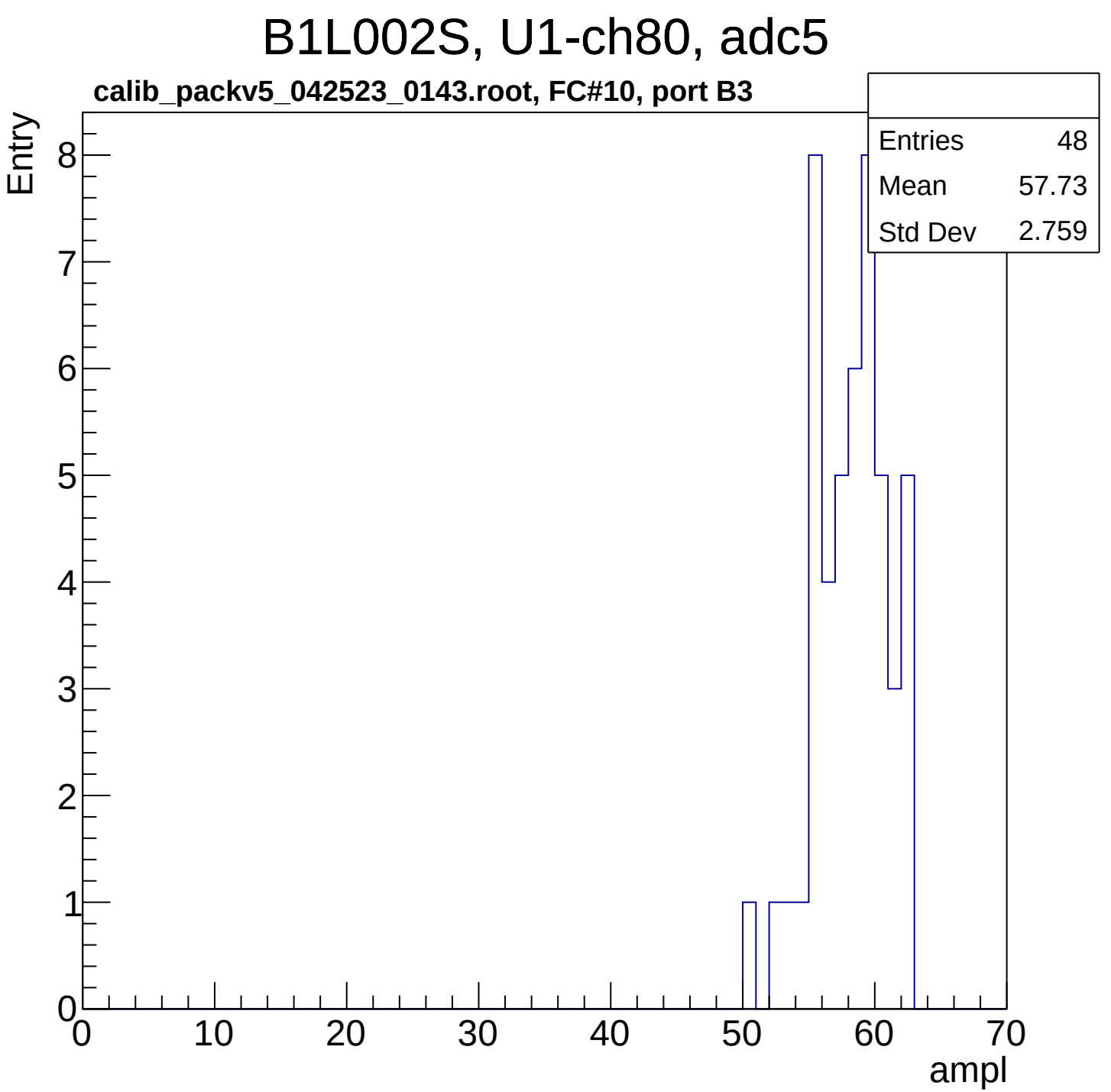
B1L002S, U1-ch80, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 57.73
Std Dev 2.759
Entry
ampl
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1
2
3
4
5
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60
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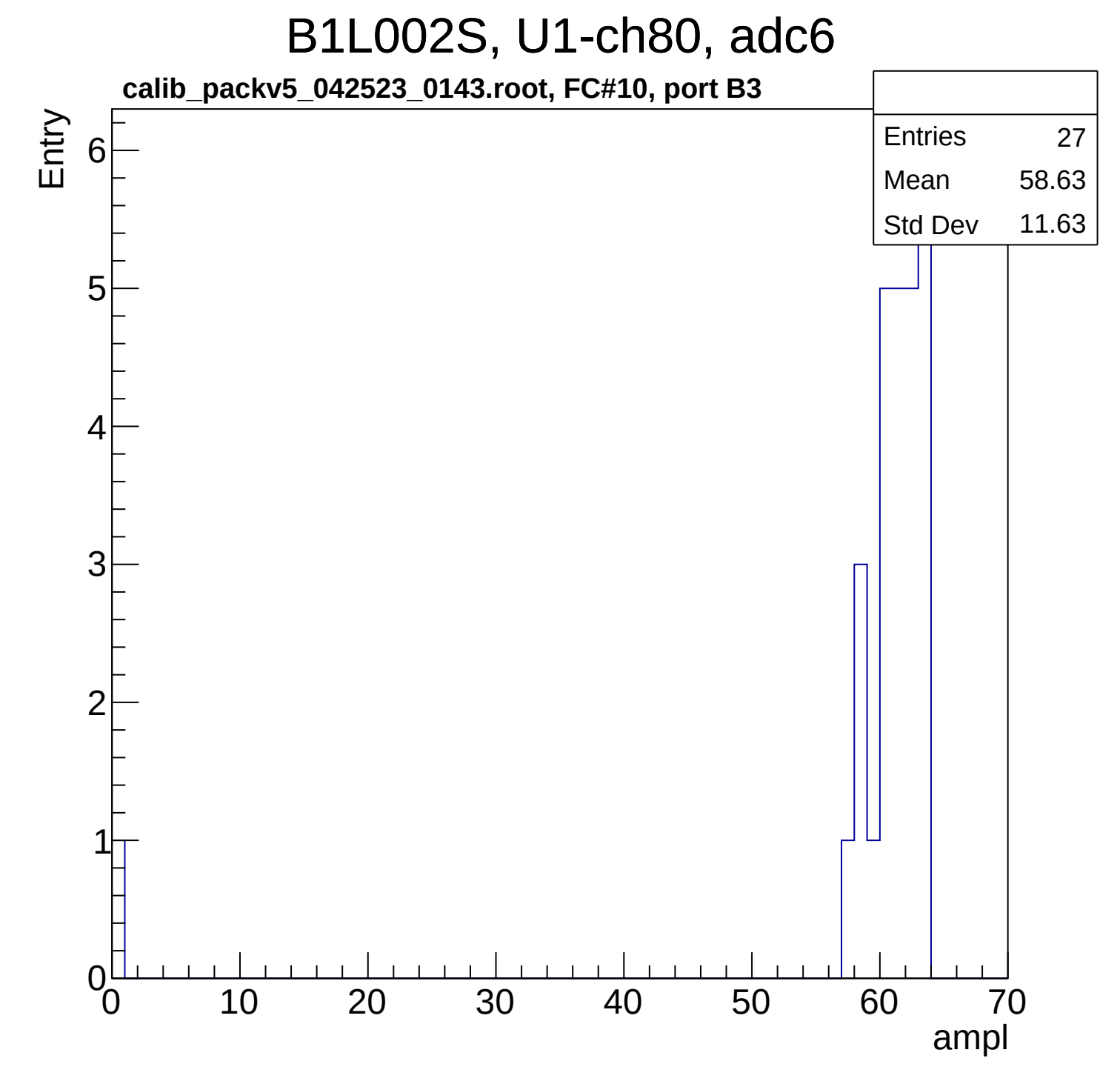
B1L002S, U1-ch80, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.63
Std Dev 11.63
Entry
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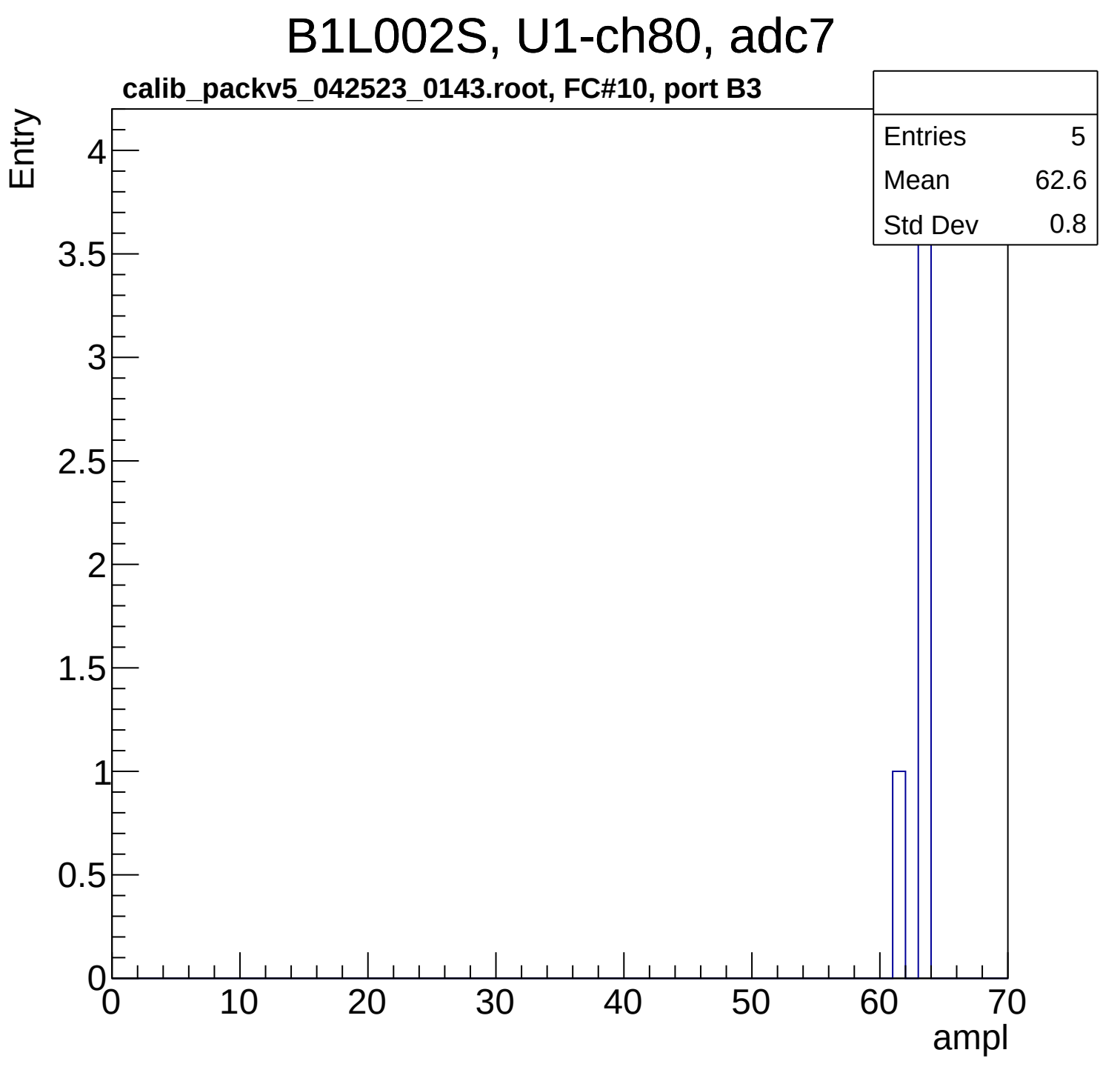
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch80, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 62.6
Std Dev 0.8
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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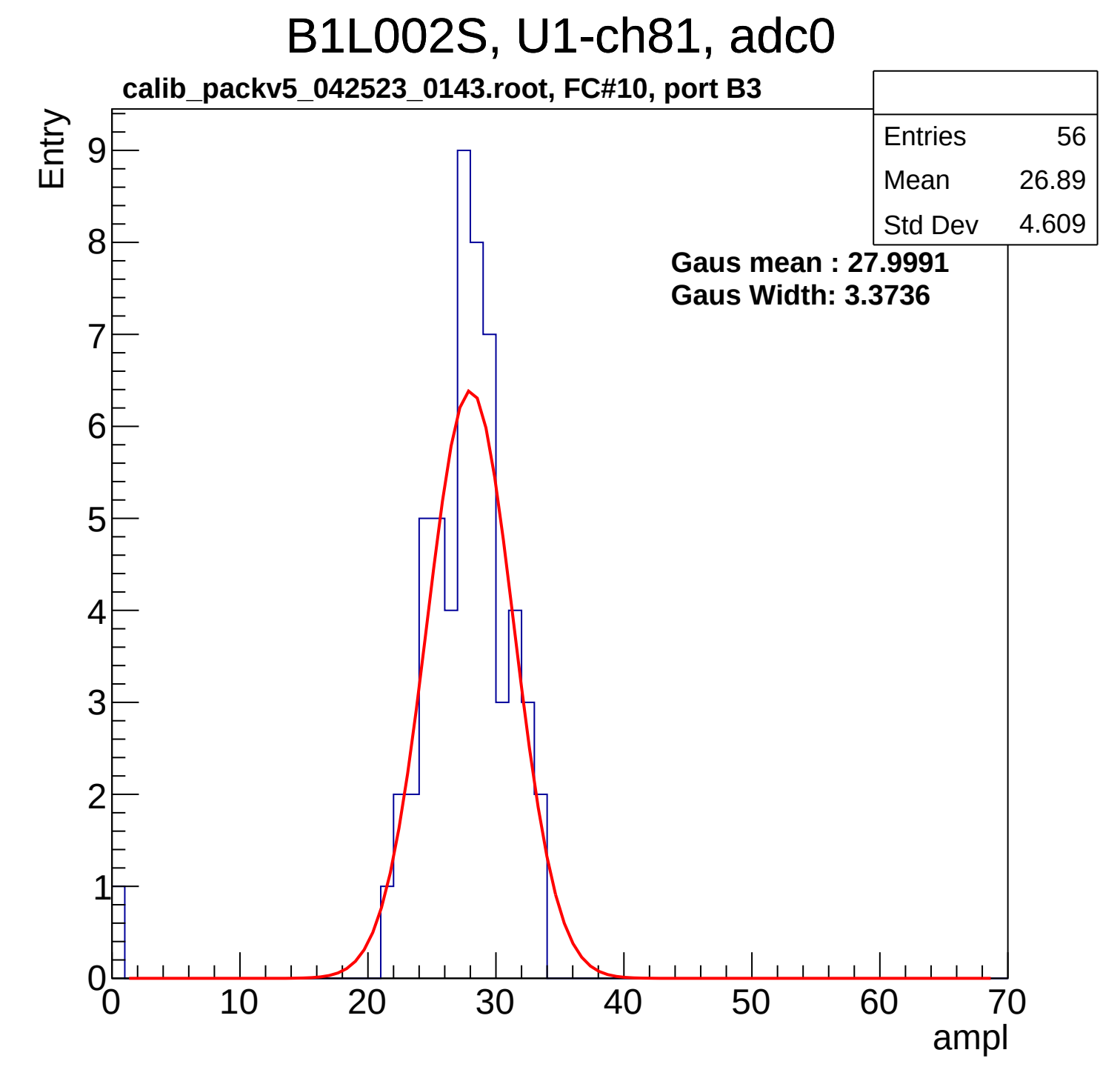
40
50
60
70


B1L002S, U1-ch81, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 26.89
Std Dev 4.609
Gaus mean : 27.9991
Gaus Width: 3.3736
Entry
ampl
0
1
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6
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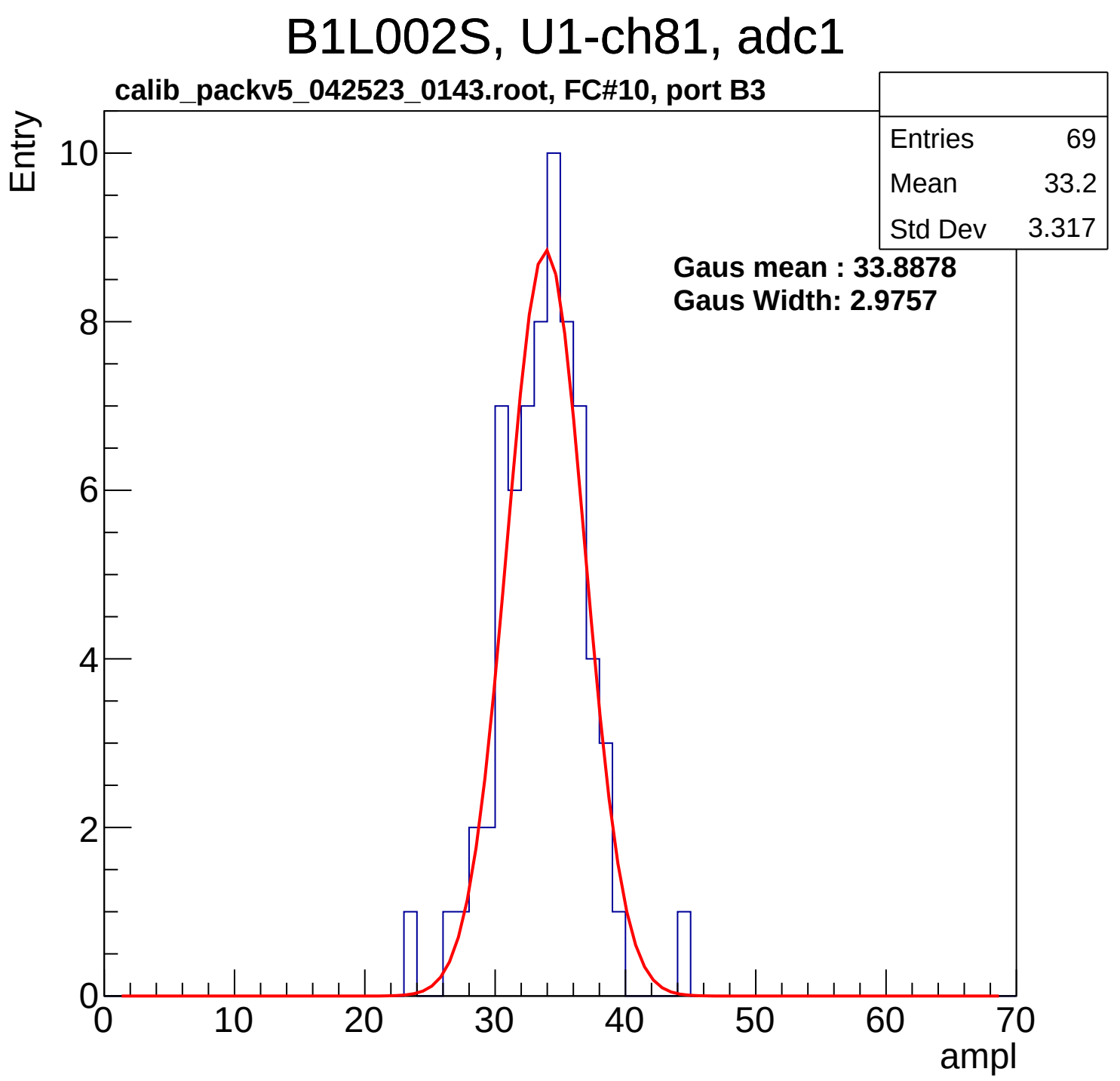
B1L002S, U1-ch81, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 33.2
Std Dev 3.317
Gaus mean : 33.8878
Gaus Width: 2.9757
Entry
ampl
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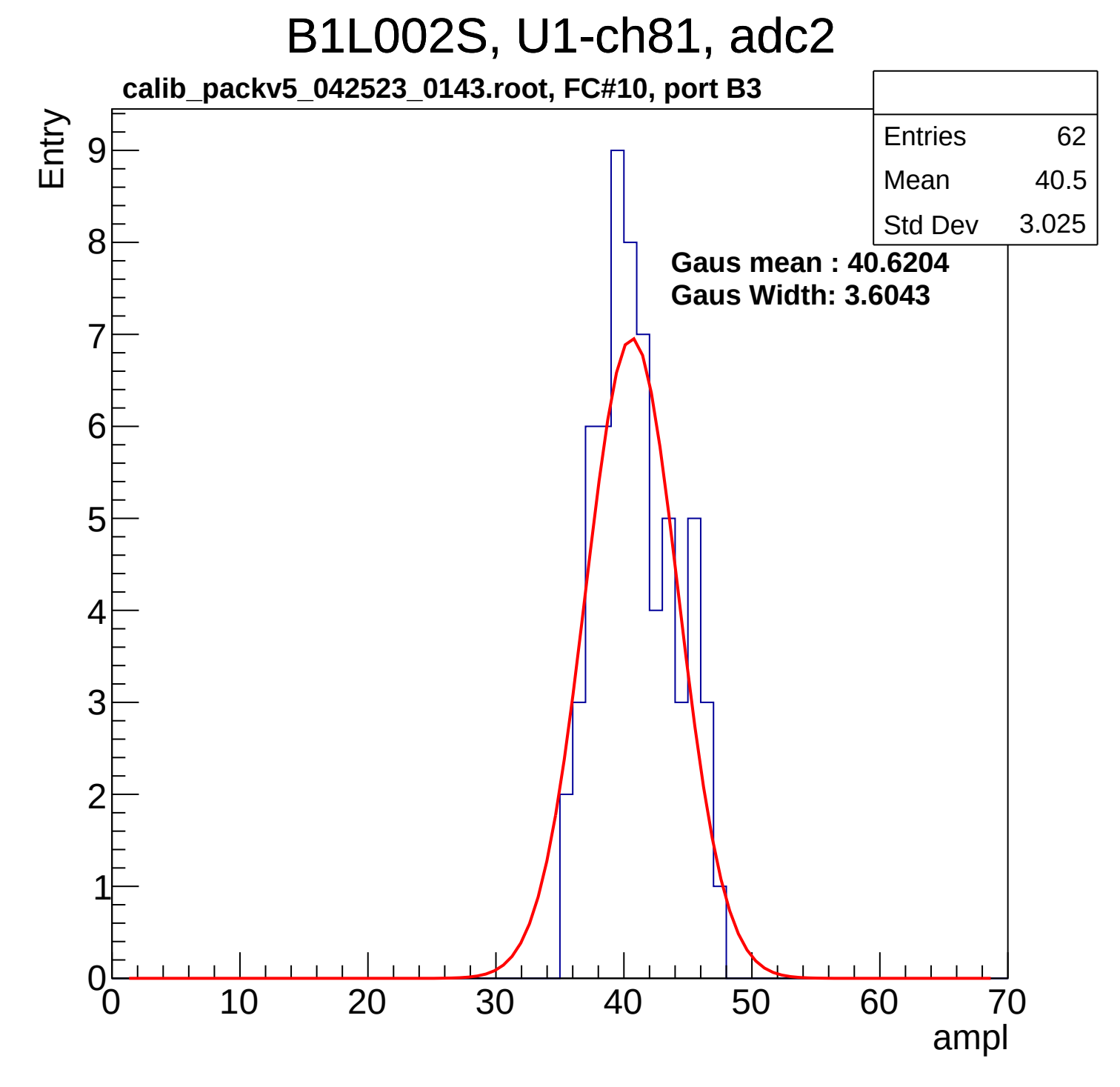
B1L002S, U1-ch81, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.5
Std Dev 3.025
Gaus mean : 40.6204
Gaus Width: 3.6043
Entry
ampl
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2
3
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6
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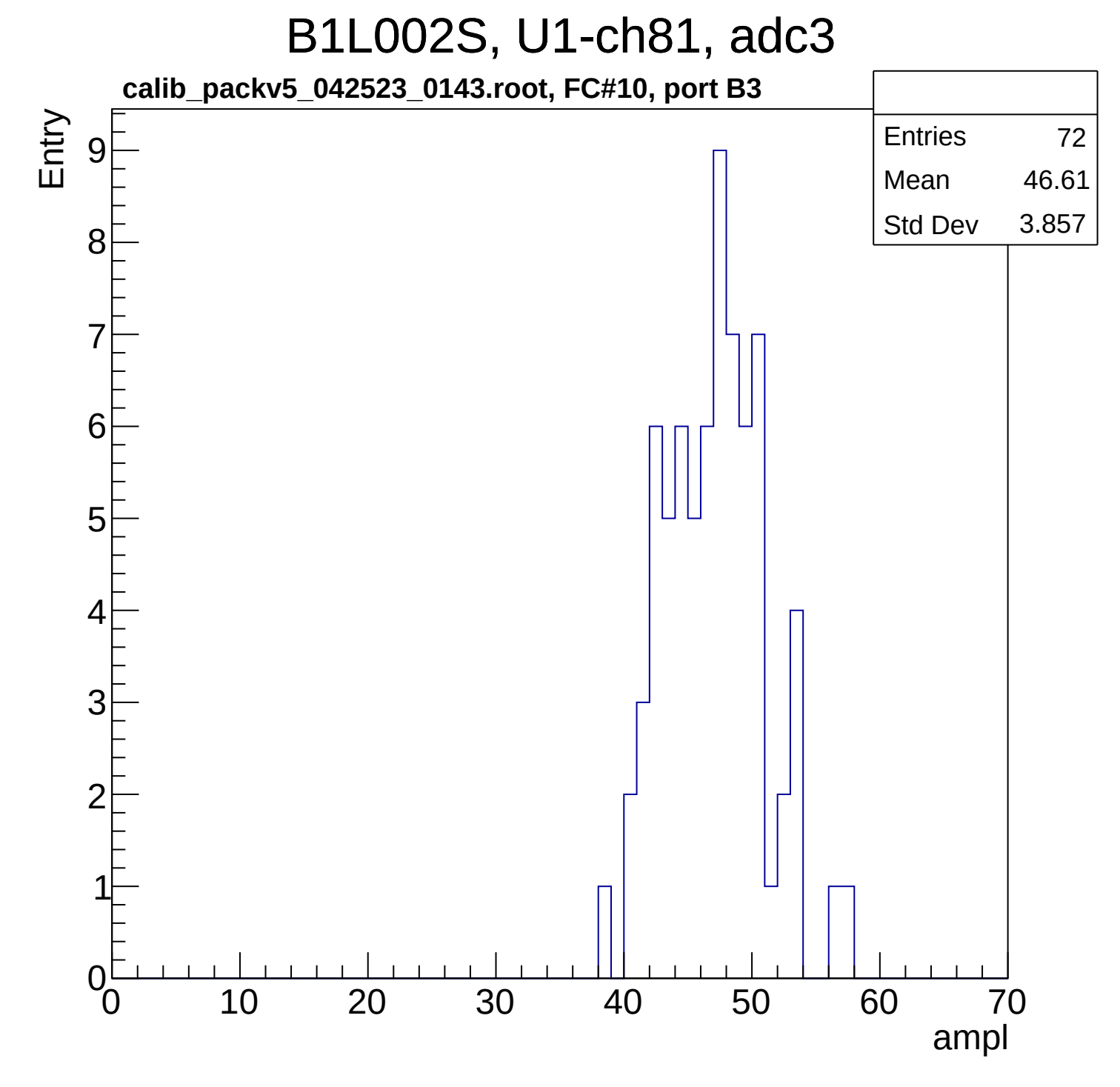
B1L002S, U1-ch81, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.61
Std Dev 3.857
Entry
ampl
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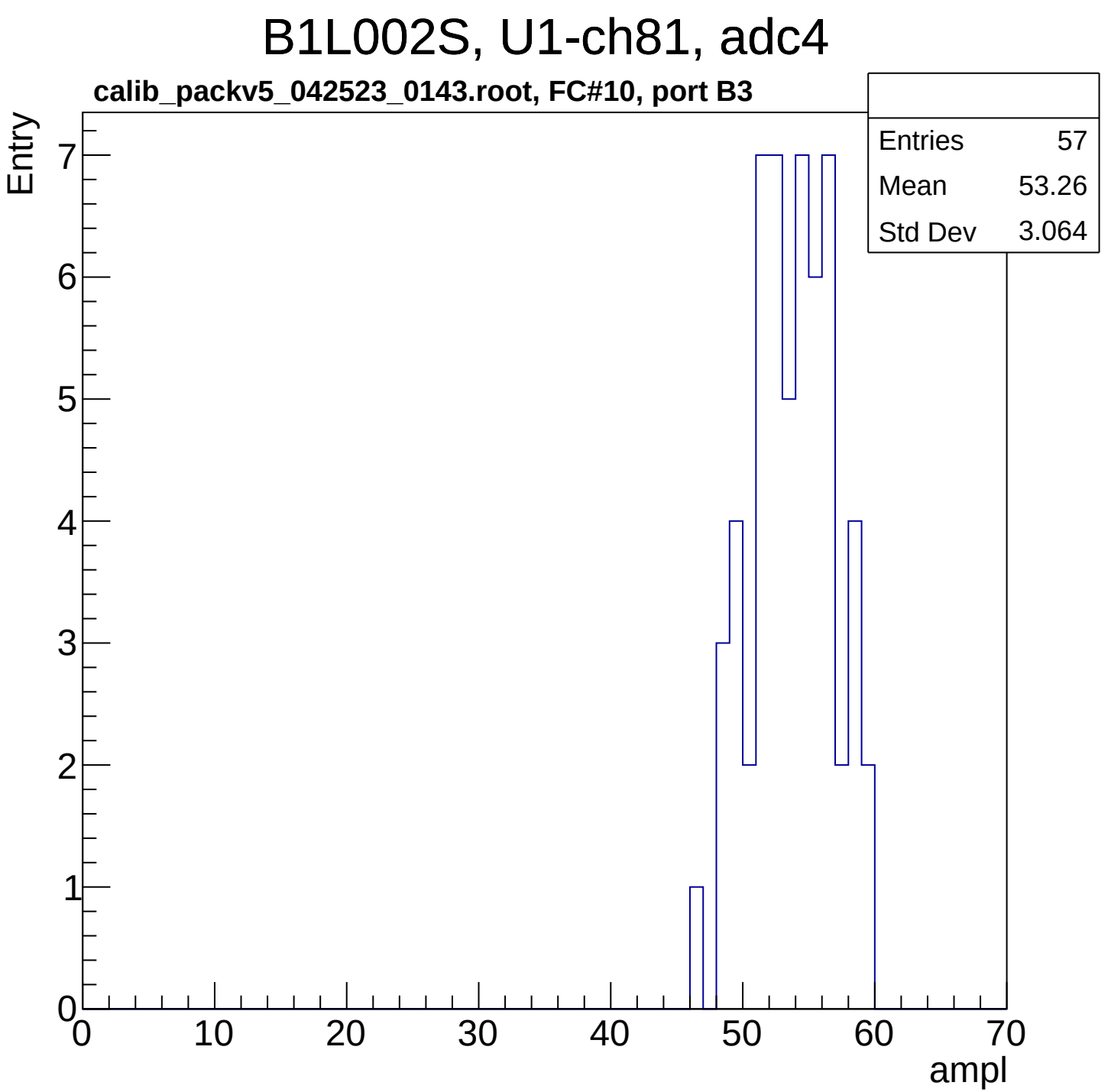
B1L002S, U1-ch81, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.26
Std Dev 3.064
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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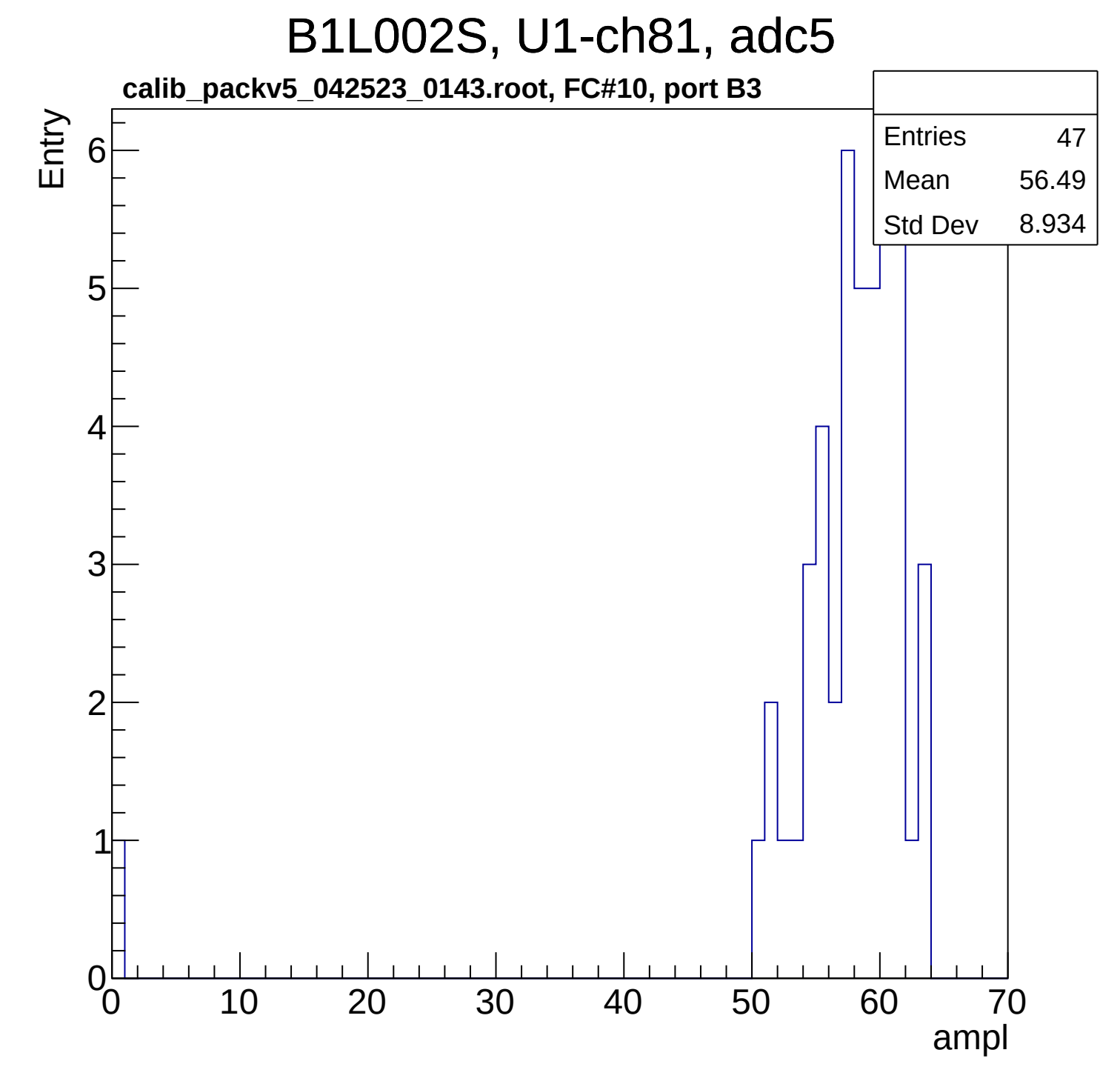
B1L002S, U1-ch81, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 56.49
Std Dev 8.934
Entry
ampl
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1
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3
4
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0
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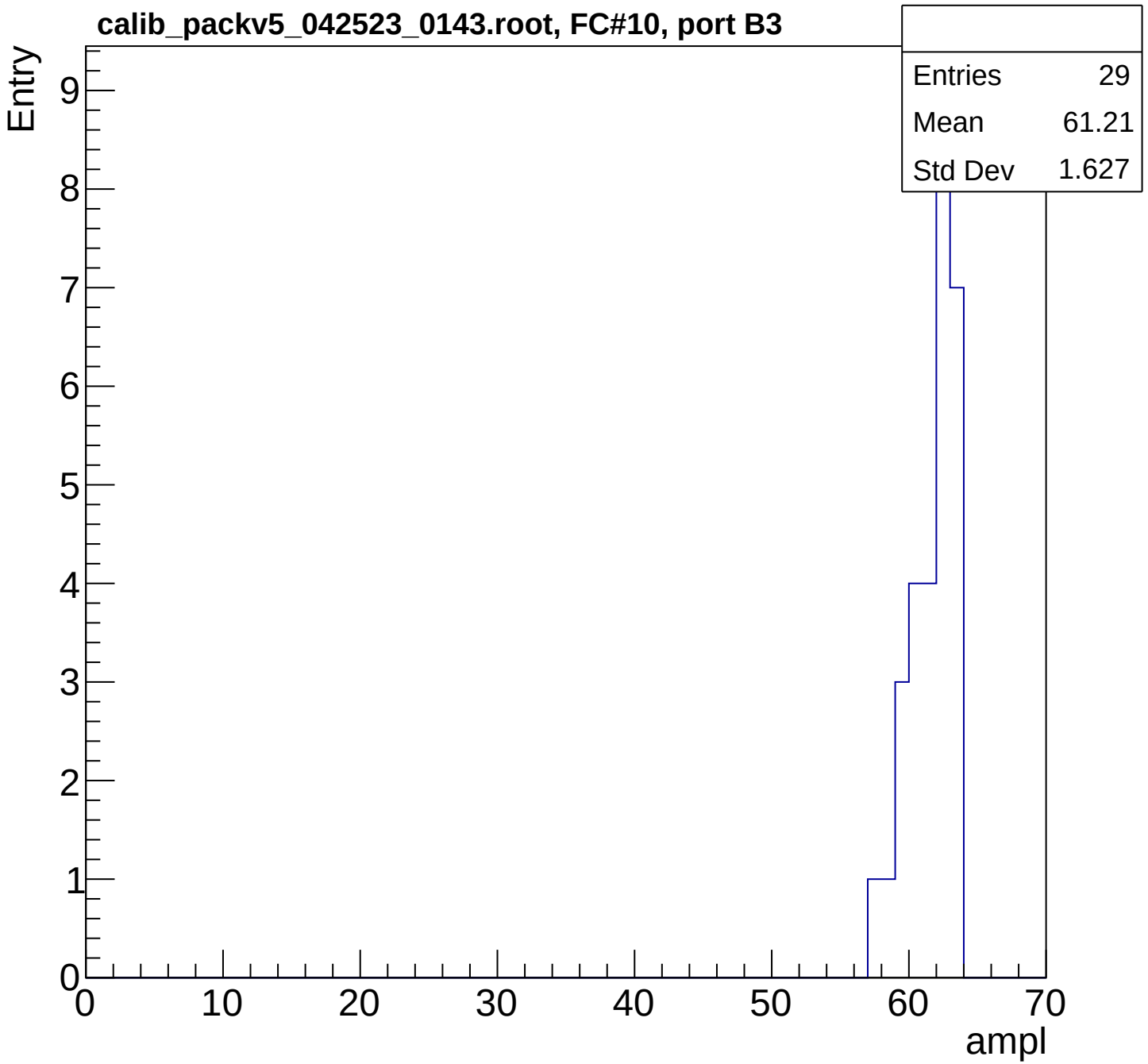
B1L002S, U1-ch81, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 61.21
Std Dev 1.627
Entry
ampl
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2
3
4
5
6
7
8
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0
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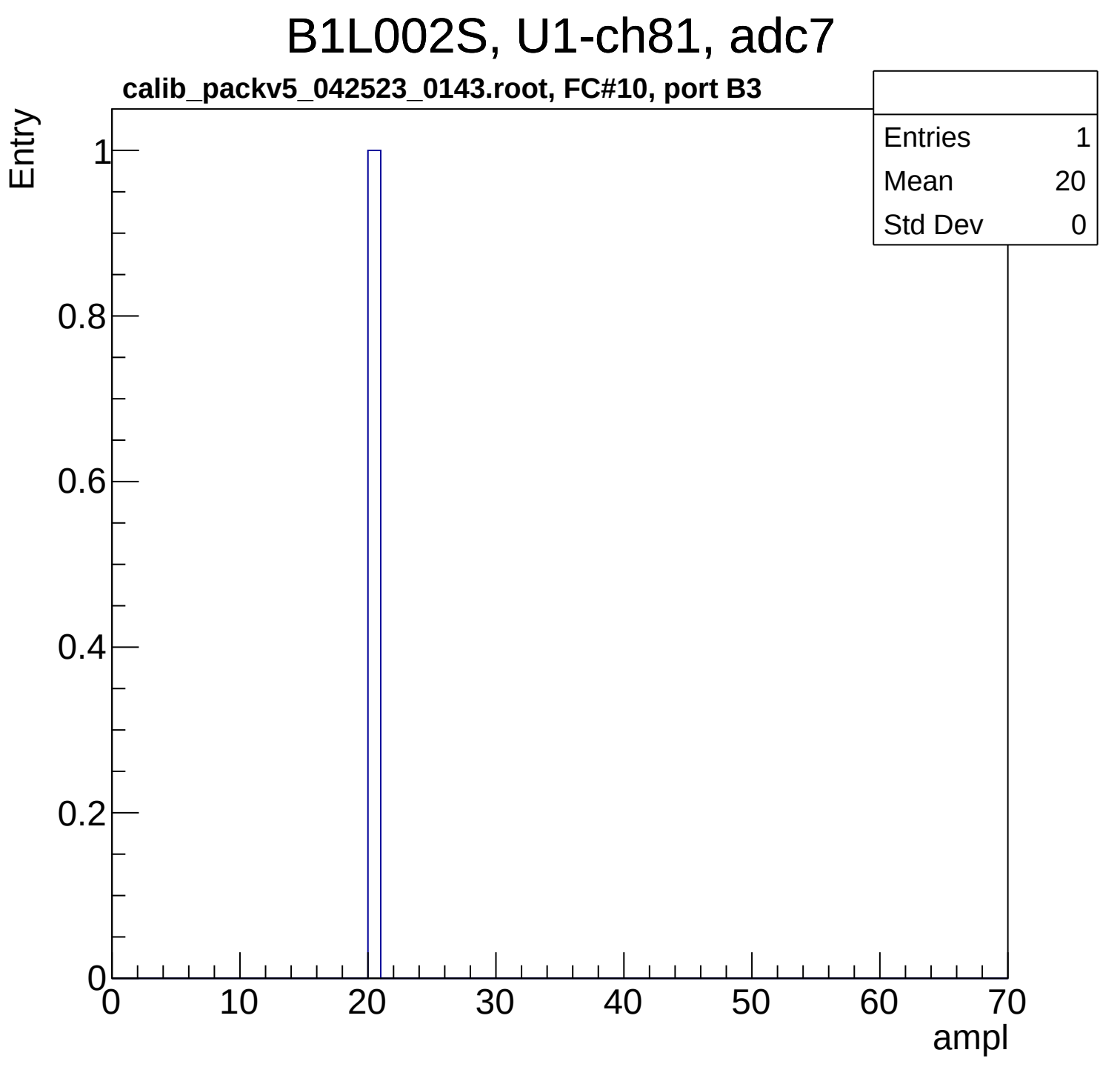
B1L002S, U1-ch81, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 20
Std Dev 0
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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60
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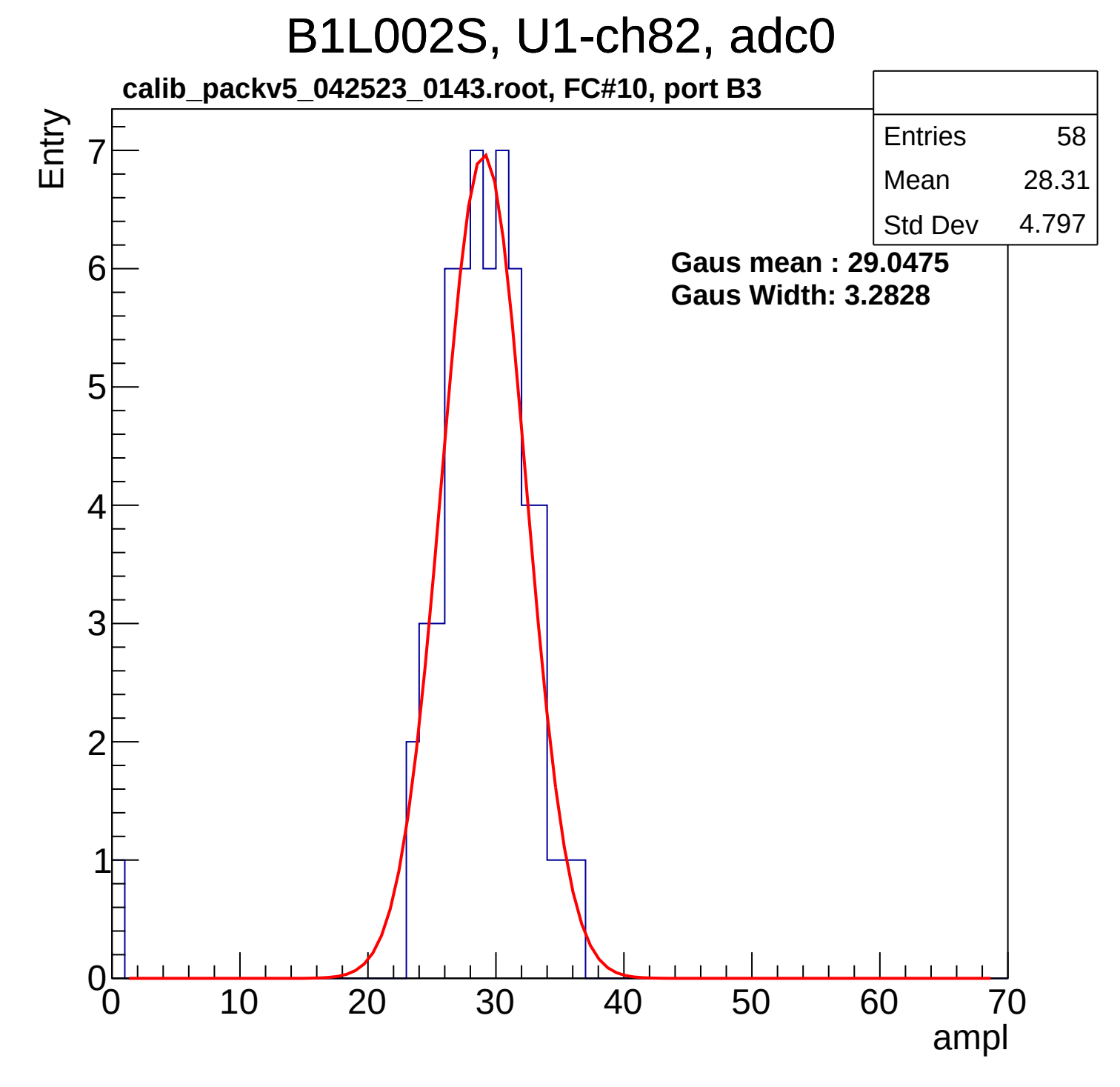
B1L002S, U1-ch82, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 28.31
Std Dev 4.797
Gaus mean : 29.0475
Gaus Width: 3.2828
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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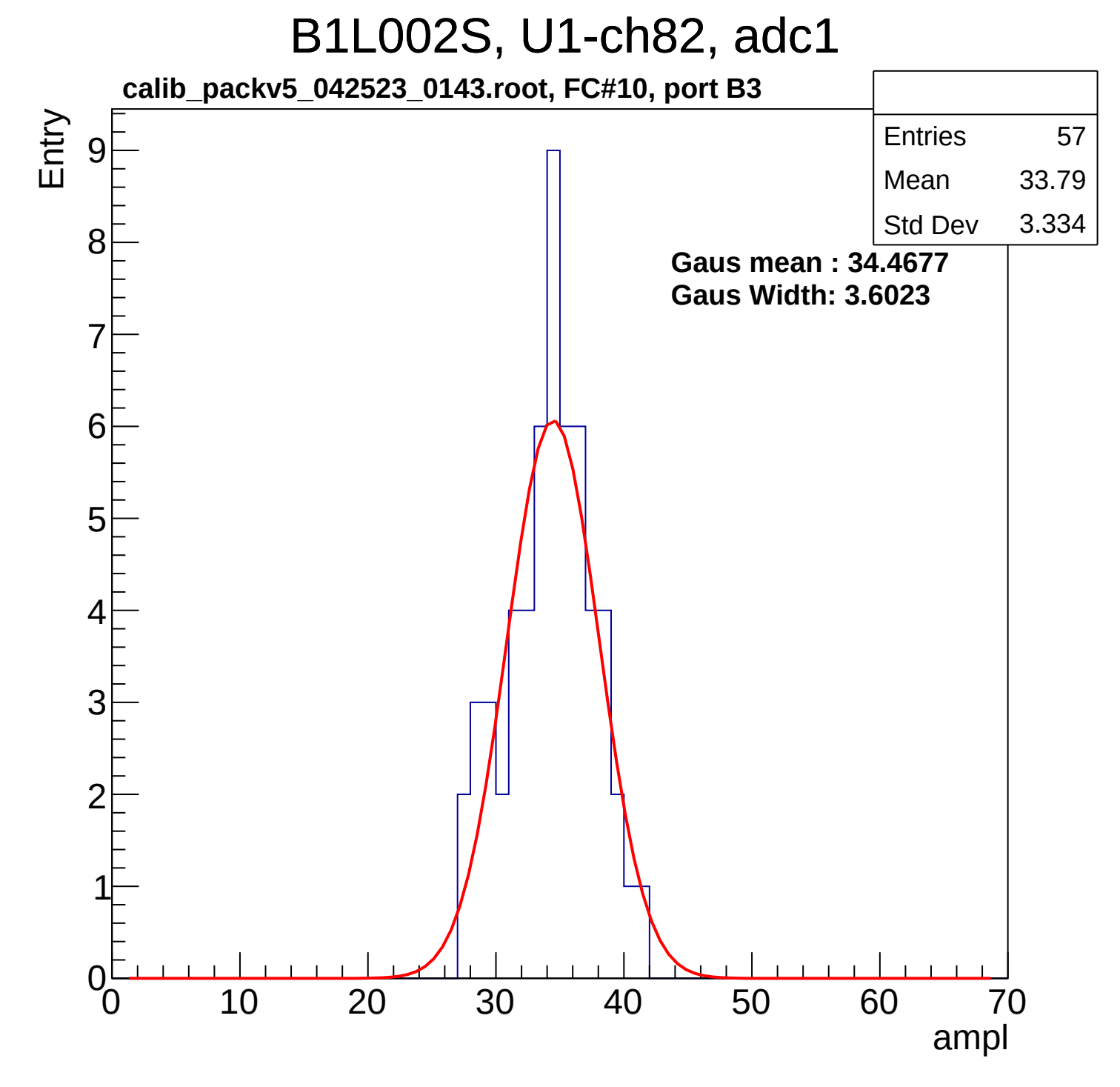
B1L002S, U1-ch82, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 33.79
Std Dev 3.334
Gaus mean : 34.4677
Gaus Width: 3.6023
Entry
ampl
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2
3
4
5
6
7
8
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0
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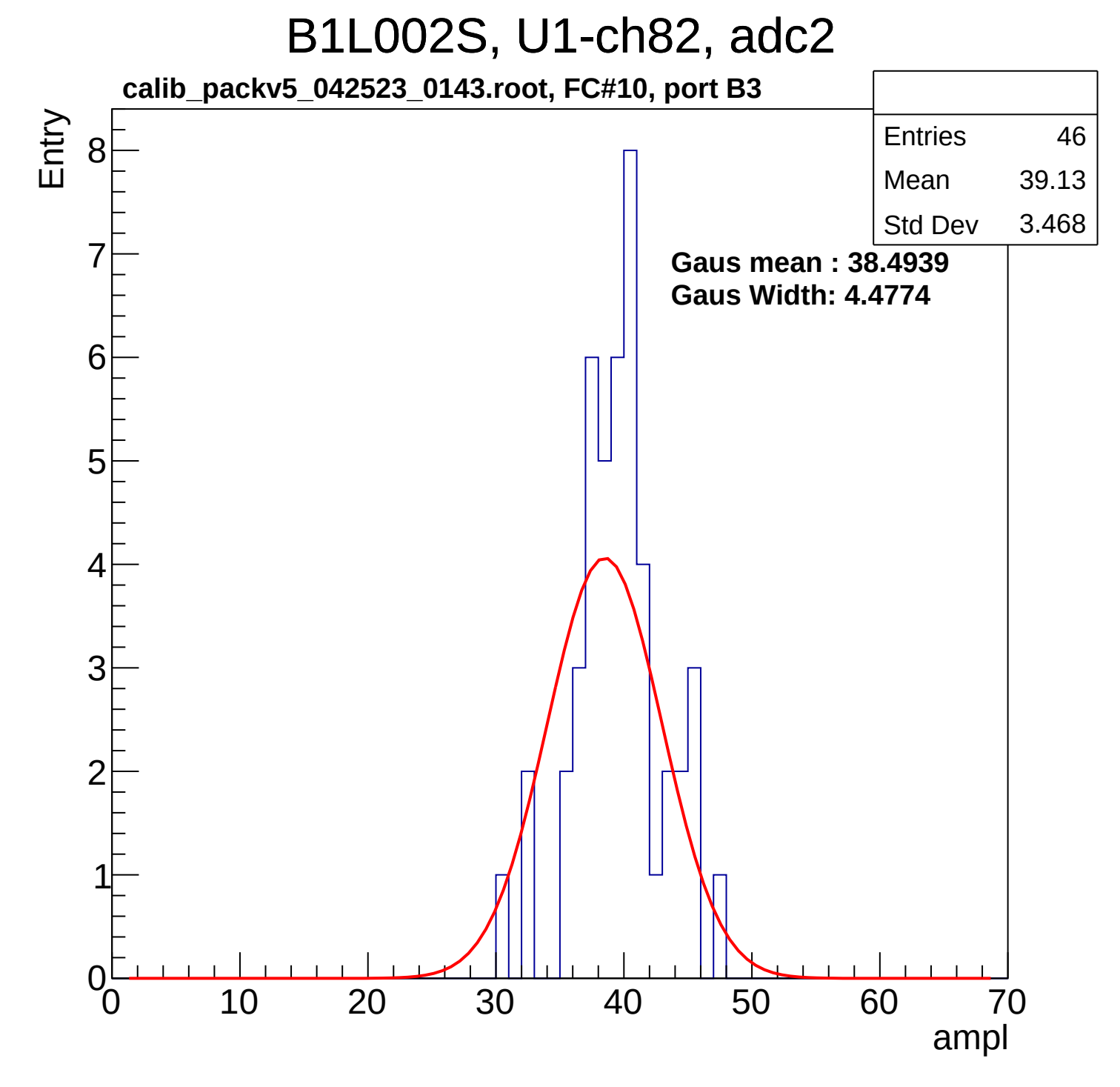
B1L002S, U1-ch82, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 39.13
Std Dev 3.468
Gaus mean : 38.4939
Gaus Width: 4.4774
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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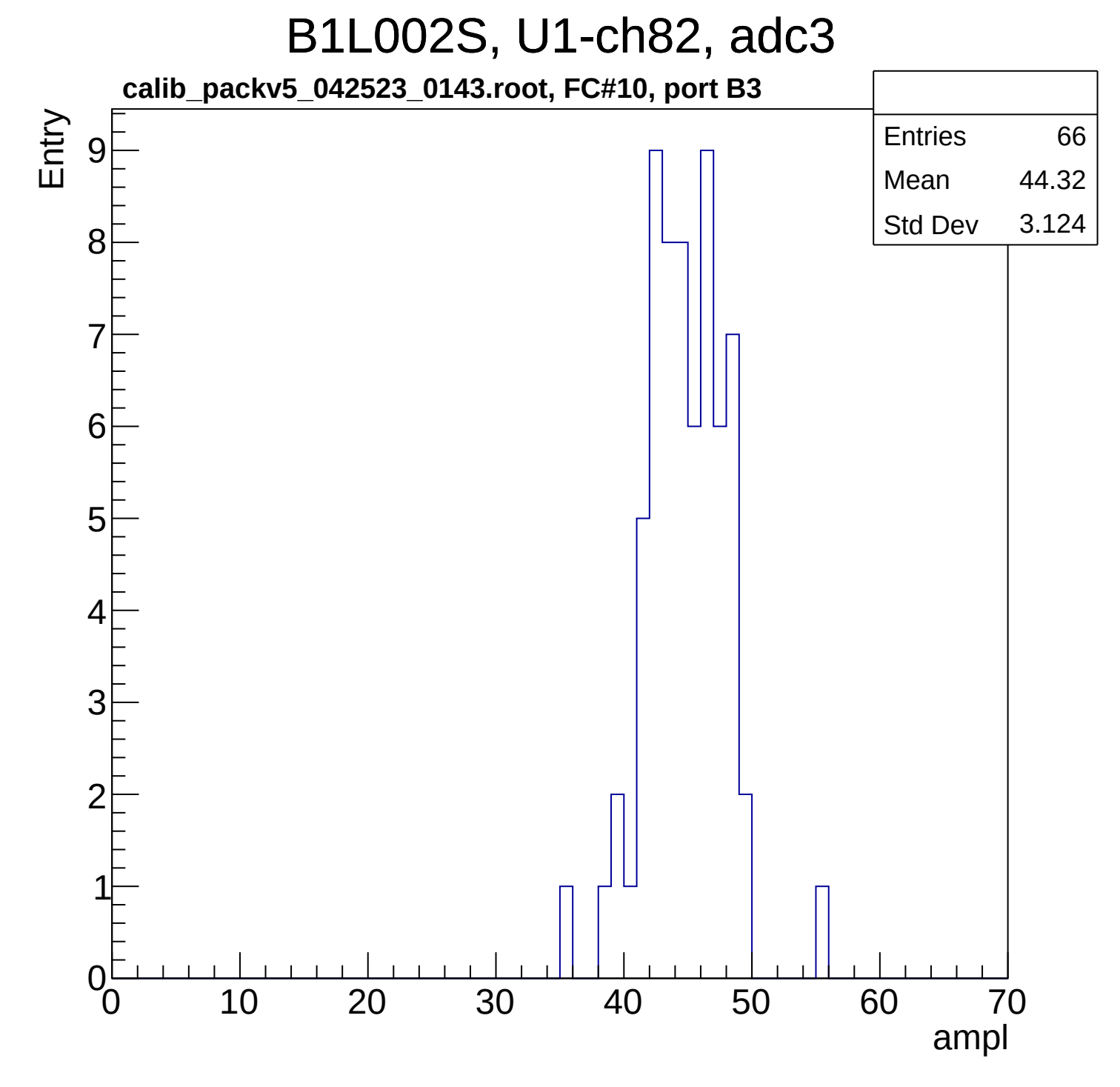
B1L002S, U1-ch82, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 44.32
Std Dev 3.124
Entry
ampl
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6
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0
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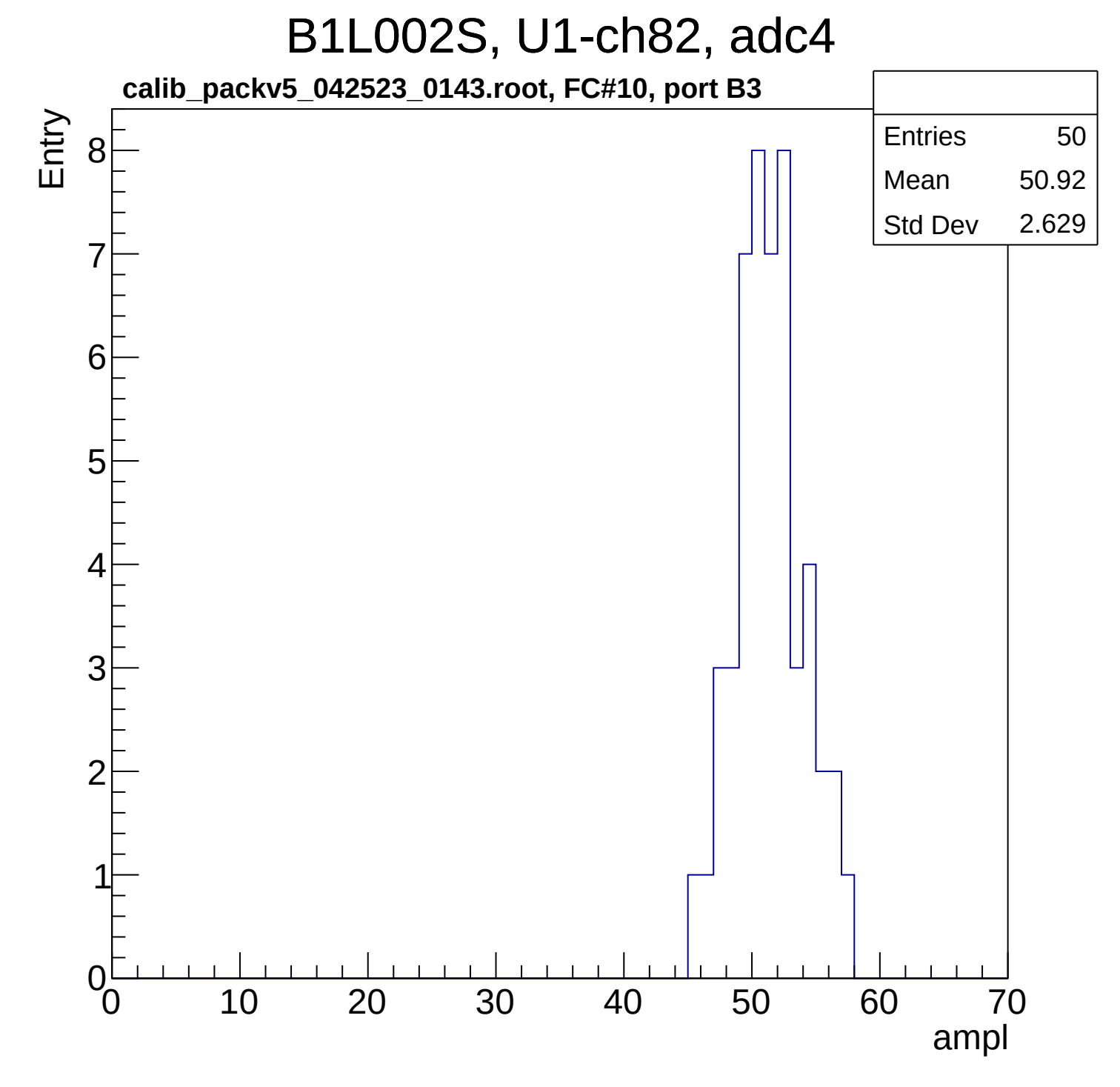
B1L002S, U1-ch82, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 50.92
Std Dev 2.629
Entry
ampl
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2
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5
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0
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60
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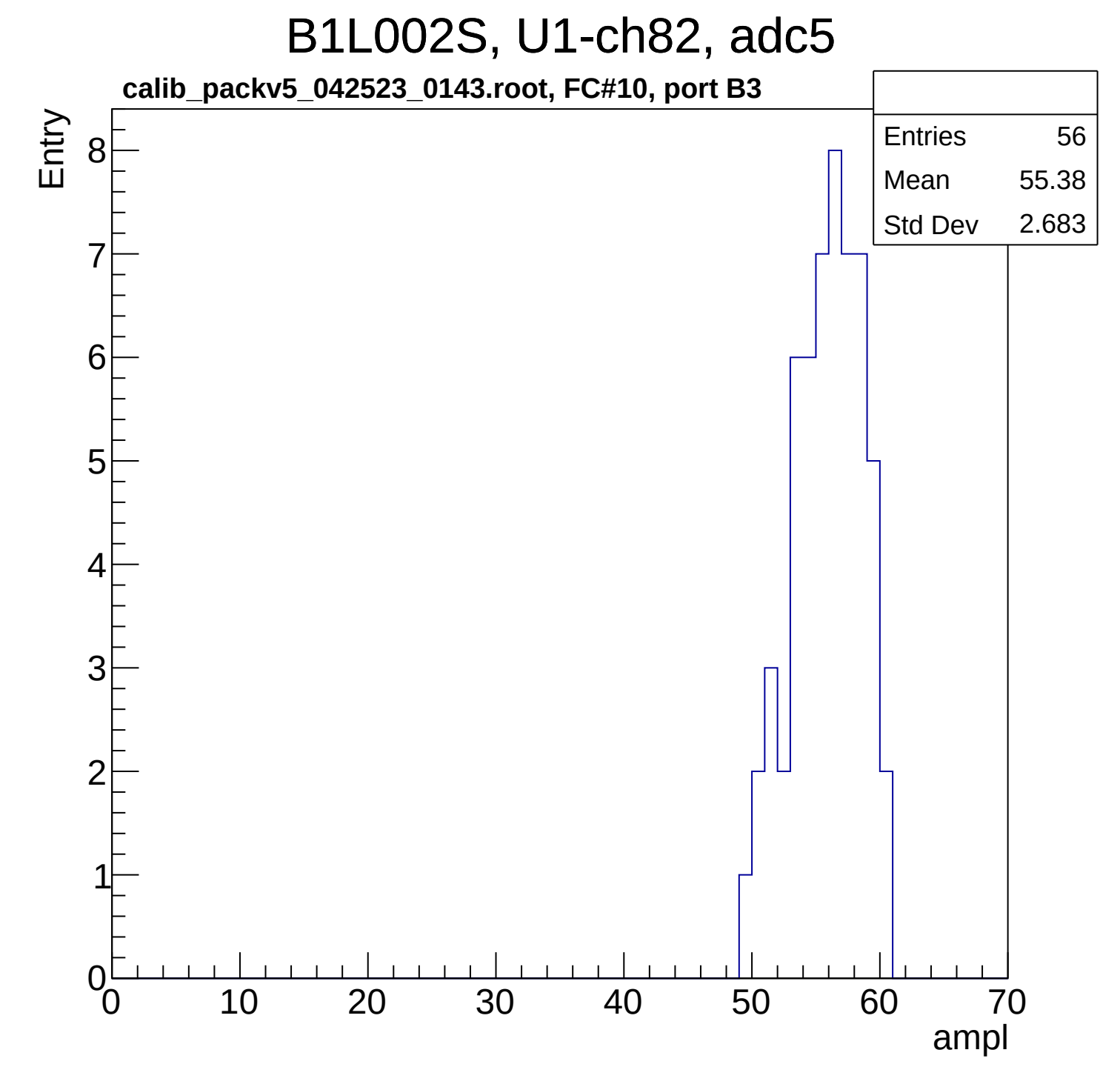
B1L002S, U1-ch82, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 55.38
Std Dev 2.683
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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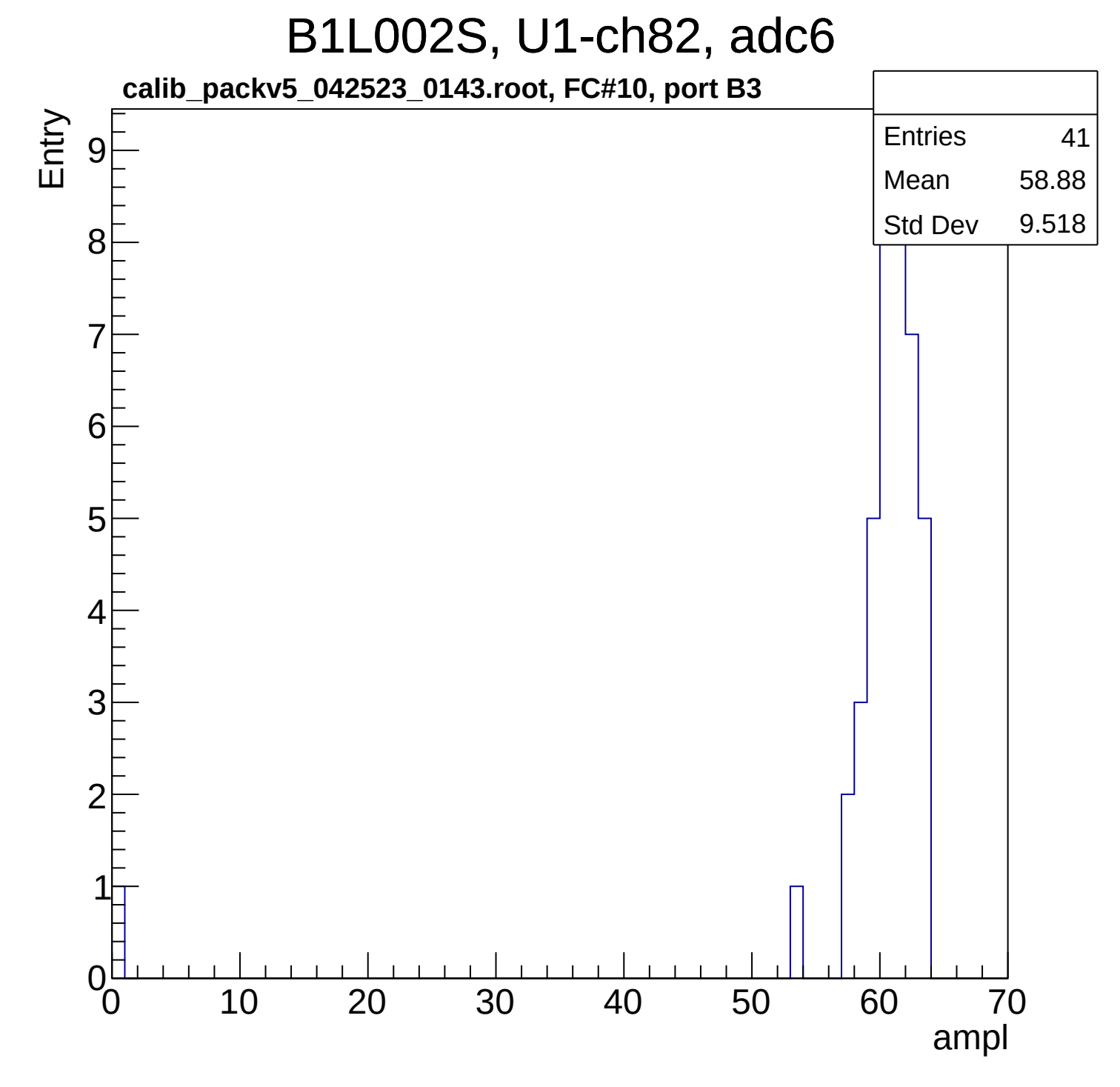
B1L002S, U1-ch82, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 58.88
Std Dev 9.518
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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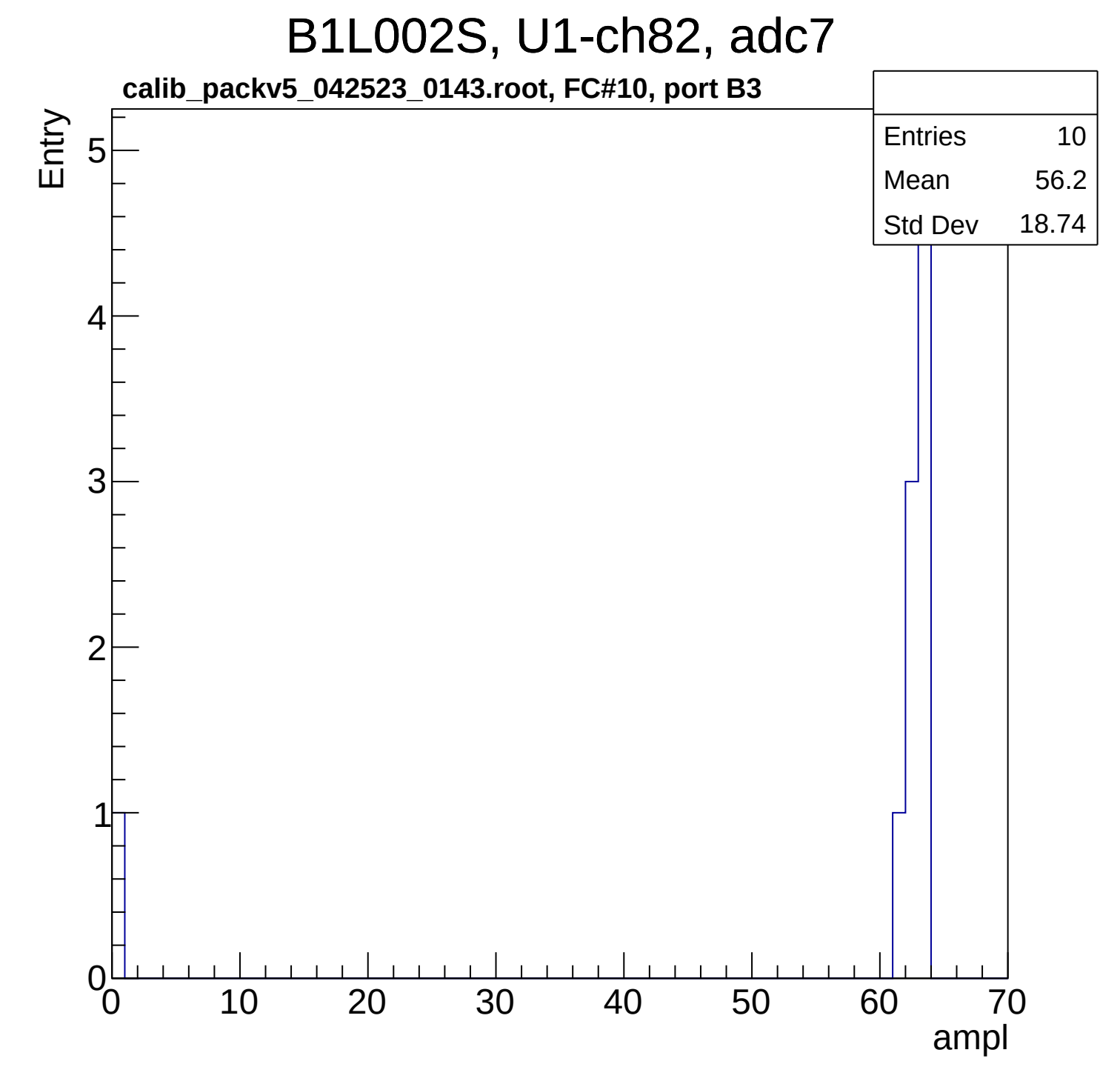
B1L002S, U1-ch82, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 56.2
Std Dev 18.74
Entry
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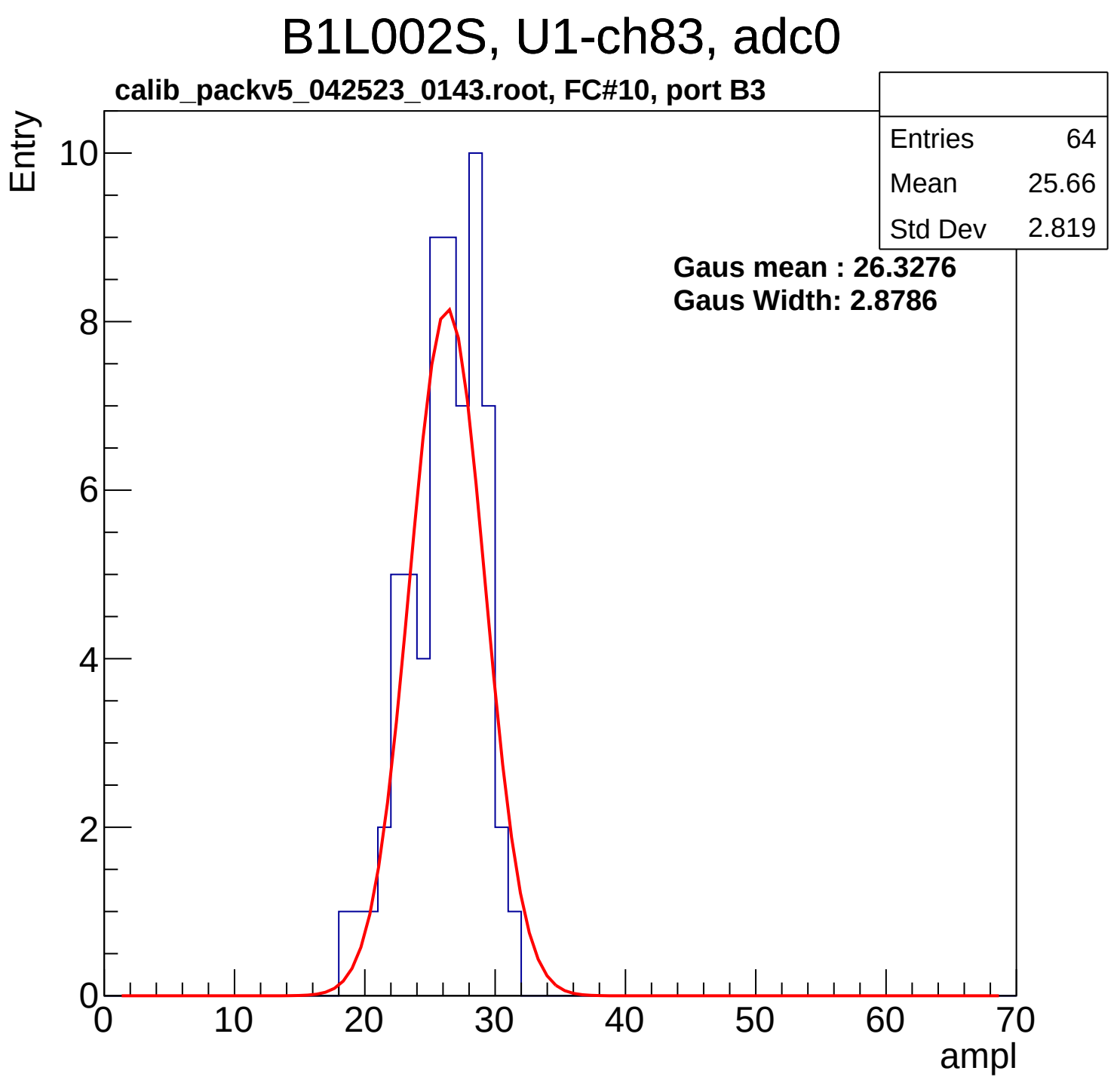
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch83, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 25.66
Std Dev 2.819
Gaus mean : 26.3276
Gaus Width: 2.8786
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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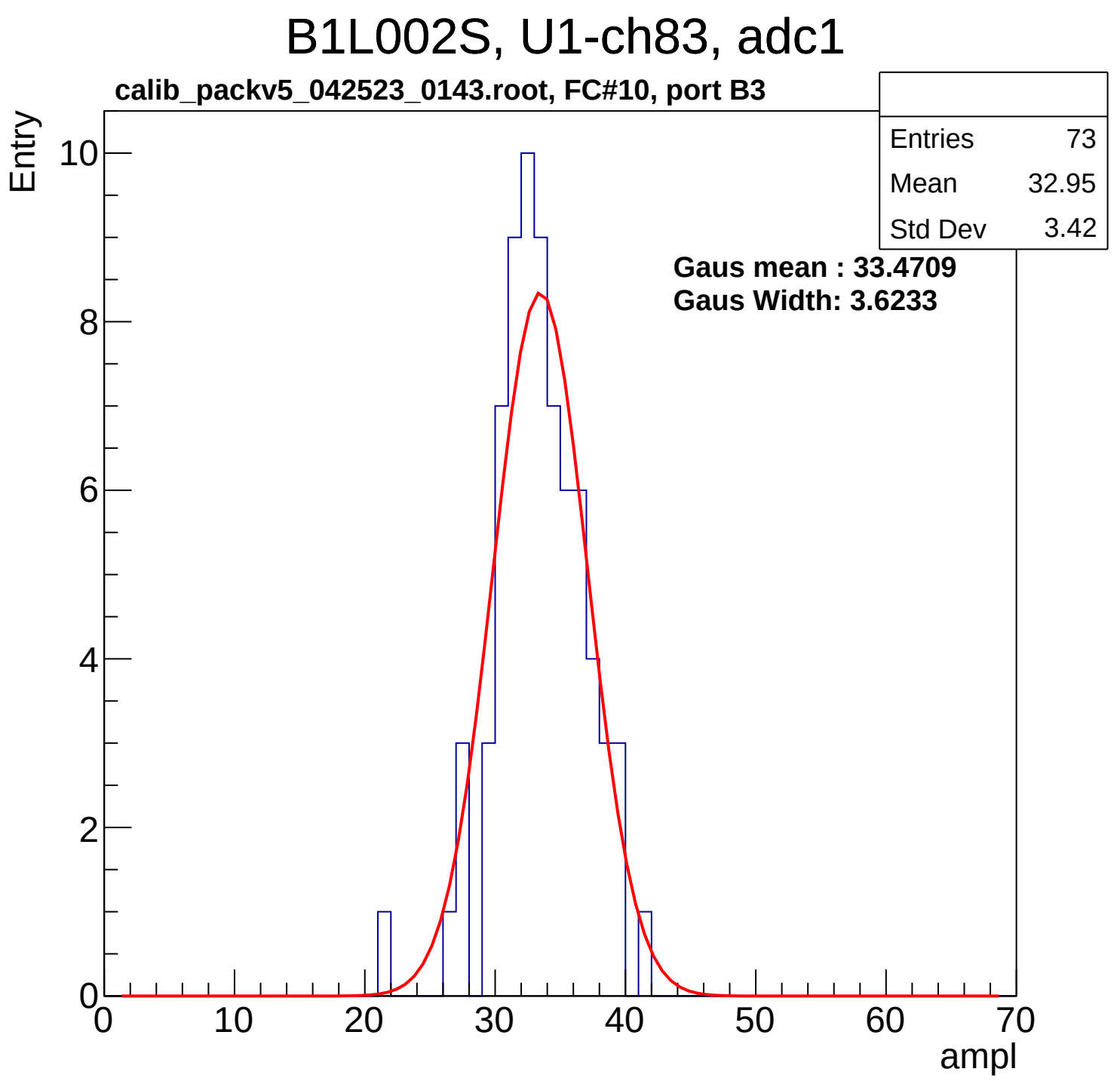
B1L002S, U1-ch83, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 32.95
Std Dev 3.42
Gaus mean : 33.4709
Gaus Width: 3.6233
Entry
ampl
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4
6
8
10
0
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20
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40
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60
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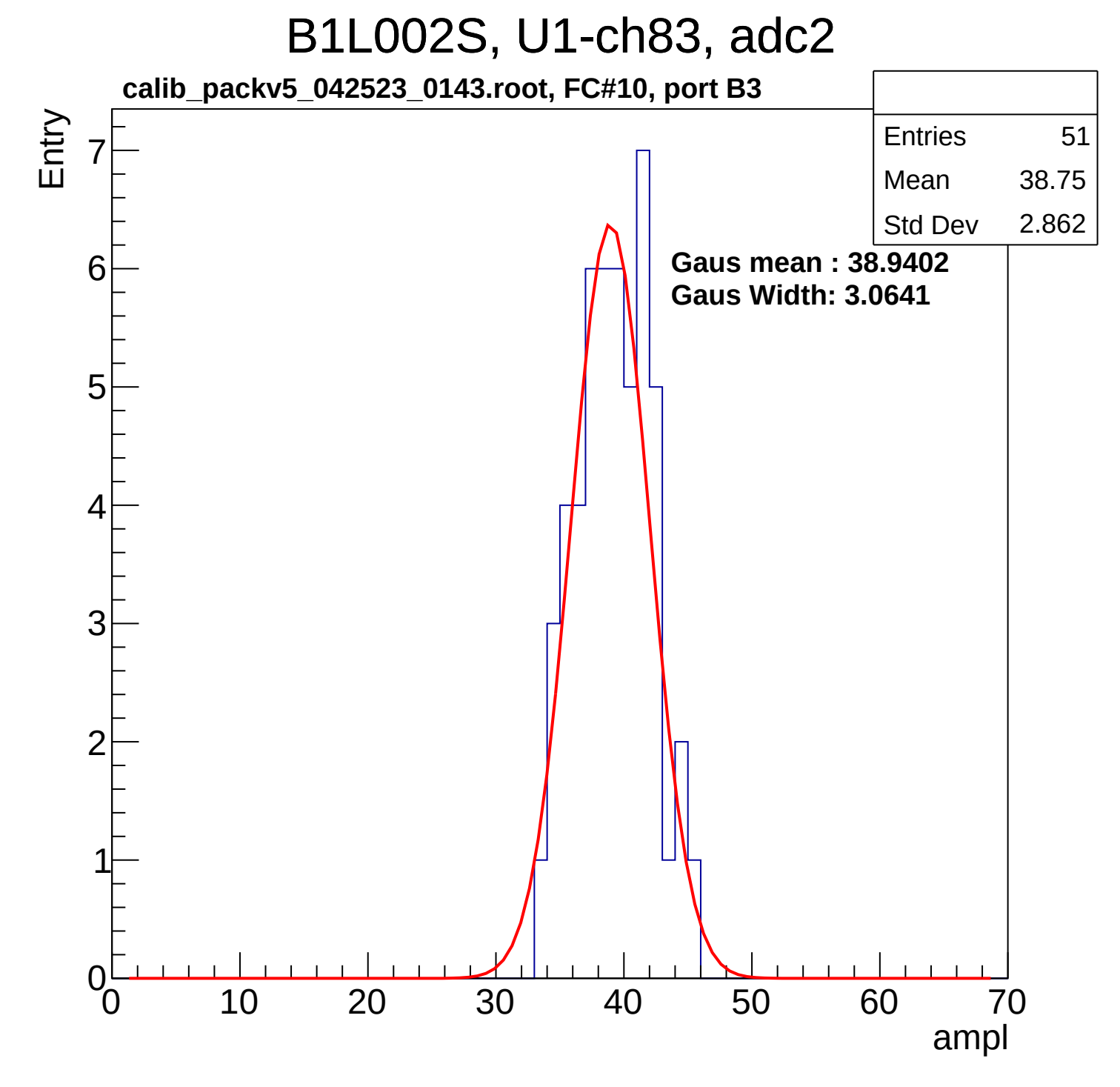
B1L002S, U1-ch83, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 38.75
Std Dev 2.862
Gaus mean : 38.9402
Gaus Width: 3.0641
Entry
ampl
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3
4
5
6
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0
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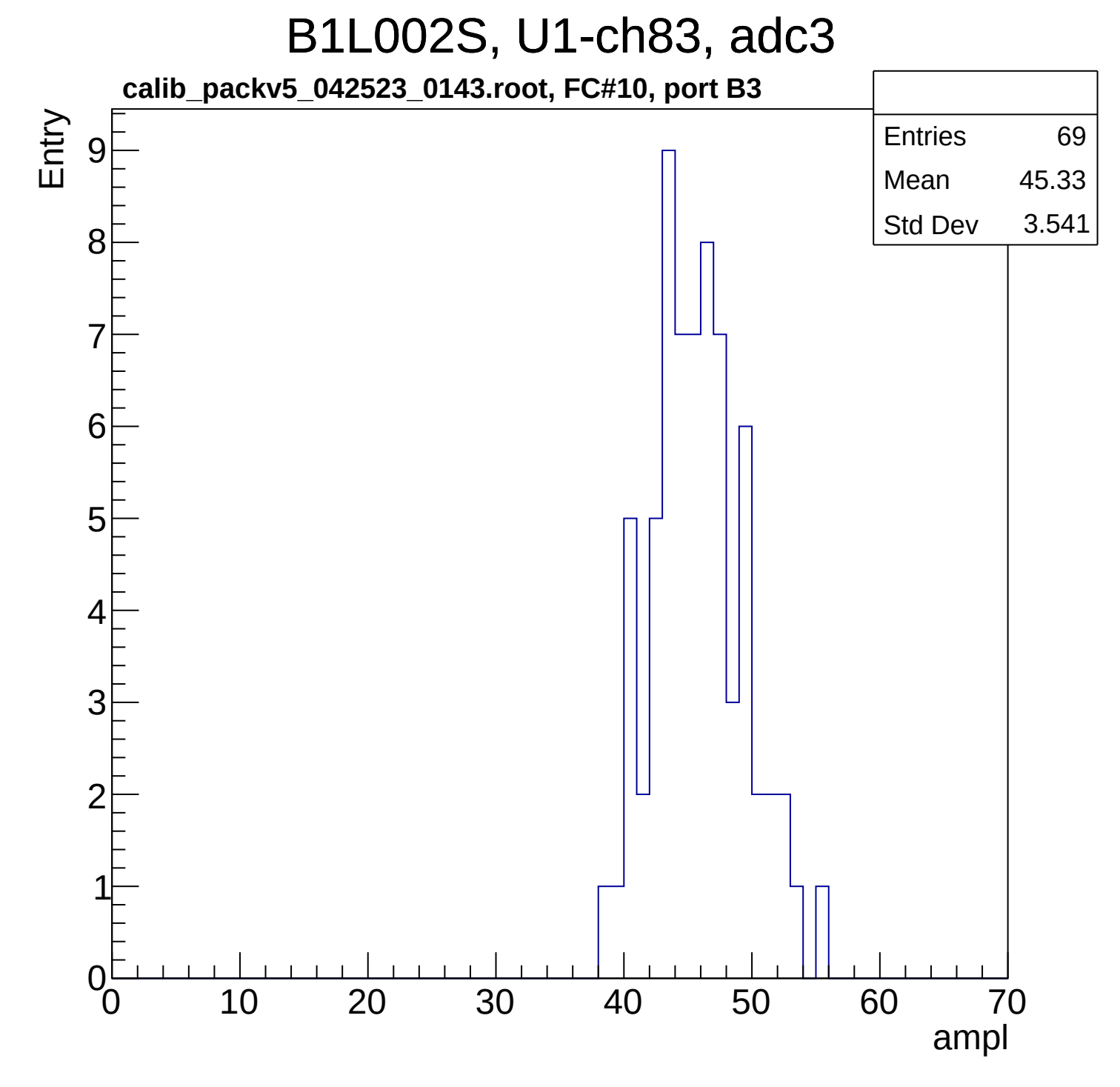
B1L002S, U1-ch83, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 45.33
Std Dev 3.541
Entry
ampl
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0
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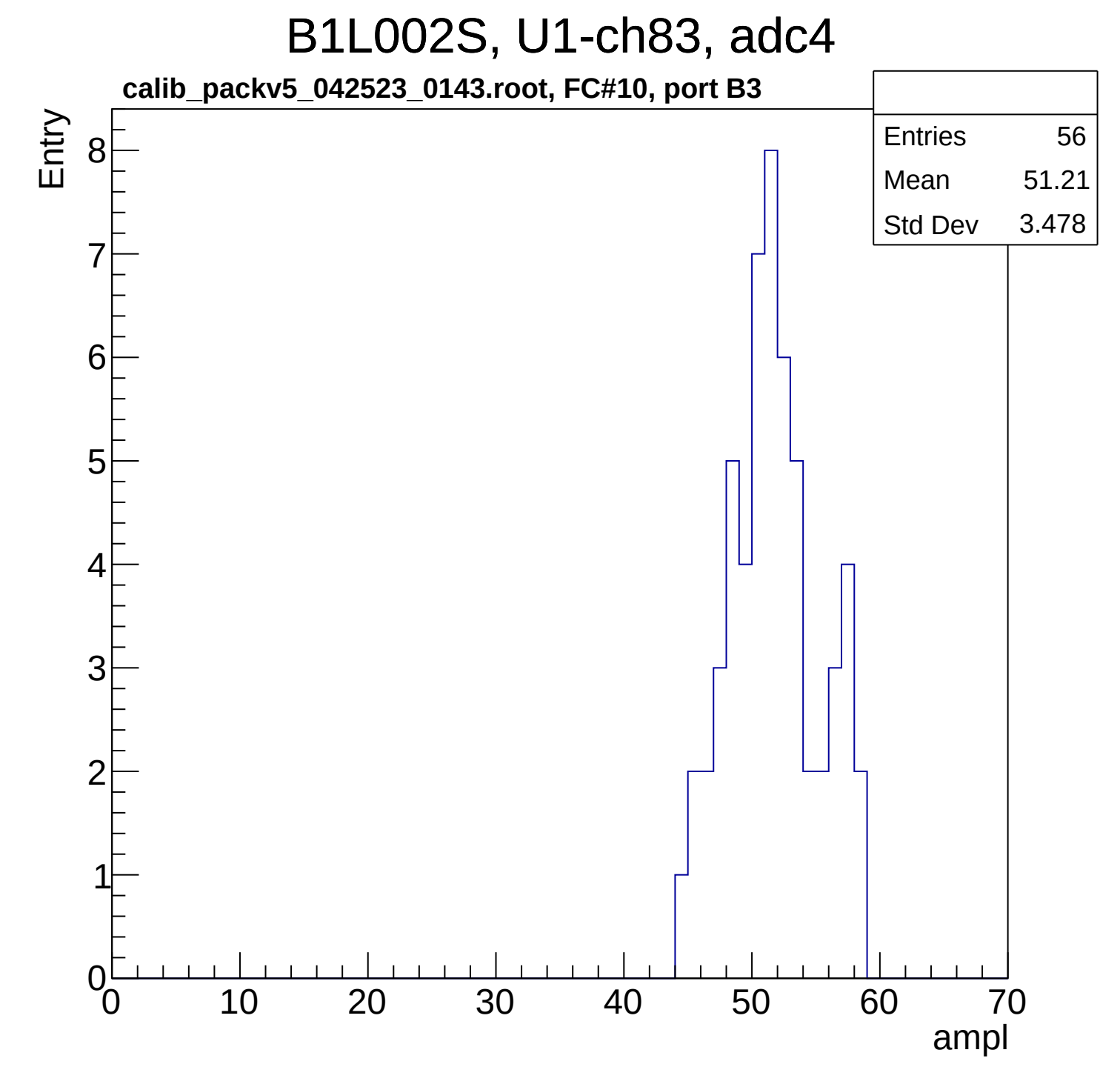
B1L002S, U1-ch83, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 51.21
Std Dev 3.478
Entry
ampl
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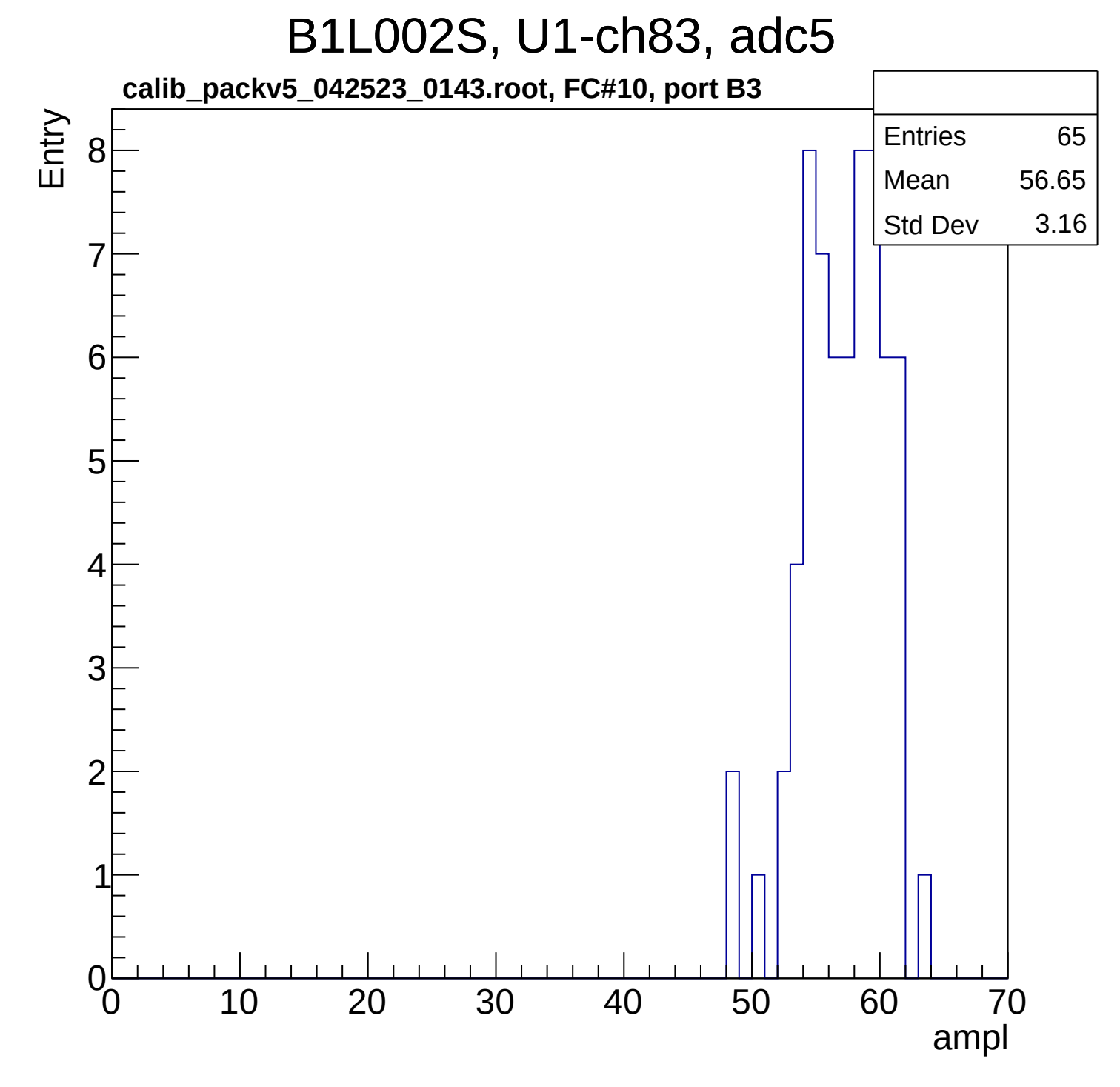
B1L002S, U1-ch83, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 56.65
Std Dev 3.16
Entry
ampl
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0
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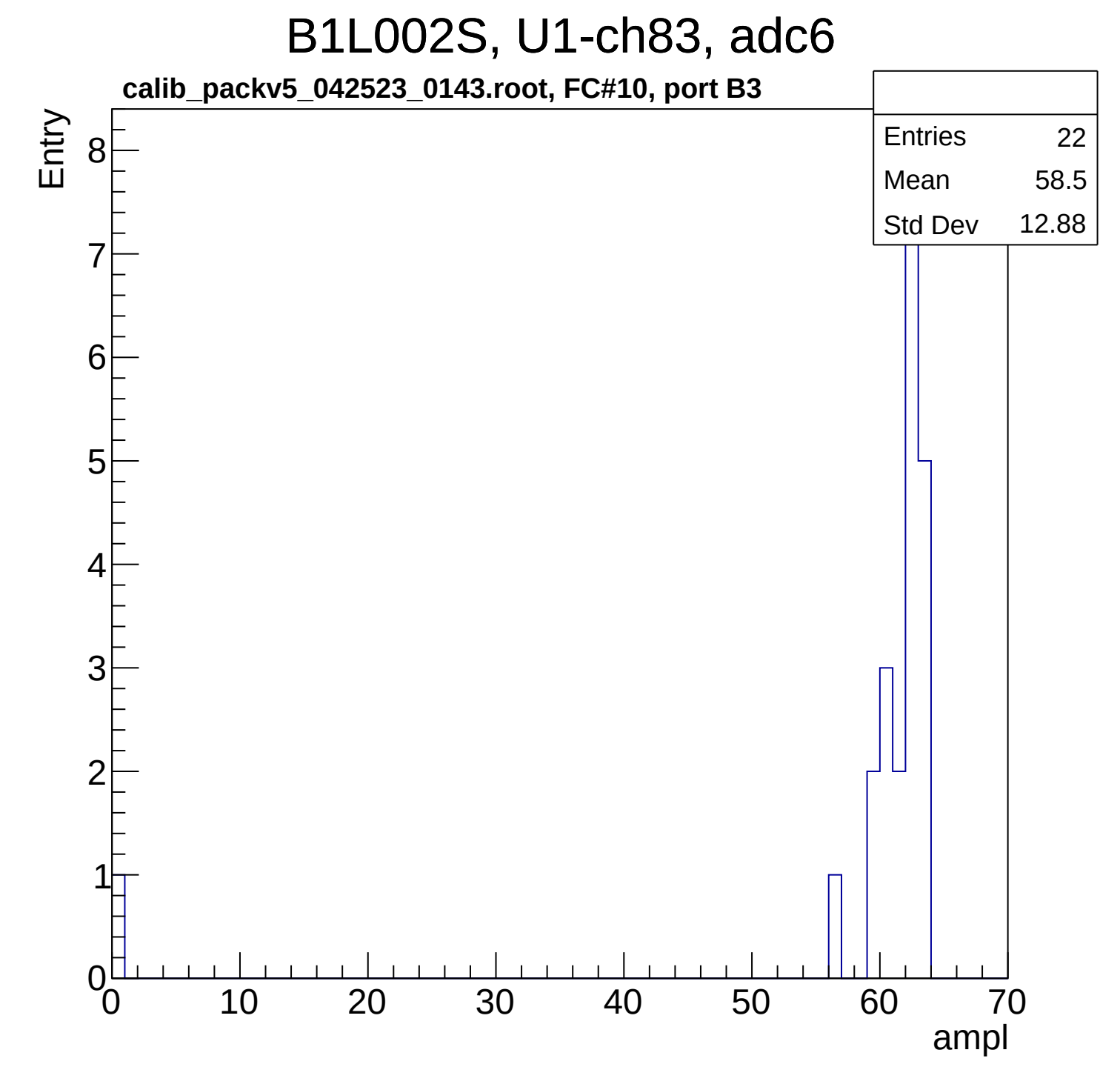
B1L002S, U1-ch83, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.5
Std Dev 12.88
Entry
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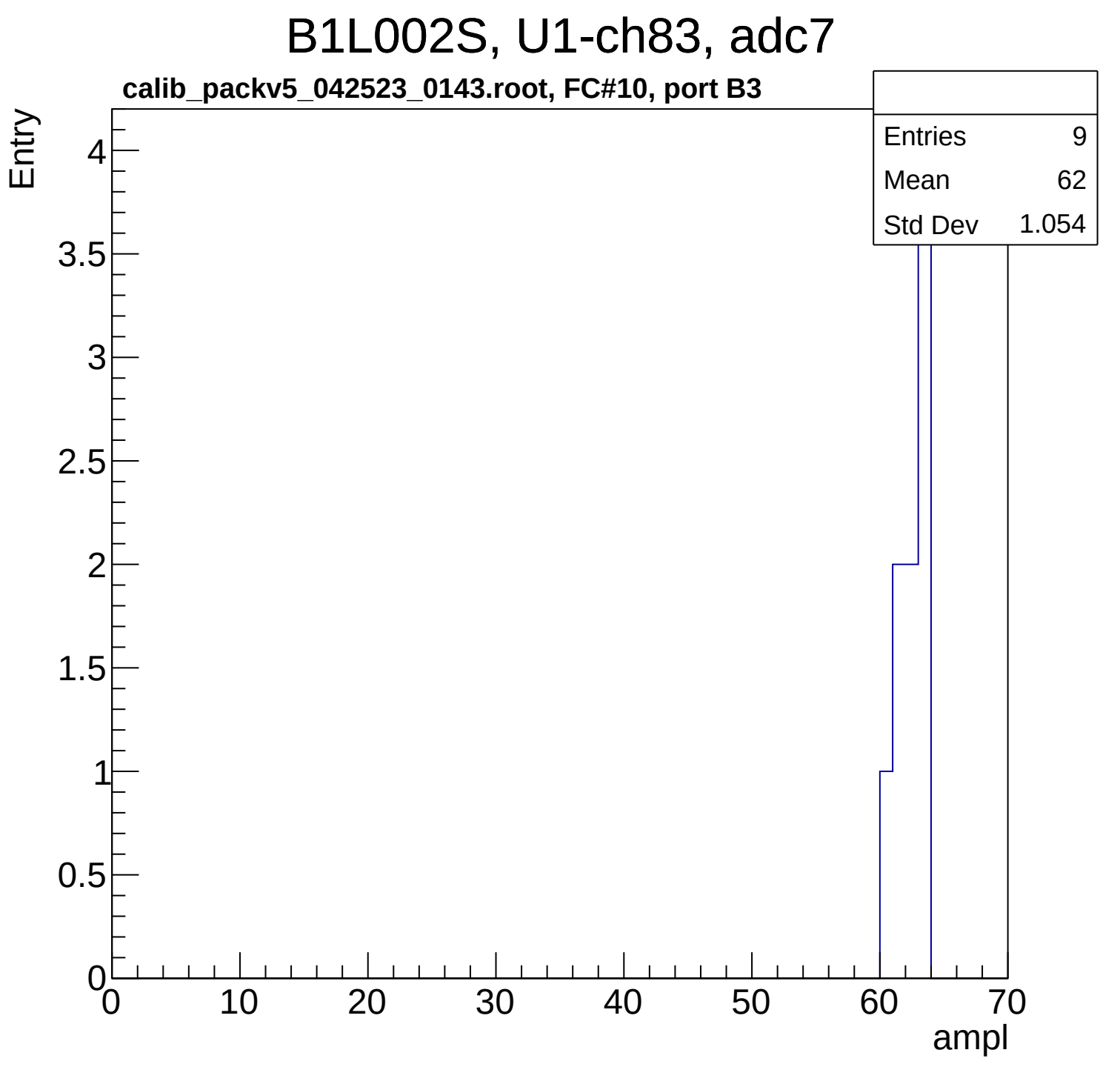
70
ampl


B1L002S, U1-ch83, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 62
Std Dev 1.054
Entry
ampl
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2
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0
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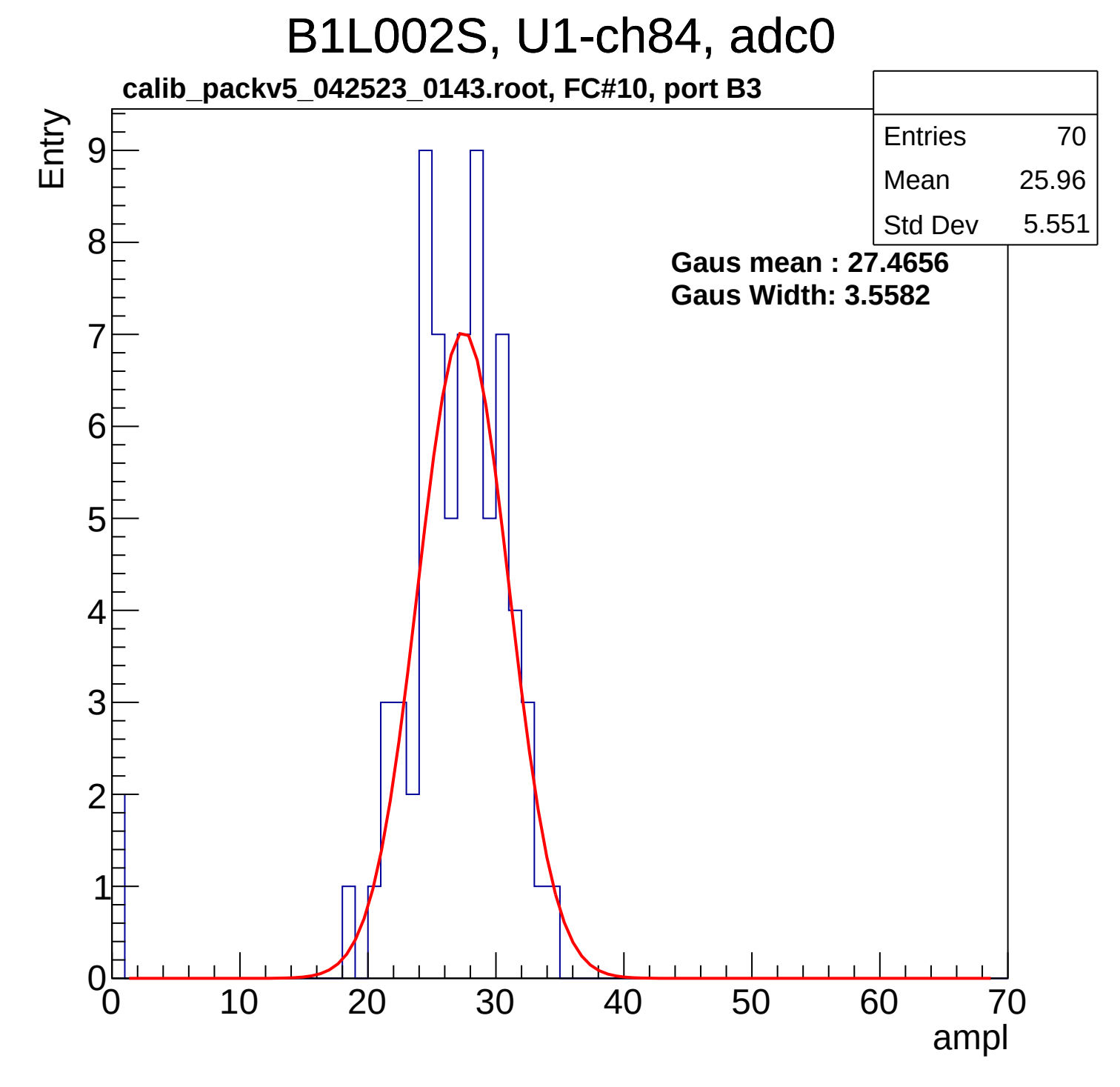
B1L002S, U1-ch84, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 25.96
Std Dev 5.551
Gaus mean : 27.4656
Gaus Width: 3.5582
Entry
ampl
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2
3
4
5
6
7
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0
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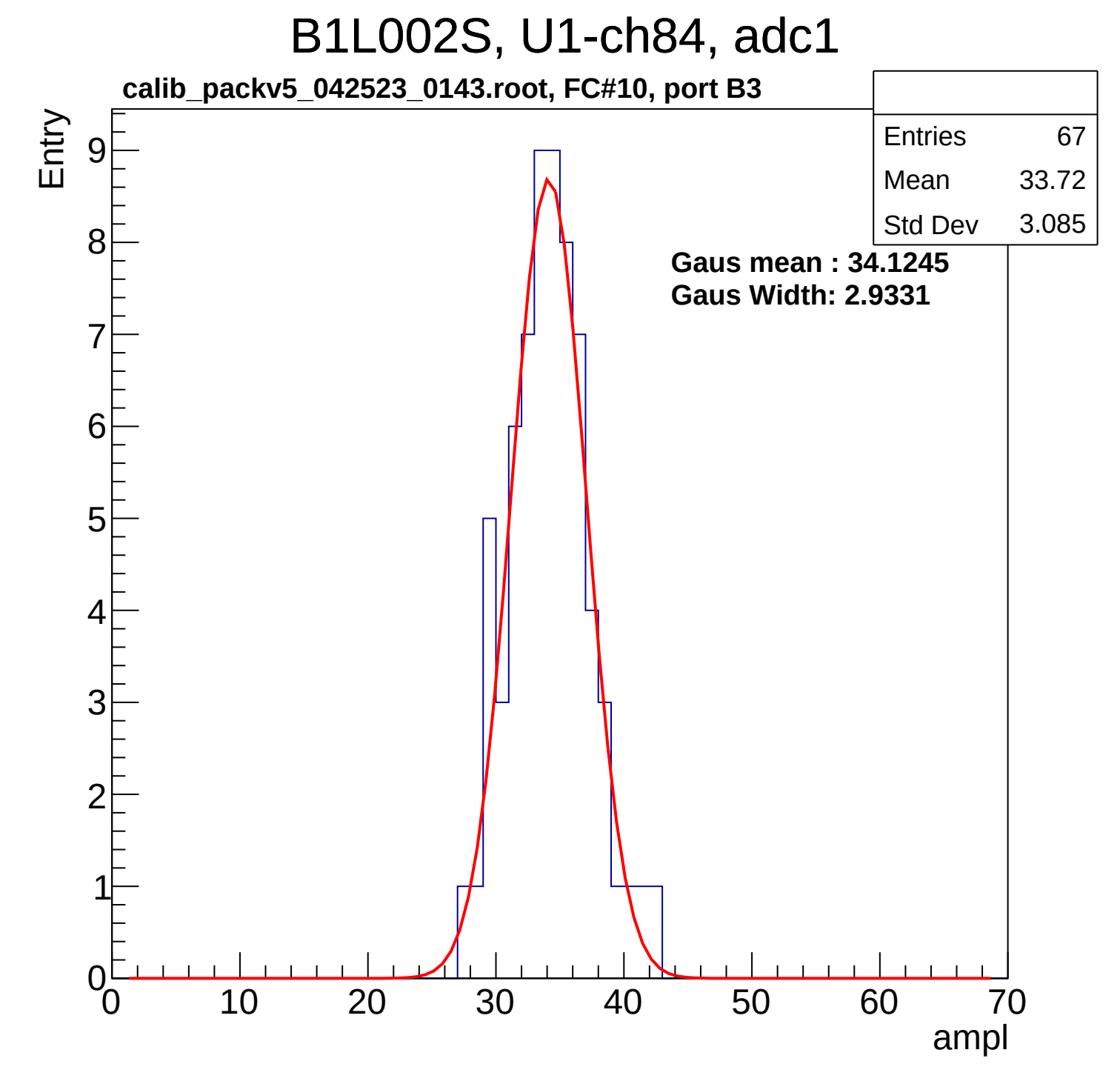
B1L002S, U1-ch84, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.72
Std Dev 3.085
Gaus mean : 34.1245
Gaus Width: 2.9331
Entry
ampl
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3
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0
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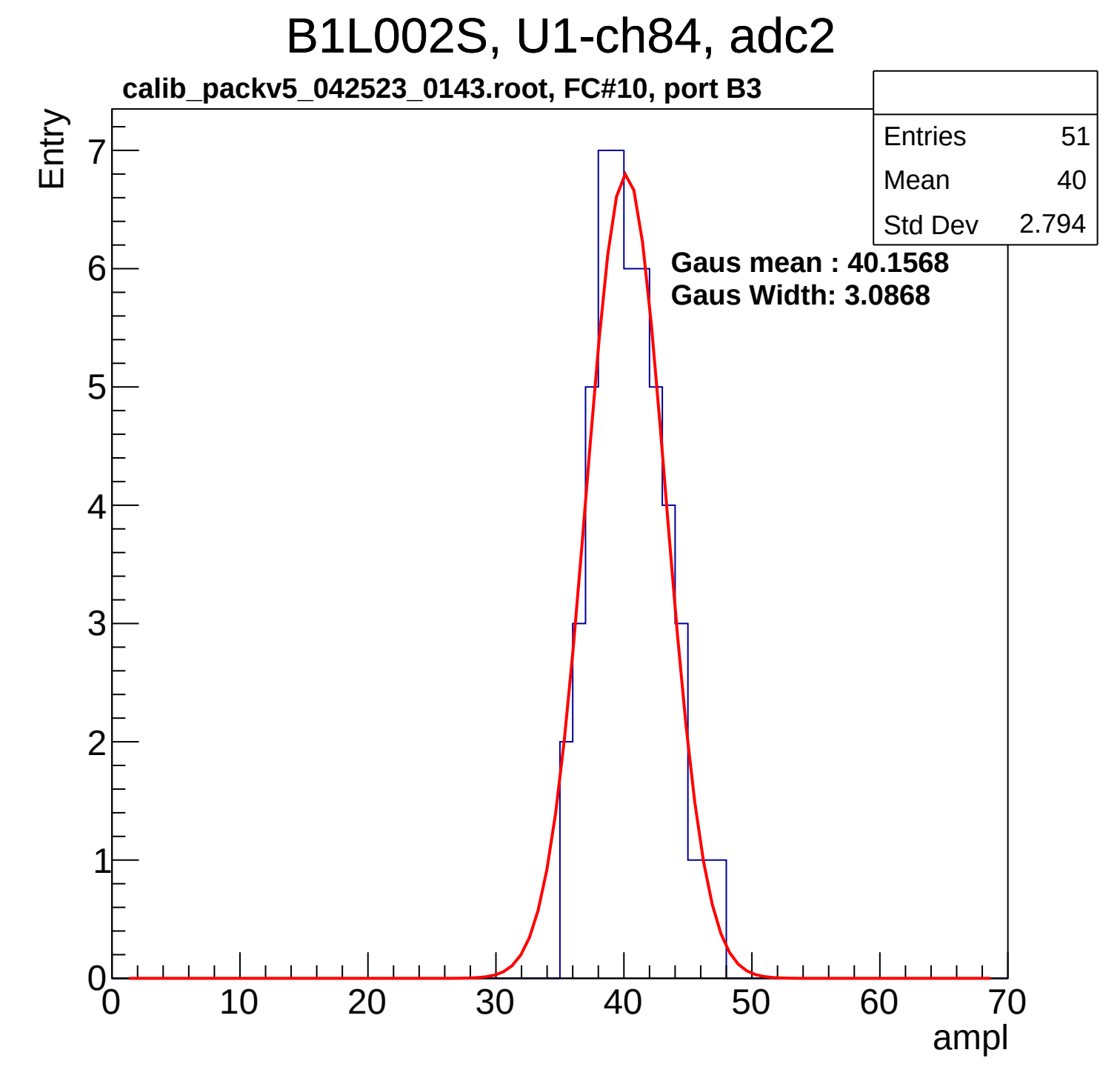
B1L002S, U1-ch84, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 40
Std Dev 2.794
Gaus mean : 40.1568
Gaus Width: 3.0868
Entry
ampl
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3
4
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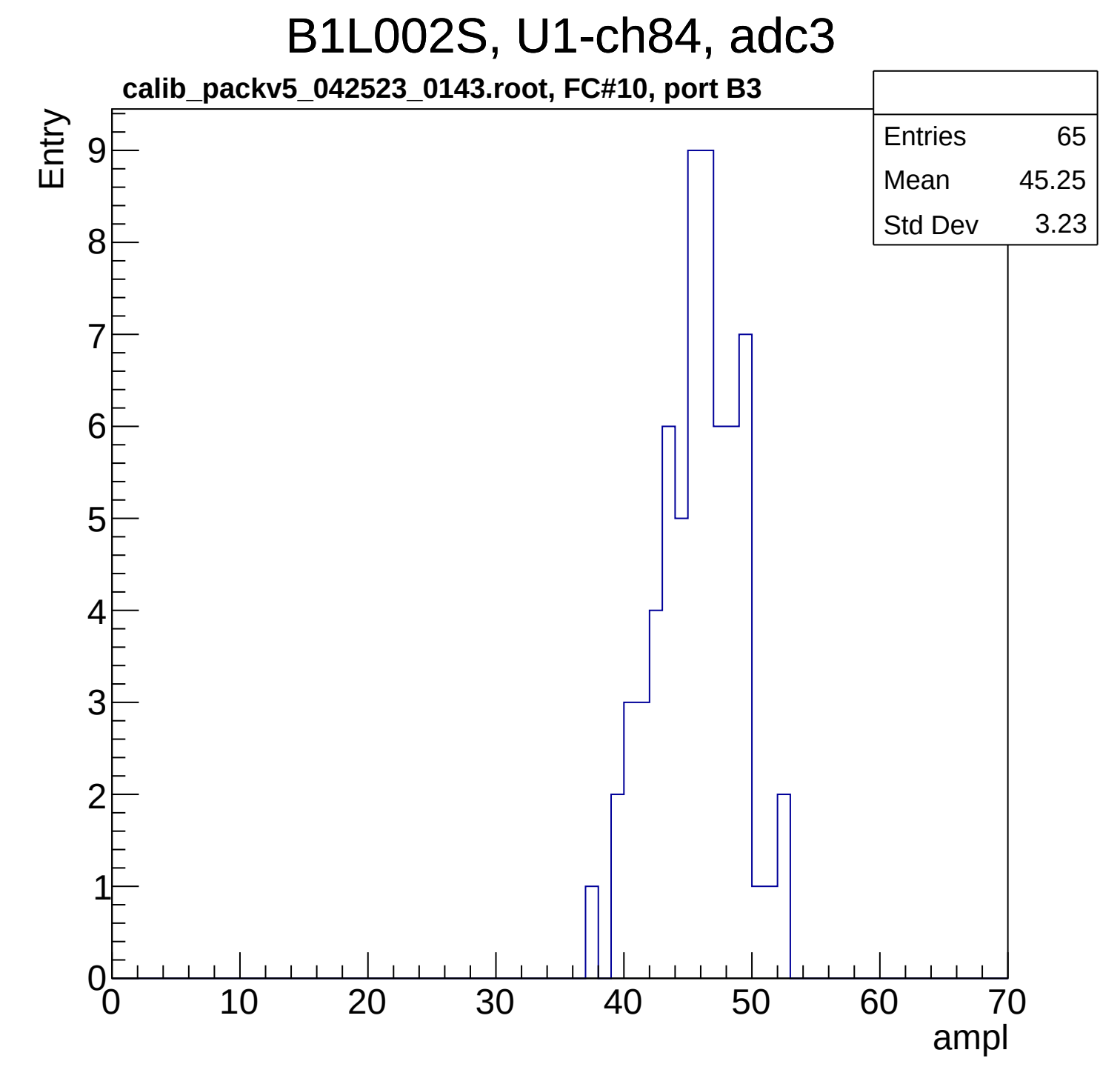
B1L002S, U1-ch84, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.25
Std Dev 3.23
Entry
ampl
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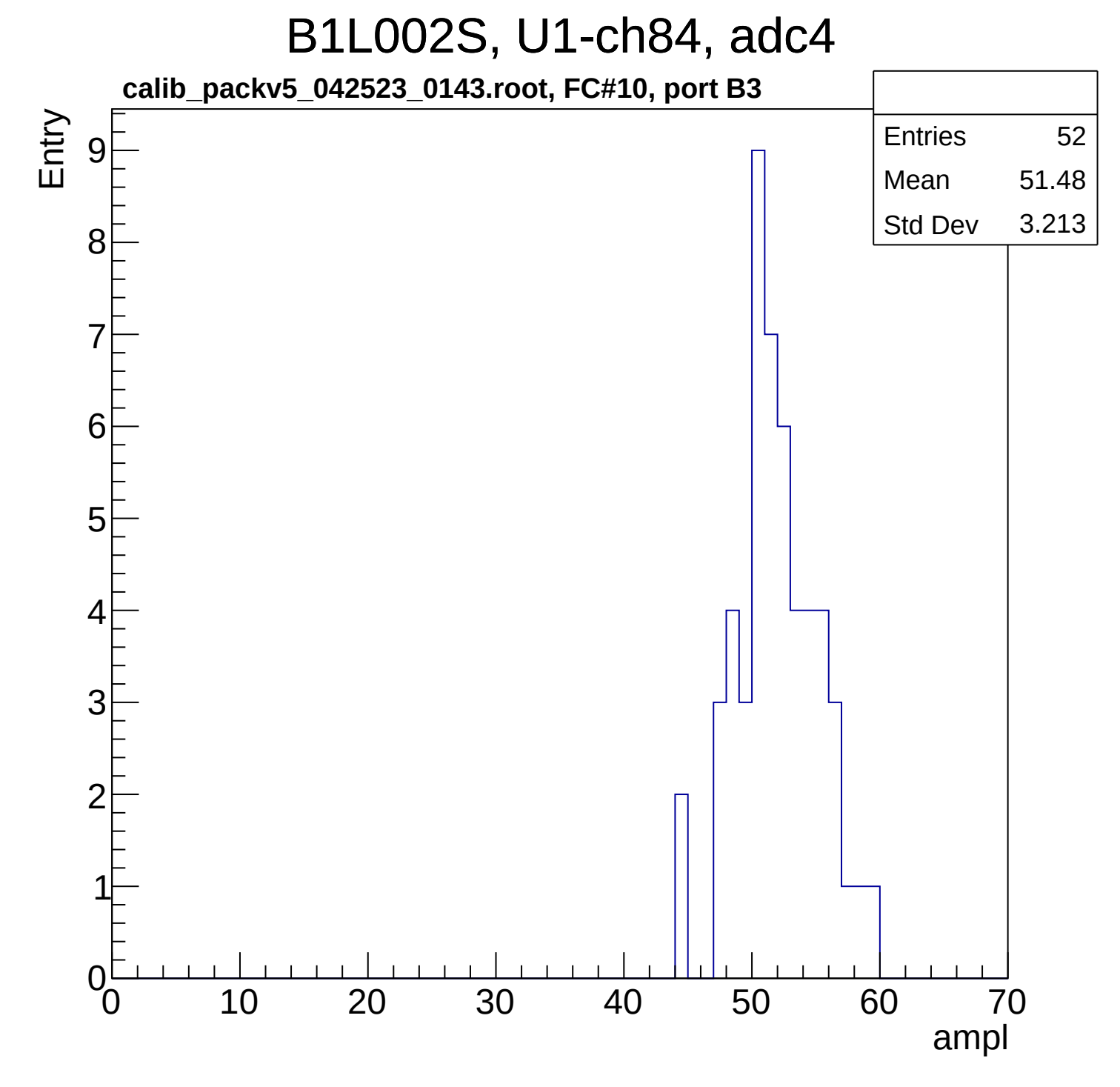
B1L002S, U1-ch84, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 51.48
Std Dev 3.213
Entry
ampl
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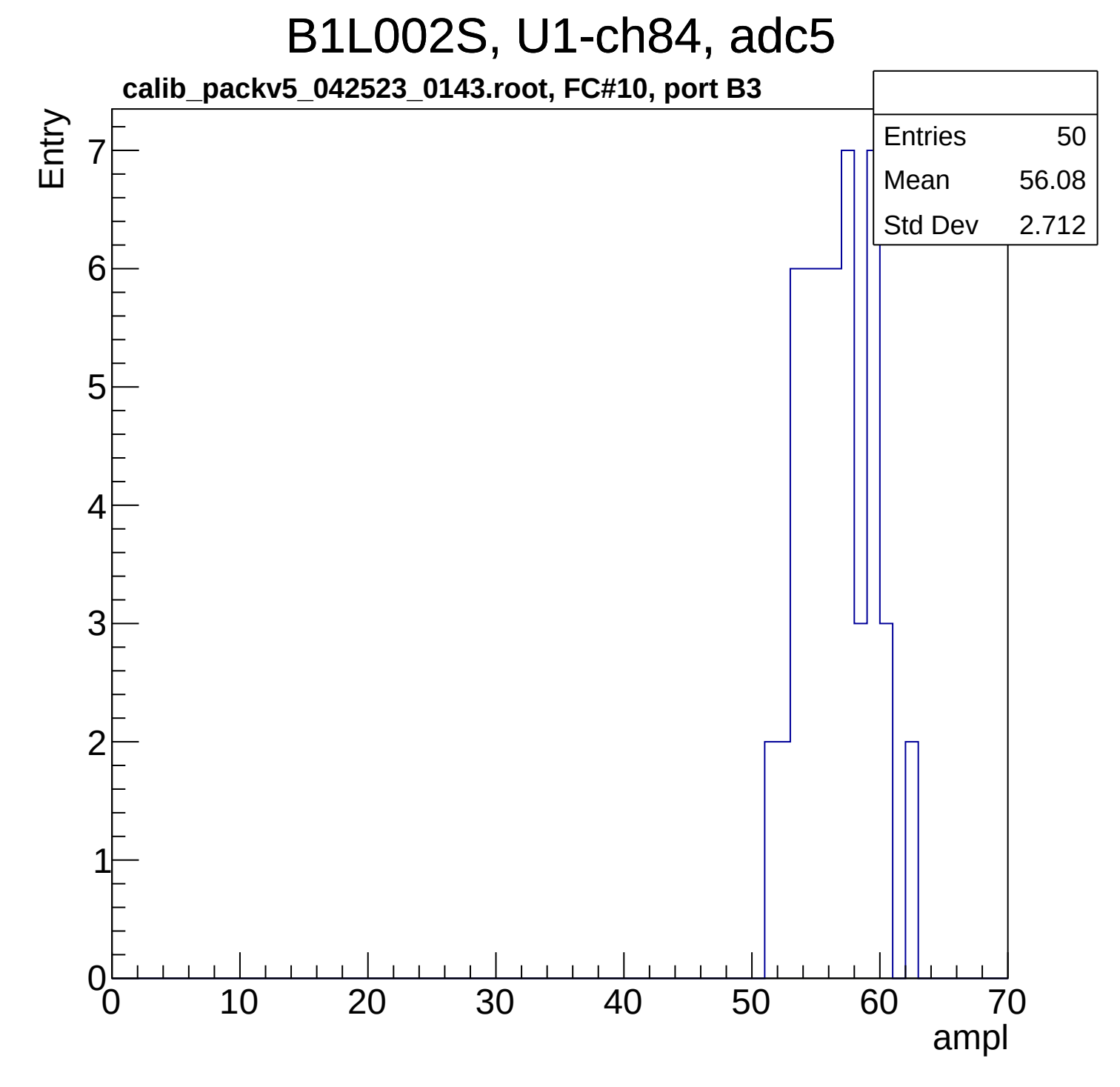
B1L002S, U1-ch84, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 56.08
Std Dev 2.712
Entry
ampl
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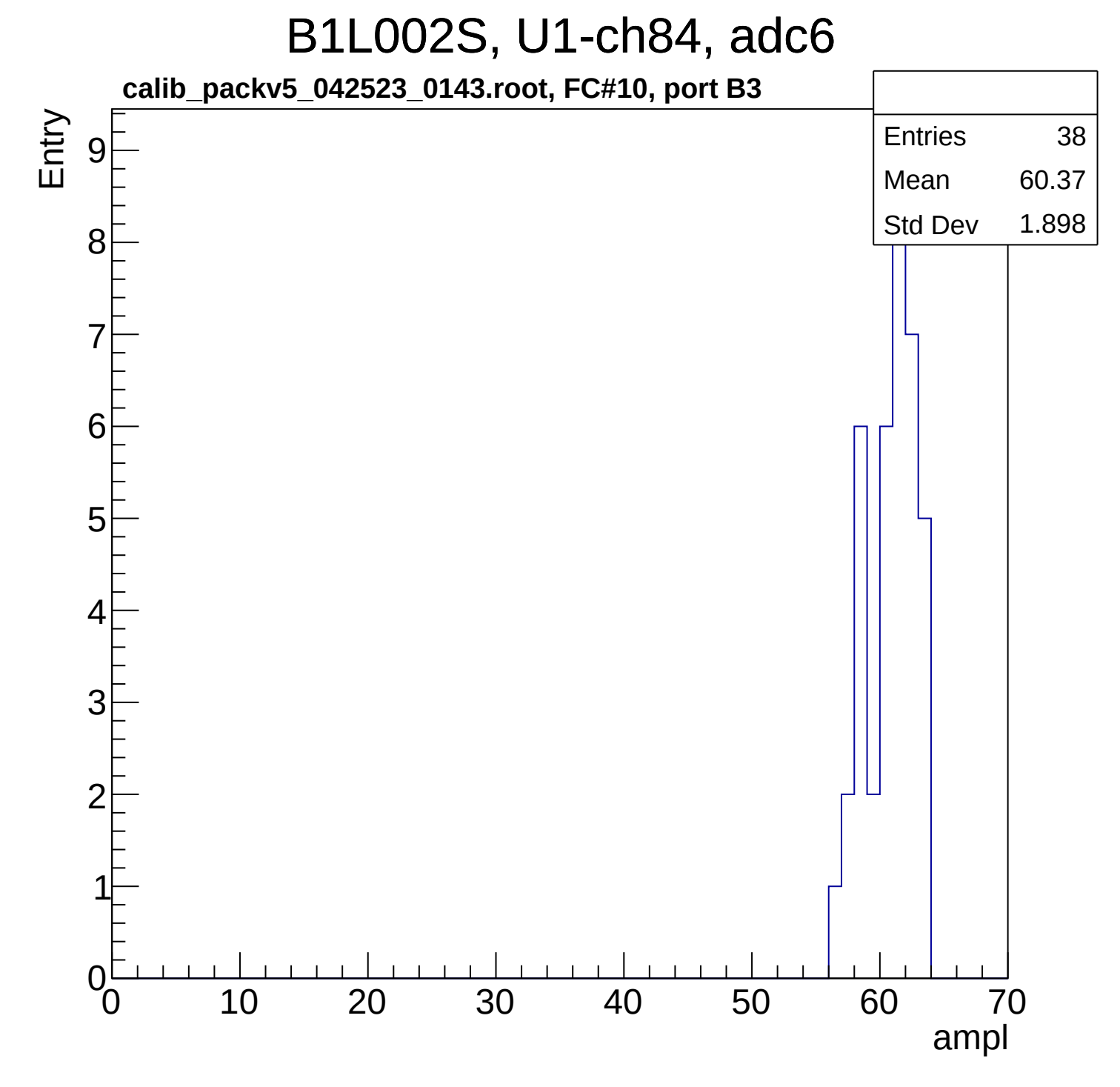
B1L002S, U1-ch84, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 60.37
Std Dev 1.898
Entry
ampl
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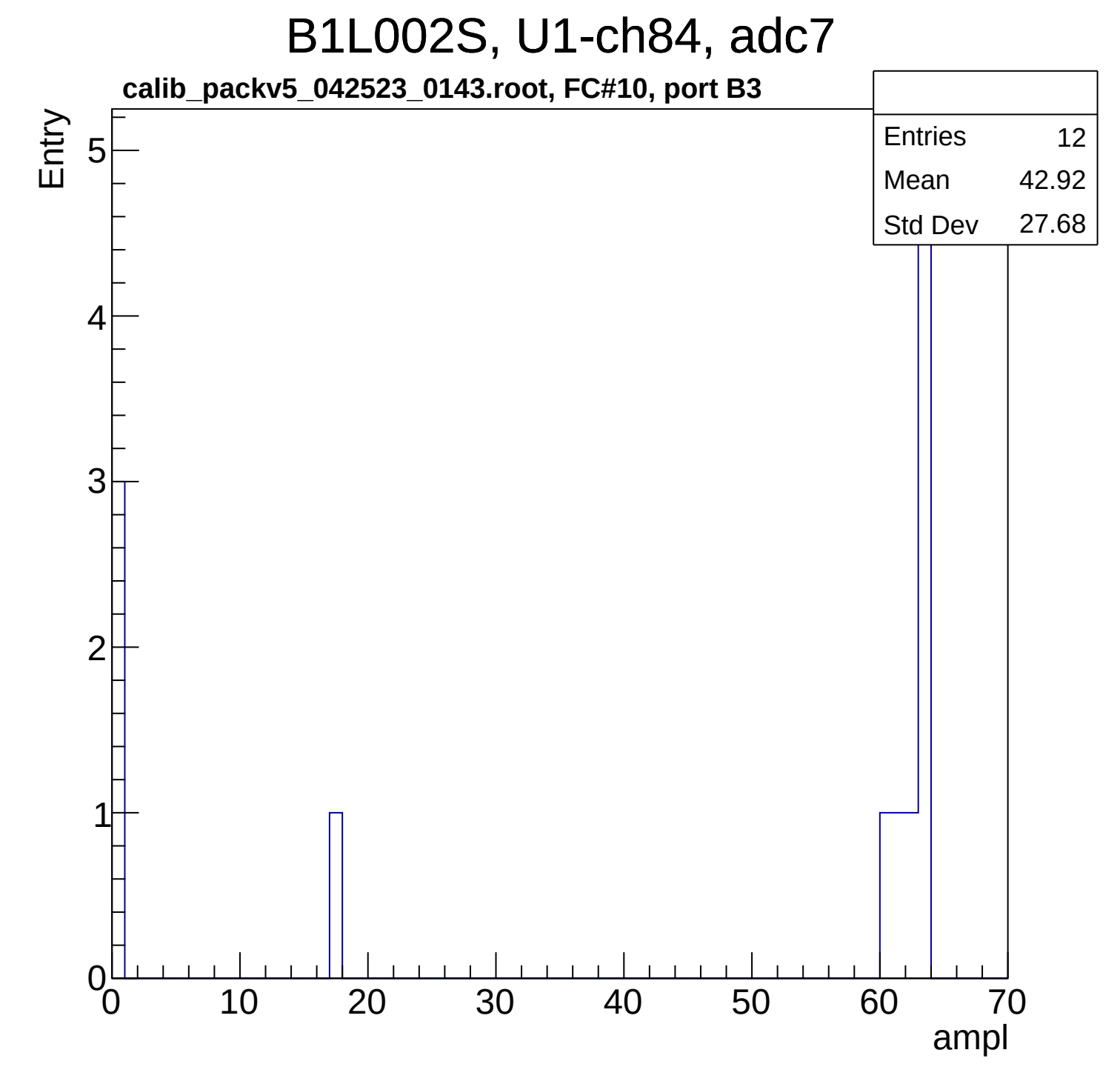
B1L002S, U1-ch84, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 42.92
Std Dev 27.68
Entry
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2
3
4
5
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10
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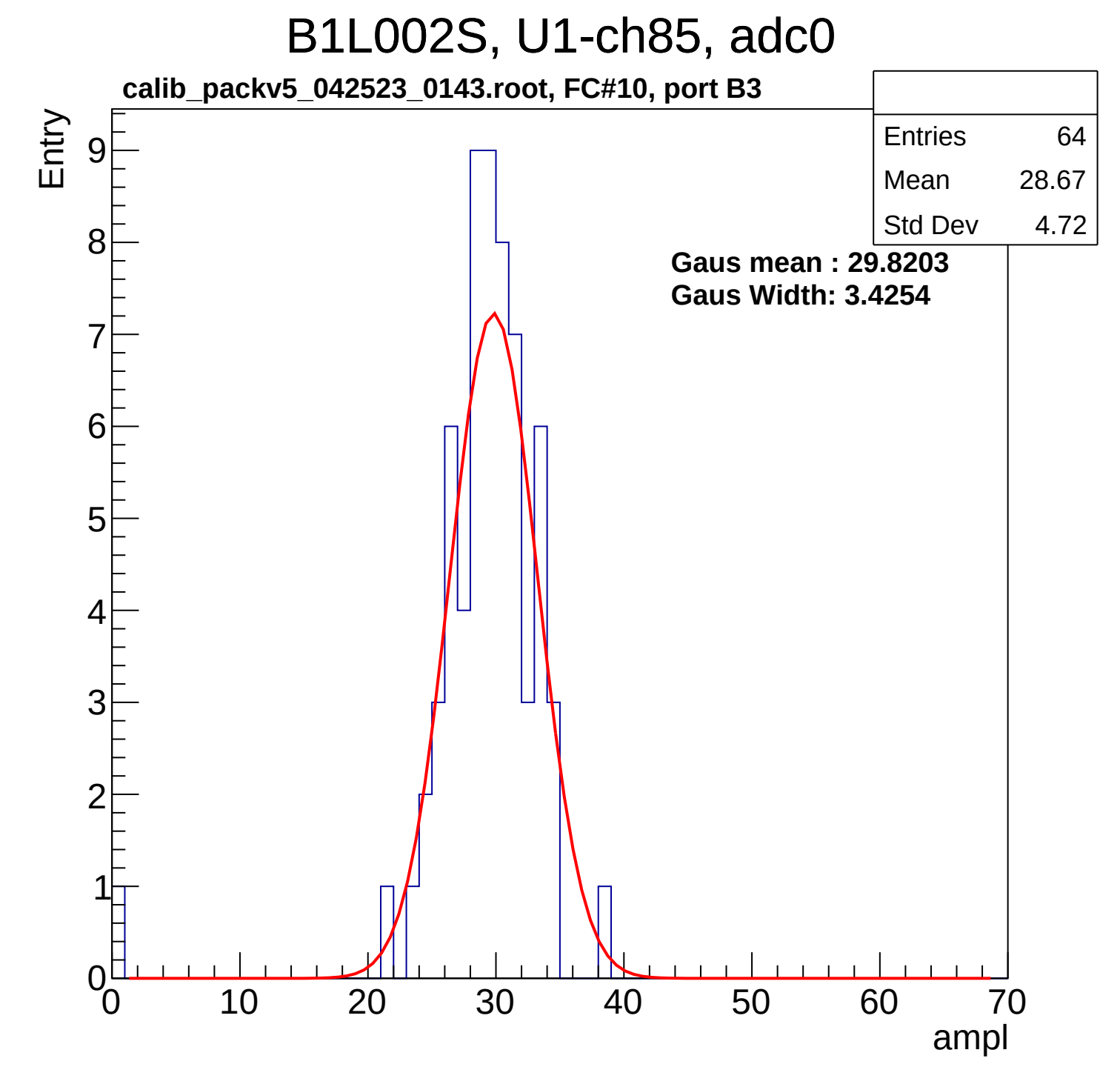
70
ampl


B1L002S, U1-ch85, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.67
Std Dev 4.72
Gaus mean : 29.8203
Gaus Width: 3.4254
Entry
ampl
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2
3
4
5
6
7
8
9
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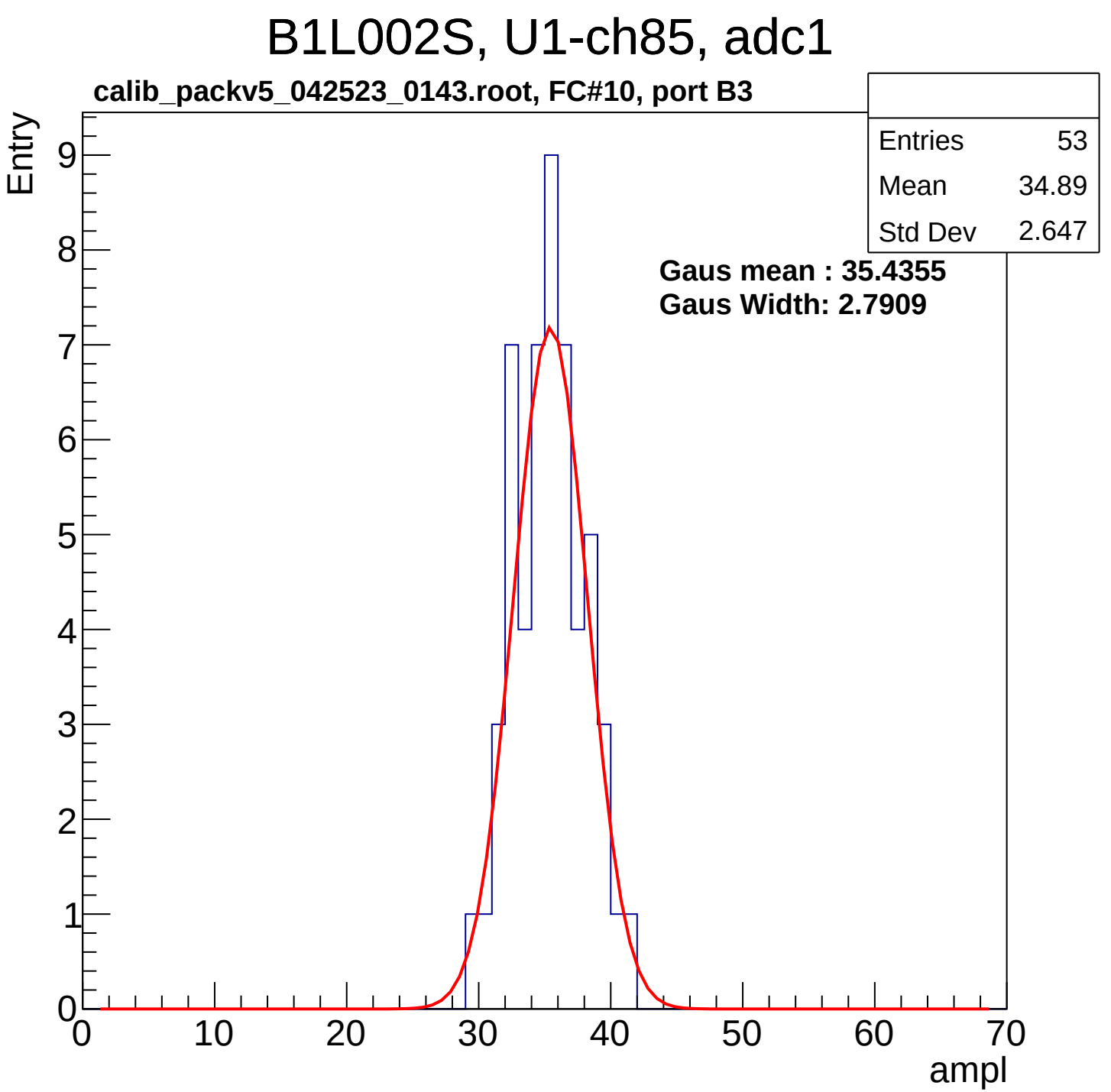
B1L002S, U1-ch85, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 34.89
Std Dev 2.647
Gaus mean : 35.4355
Gaus Width: 2.7909
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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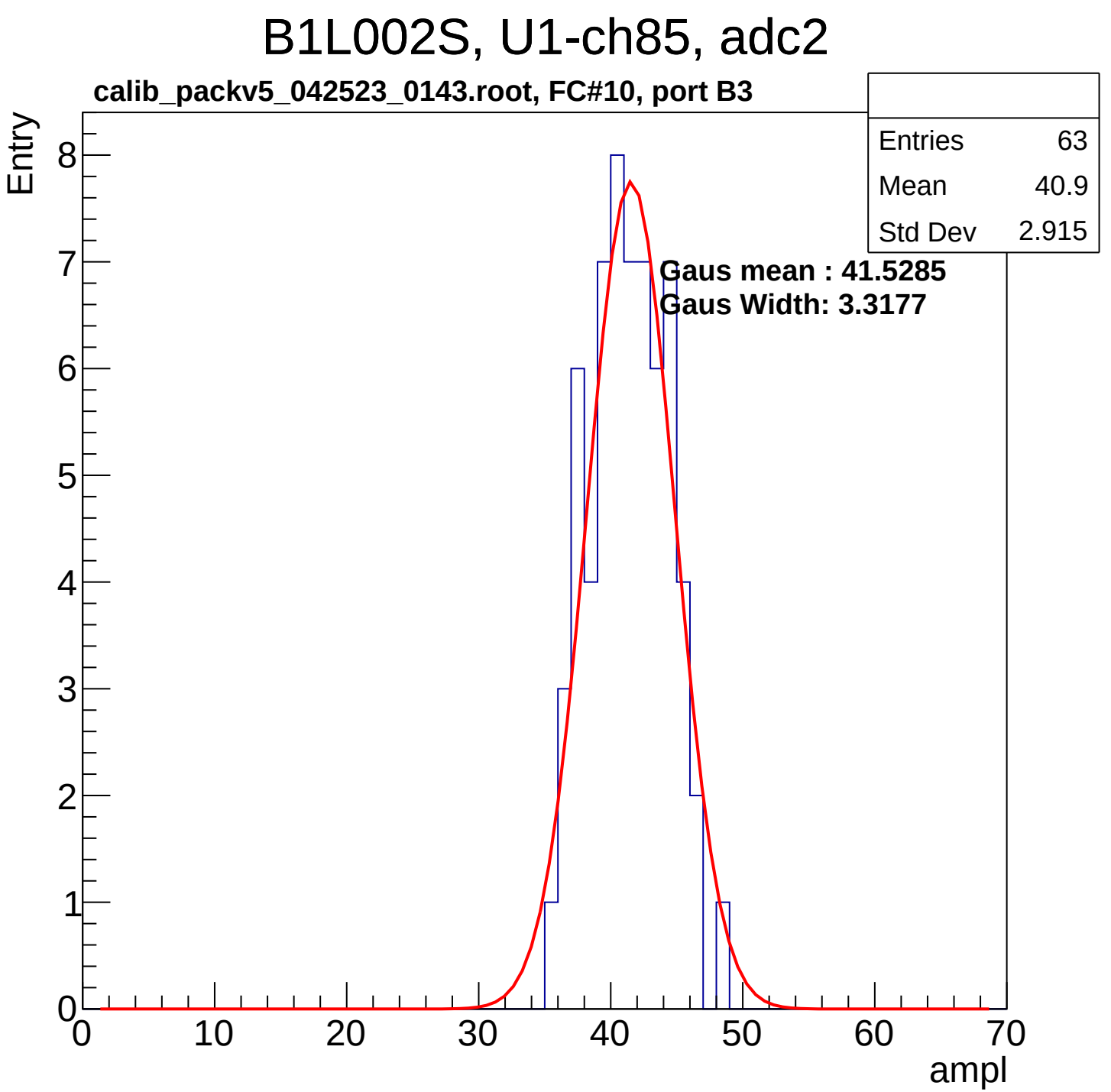
B1L002S, U1-ch85, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.9
Std Dev 2.915
Gaus mean : 41.5285
Gaus Width: 3.3177
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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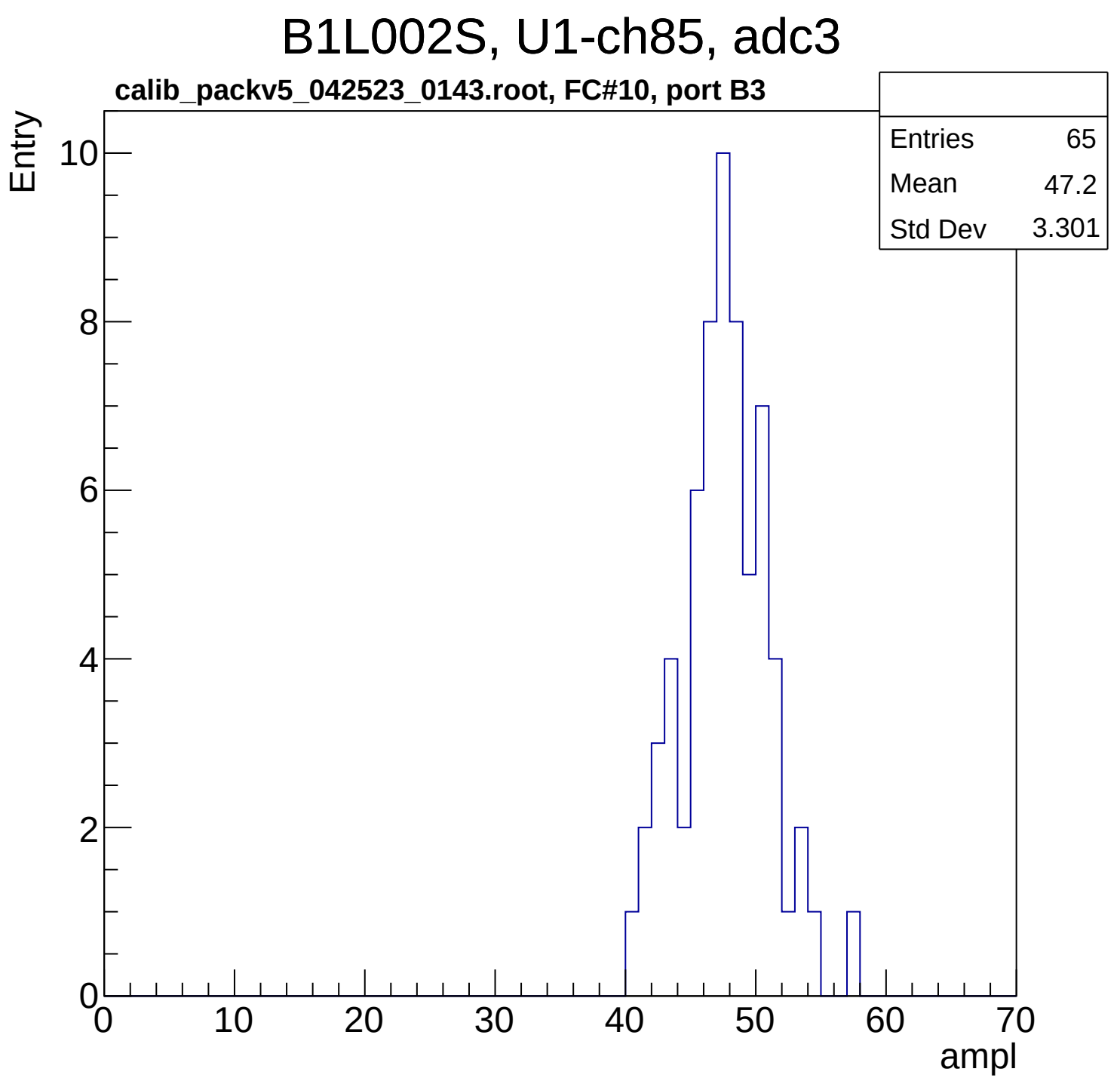
B1L002S, U1-ch85, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.2
Std Dev 3.301
Entry
0
2
4
6
8
10
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10
20
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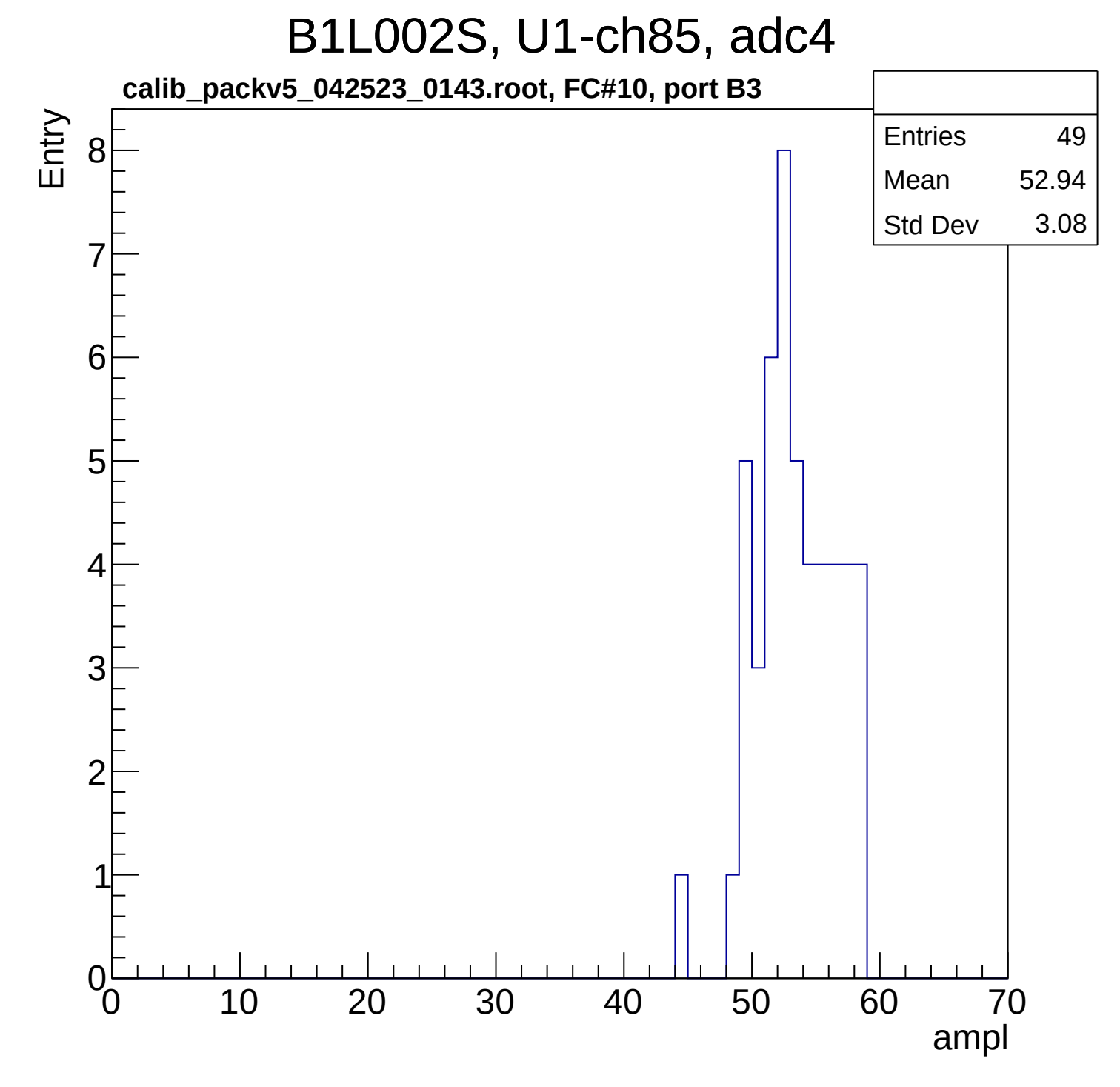
50
60
70
ampl


B1L002S, U1-ch85, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.94
Std Dev 3.08
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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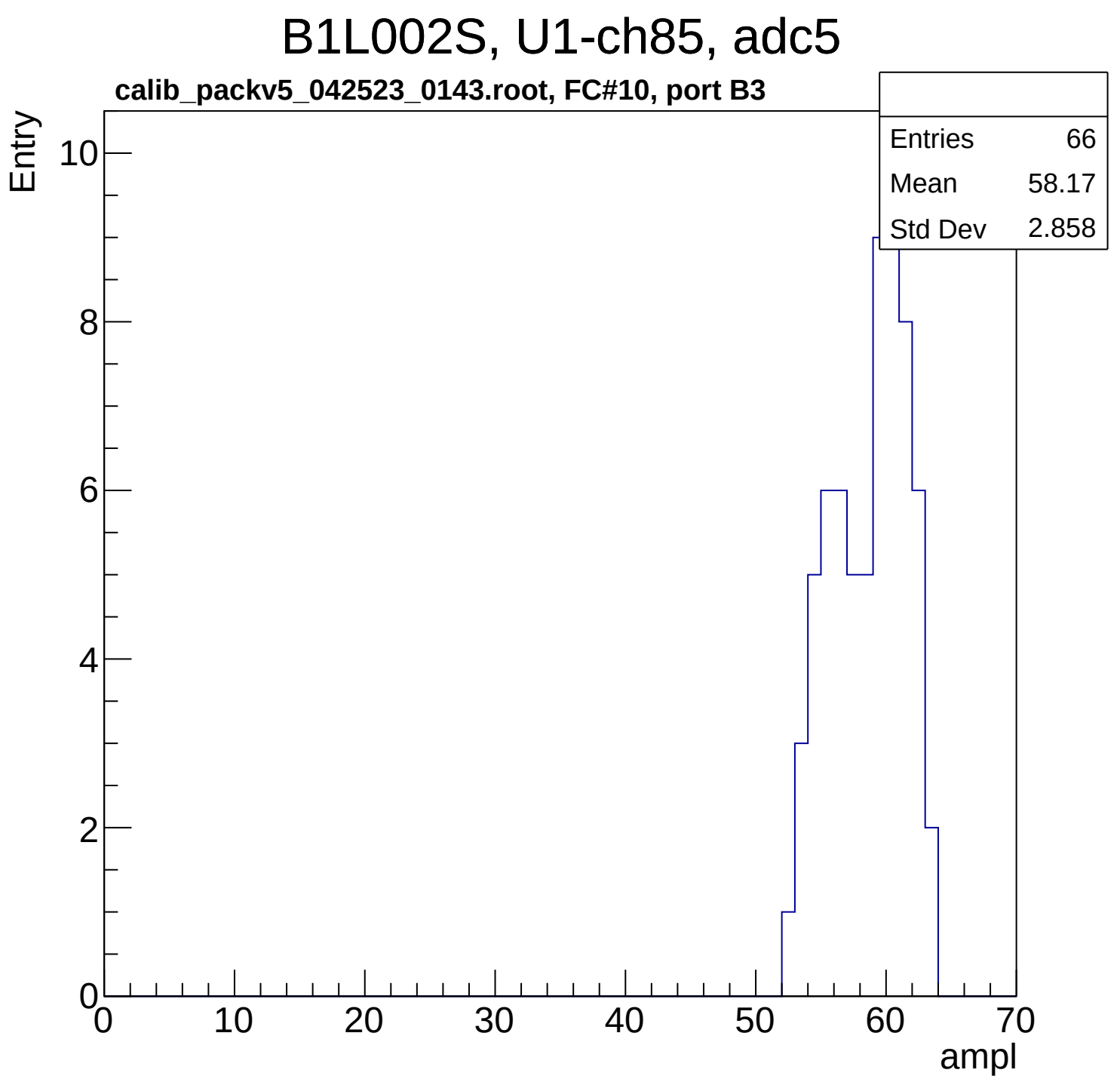
B1L002S, U1-ch85, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 58.17
Std Dev 2.858
Entry
0
2
4
6
8
10
0
10
20
30
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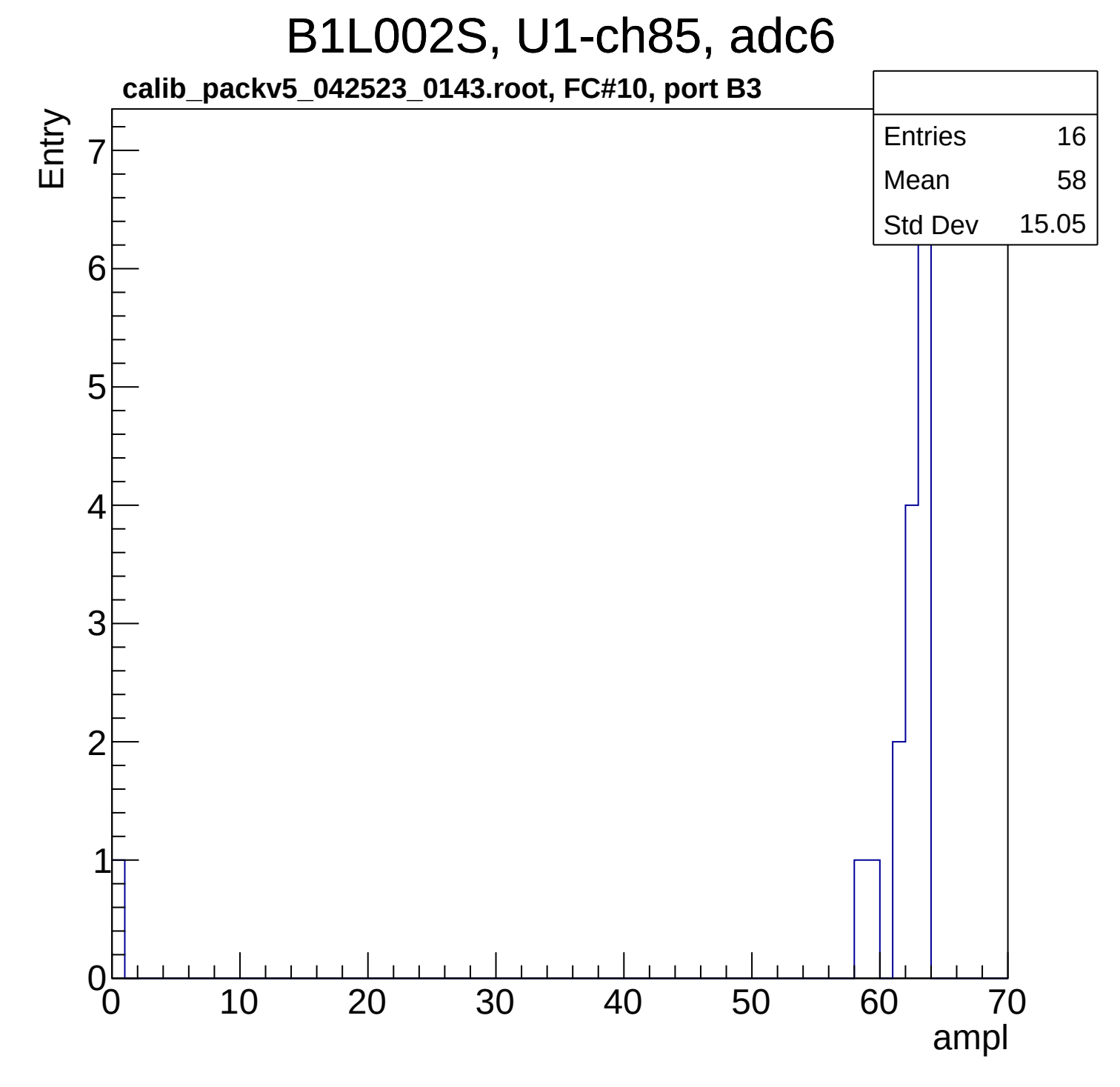
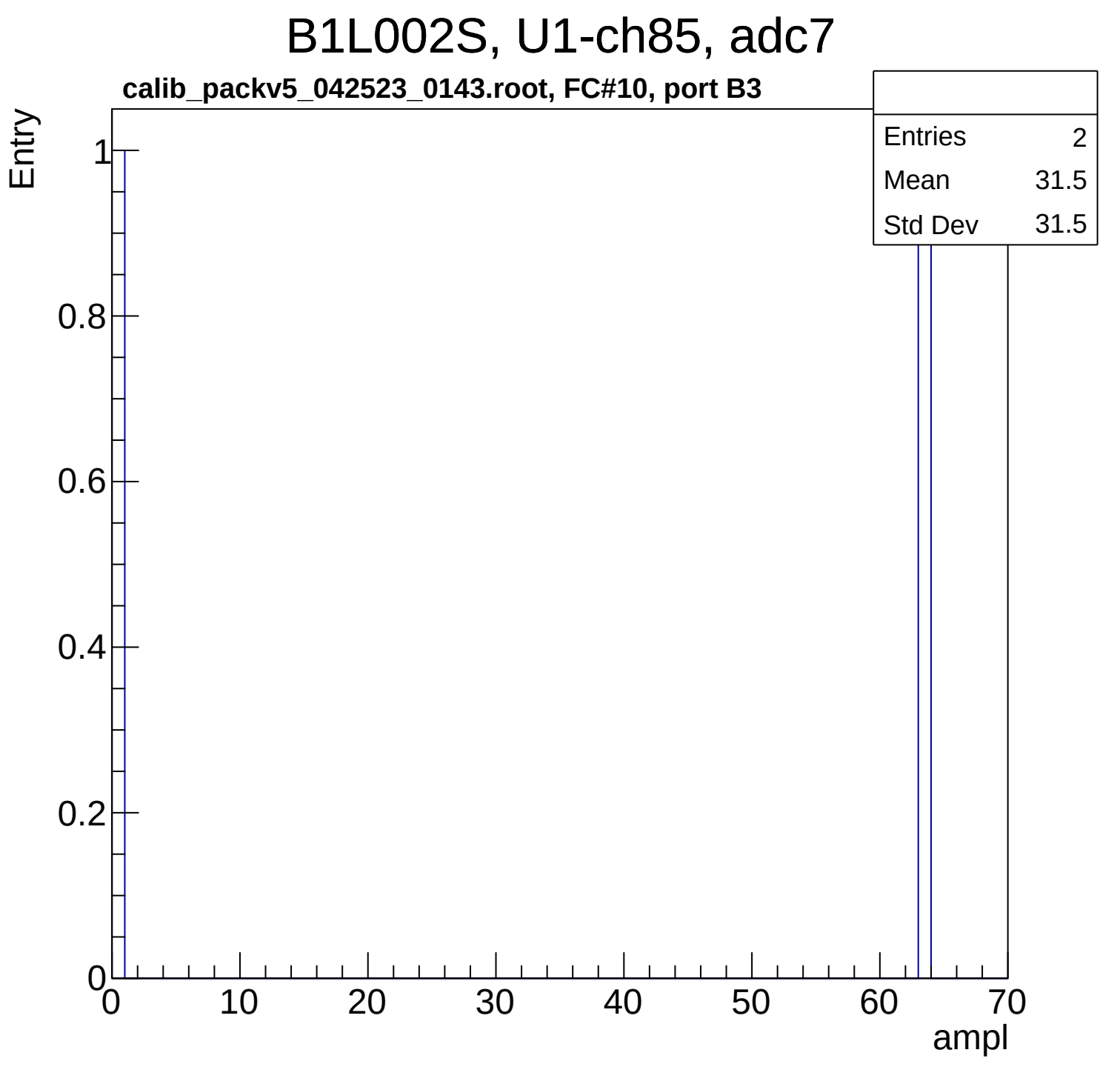
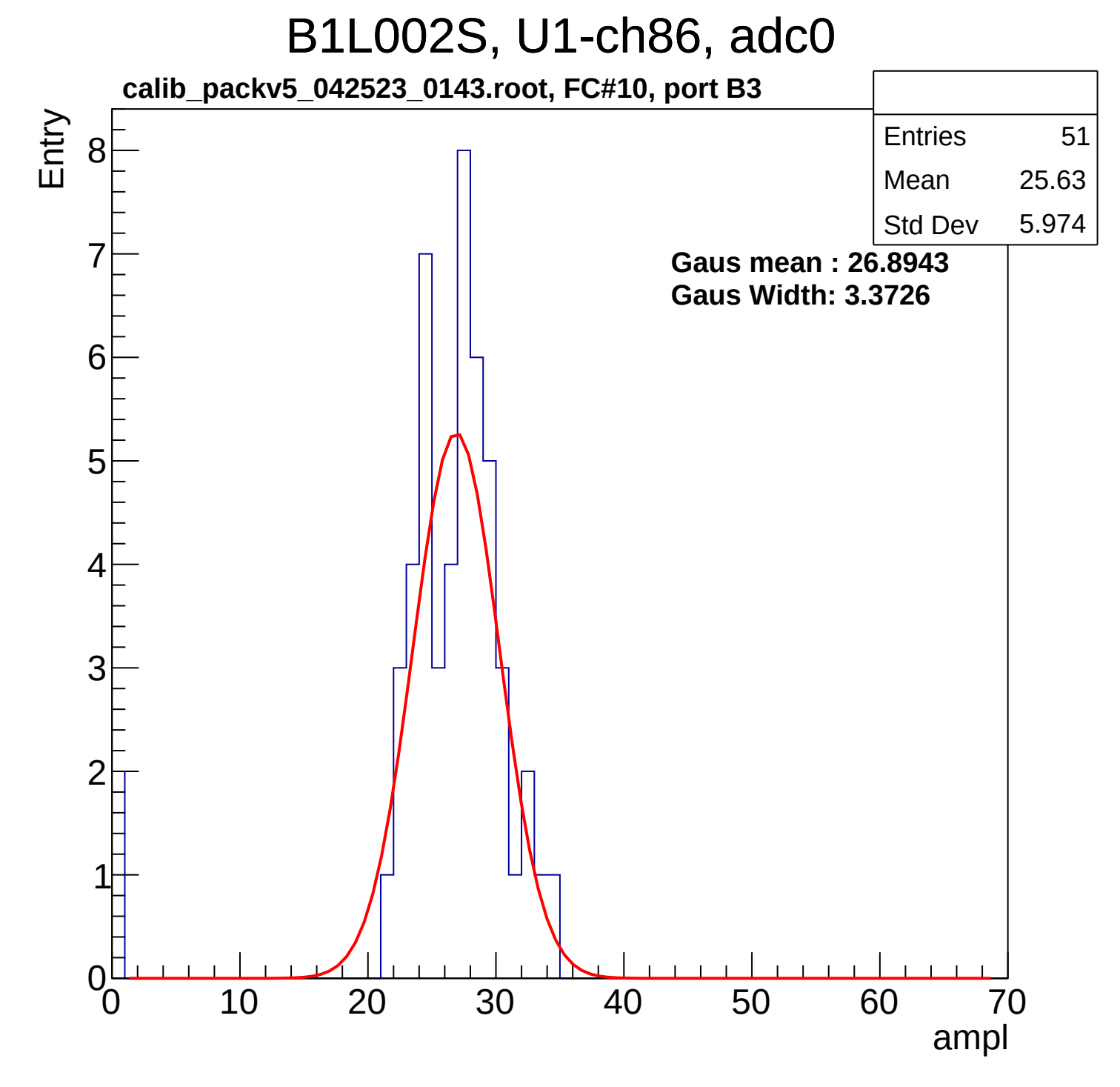
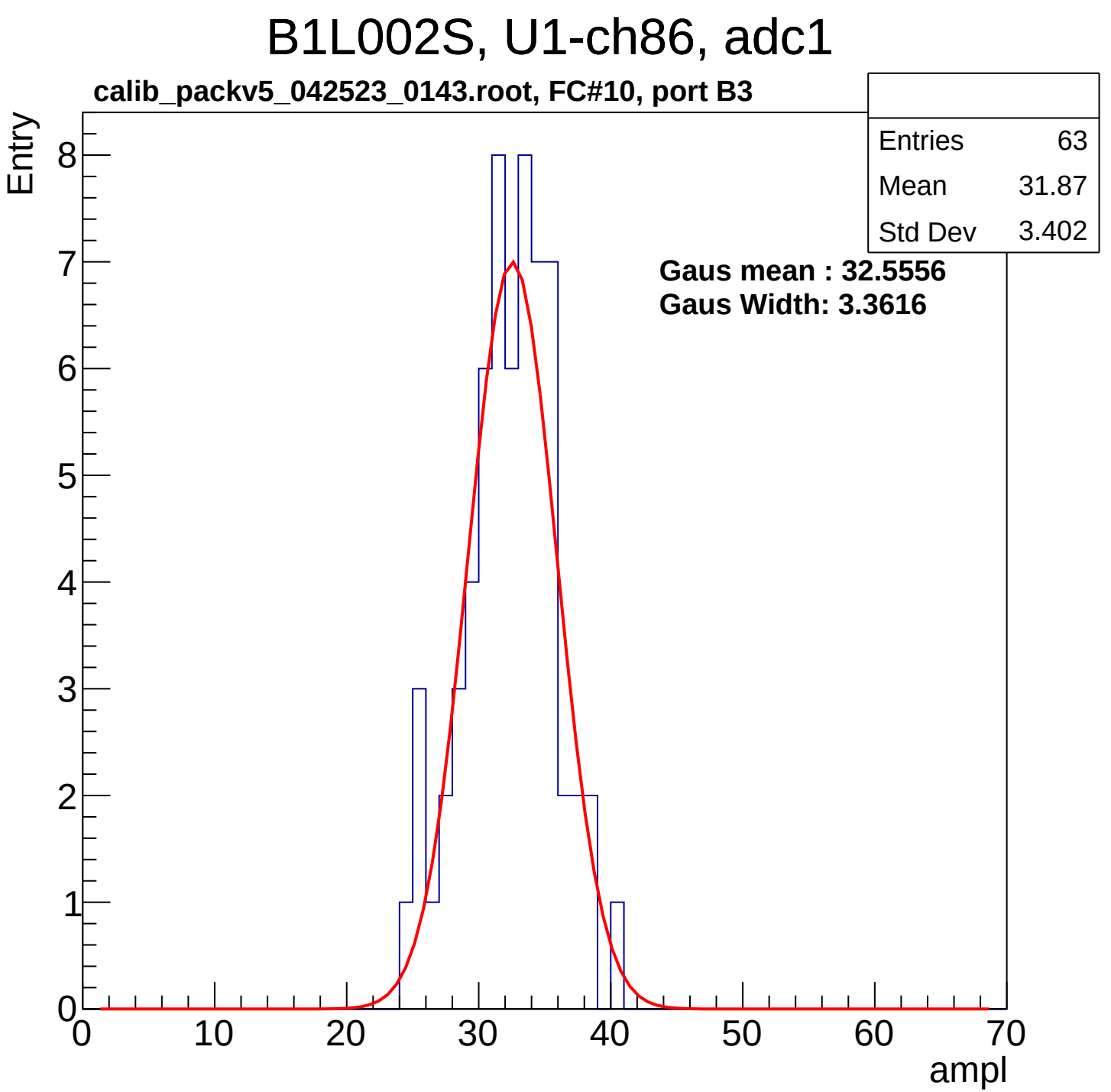
50
60
70
ampl


B1L002S, U1-ch85, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 58
Std Dev 15.05
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch85, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch86, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 25.63
Std Dev 5.974
Gaus mean : 26.8943
Gaus Width: 3.3726
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch86, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 31.87
Std Dev 3.402
Gaus mean : 32.5556
Gaus Width: 3.3616
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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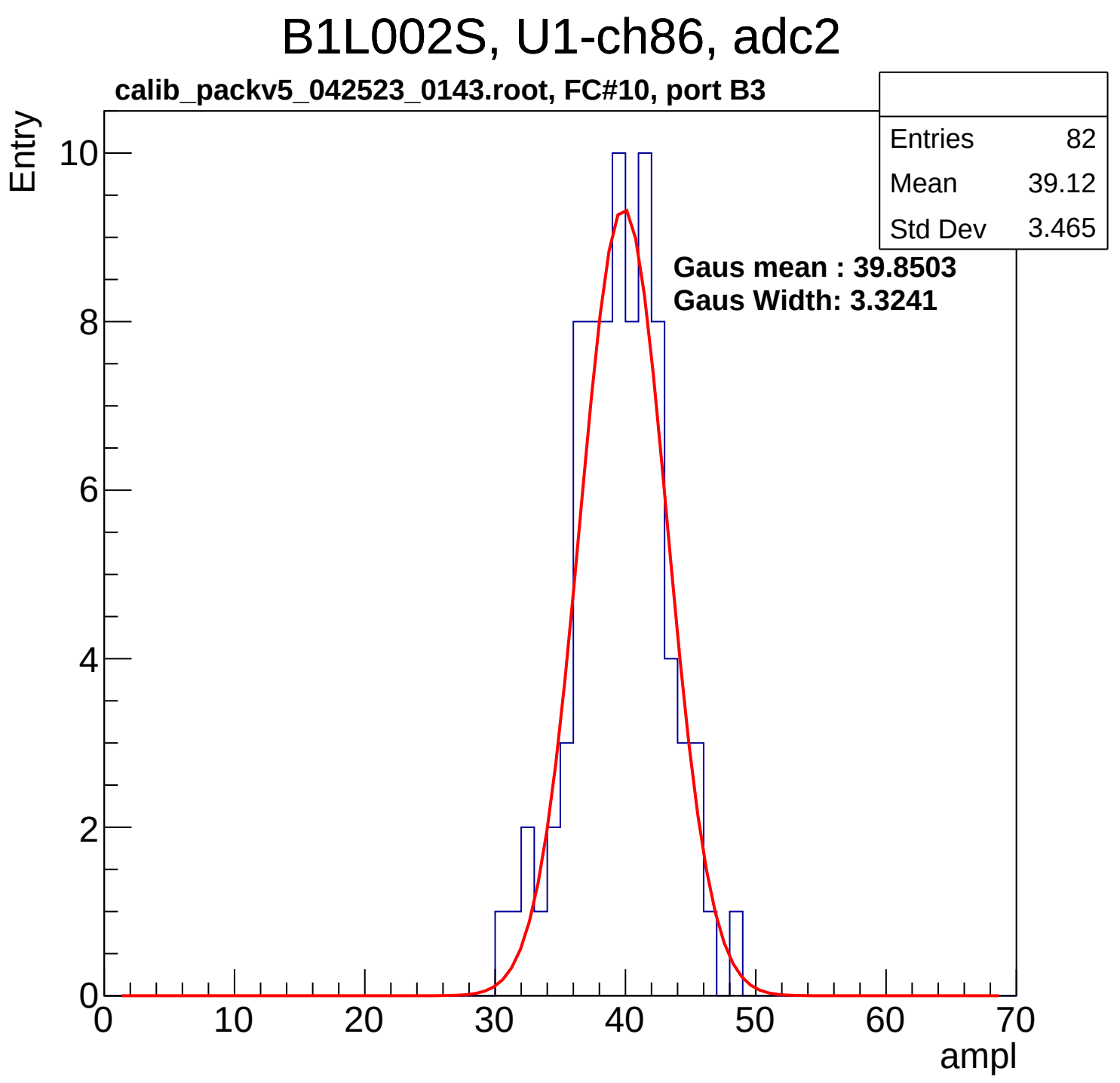
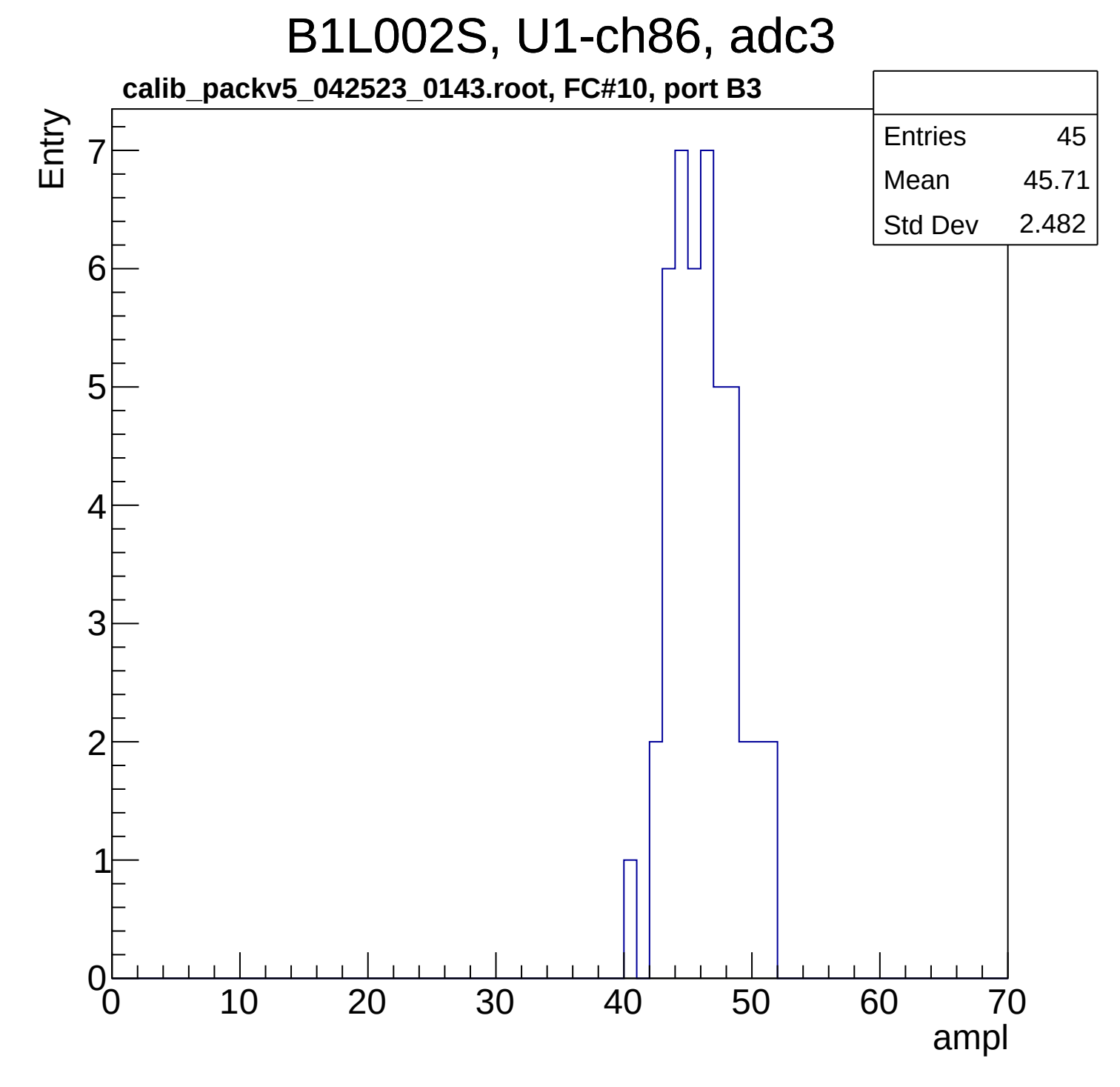
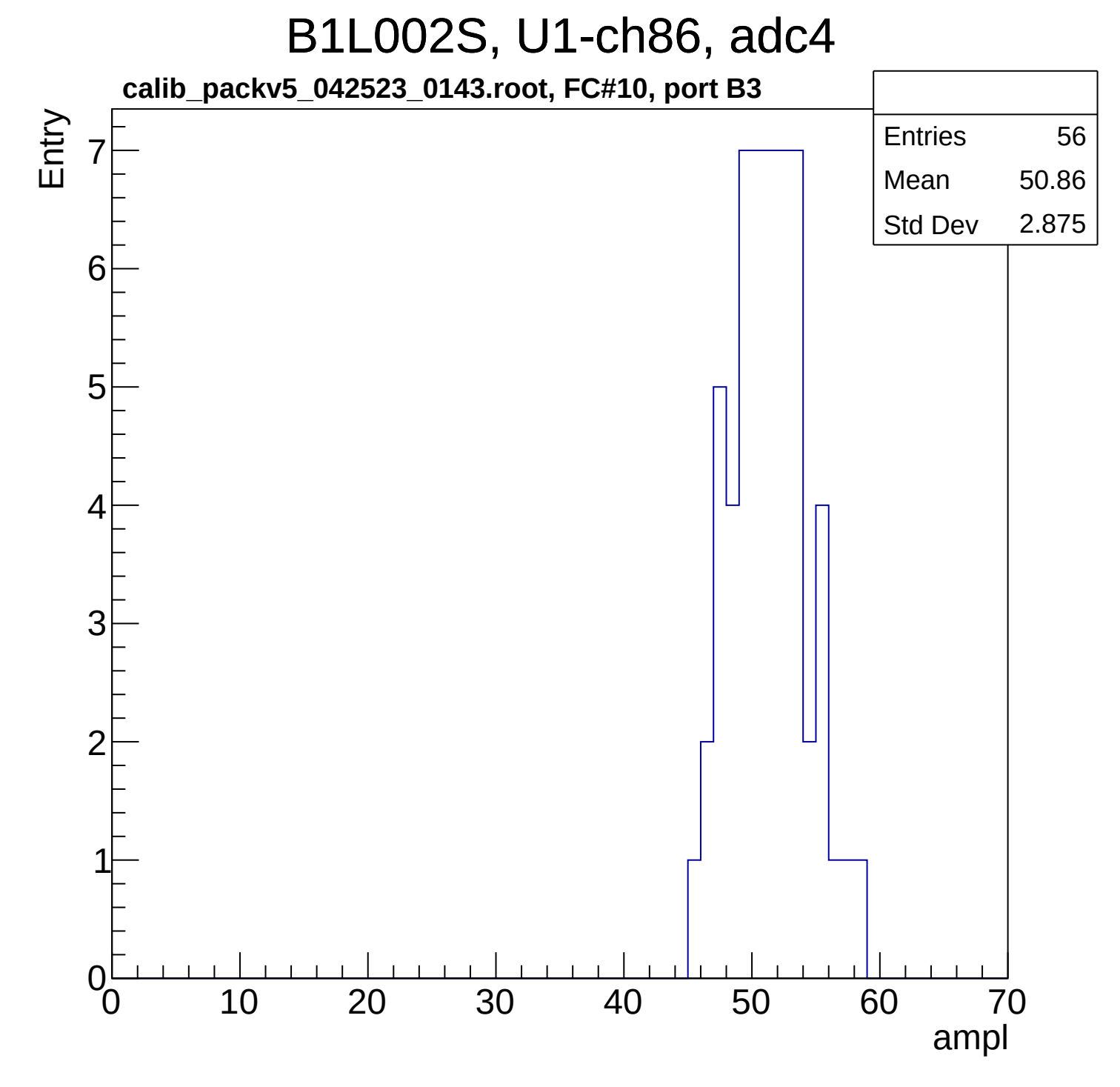
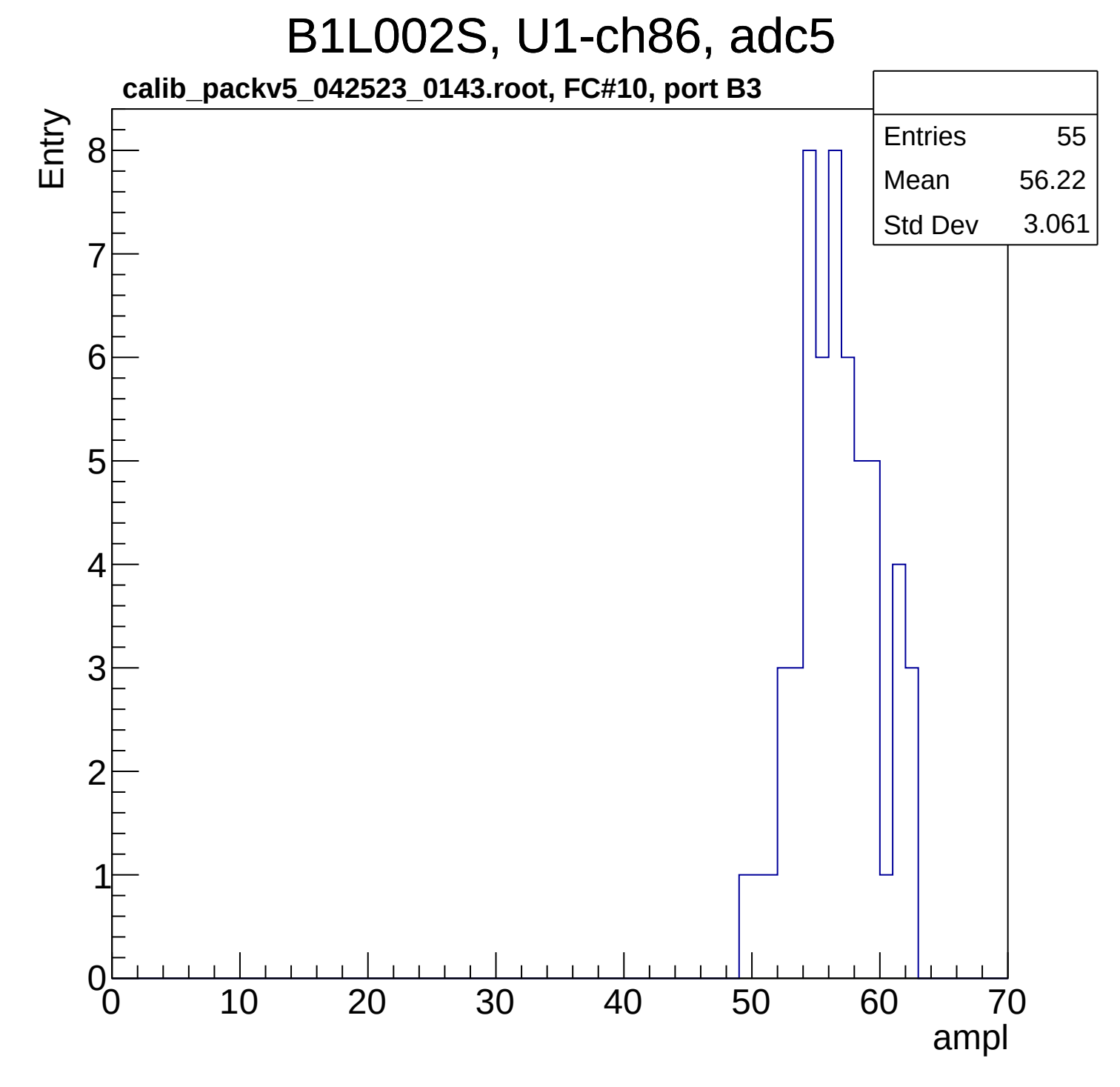

B1L002S, U1-ch86, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 39.12
Std Dev 3.465
Gaus mean : 39.8503
Gaus Width: 3.3241
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch86, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 45.71
Std Dev 2.482
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch86, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 50.86
Std Dev 2.875
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch86, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 56.22
Std Dev 3.061
Entry
ampl
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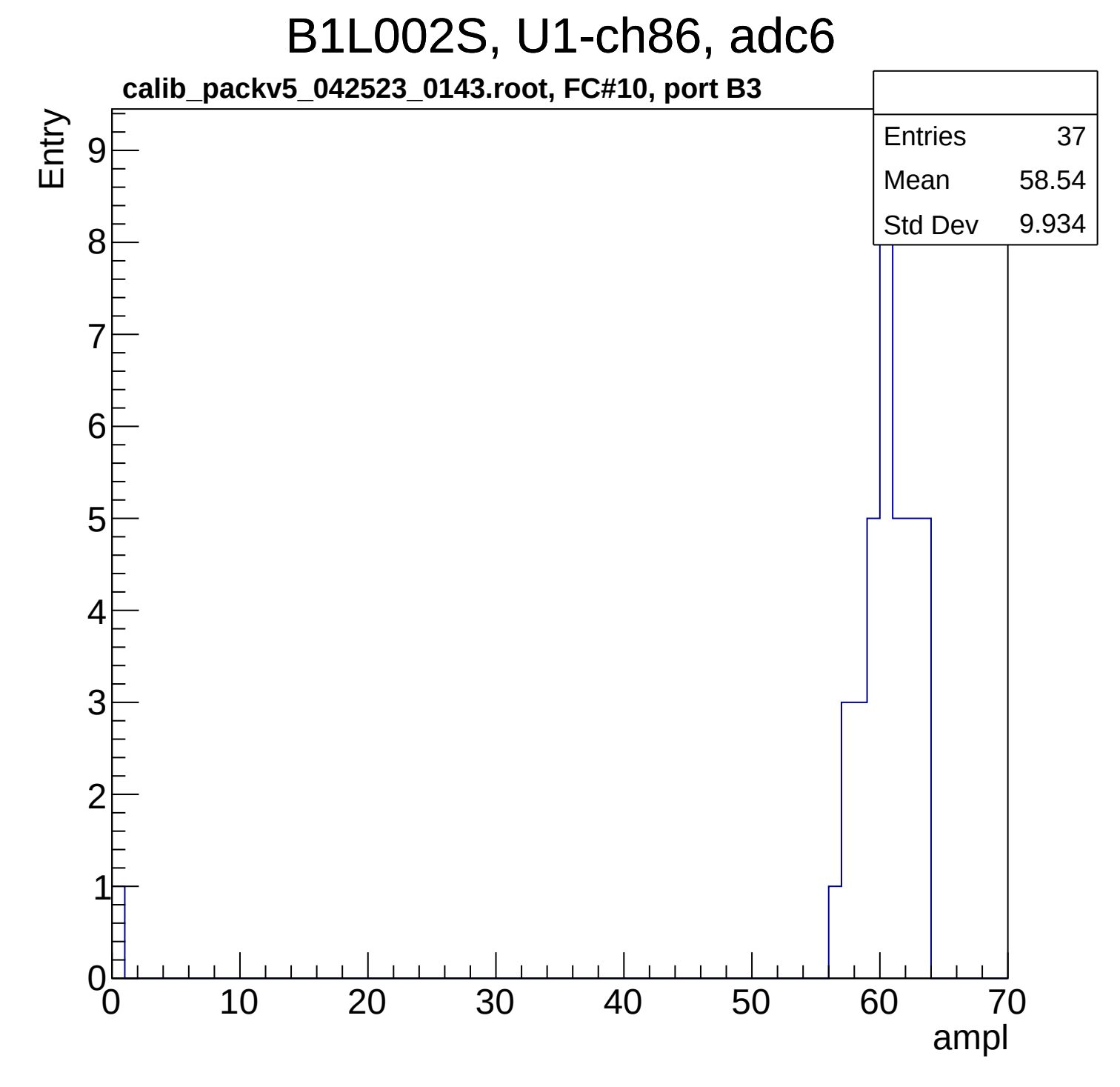
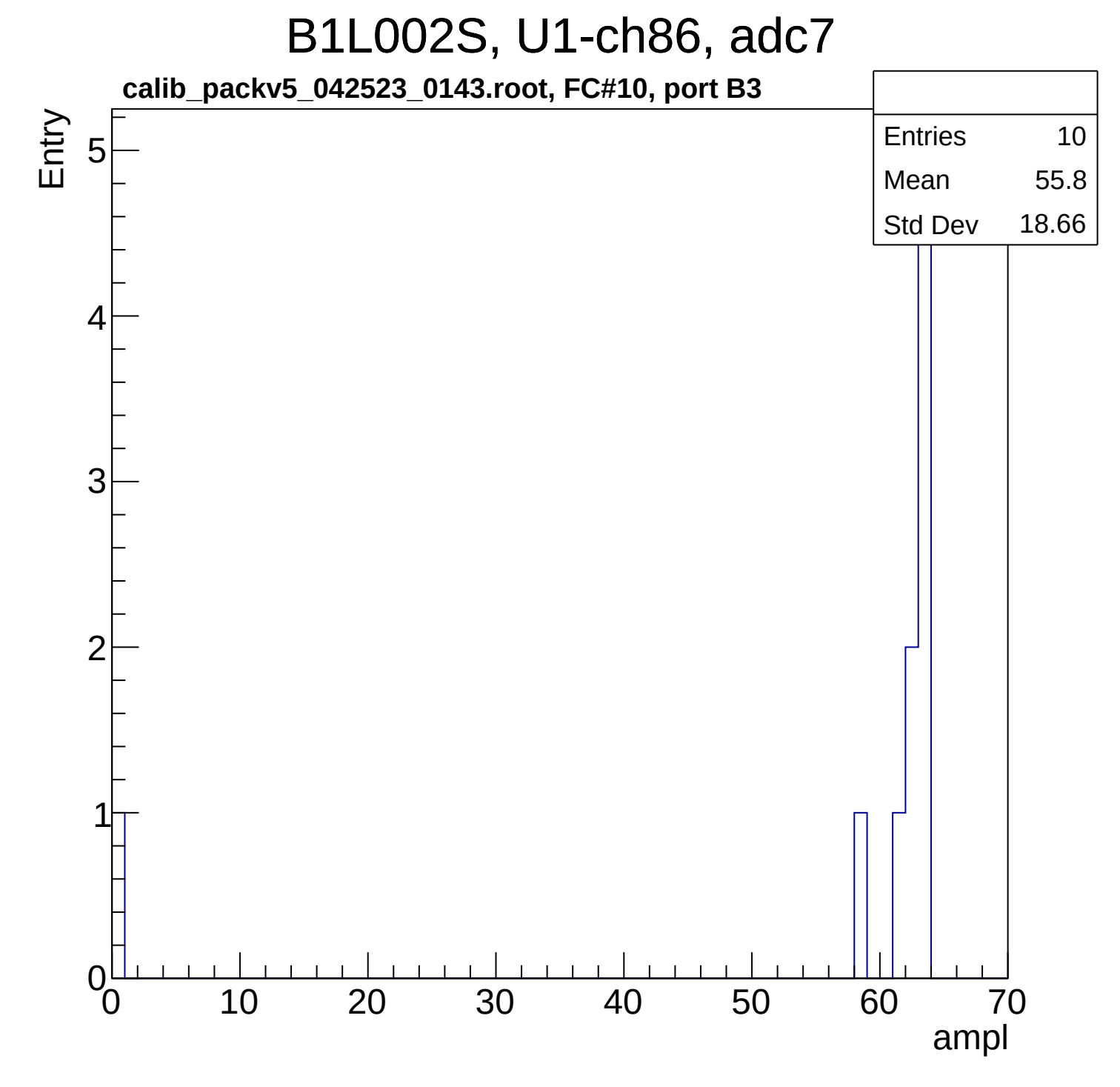
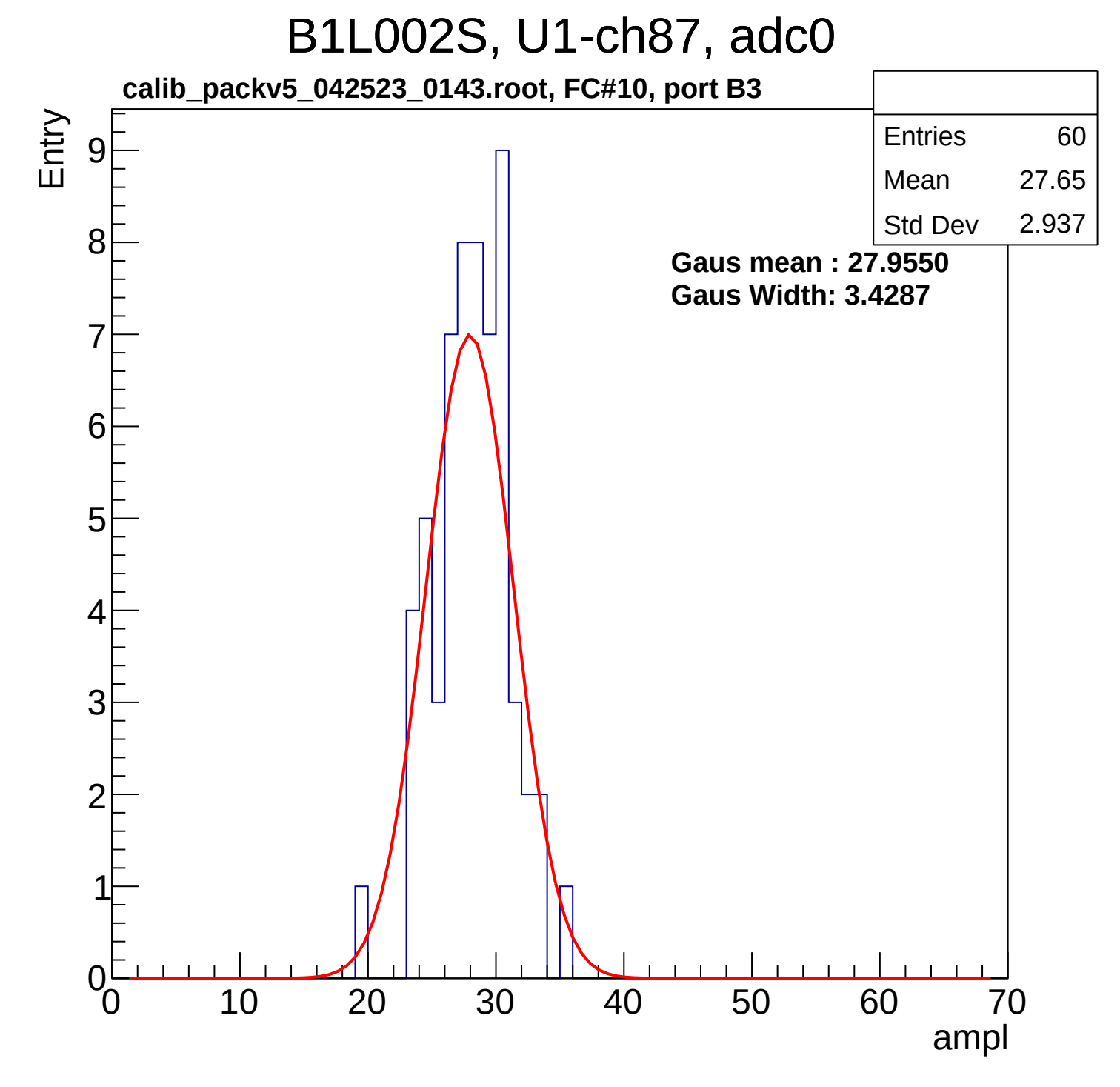
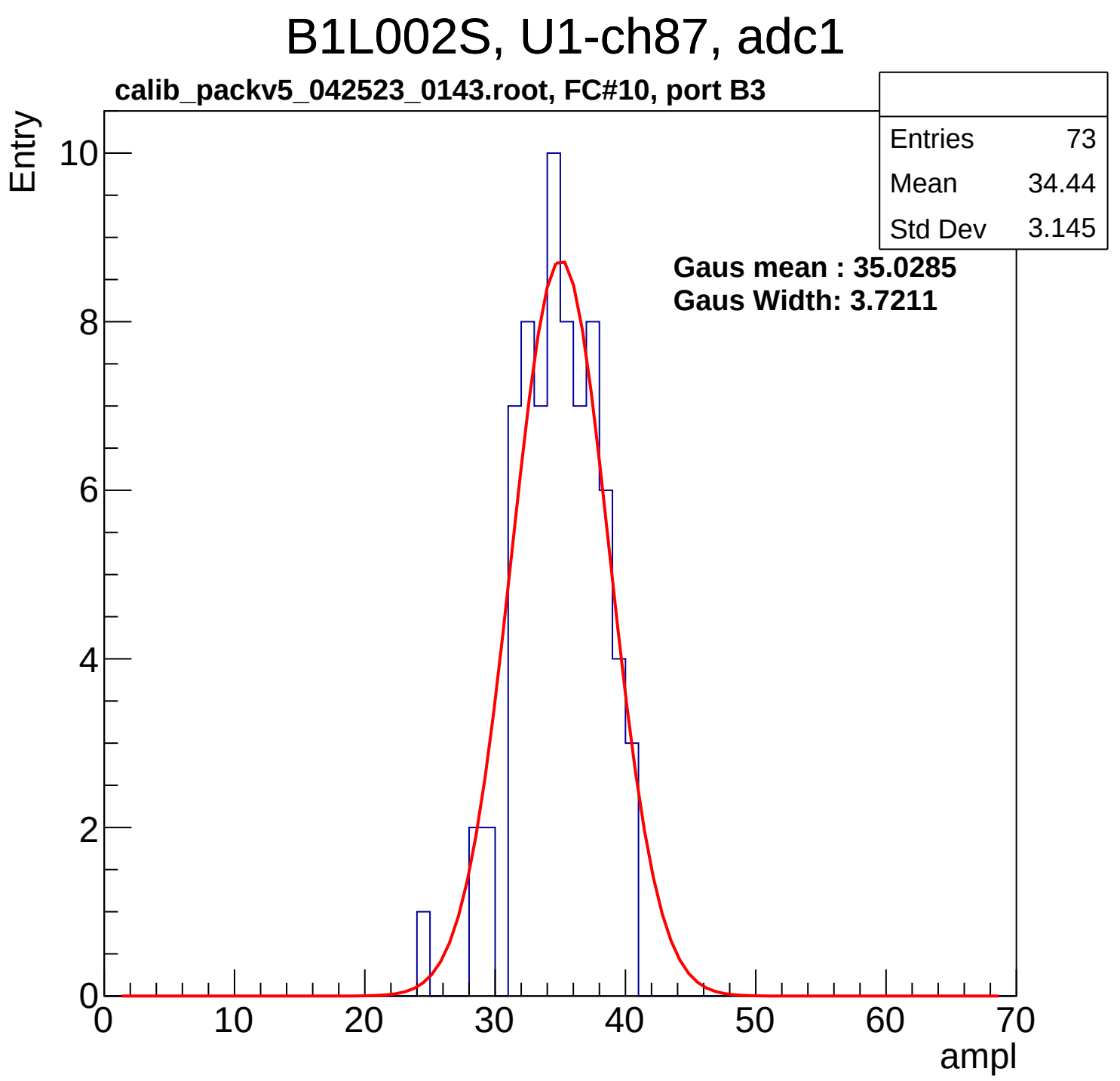
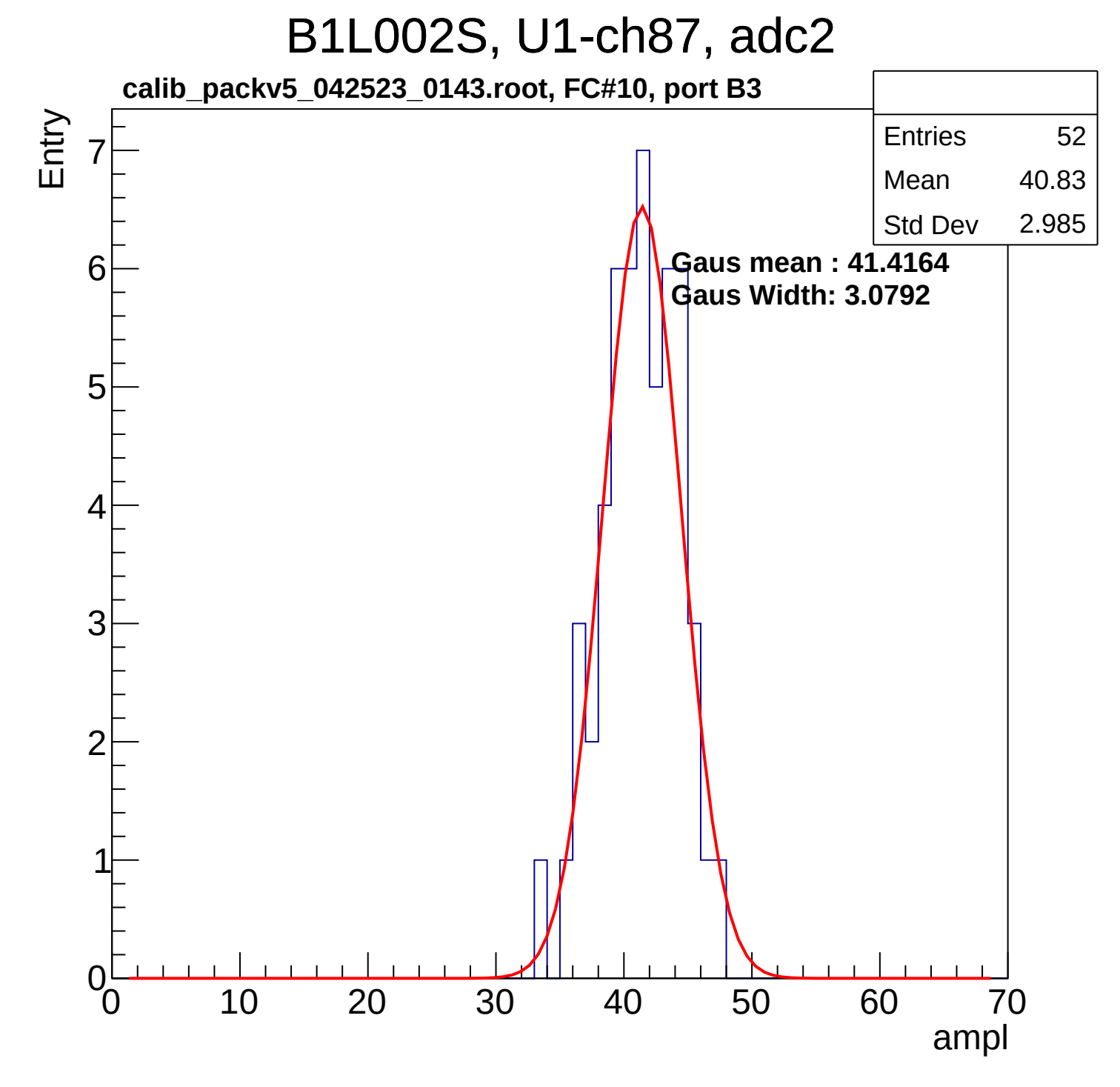
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch86, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 58.54
Std Dev 9.934
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch86, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.8
Std Dev 18.66
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch87, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.65
Std Dev 2.937
Gaus mean : 27.9550
Gaus Width: 3.4287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch87, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.44
Std Dev 3.145
Gaus mean : 35.0285
Gaus Width: 3.7211
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch87, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.83
Std Dev 2.985
Gaus mean : 41.4164
Gaus Width: 3.0792
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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50
60
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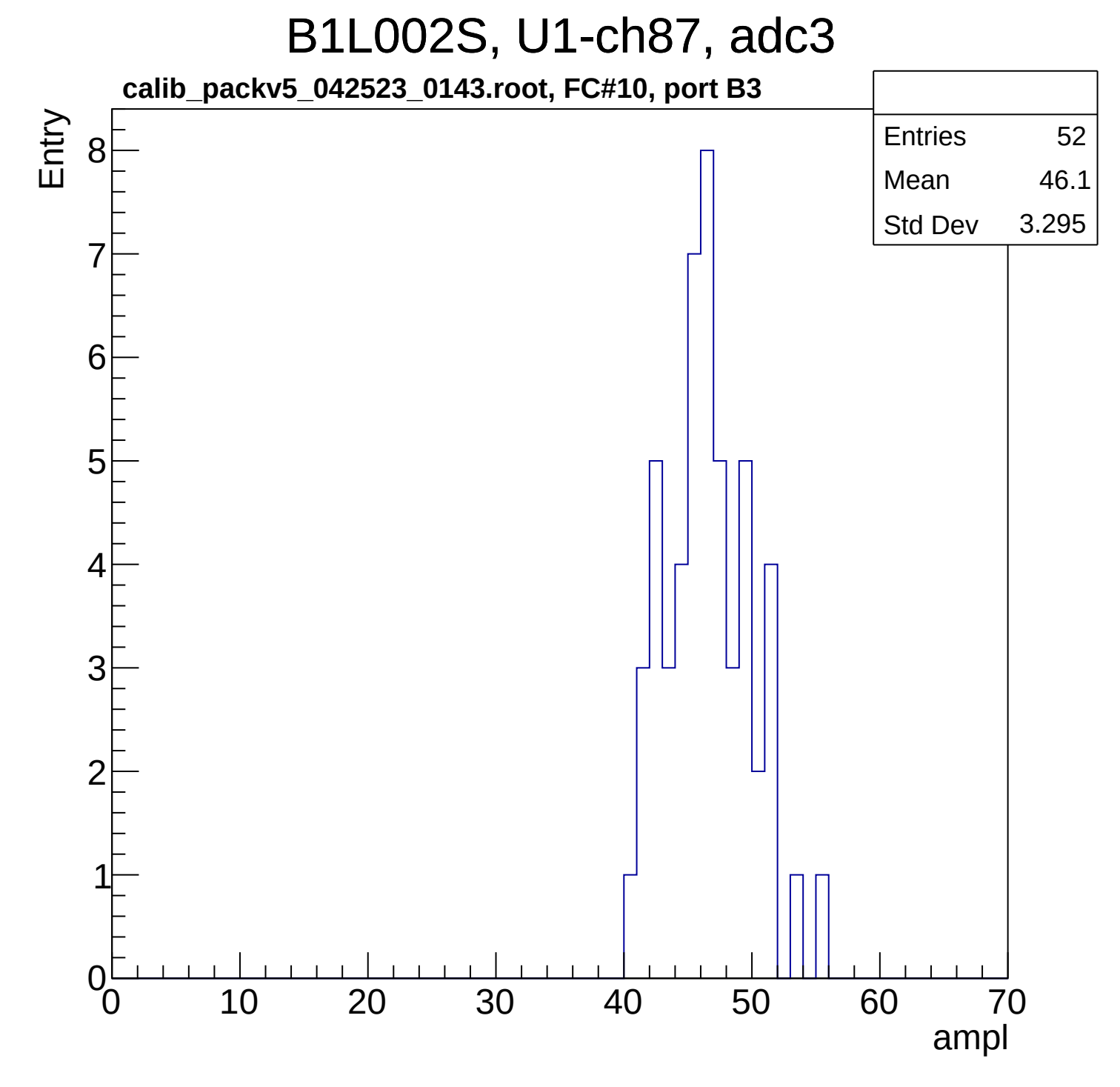
B1L002S, U1-ch87, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 46.1
Std Dev 3.295
Entry
ampl
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4
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60
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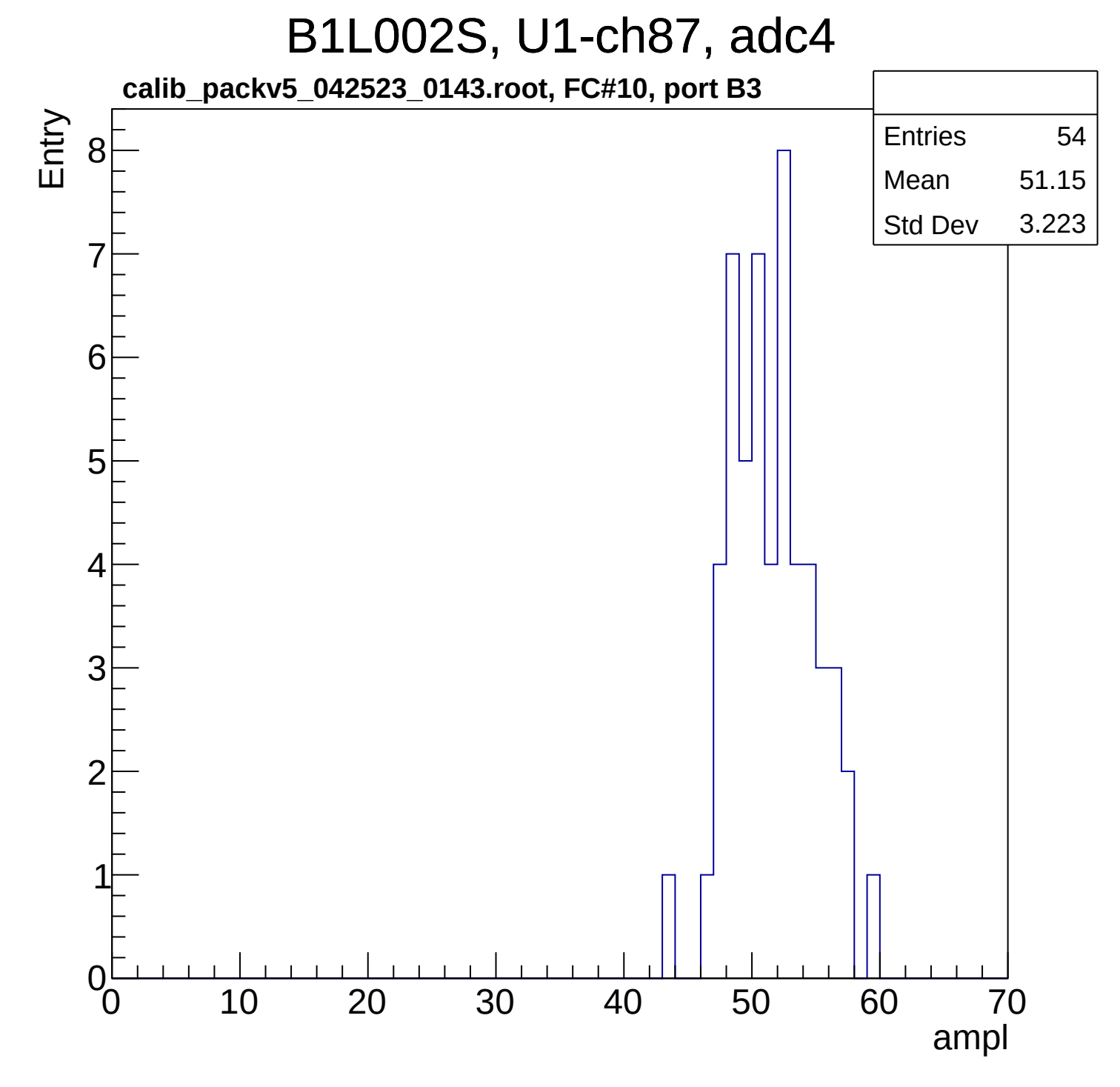
B1L002S, U1-ch87, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 51.15
Std Dev 3.223
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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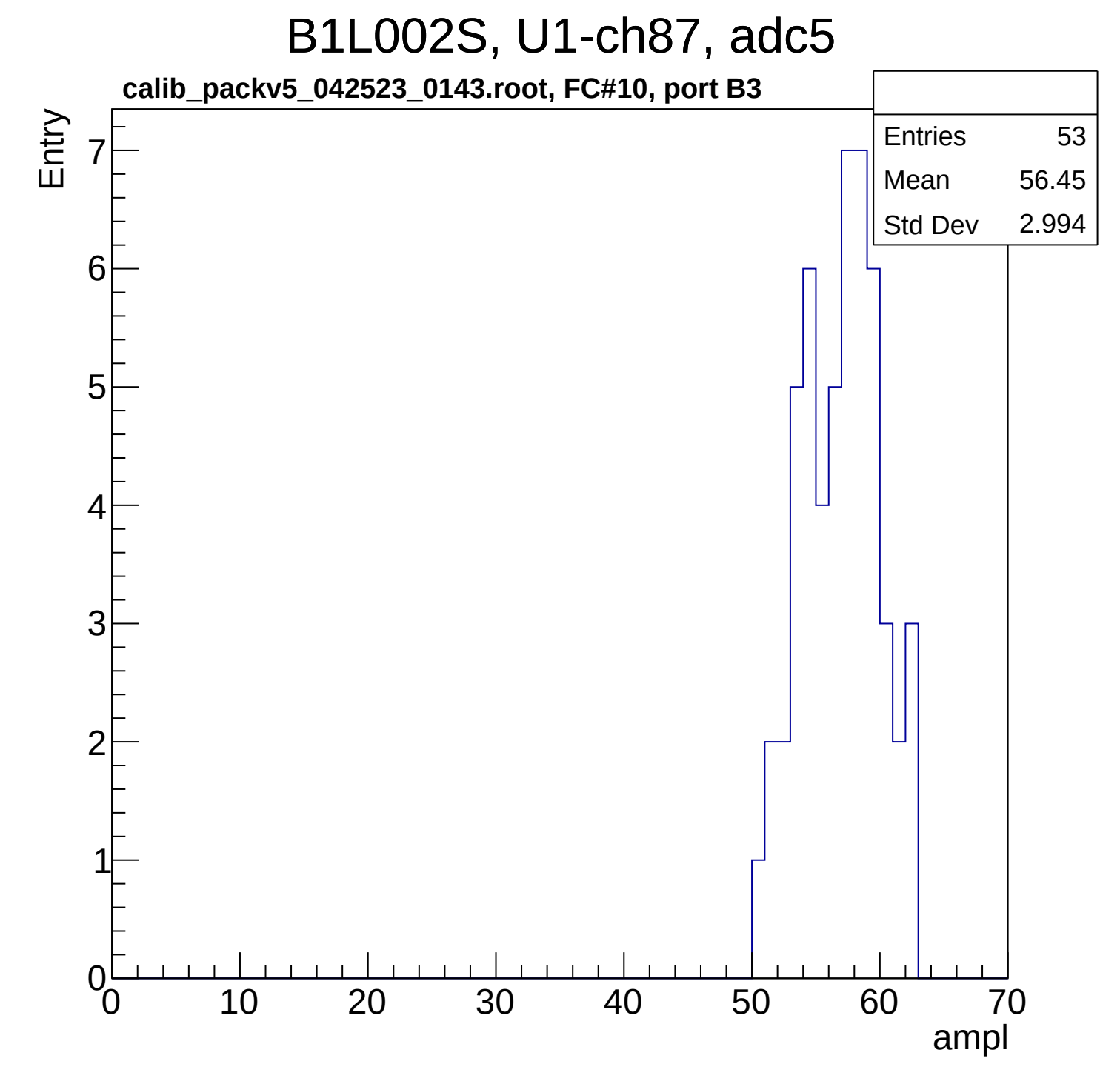
B1L002S, U1-ch87, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 56.45
Std Dev 2.994
Entry
ampl
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4
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6
7
0
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20
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40
50
60
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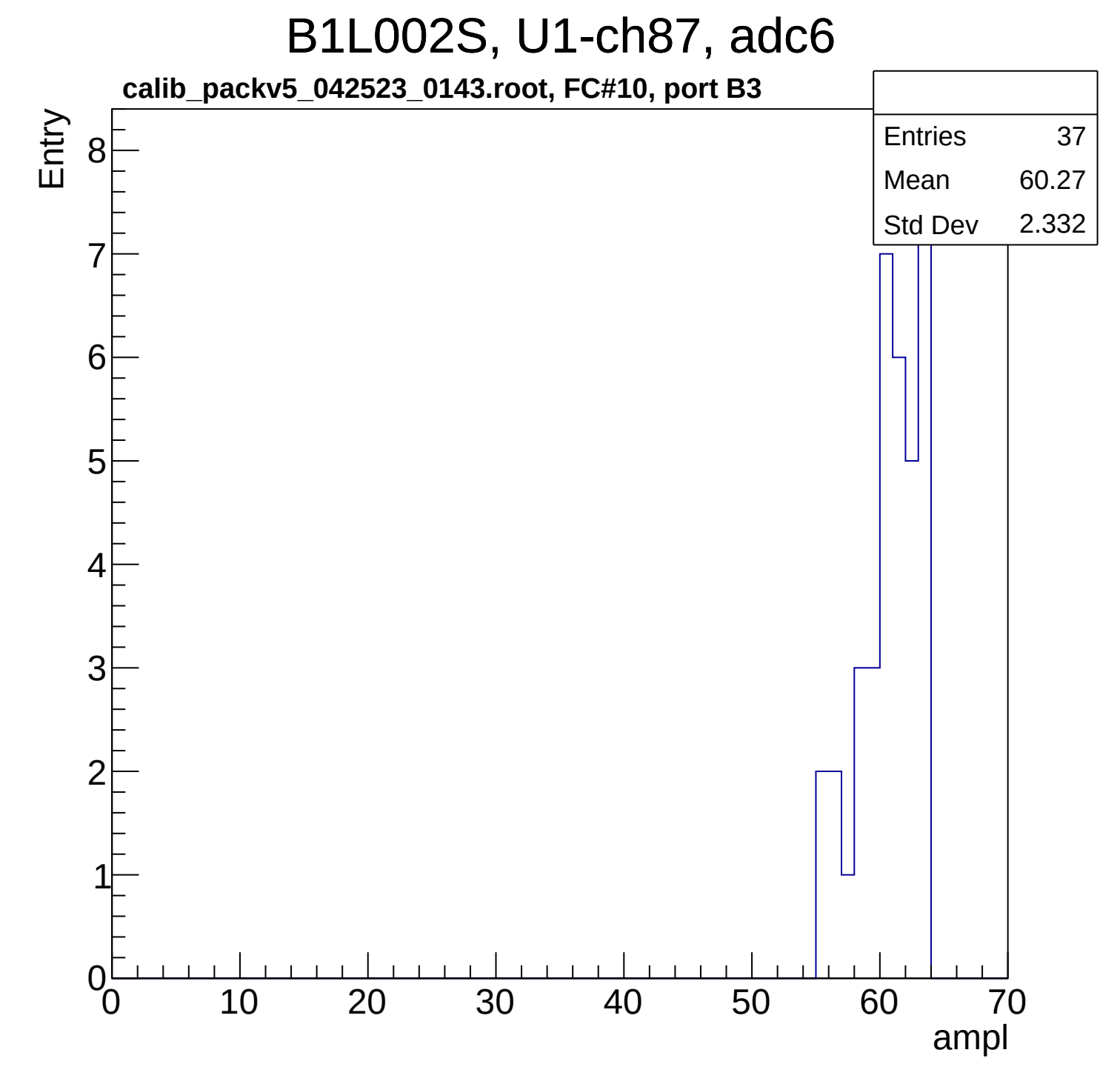
B1L002S, U1-ch87, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 60.27
Std Dev 2.332
Entry
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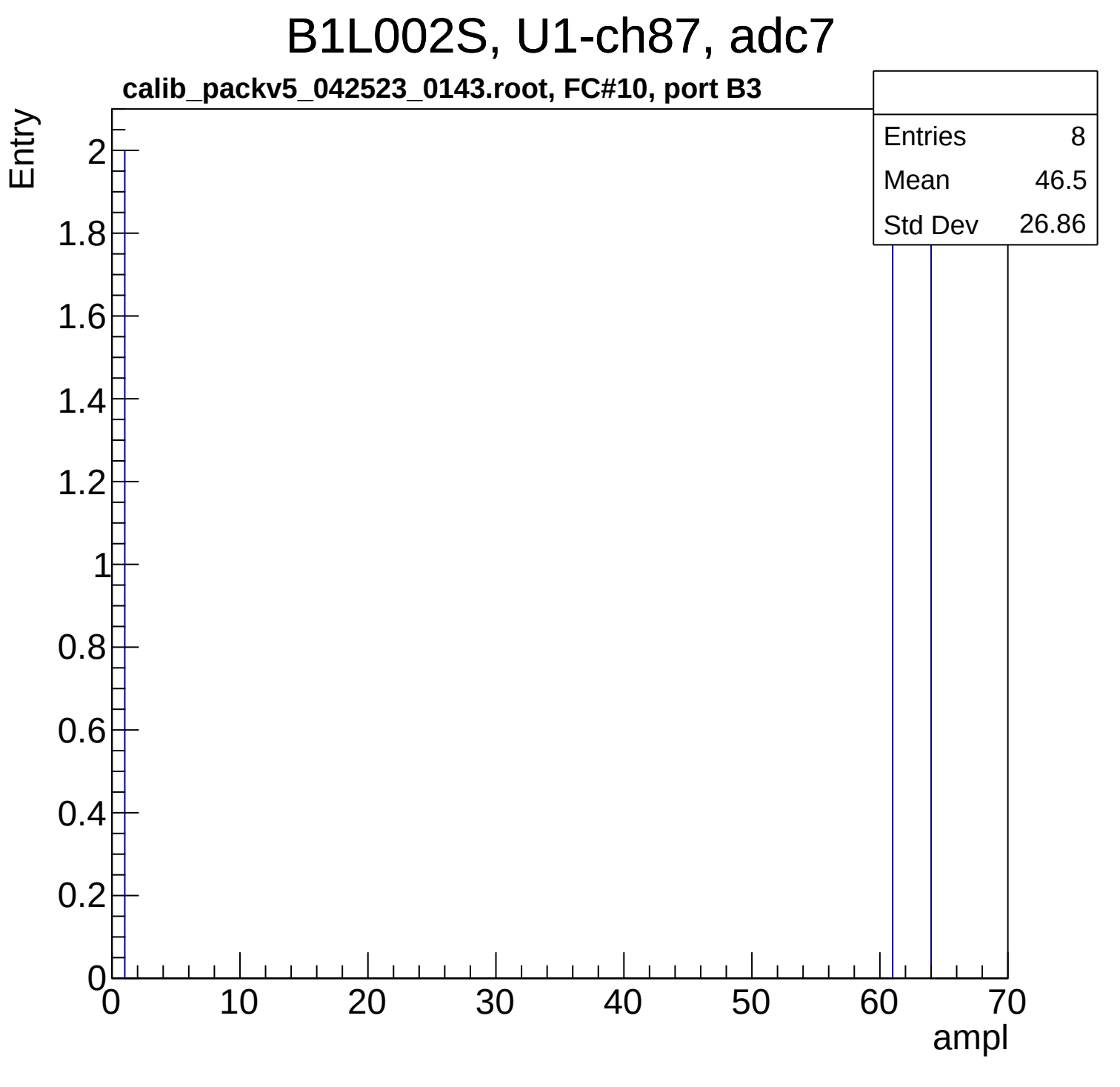
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch87, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 46.5
Std Dev 26.86
Entry
ampl
0
0.2
0.4
0.6
0.8
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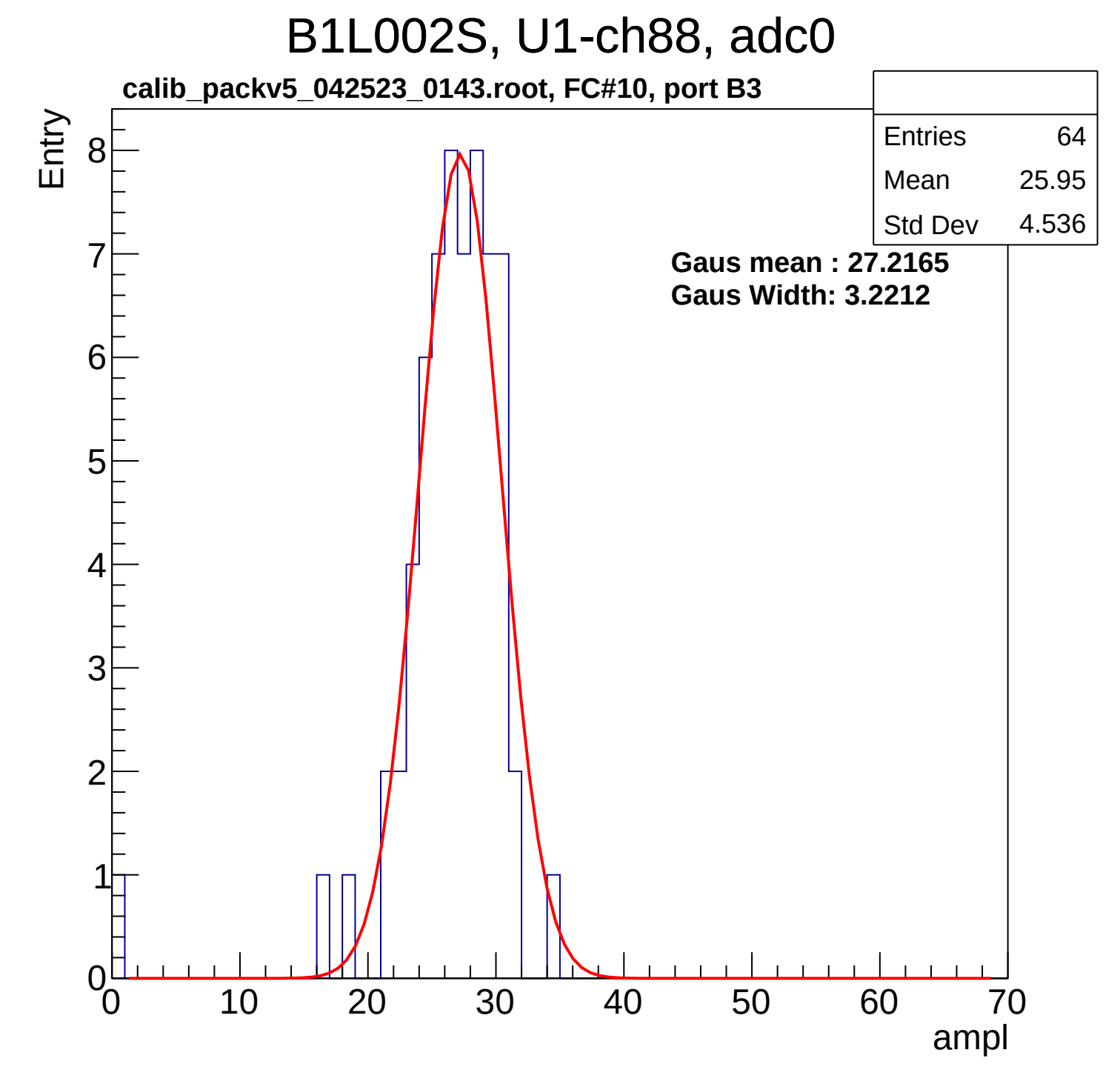
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch88, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 25.95
Std Dev 4.536
Gaus mean : 27.2165
Gaus Width: 3.2212
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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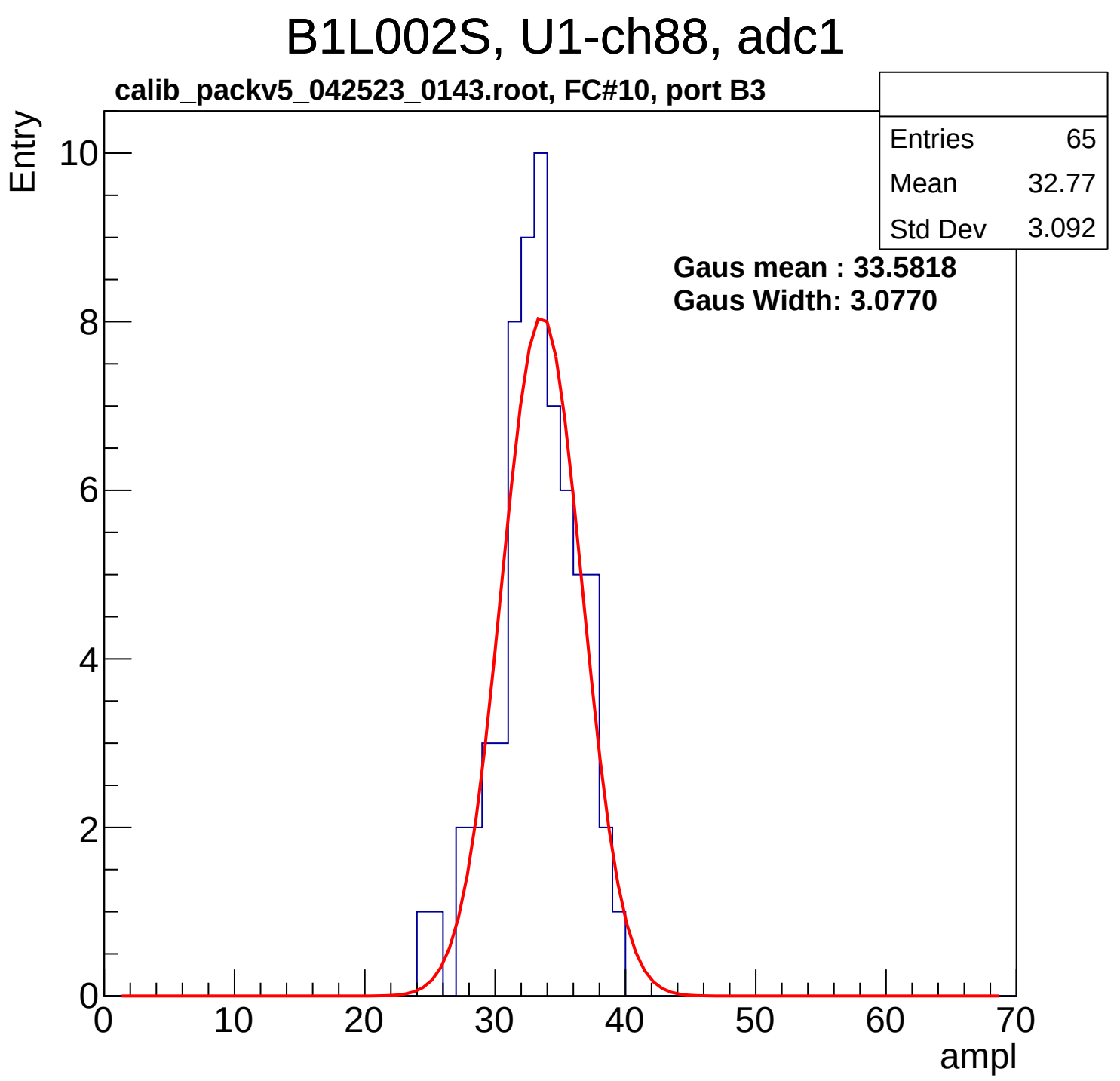
B1L002S, U1-ch88, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 32.77
Std Dev 3.092
Gaus mean : 33.5818
Gaus Width: 3.0770
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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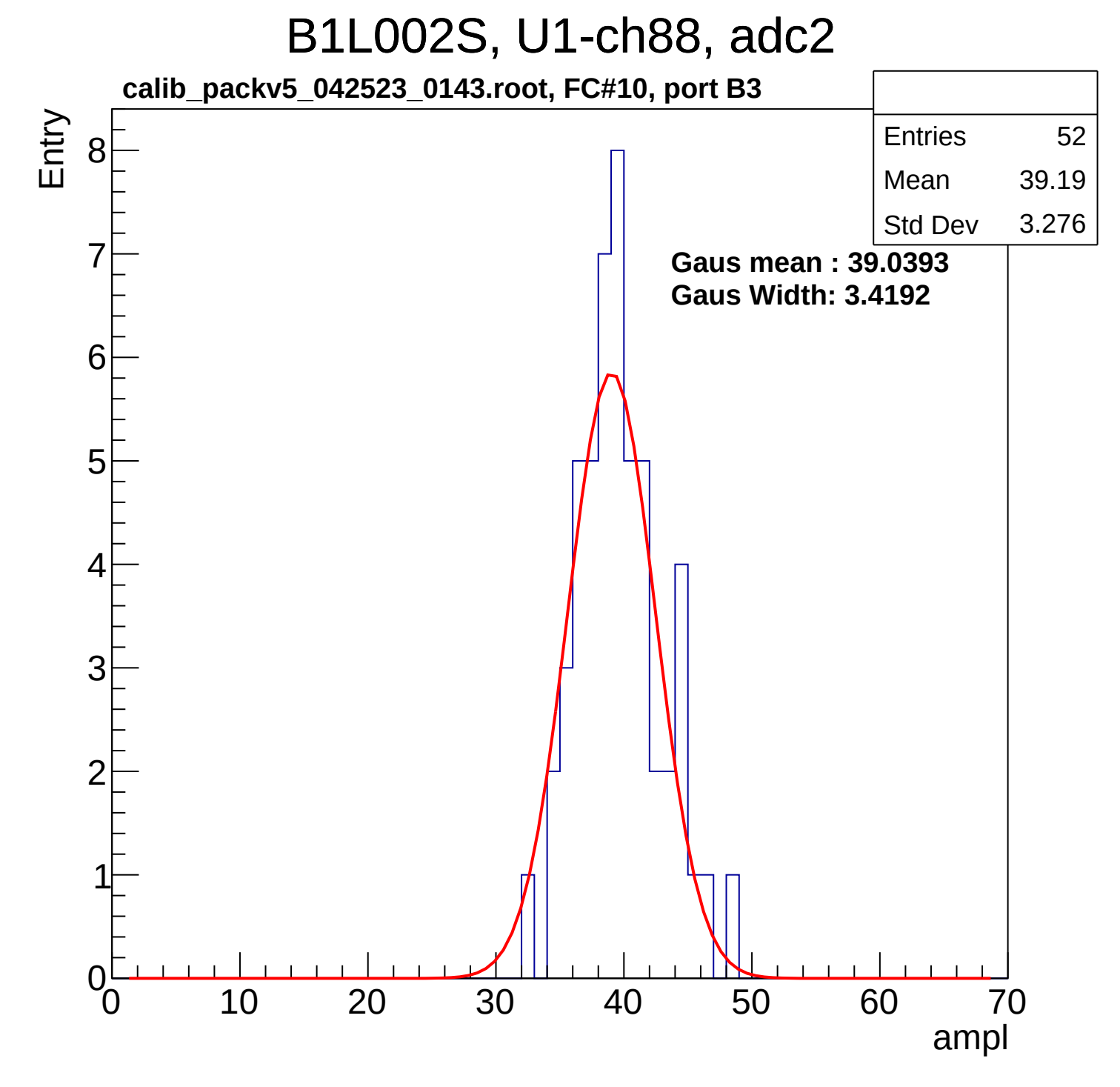
B1L002S, U1-ch88, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 39.19
Std Dev 3.276
Gaus mean : 39.0393
Gaus Width: 3.4192
Entry
ampl
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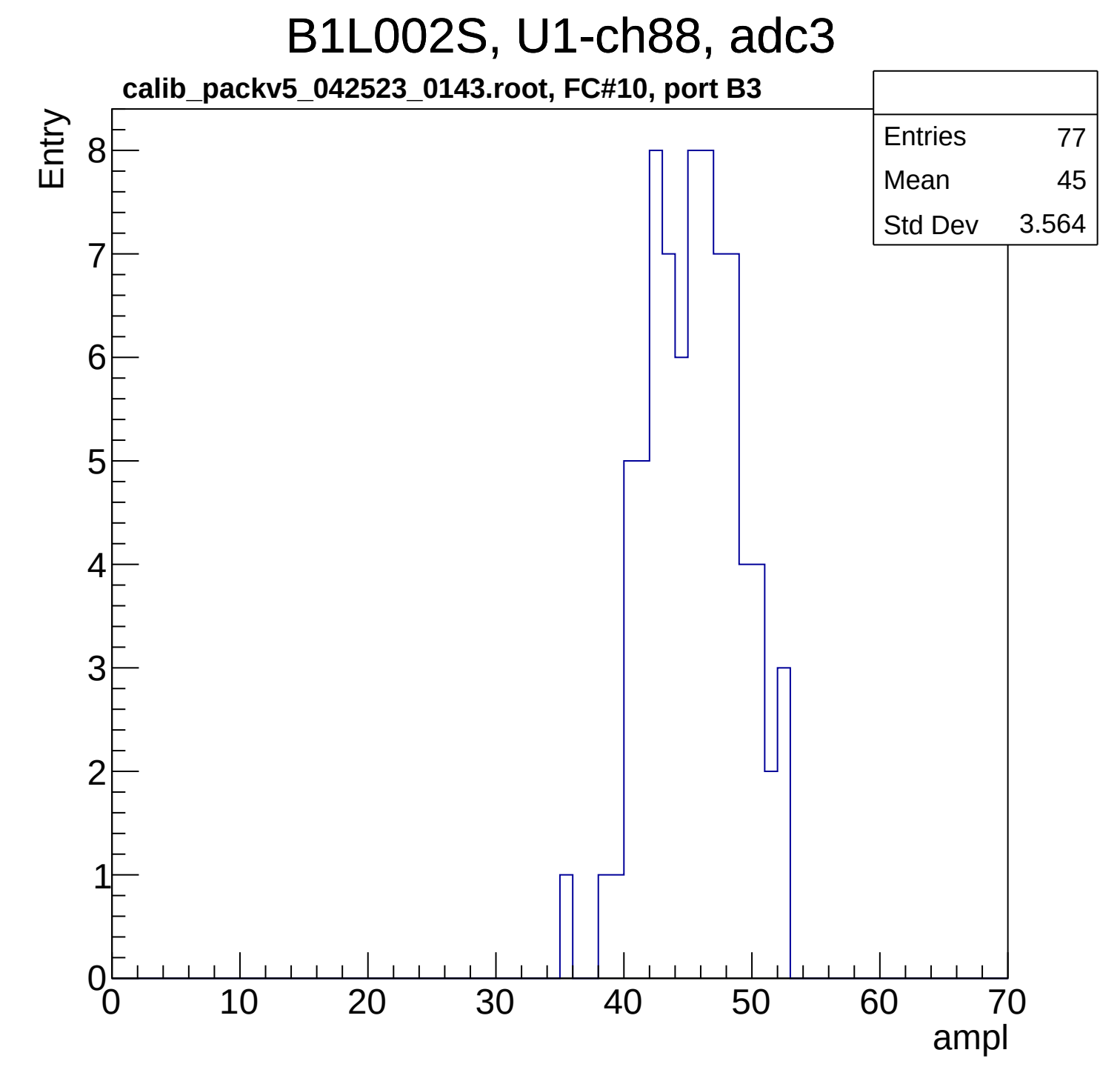
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch88, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 45
Std Dev 3.564
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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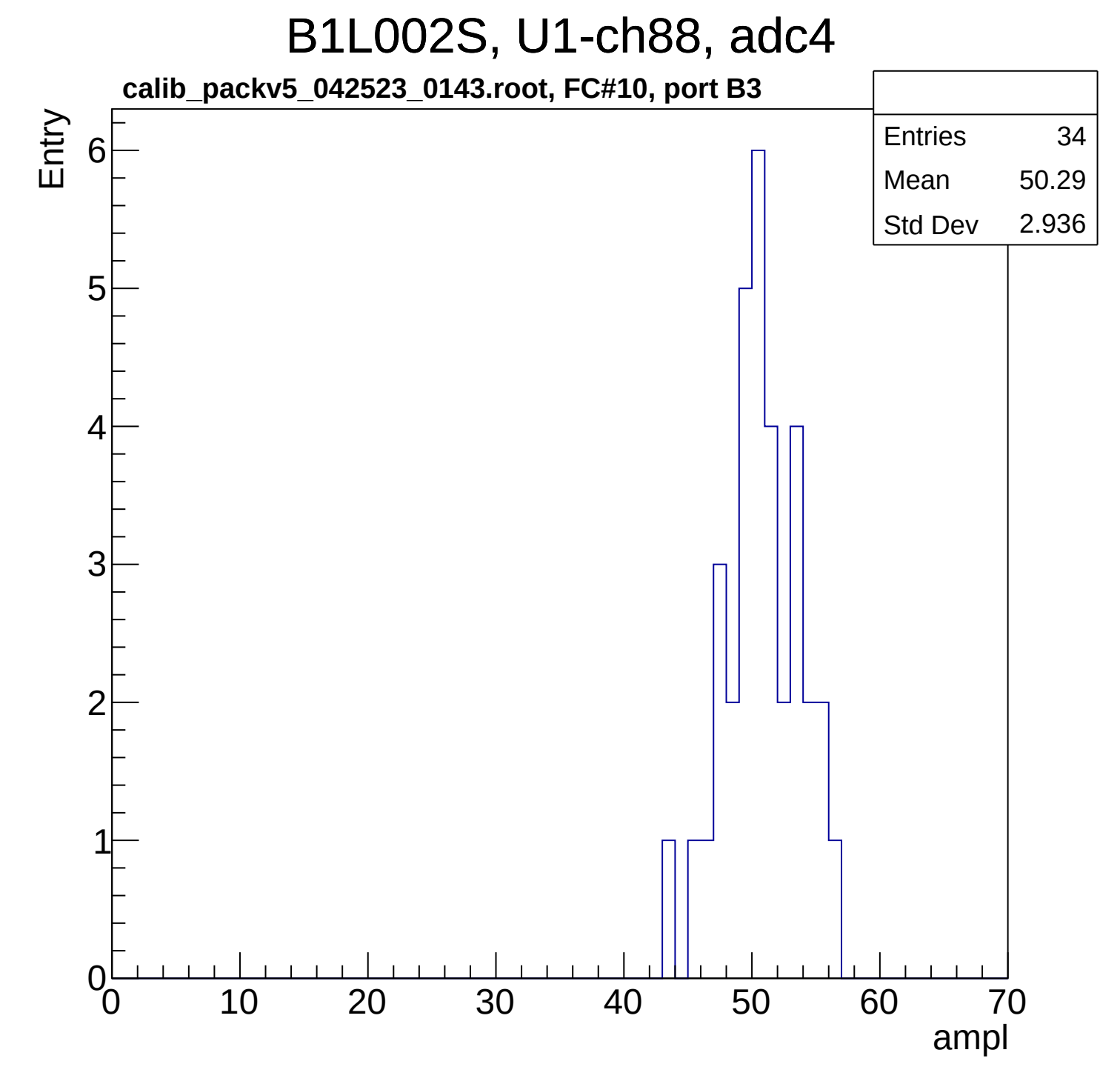
B1L002S, U1-ch88, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 50.29
Std Dev 2.936
Entry
0
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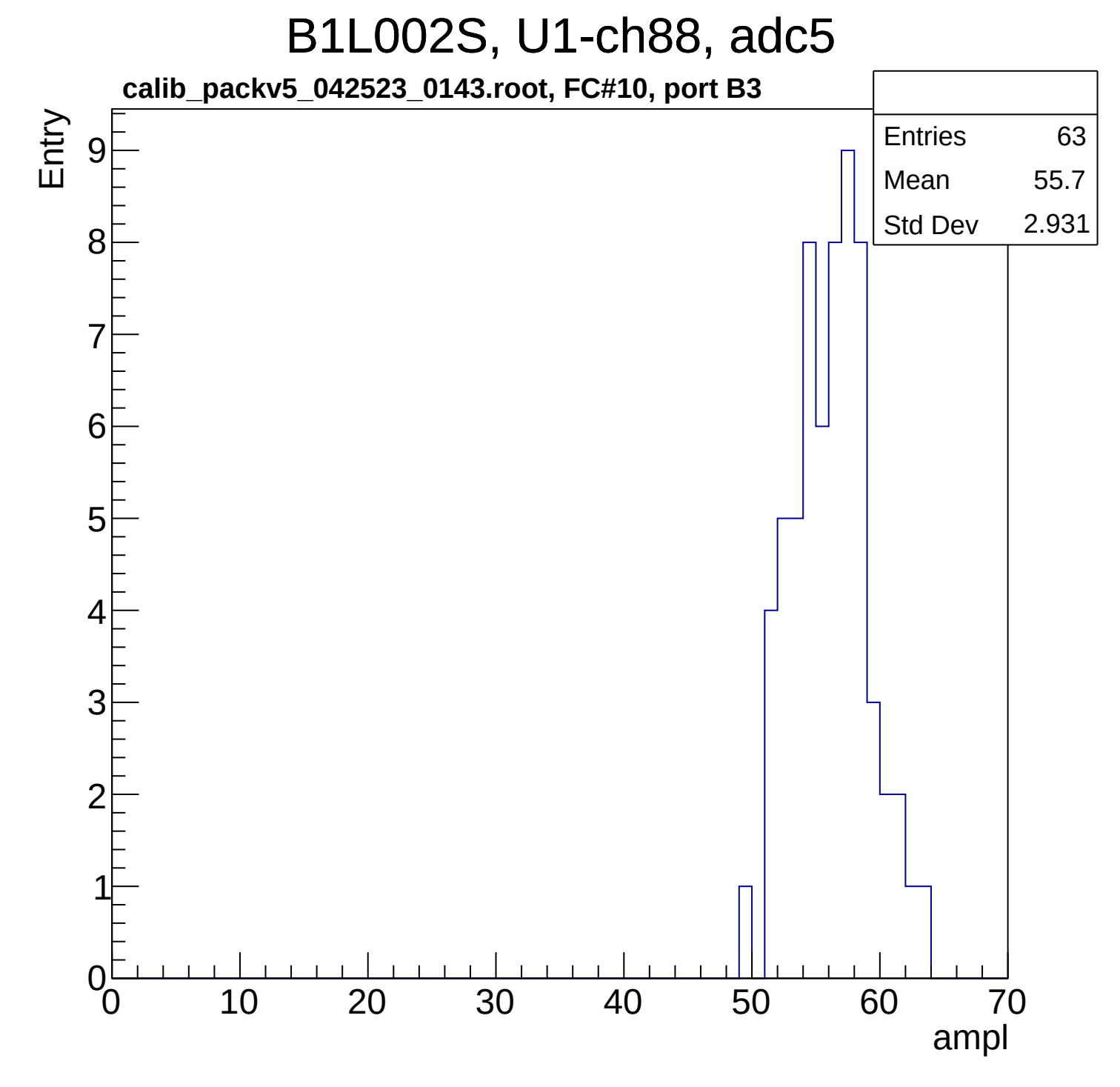
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch88, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 55.7
Std Dev 2.931
Entry
ampl
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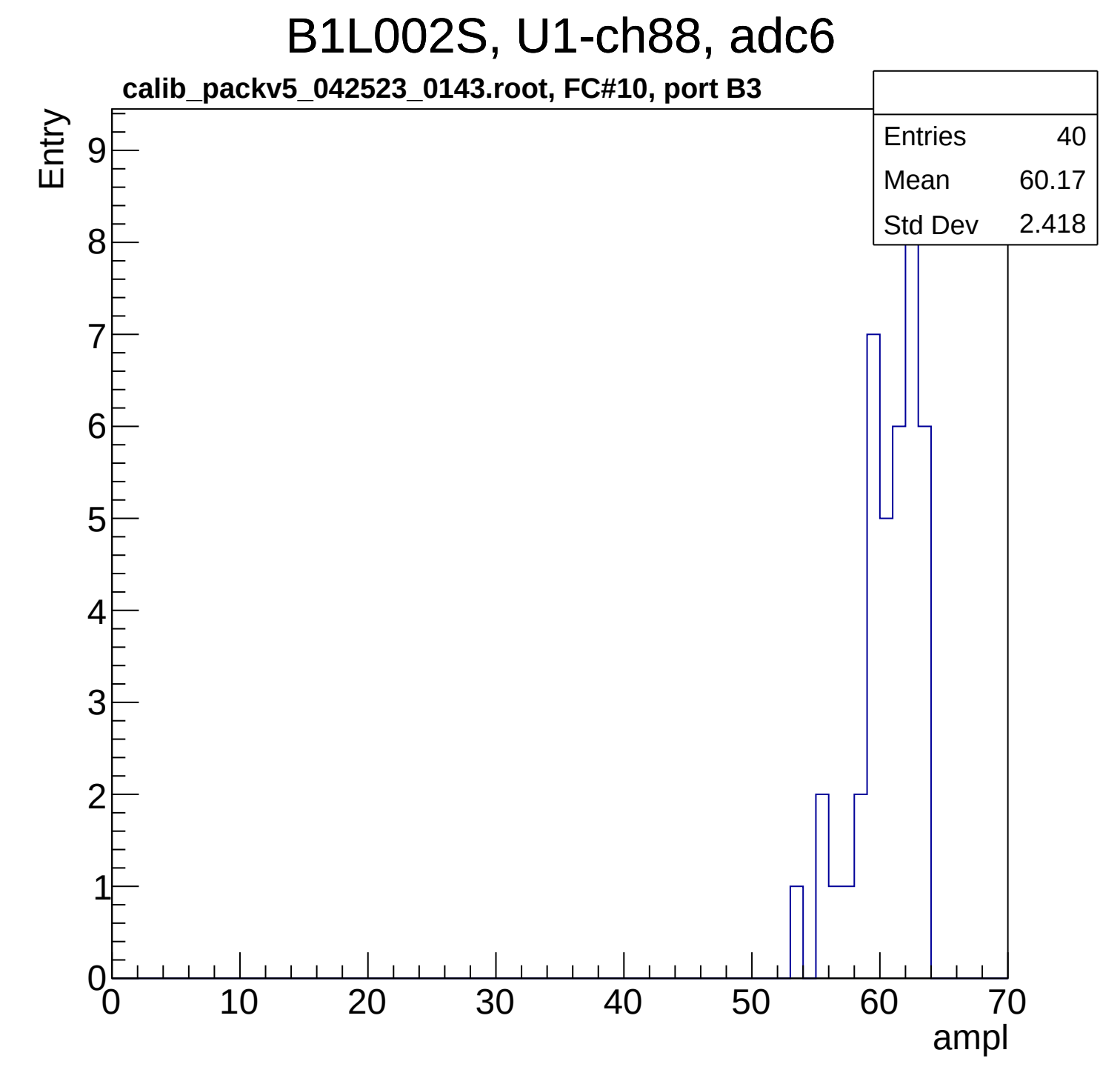
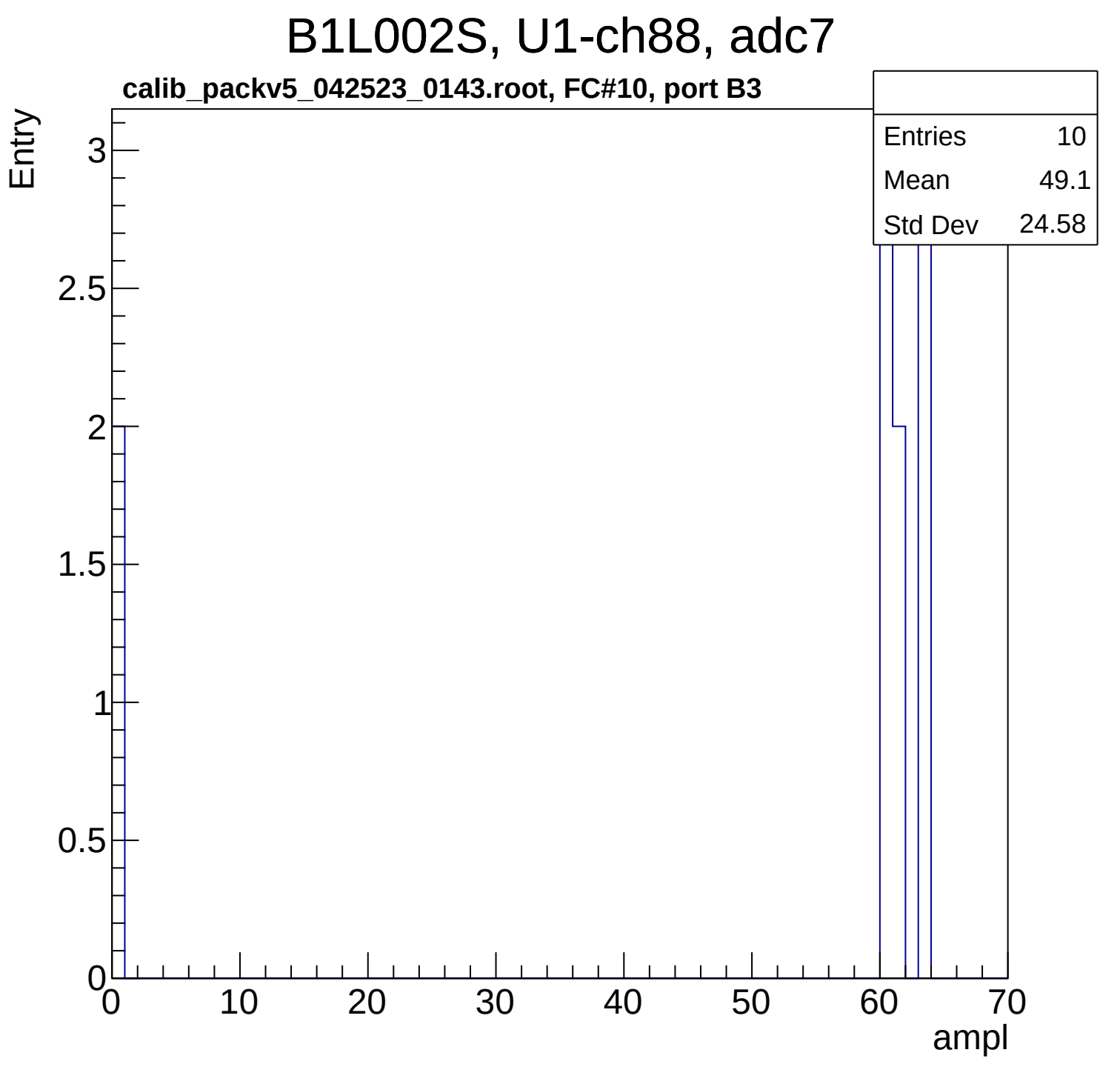
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch88, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 60.17
Std Dev 2.418
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch88, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 49.1
Std Dev 24.58
Entry
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2
2.5
3
0
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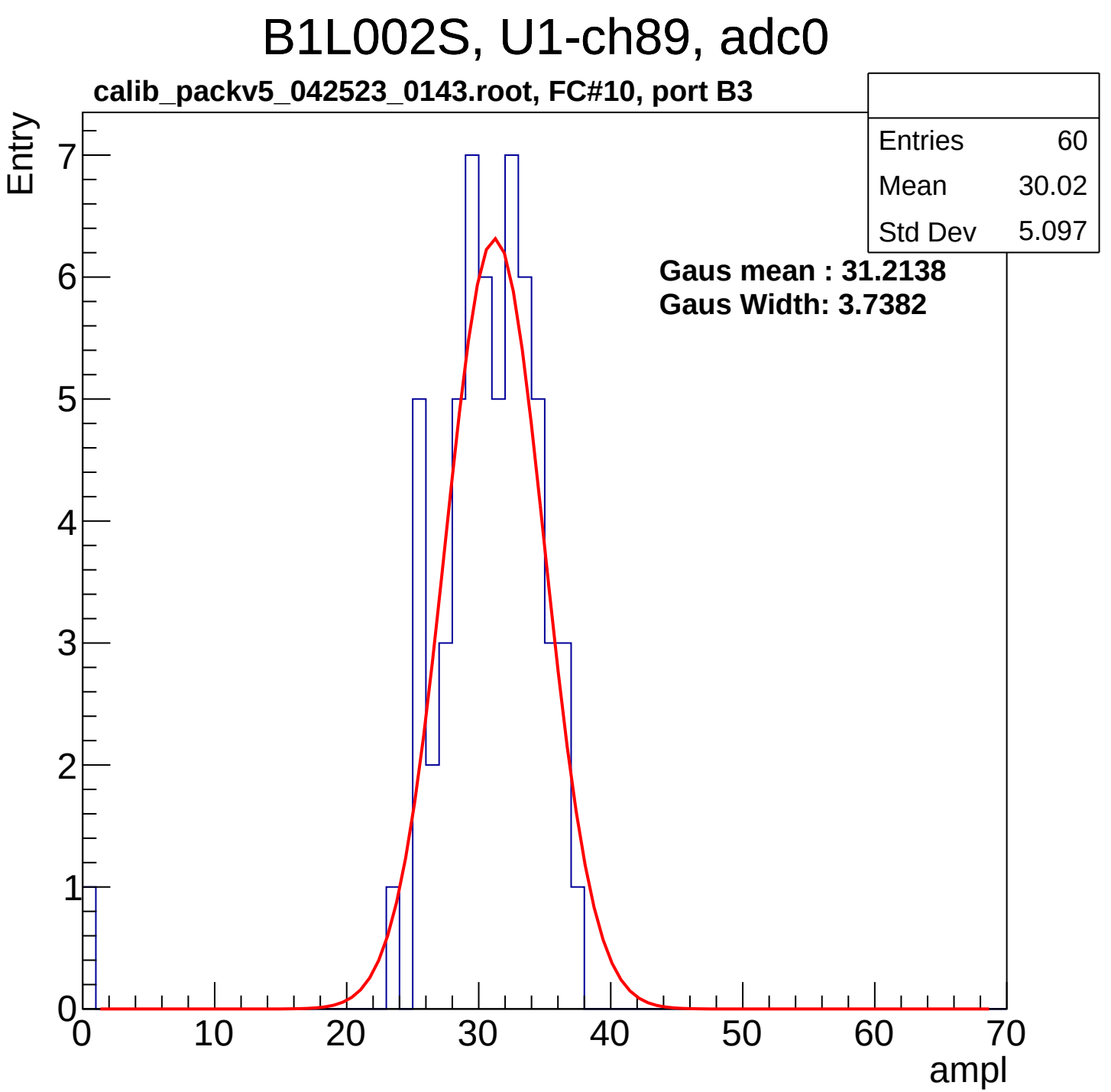
50
60
70
ampl


B1L002S, U1-ch89, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 30.02
Std Dev 5.097
Gaus mean : 31.2138
Gaus Width: 3.7382
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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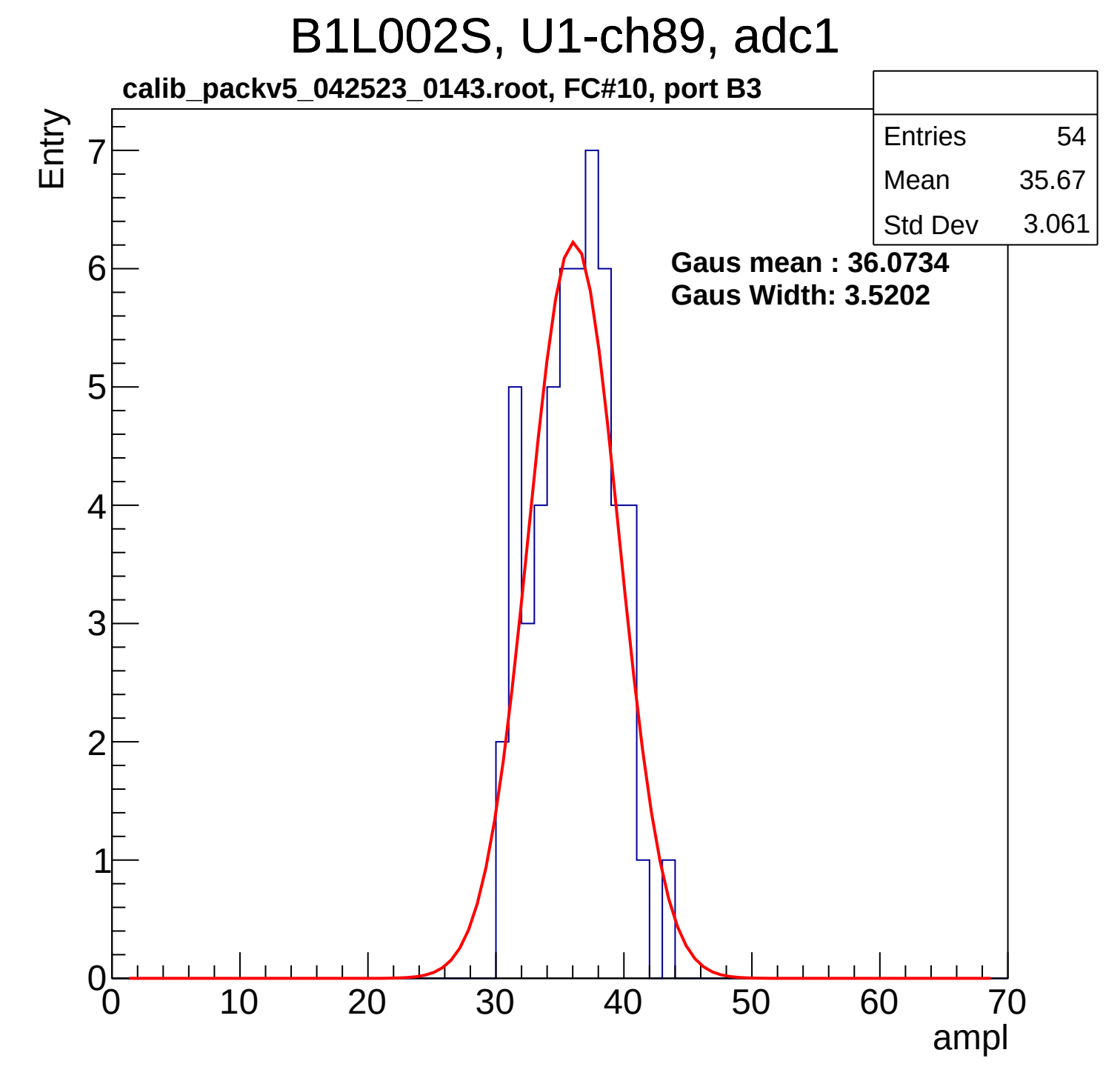
B1L002S, U1-ch89, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 35.67
Std Dev 3.061
Gaus mean : 36.0734
Gaus Width: 3.5202
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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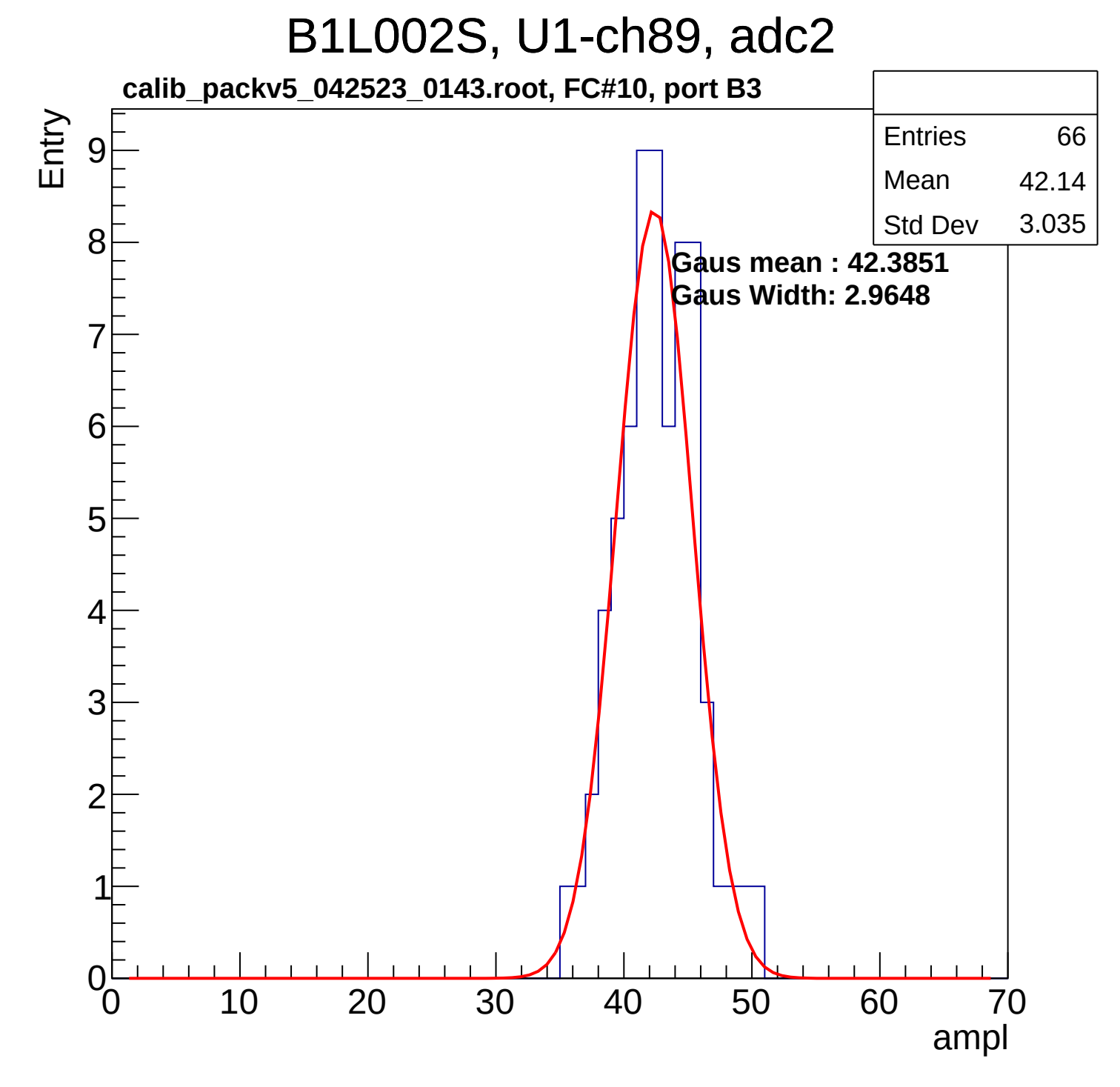
B1L002S, U1-ch89, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 42.14
Std Dev 3.035
Gaus mean : 42.3851
Gaus Width: 2.9648
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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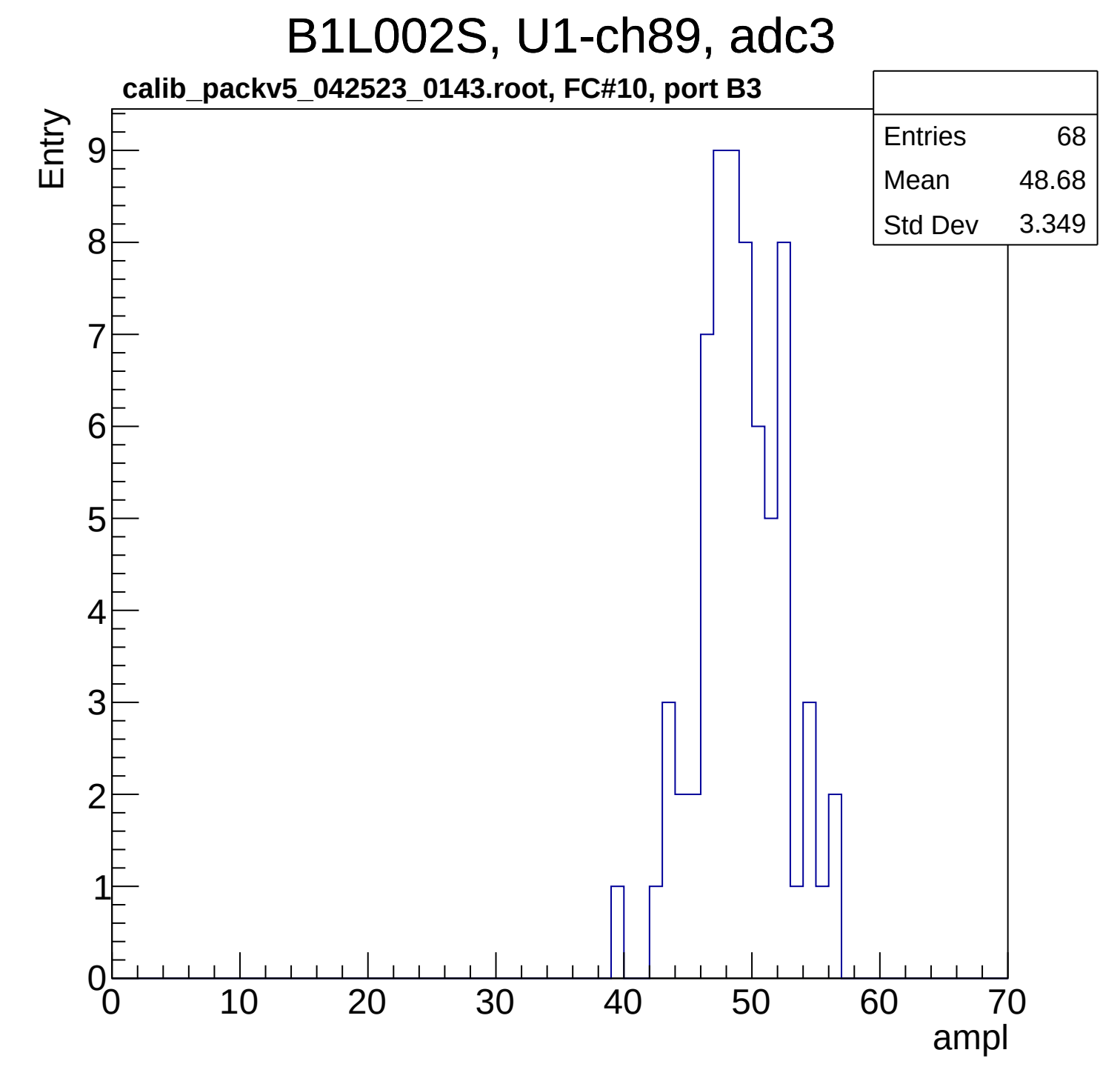
B1L002S, U1-ch89, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 48.68
Std Dev 3.349
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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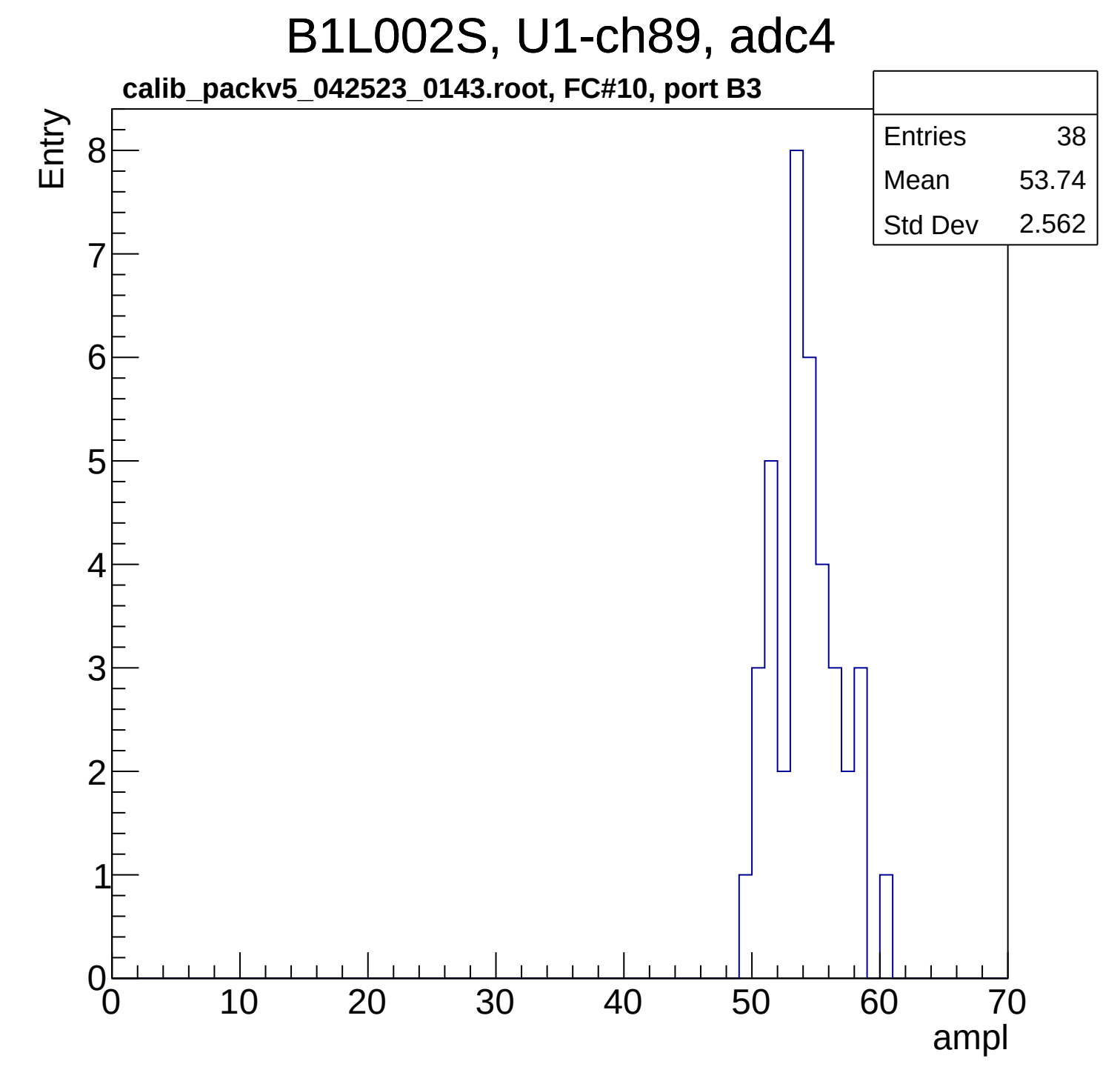
B1L002S, U1-ch89, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 53.74
Std Dev 2.562
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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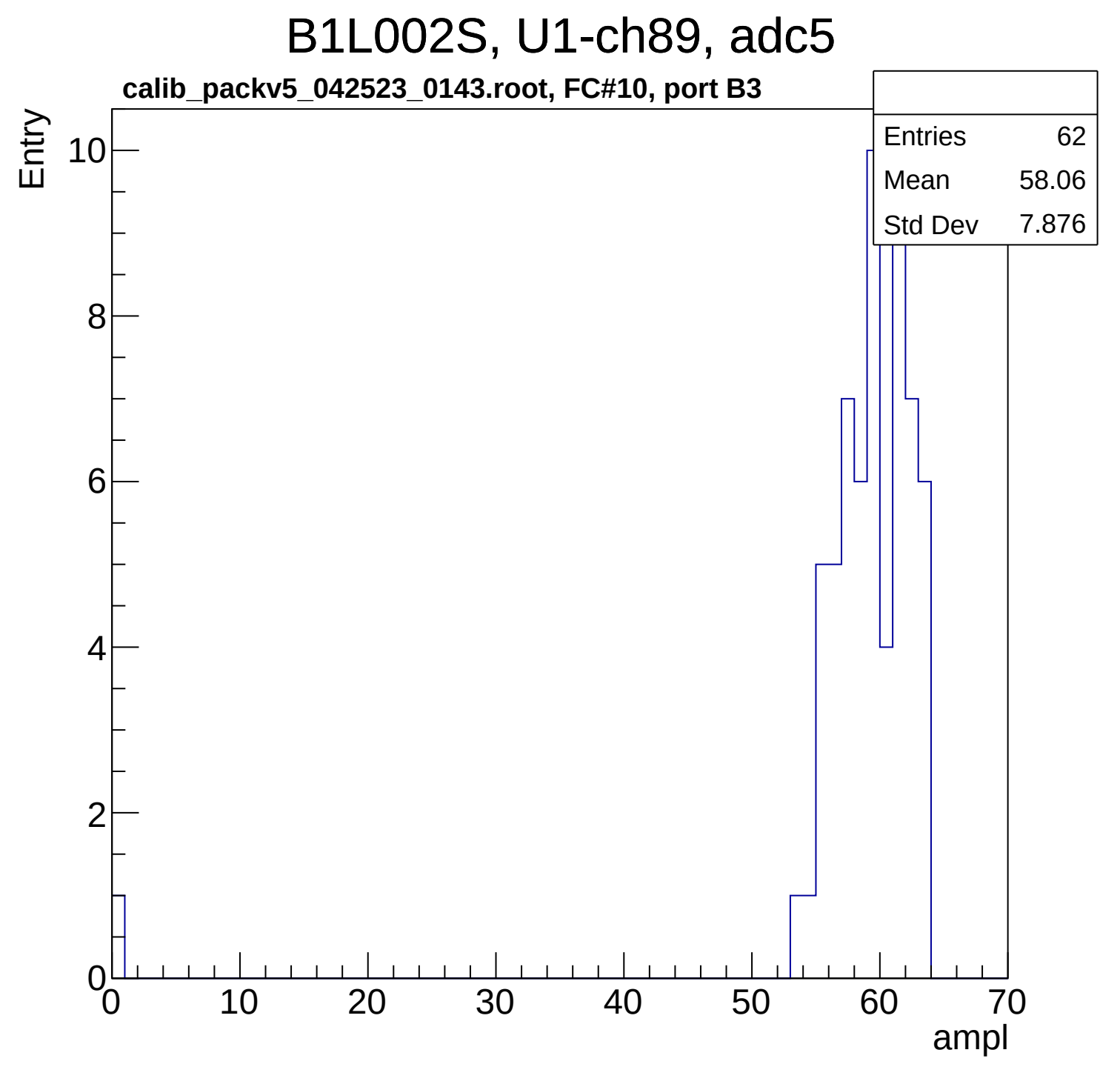
B1L002S, U1-ch89, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.06
Std Dev 7.876
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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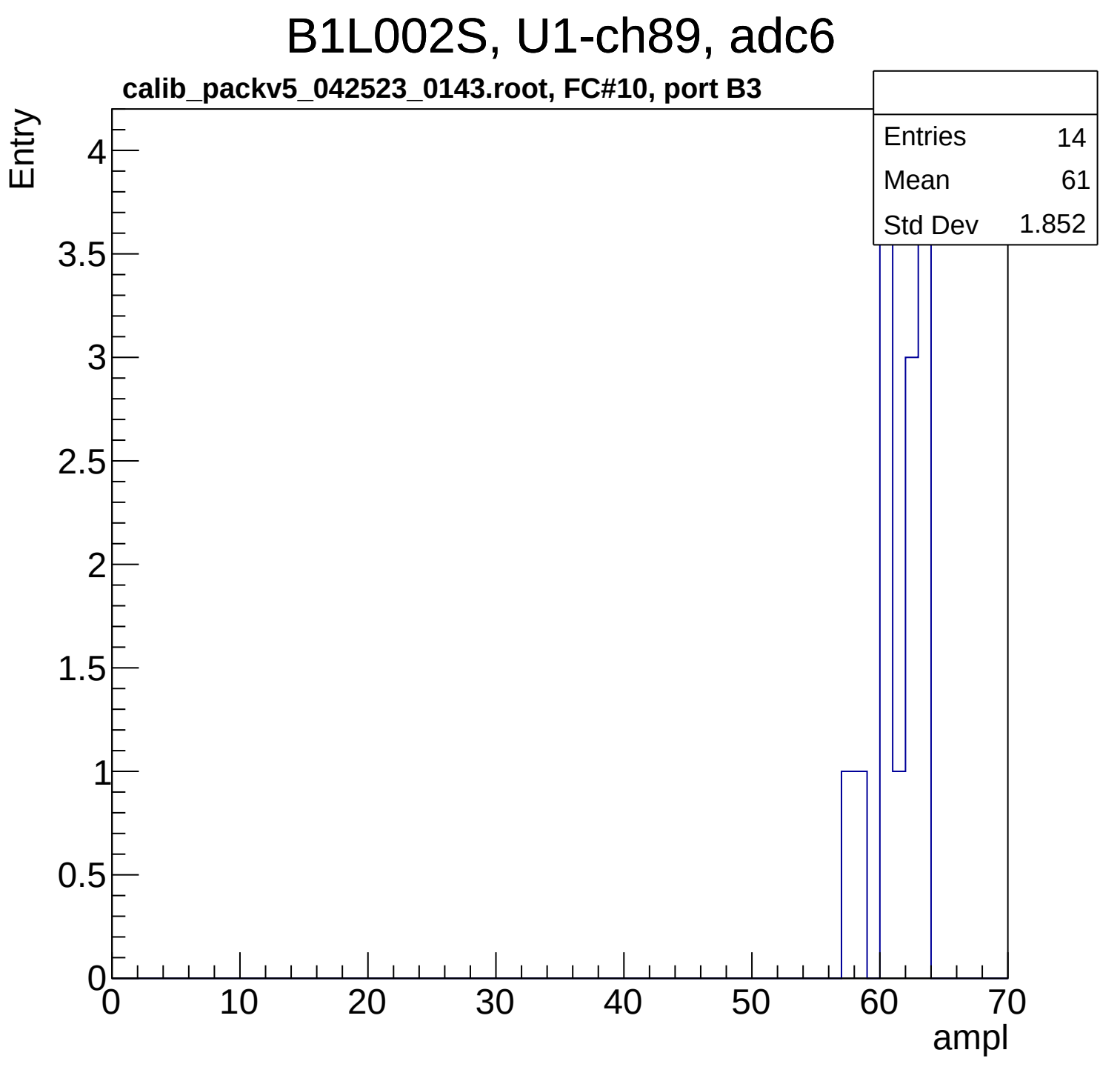
B1L002S, U1-ch89, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61
Std Dev 1.852
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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60
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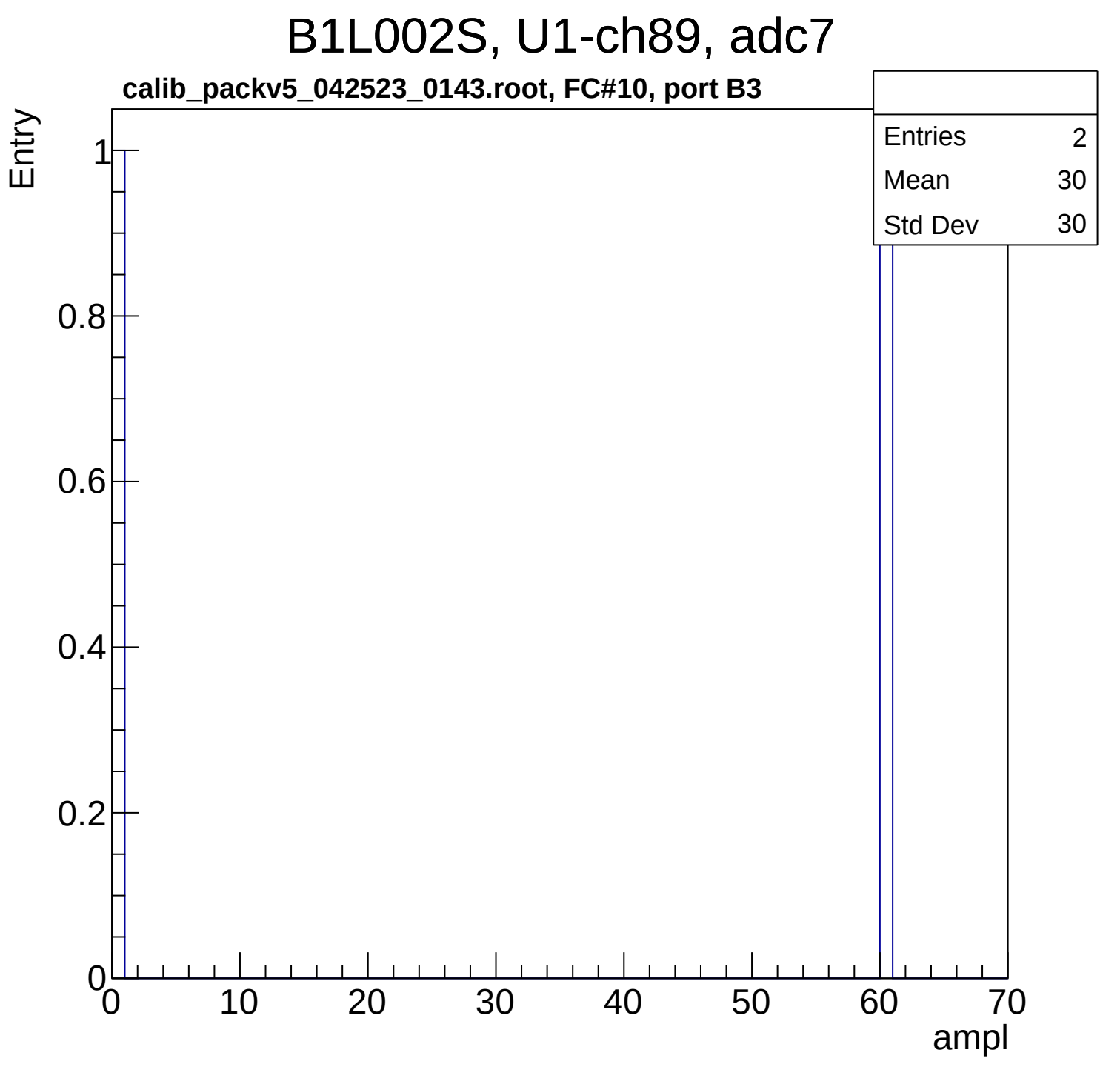
B1L002S, U1-ch89, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30
Std Dev 30
Entry
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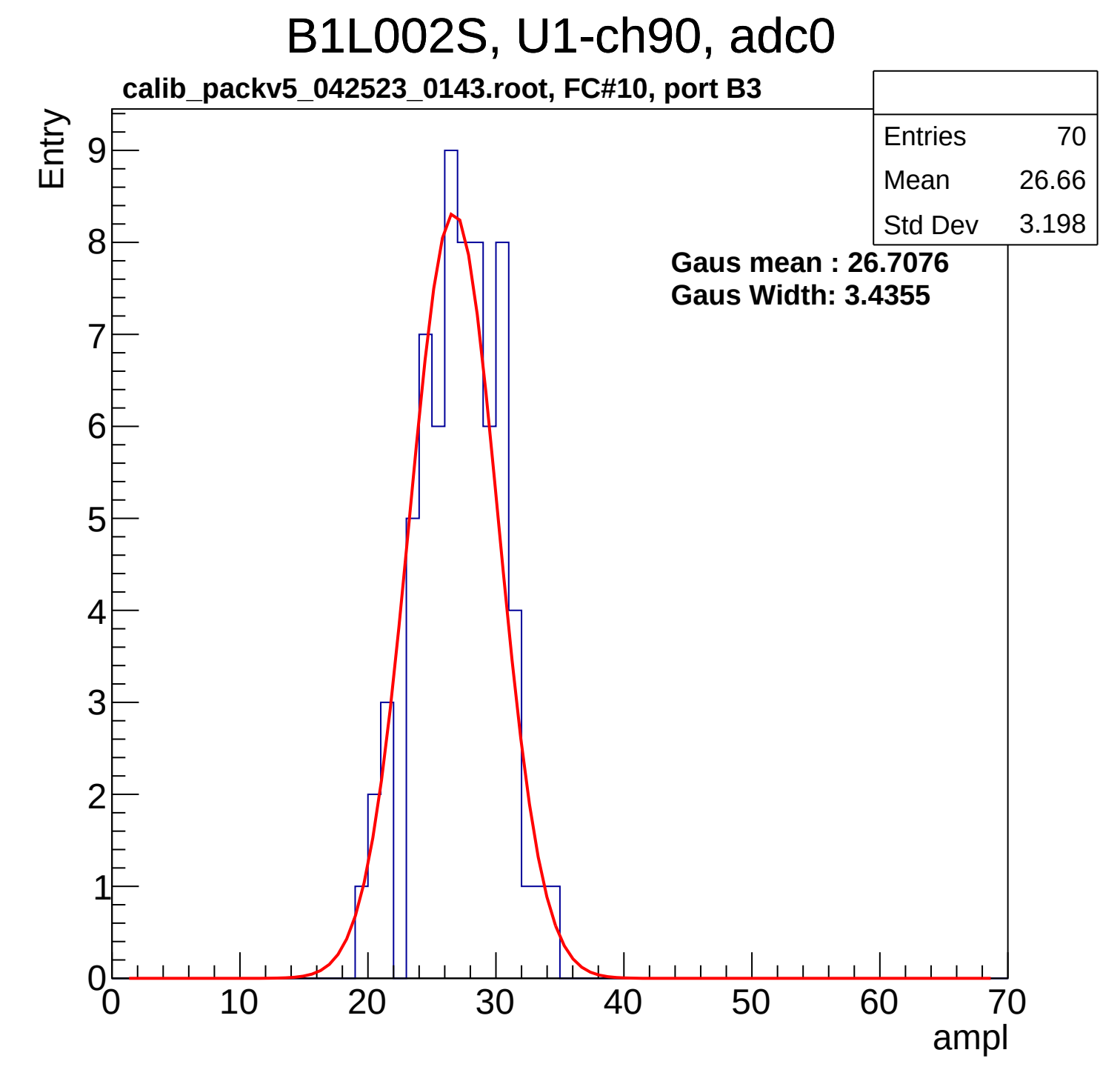
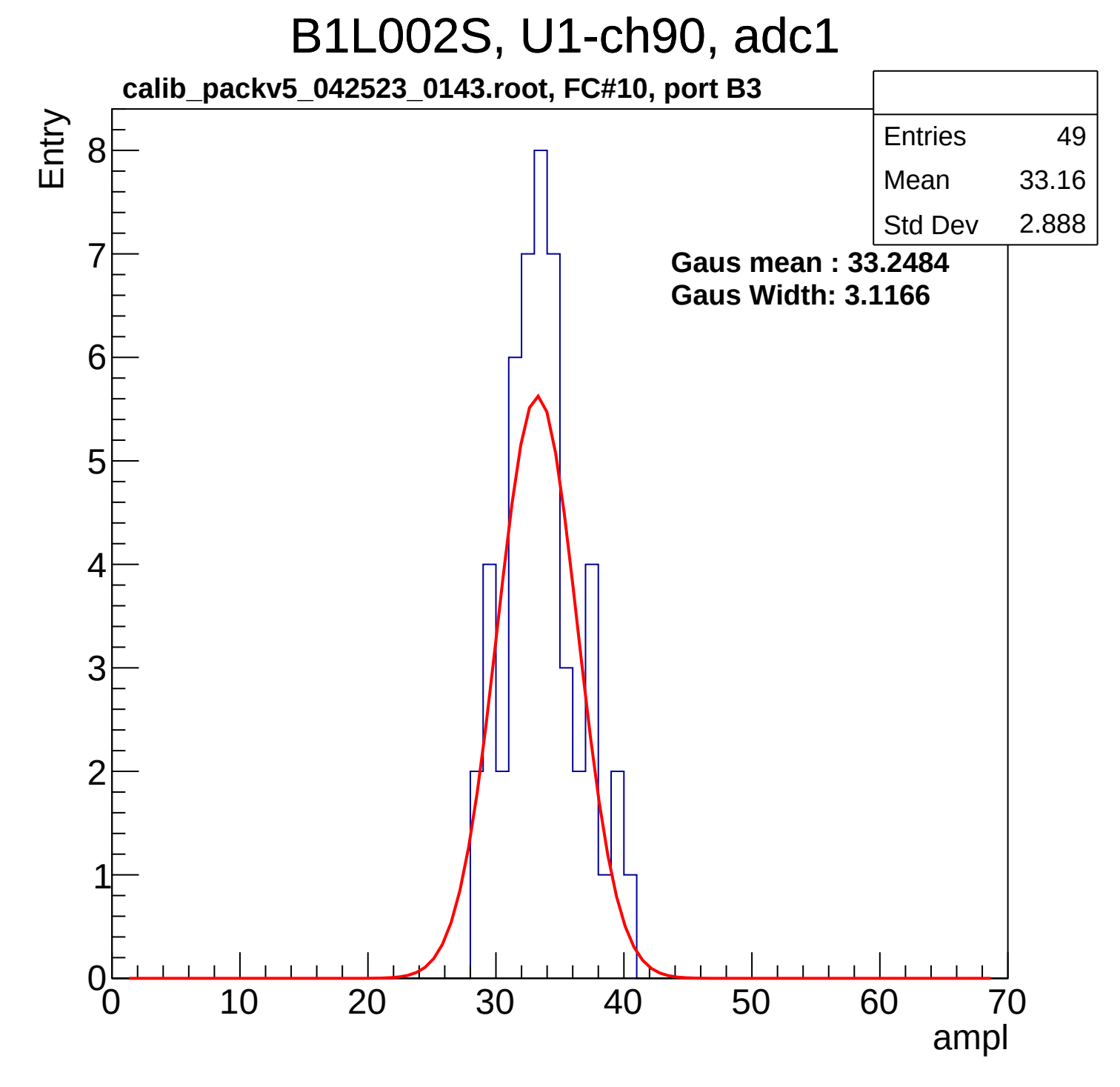
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch90, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.66
Std Dev 3.198
Gaus mean : 26.7076
Gaus Width: 3.4355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch90, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 33.16
Std Dev 2.888
Gaus mean : 33.2484
Gaus Width: 3.1166
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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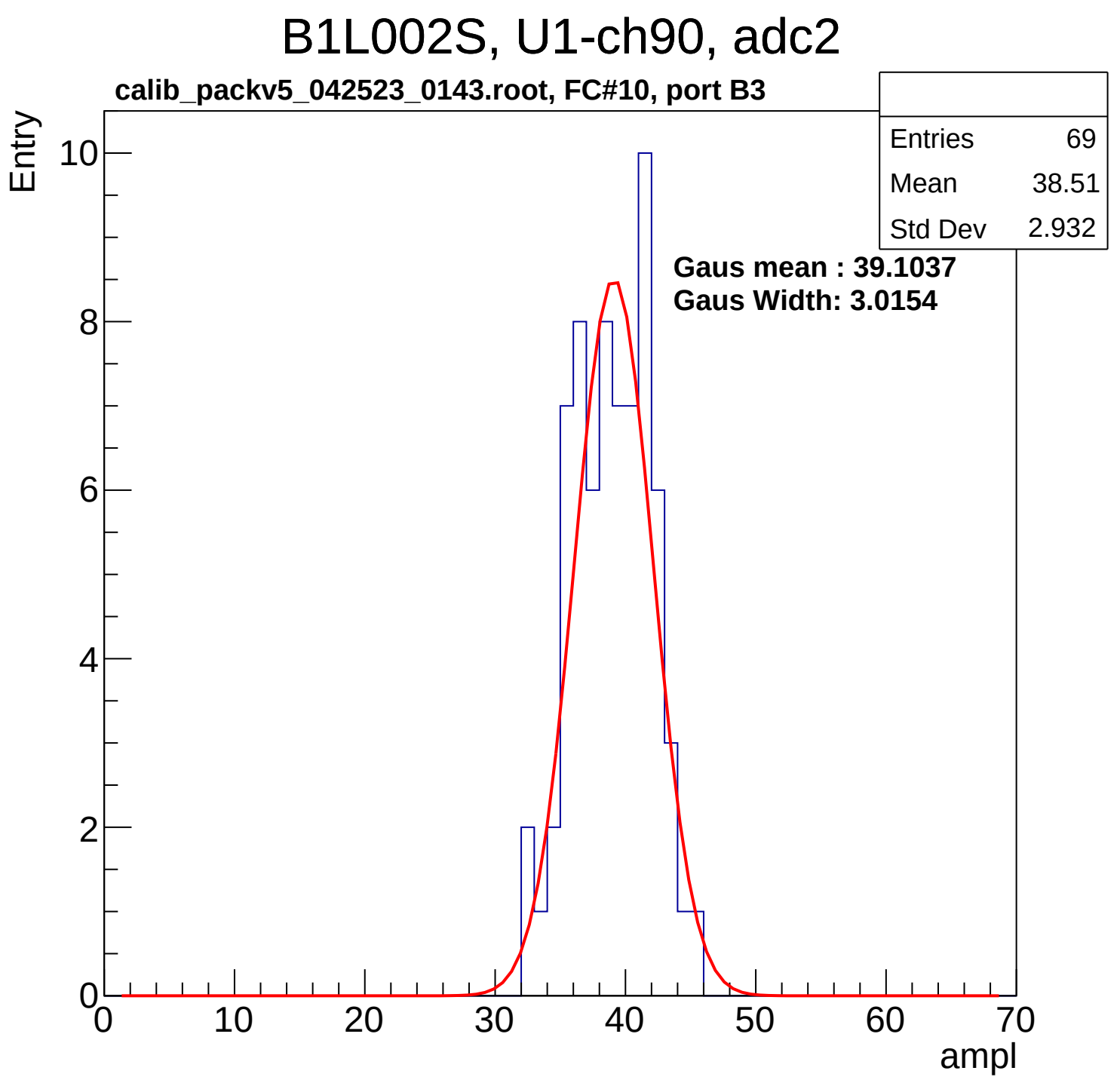
B1L002S, U1-ch90, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 38.51
Std Dev 2.932
Gaus mean : 39.1037
Gaus Width: 3.0154
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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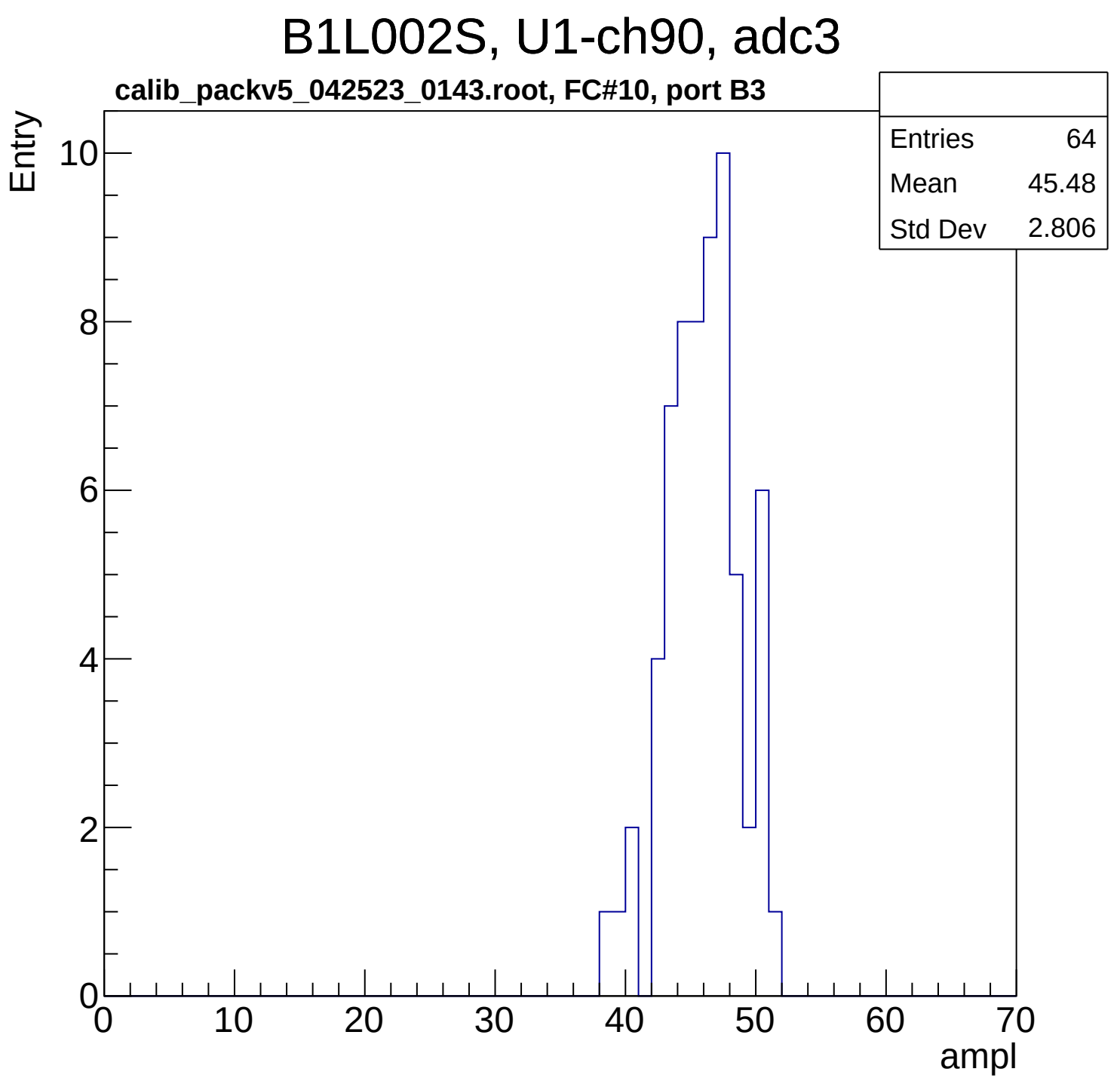
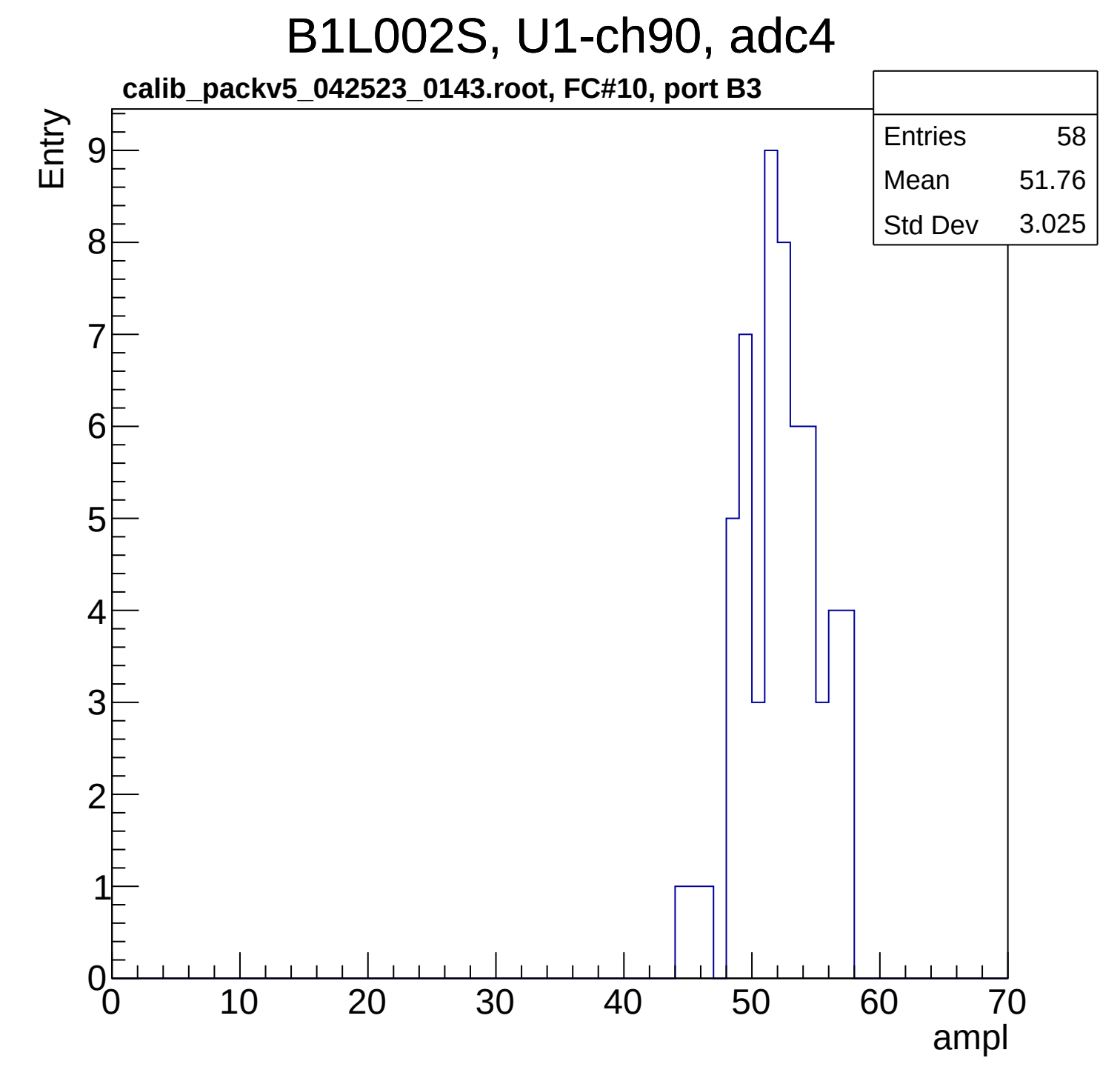

B1L002S, U1-ch90, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 45.48
Std Dev 2.806
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch90, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 51.76
Std Dev 3.025
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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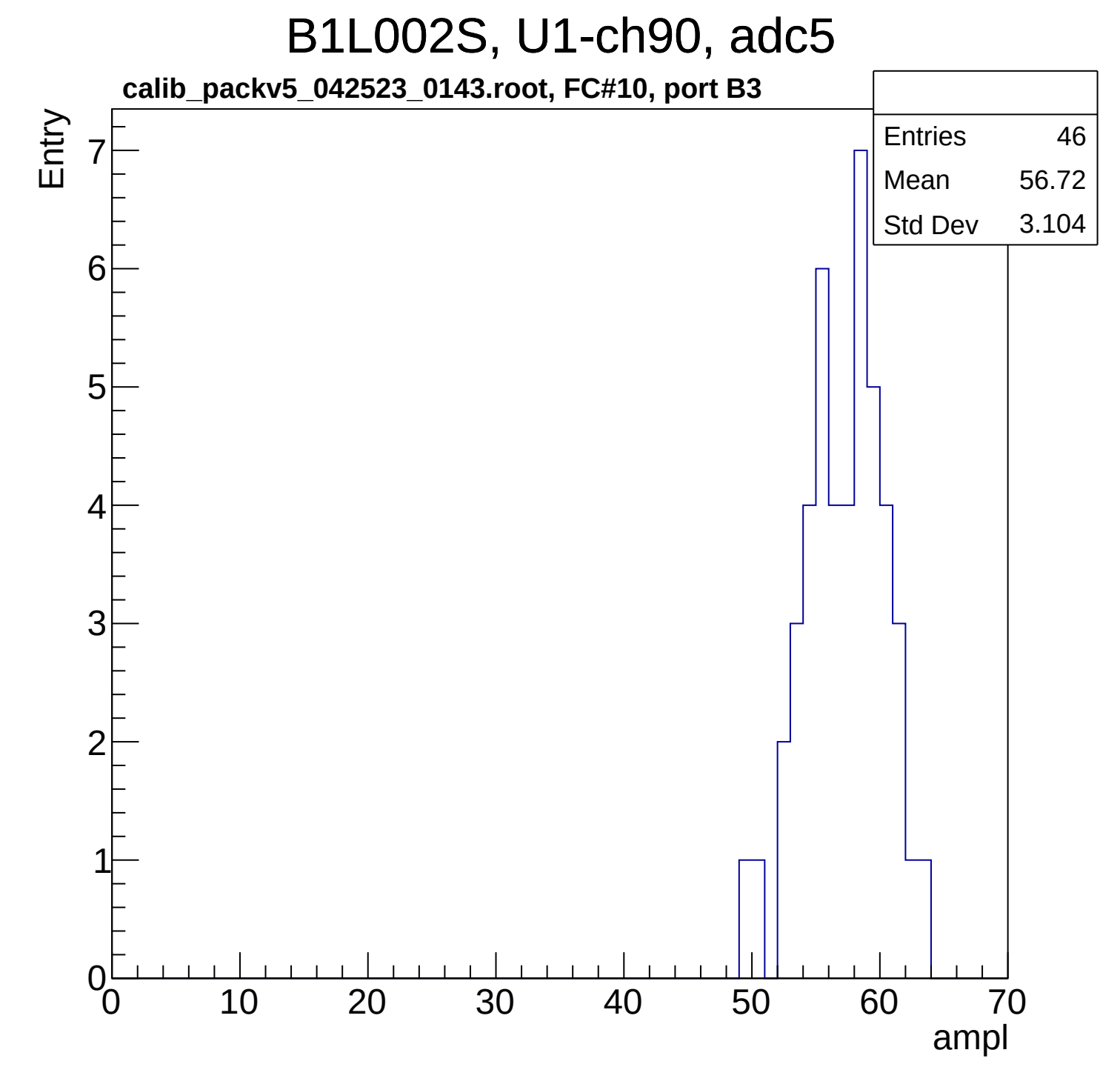
B1L002S, U1-ch90, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 56.72
Std Dev 3.104
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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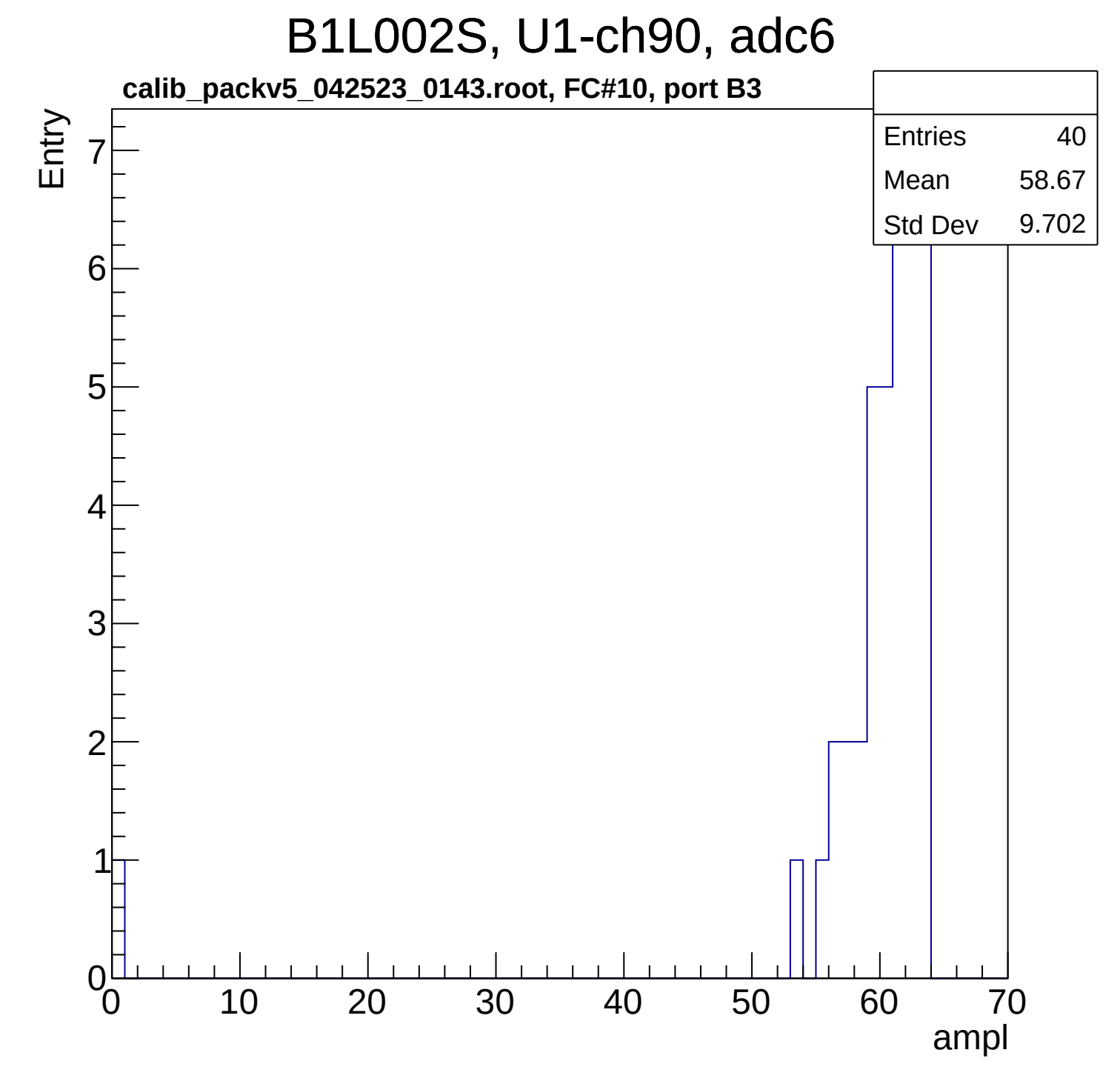
B1L002S, U1-ch90, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 58.67
Std Dev 9.702
Entry
ampl
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2
3
4
5
6
7
0
10
20
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40
50
60
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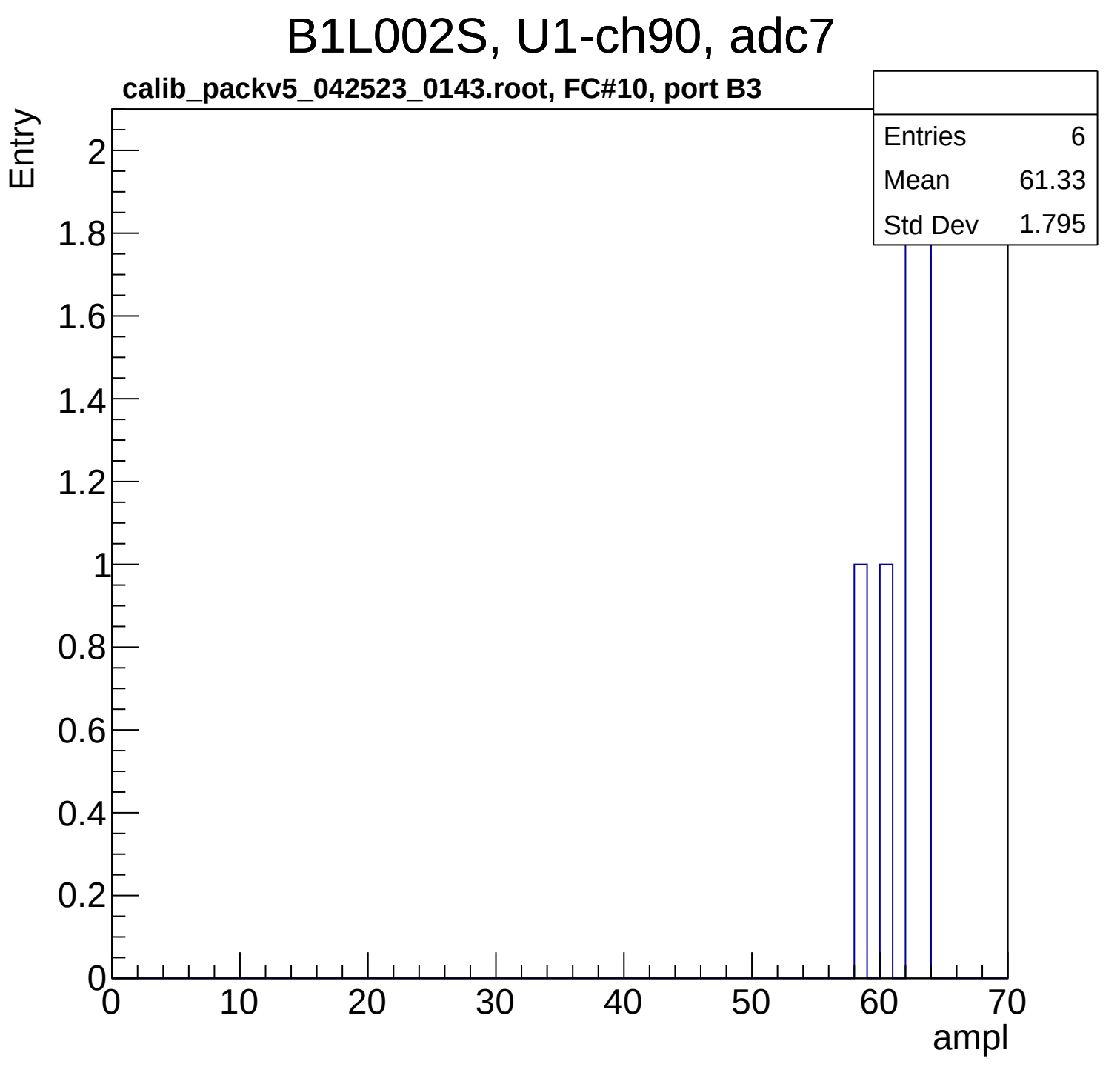
B1L002S, U1-ch90, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 61.33
Std Dev 1.795
Entry
ampl
0
0.2
0.4
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1
1.2
1.4
1.6
1.8
2
0
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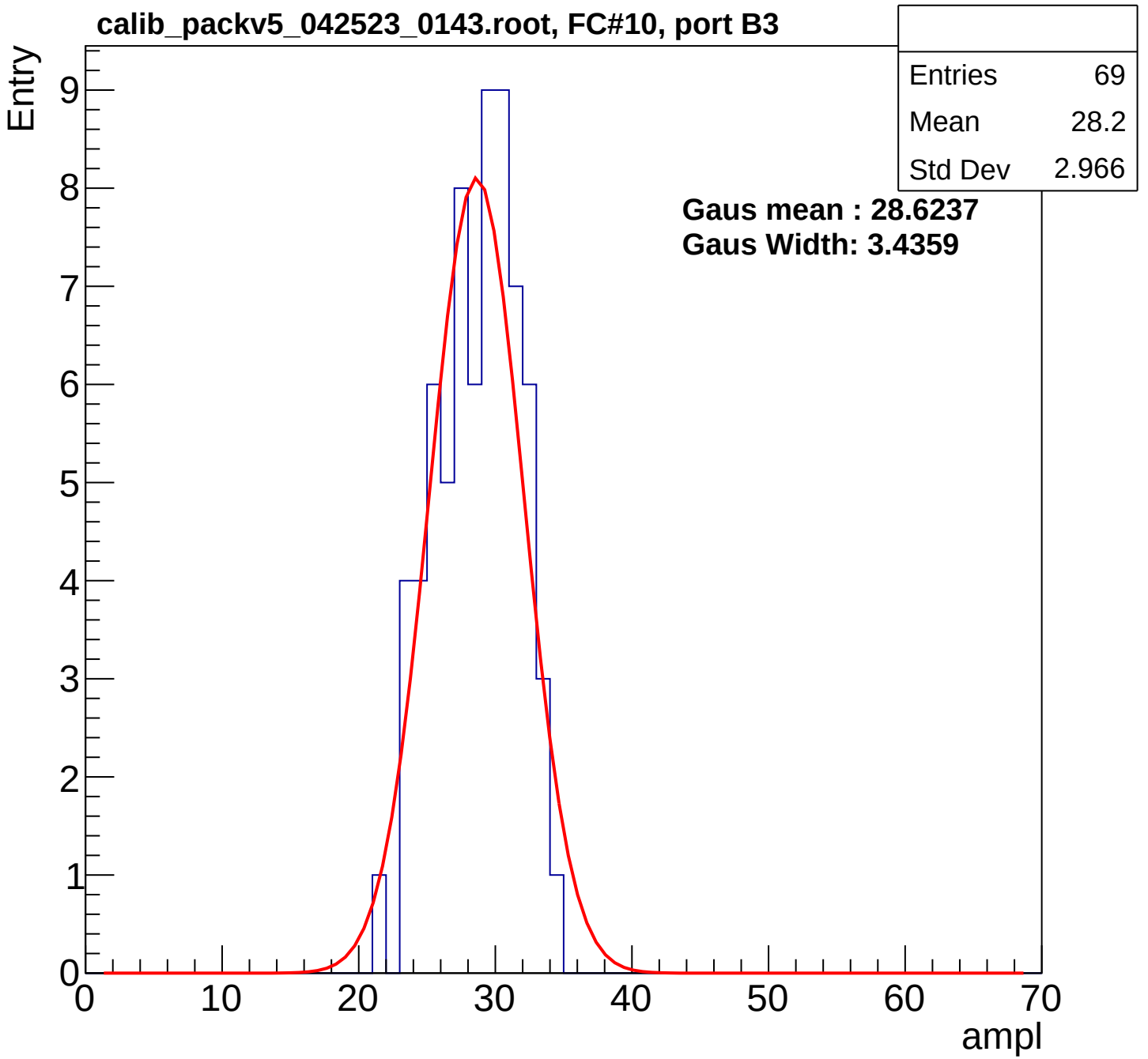
40
50
60
70


B1L002S, U1-ch91, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.2
Std Dev 2.966
Gaus mean : 28.6237
Gaus Width: 3.4359
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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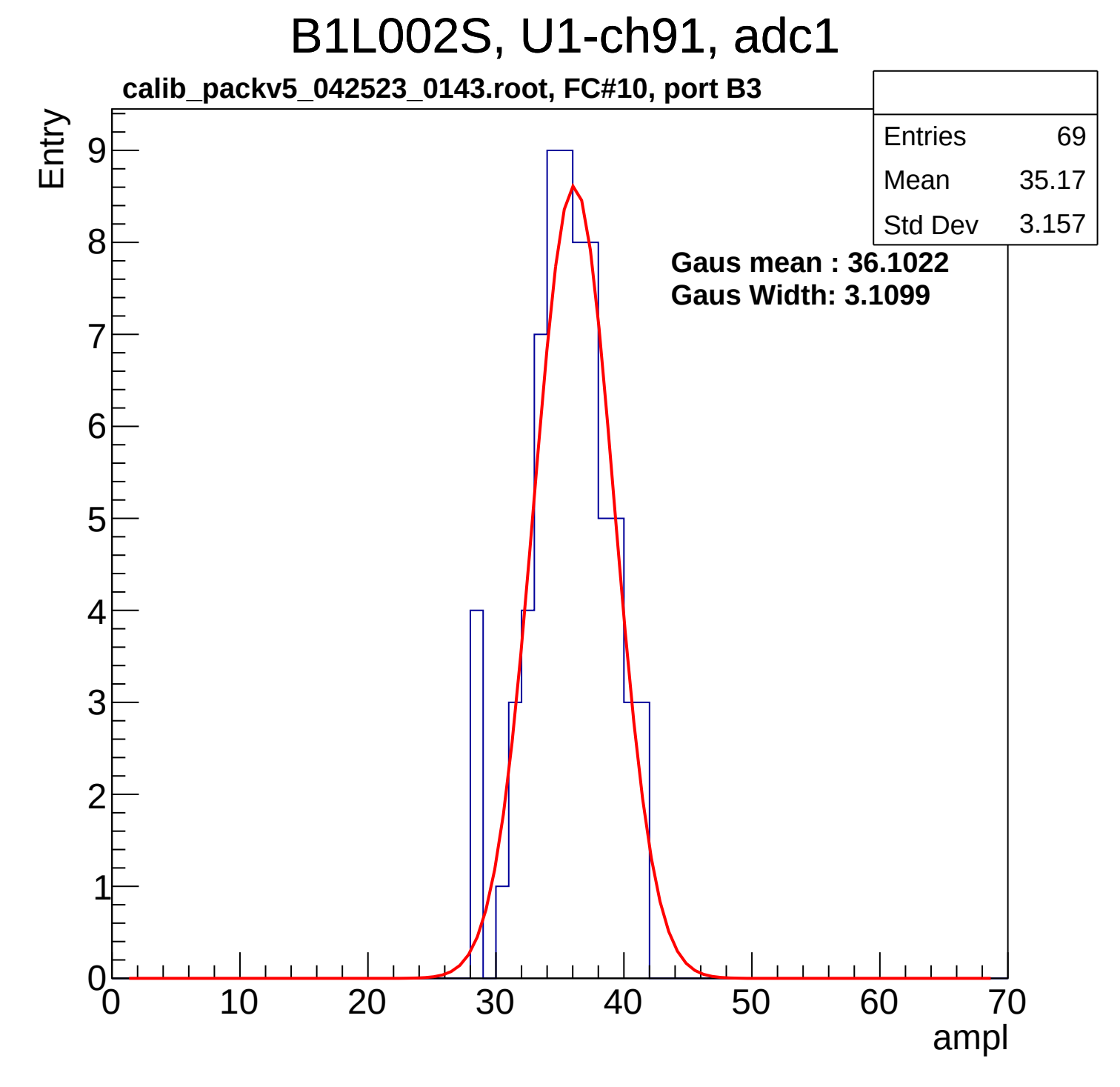
60
70


B1L002S, U1-ch91, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 35.17
Std Dev 3.157
Gaus mean : 36.1022
Gaus Width: 3.1099
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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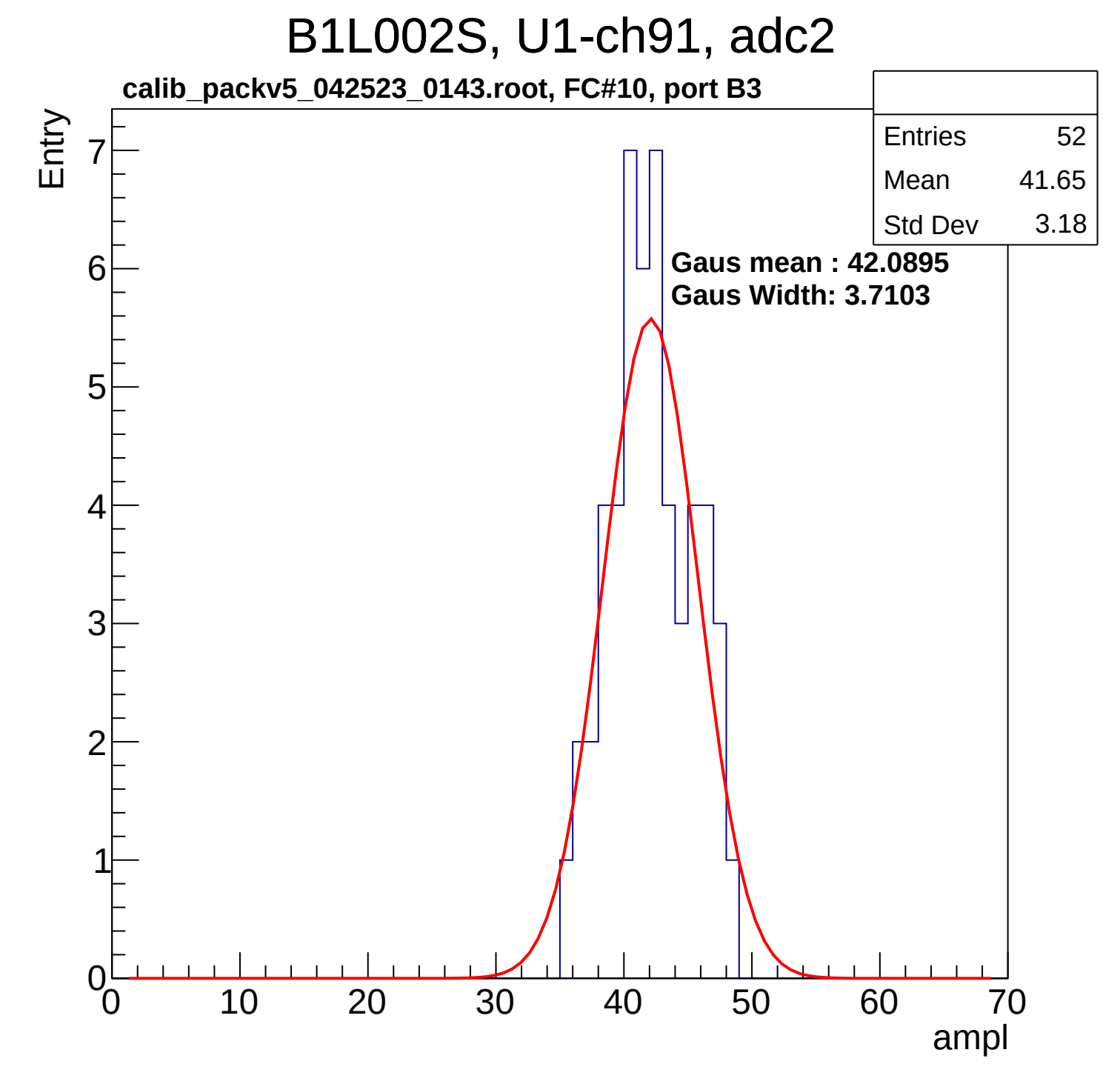
20
30
40
50
60
70


B1L002S, U1-ch91, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.65
Std Dev 3.18
Gaus mean : 42.0895
Gaus Width: 3.7103
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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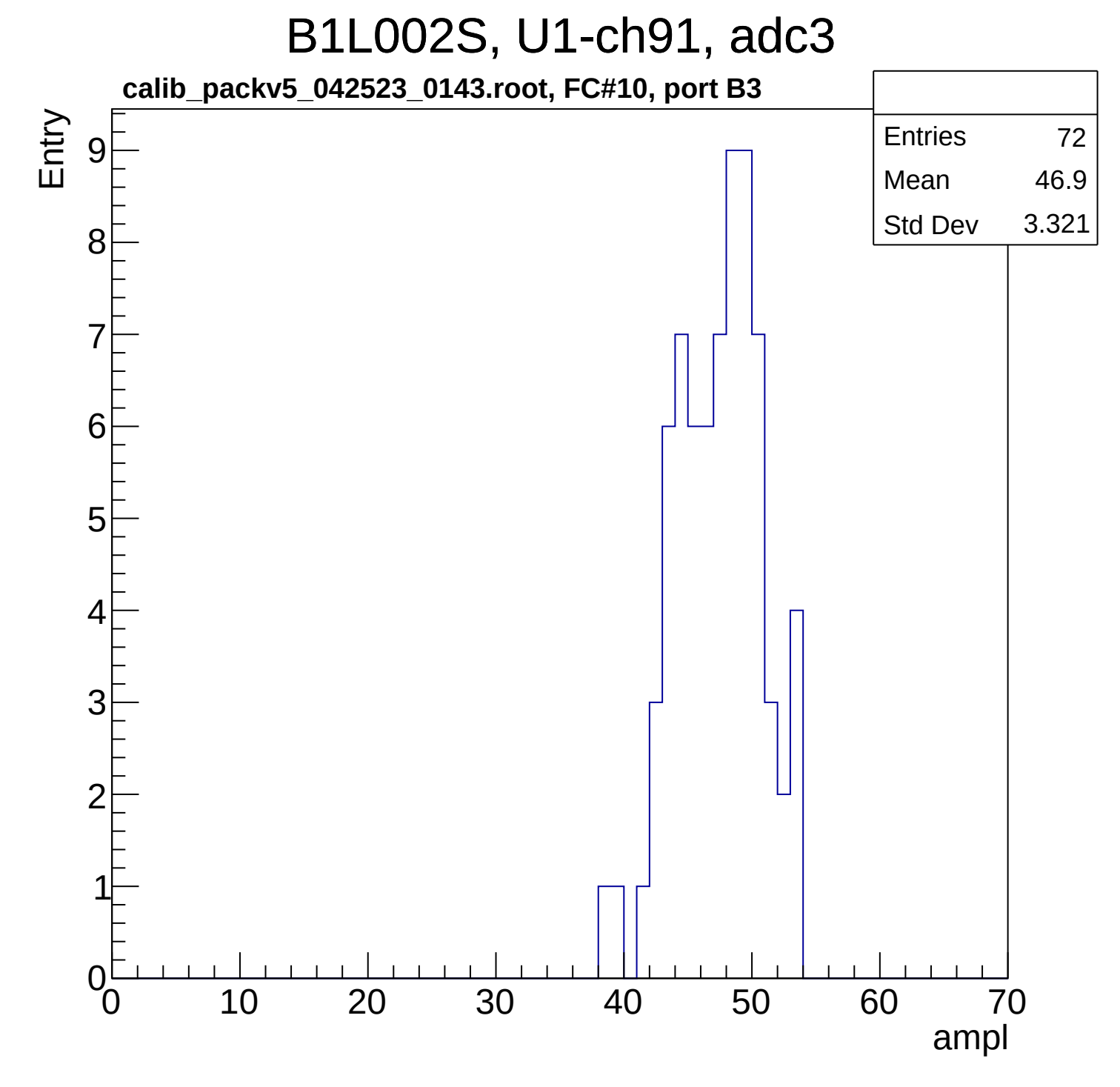
B1L002S, U1-ch91, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.9
Std Dev 3.321
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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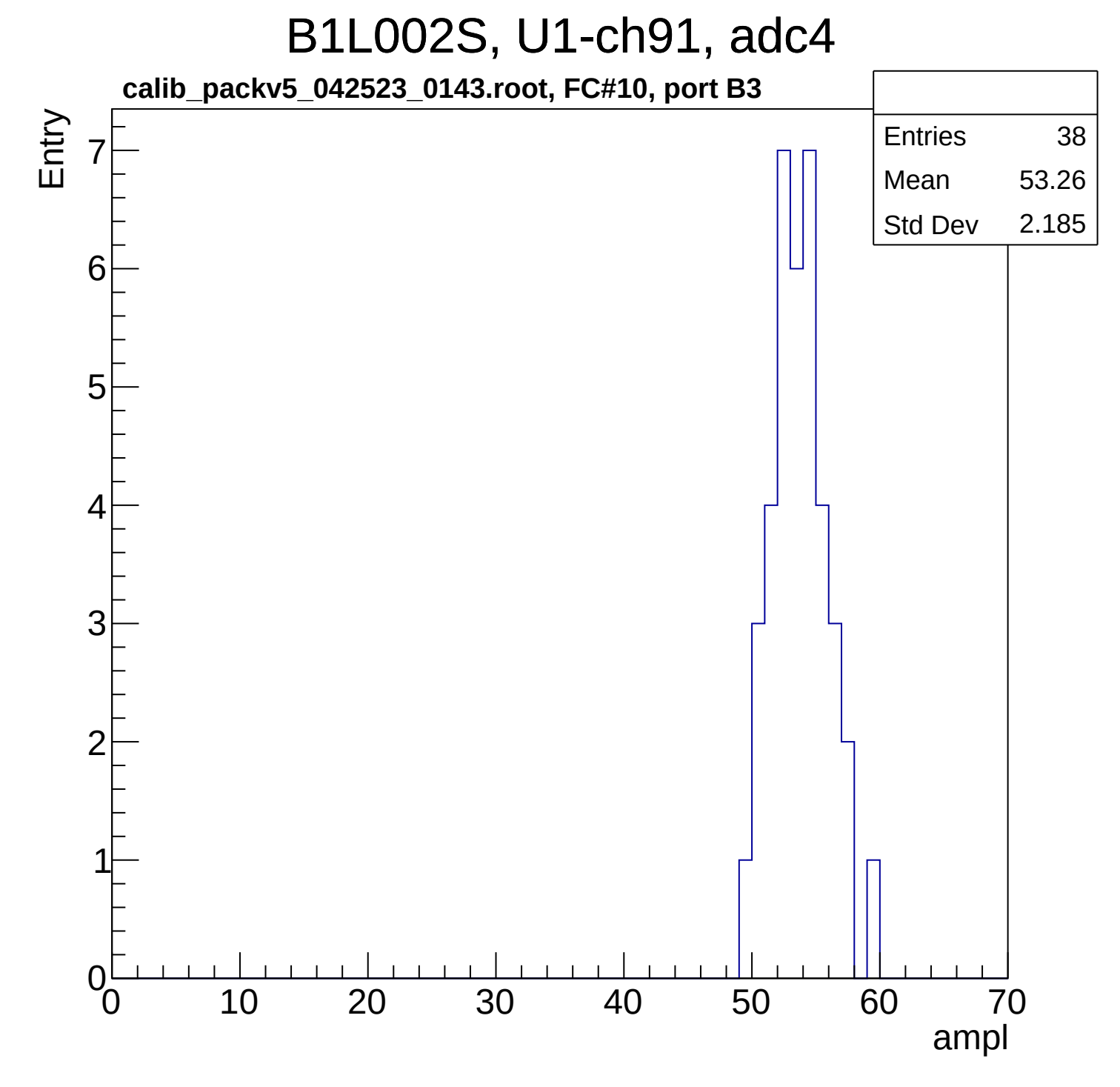
B1L002S, U1-ch91, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 53.26
Std Dev 2.185
Entry
ampl
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4
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0
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60
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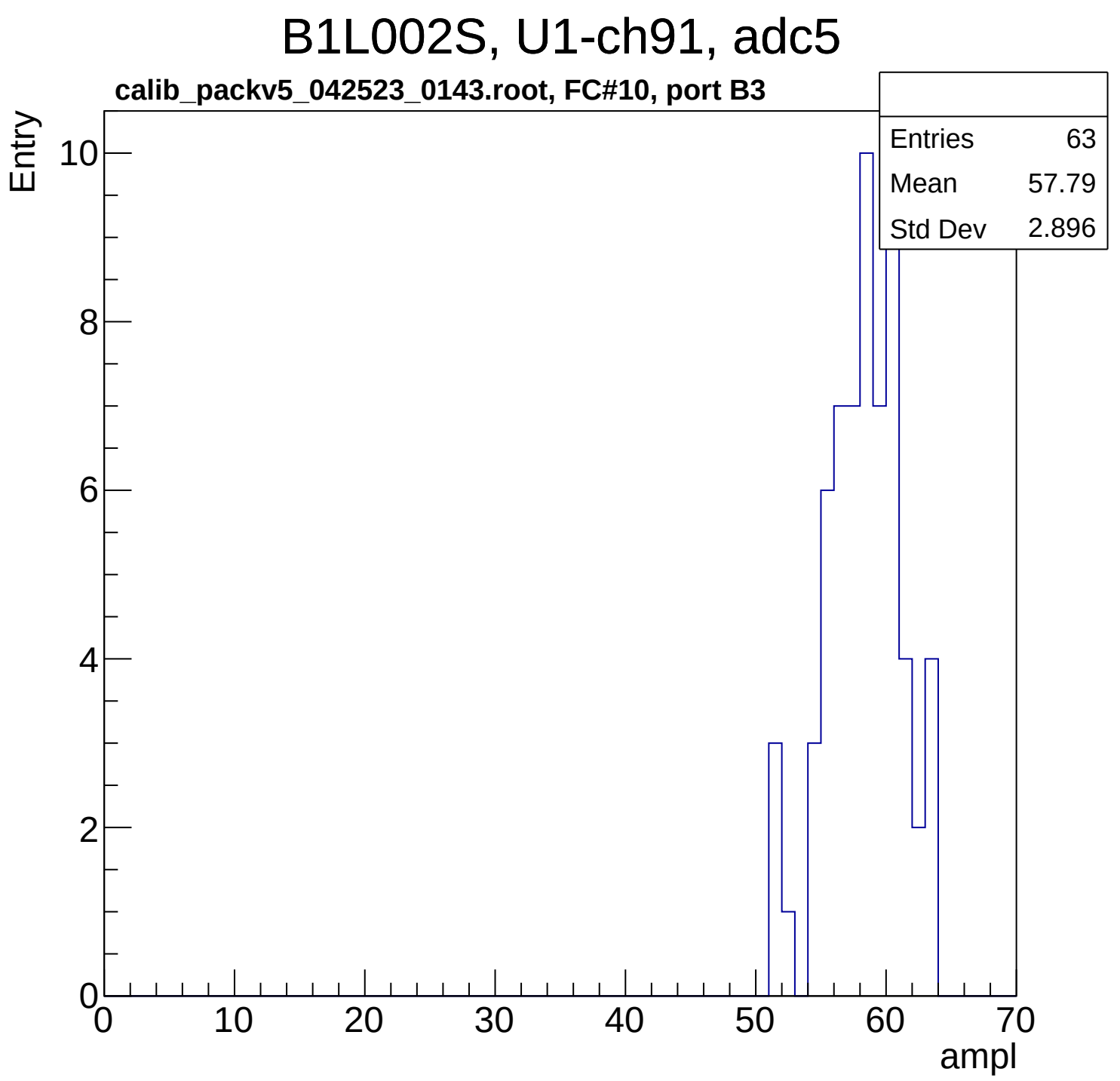
B1L002S, U1-ch91, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.79
Std Dev 2.896
Entry
ampl
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6
8
10
0
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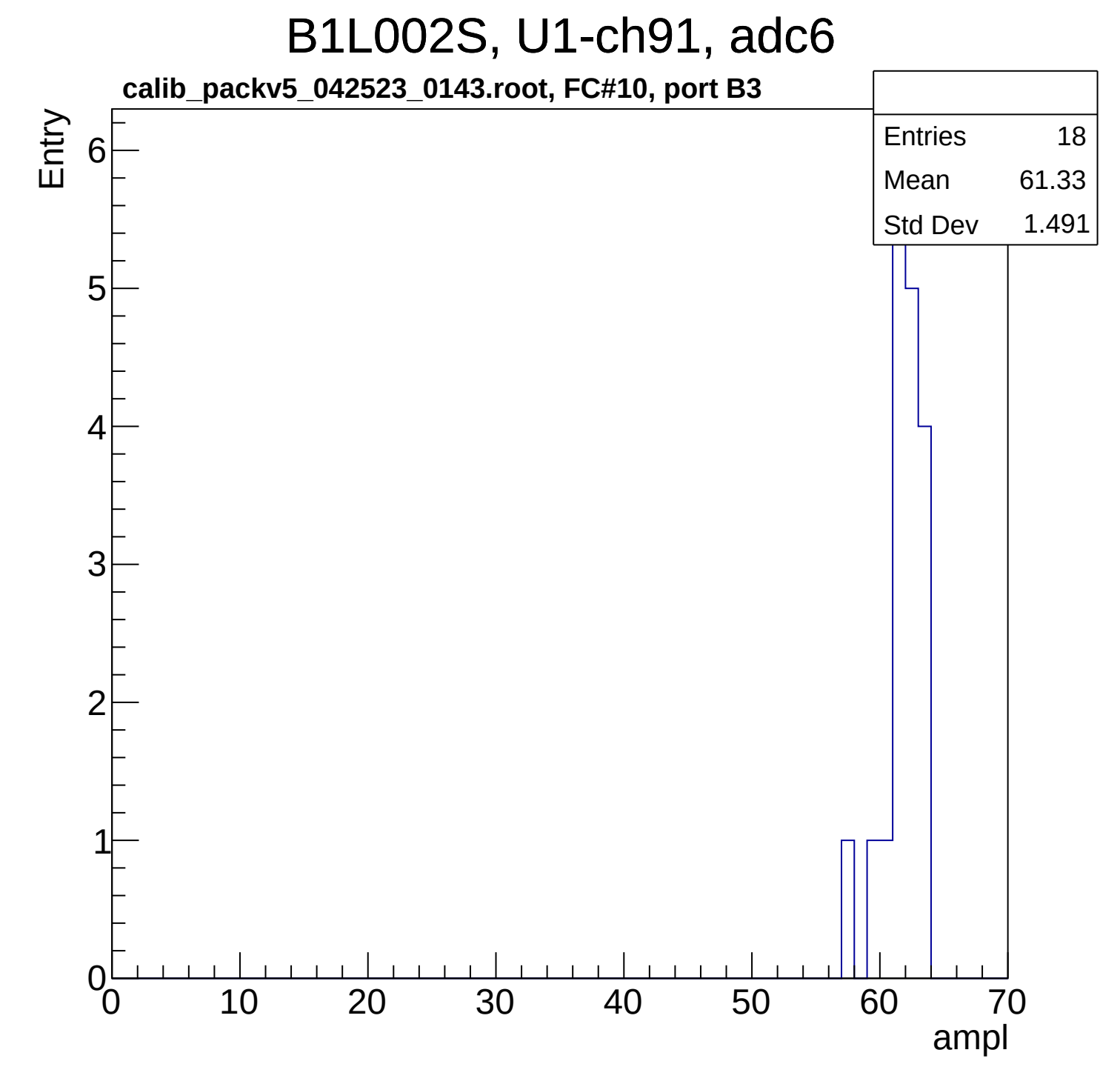
20
30
40
50
60
70


B1L002S, U1-ch91, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.33
Std Dev 1.491
Entry
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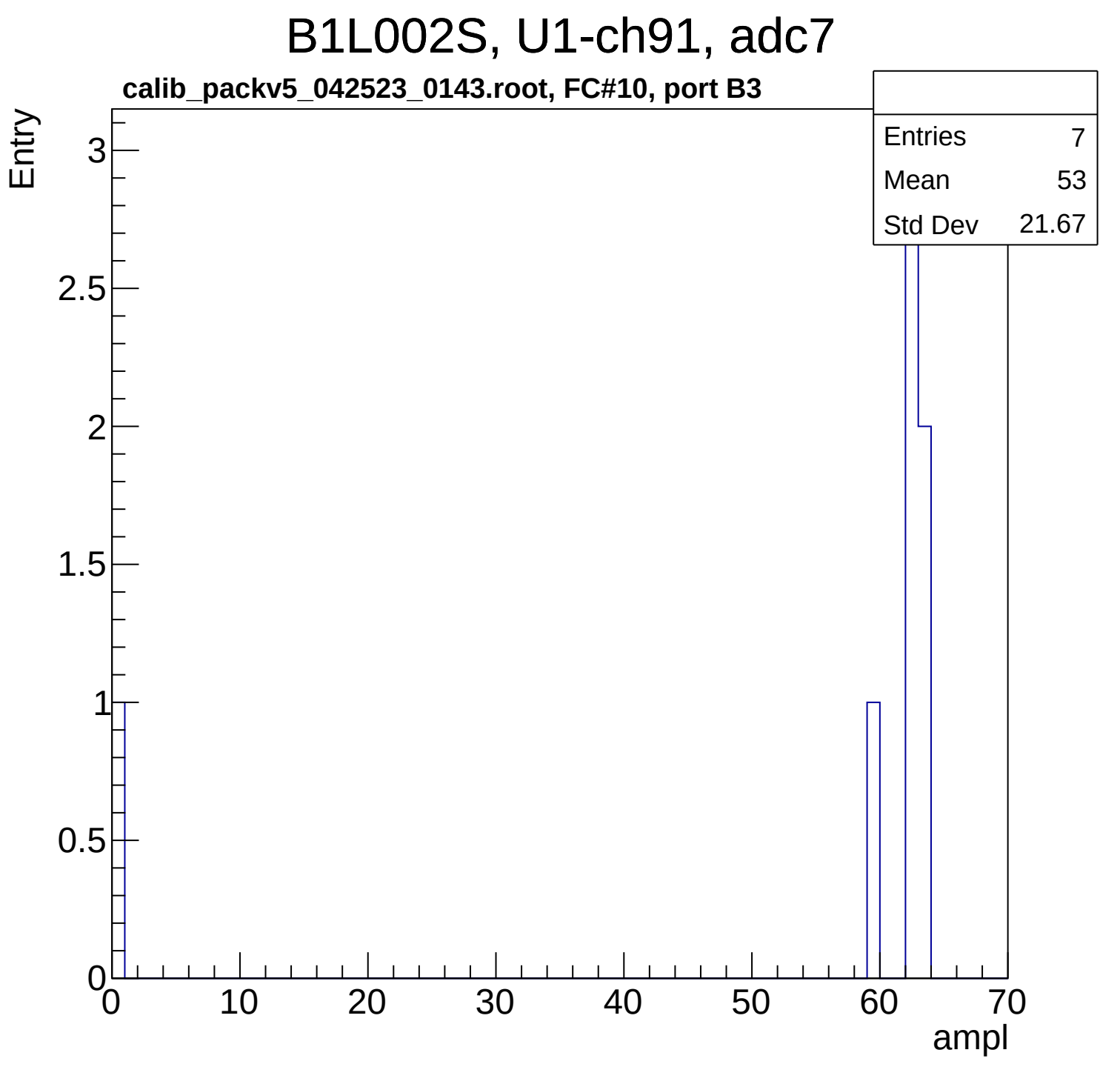
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch91, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53
Std Dev 21.67
Entry
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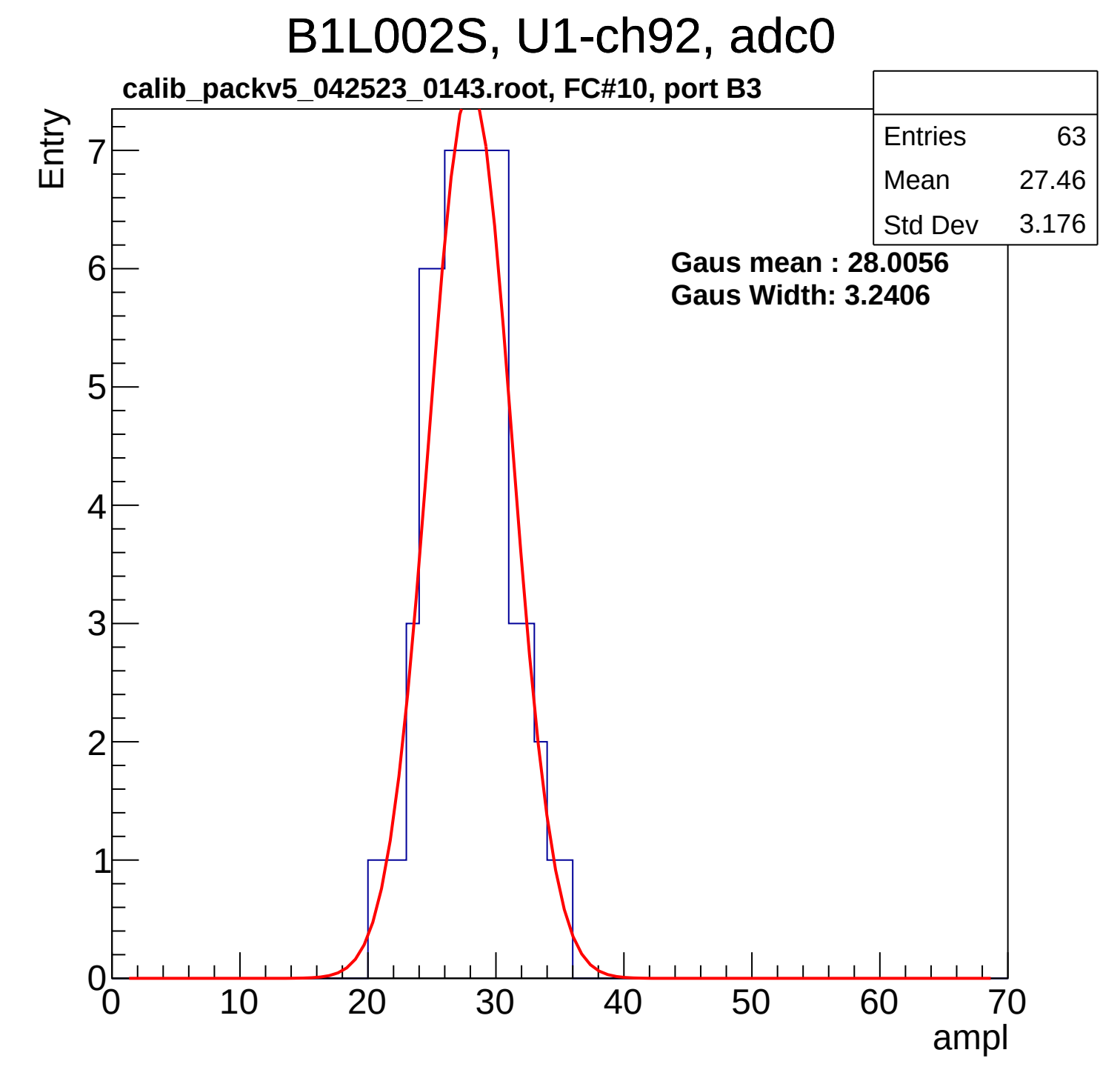
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch92, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.46
Std Dev 3.176
Gaus mean : 28.0056
Gaus Width: 3.2406
Entry
ampl
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2
3
4
5
6
7
0
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30
40
50
60
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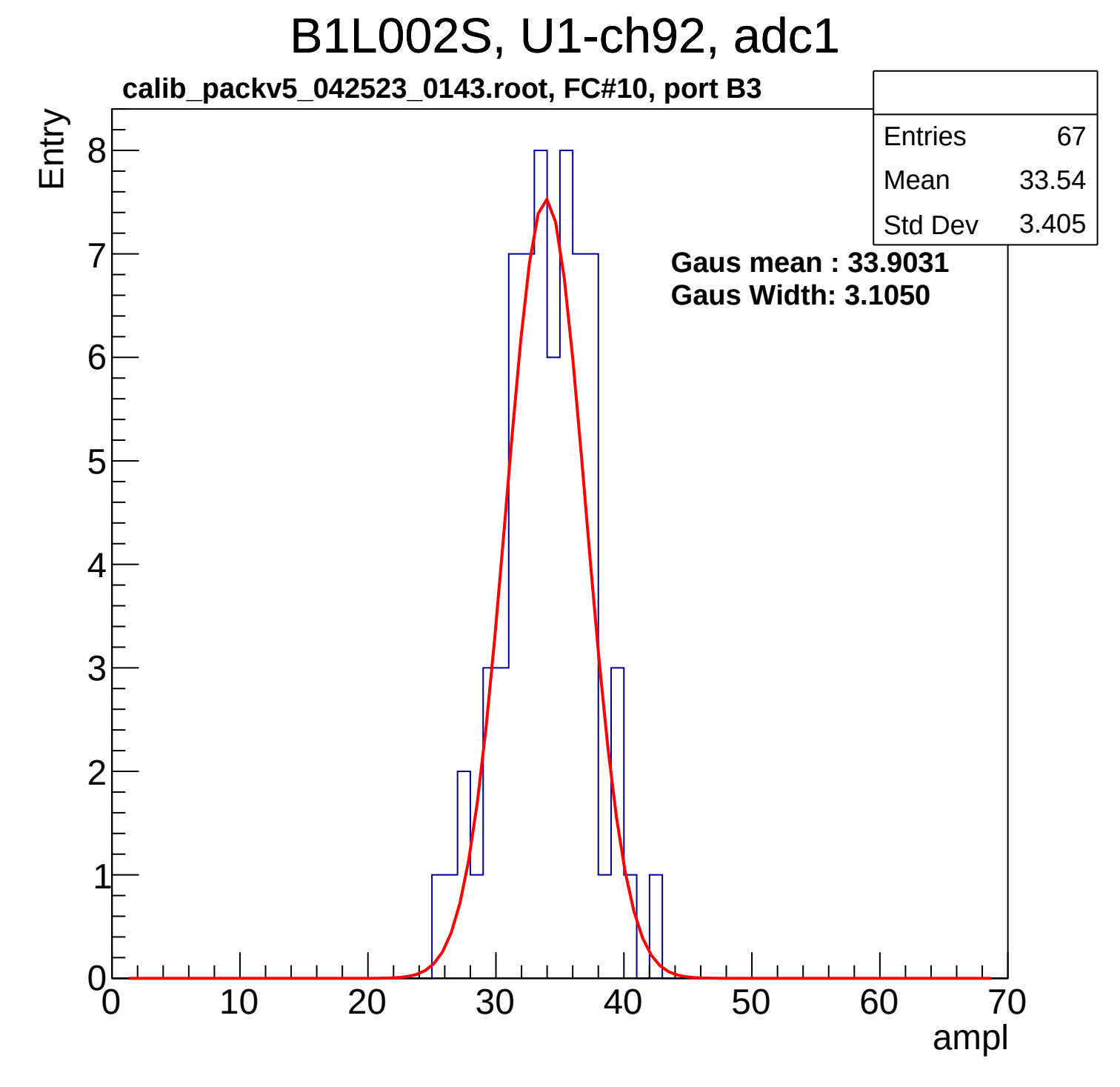
B1L002S, U1-ch92, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.54
Std Dev 3.405
Gaus mean : 33.9031
Gaus Width: 3.1050
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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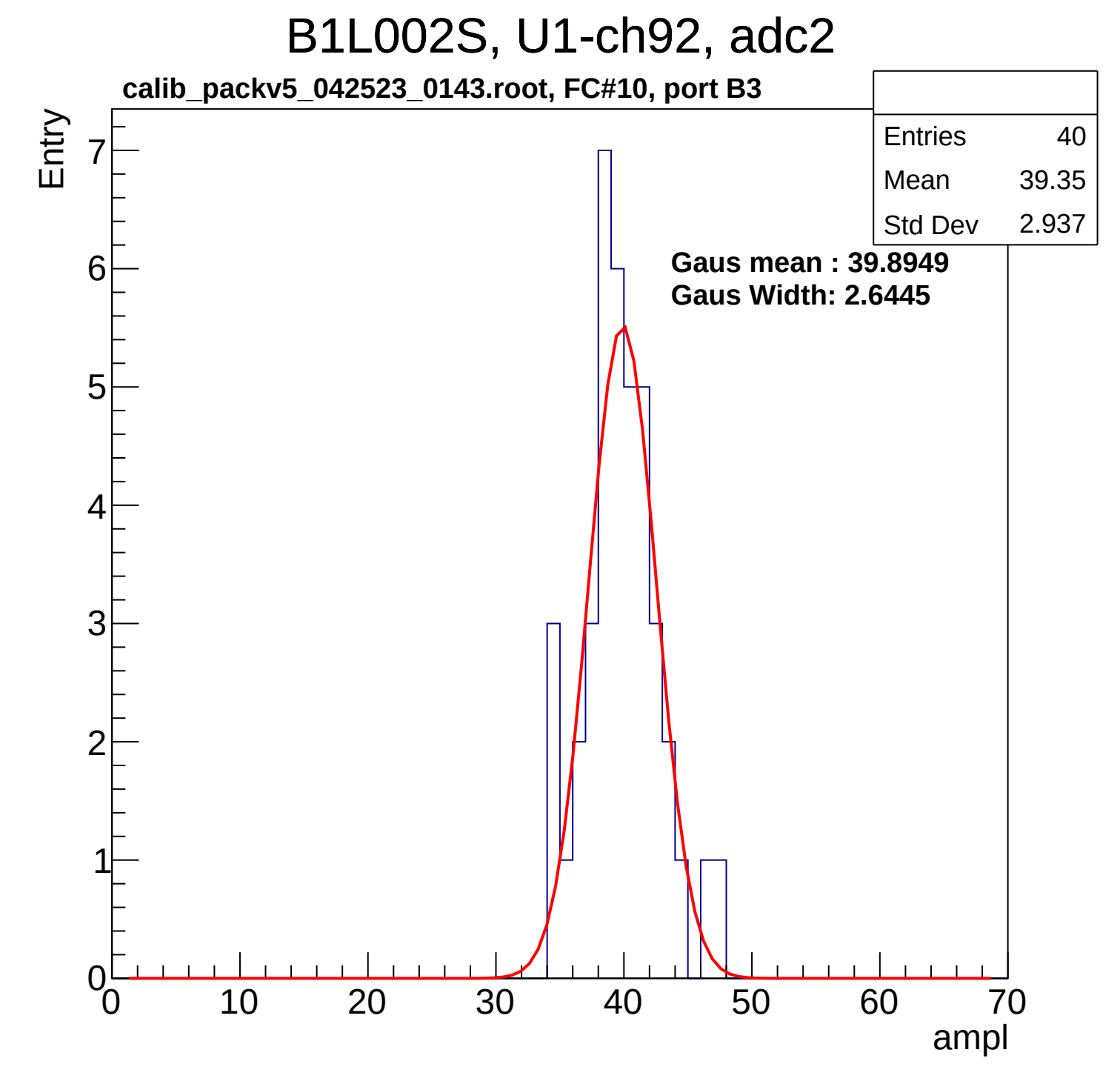
B1L002S, U1-ch92, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 39.35
Std Dev 2.937
Gaus mean : 39.8949
Gaus Width: 2.6445
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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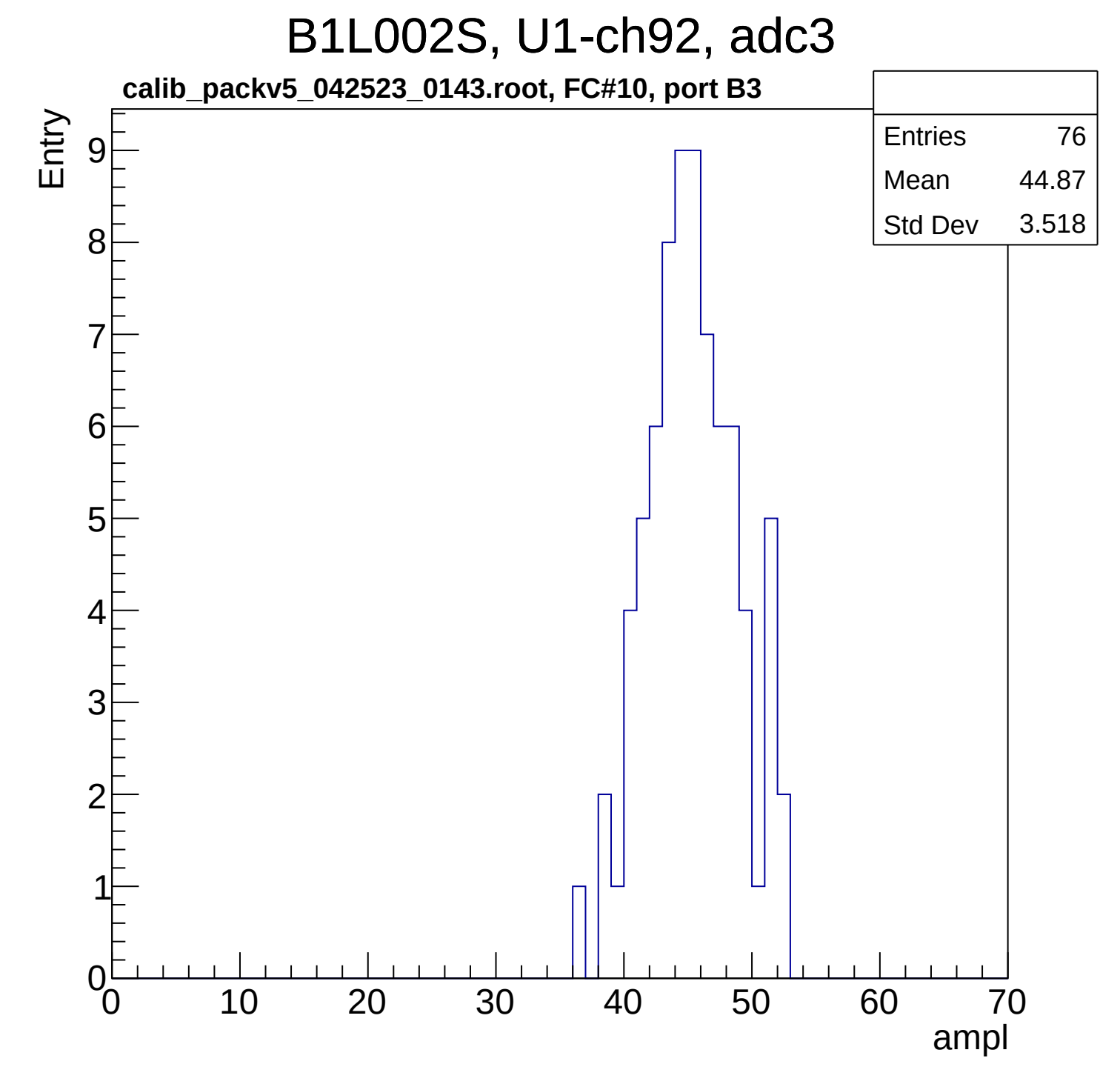
B1L002S, U1-ch92, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 44.87
Std Dev 3.518
Entry
ampl
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30
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2
3
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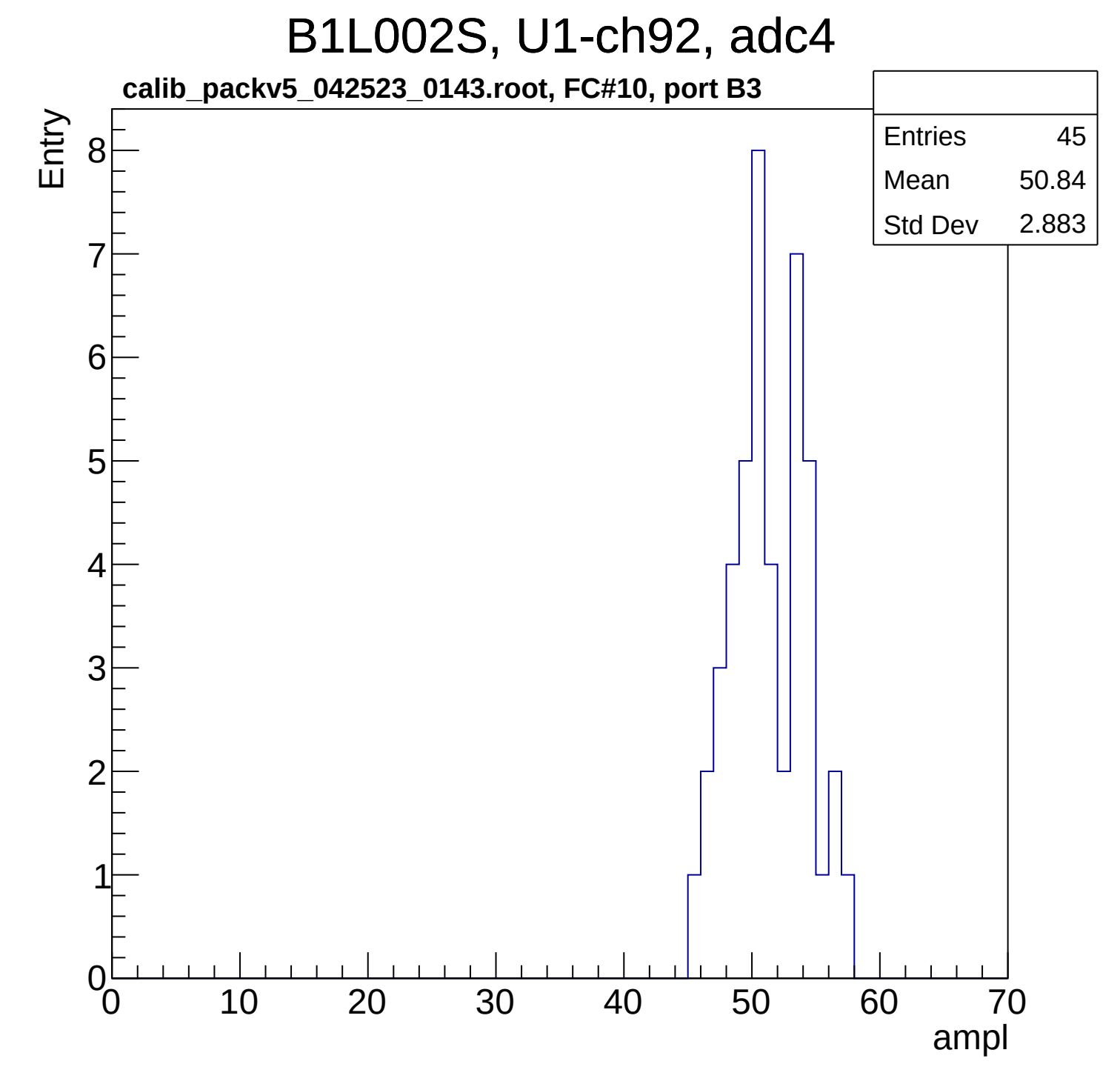
5
6
7
8
9


B1L002S, U1-ch92, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 50.84
Std Dev 2.883
Entry
ampl
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3
4
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60
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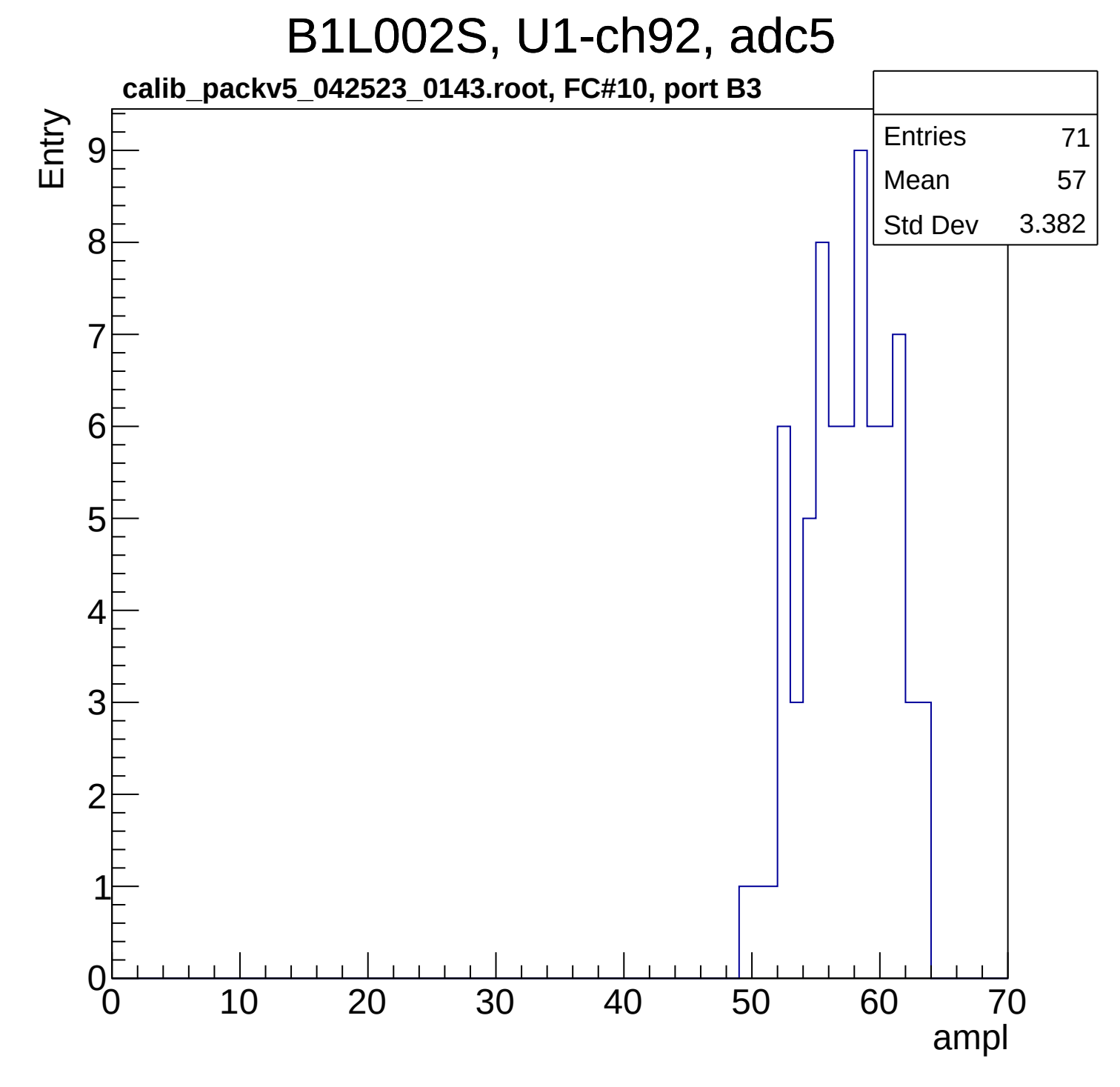
B1L002S, U1-ch92, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57
Std Dev 3.382
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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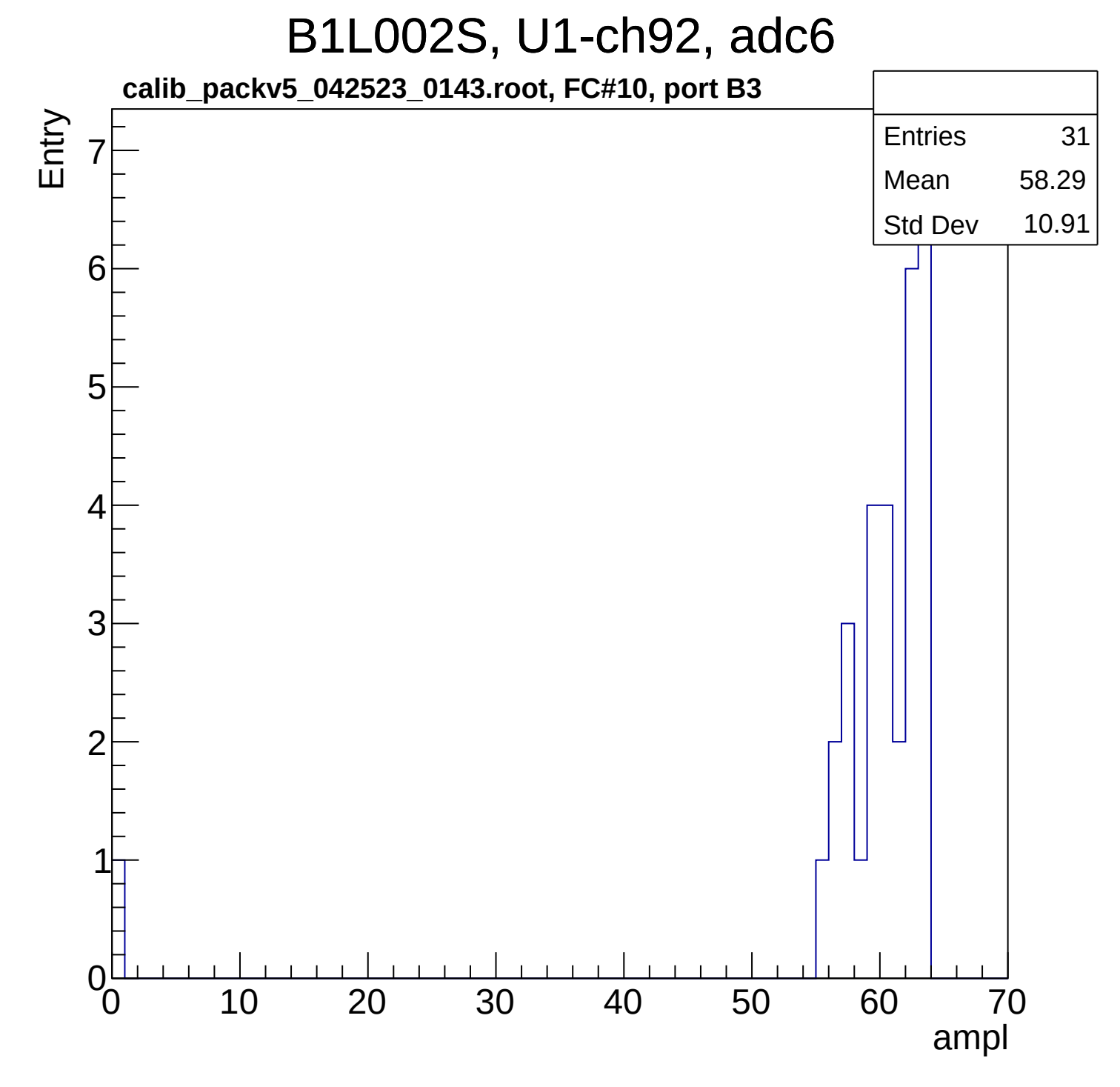
B1L002S, U1-ch92, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.29
Std Dev 10.91
Entry
ampl
0
1
2
3
4
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6
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0
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60
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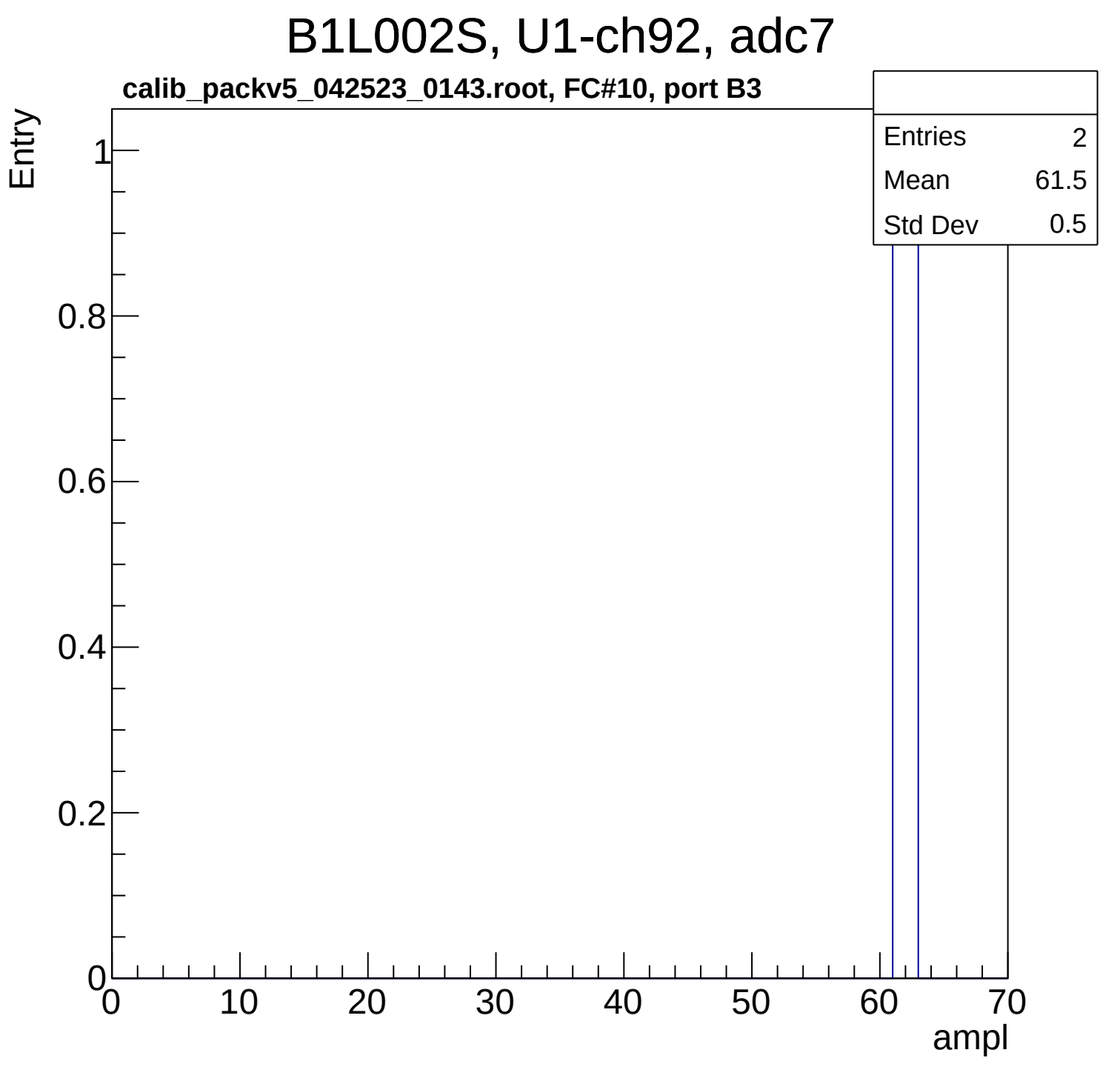
B1L002S, U1-ch92, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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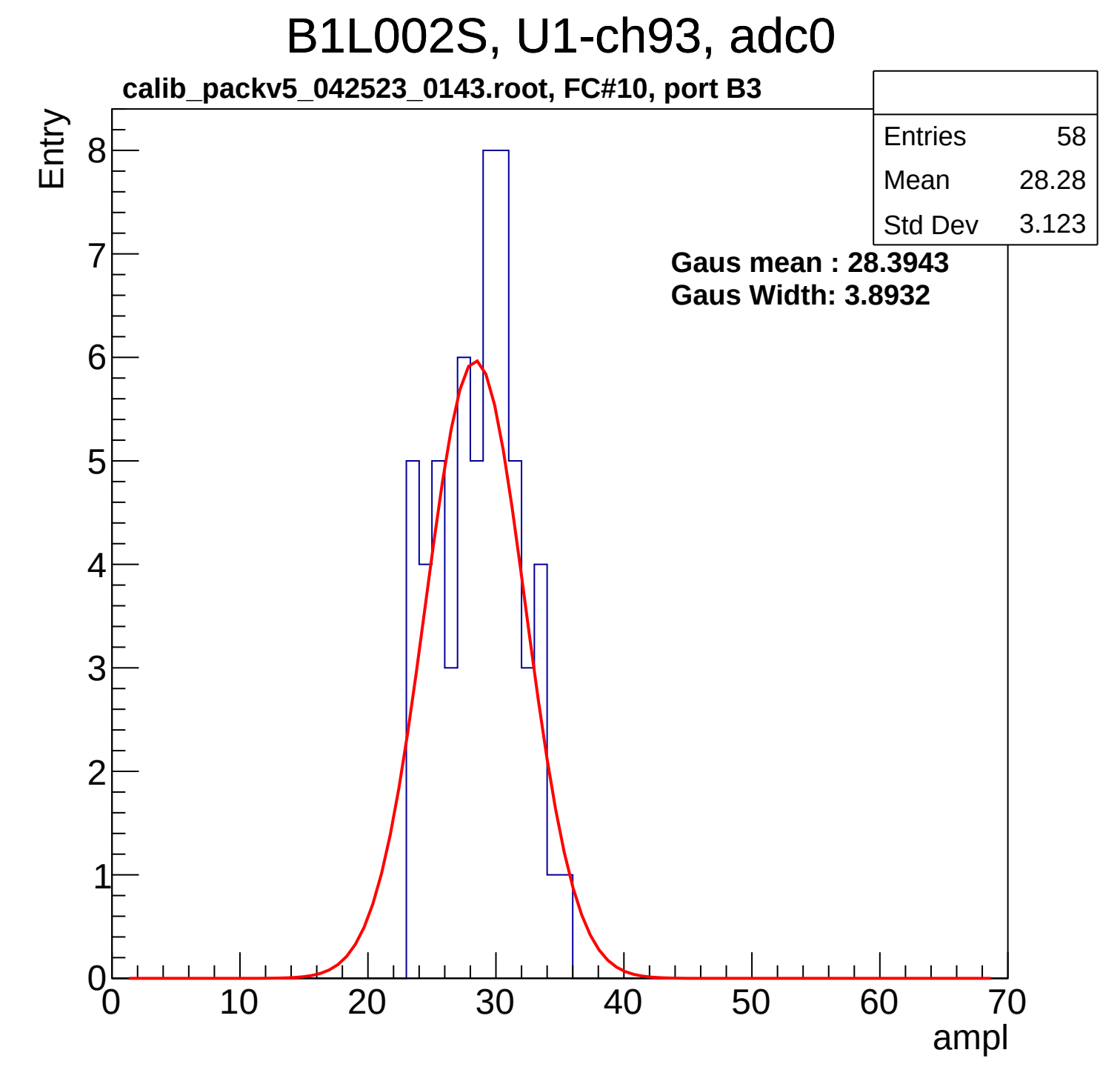
50
60
70
ampl


B1L002S, U1-ch93, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 28.28
Std Dev 3.123
Gaus mean : 28.3943
Gaus Width: 3.8932
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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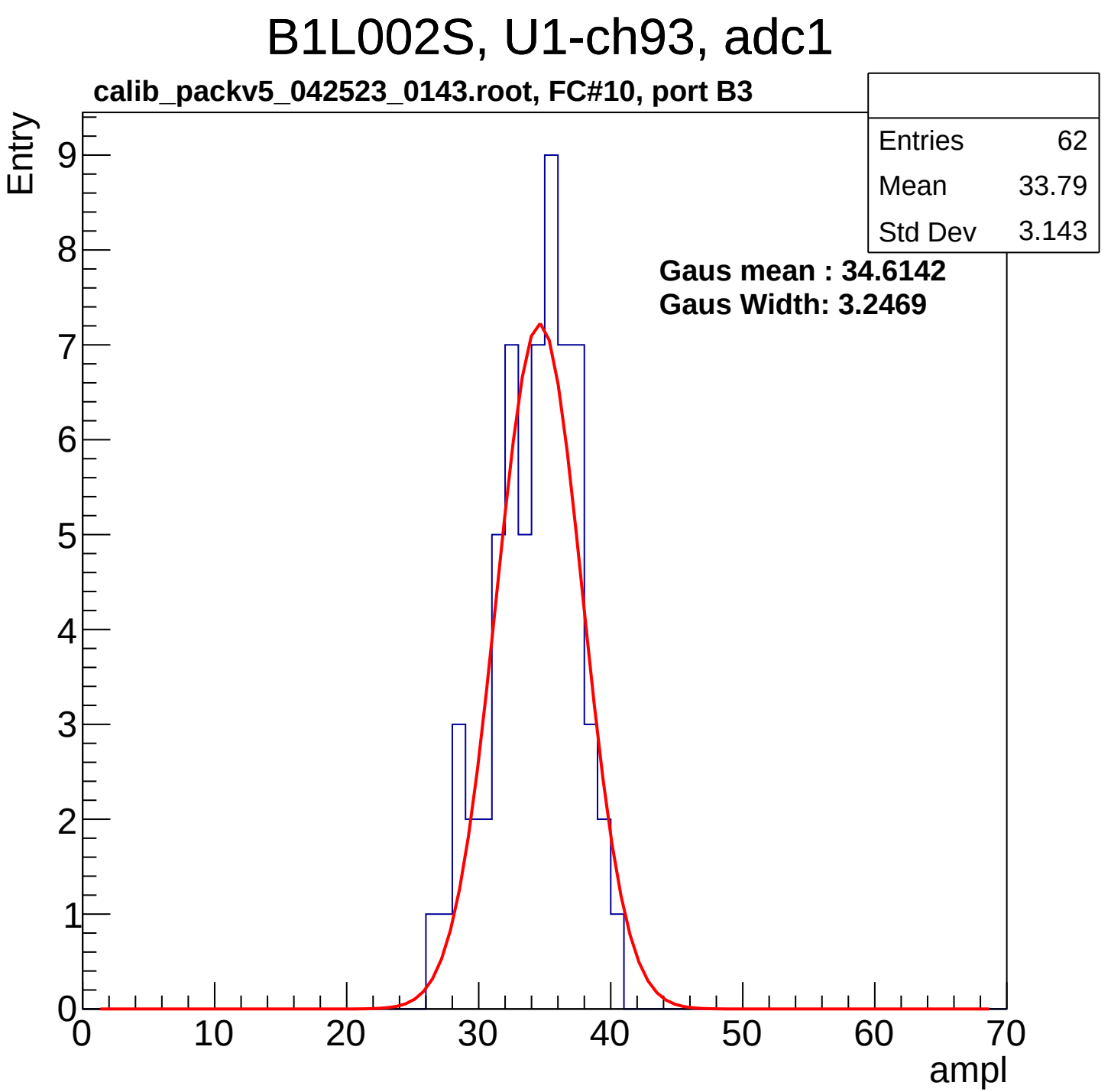
B1L002S, U1-ch93, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.79
Std Dev 3.143
Gaus mean : 34.6142
Gaus Width: 3.2469
Entry
ampl
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2
3
4
5
6
7
8
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0
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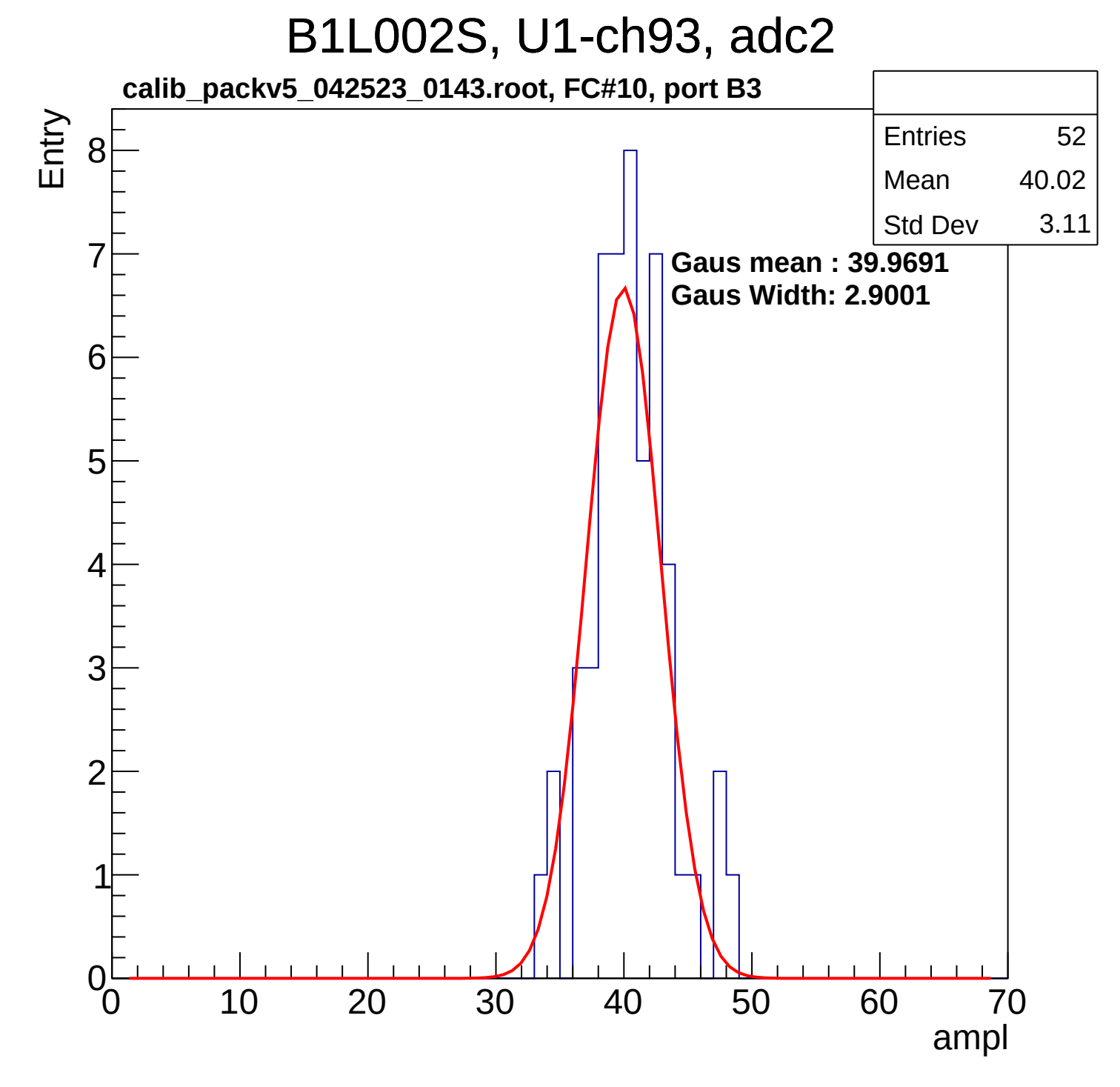
B1L002S, U1-ch93, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.02
Std Dev 3.11
Gaus mean : 39.9691
Gaus Width: 2.9001
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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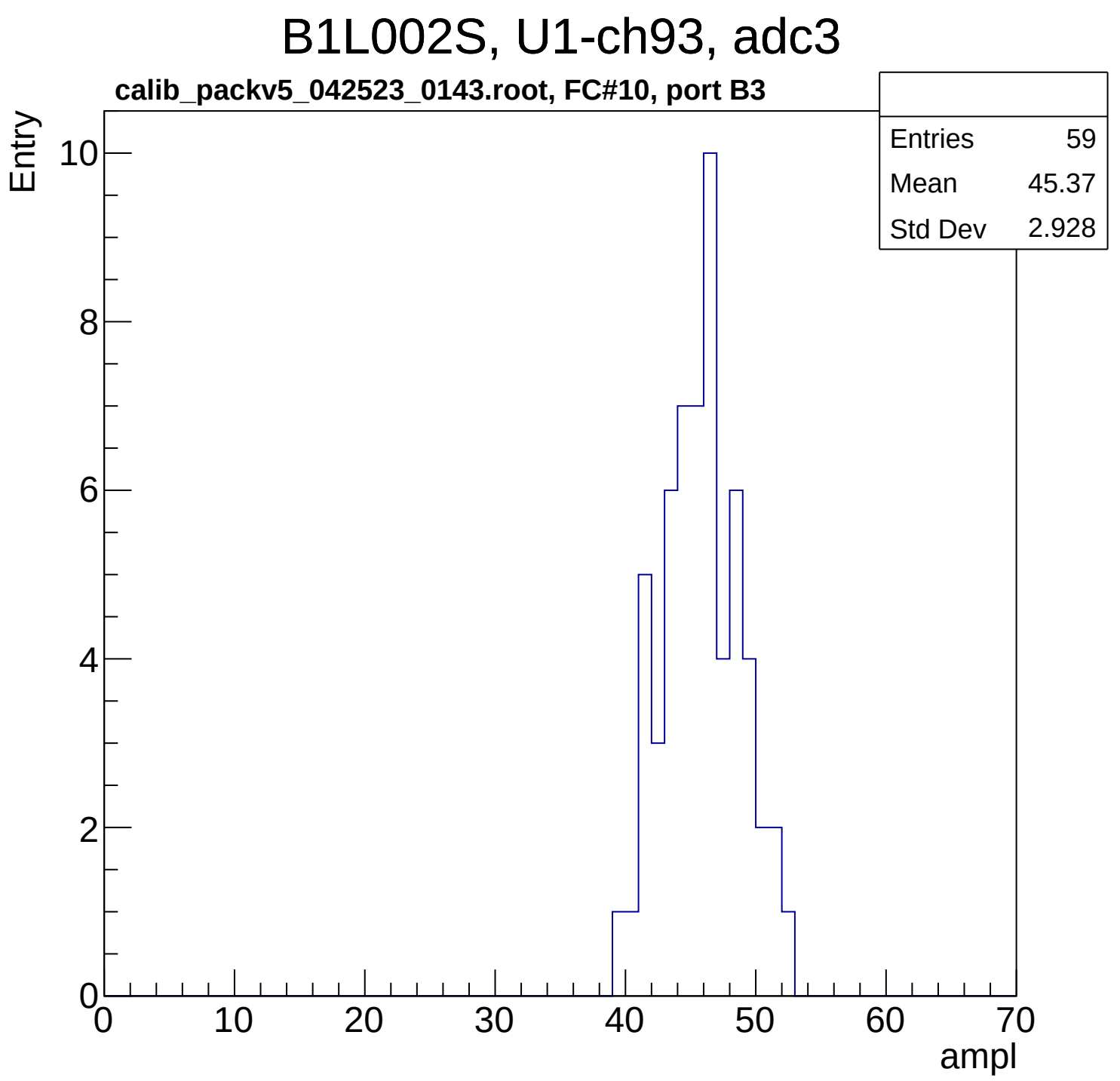
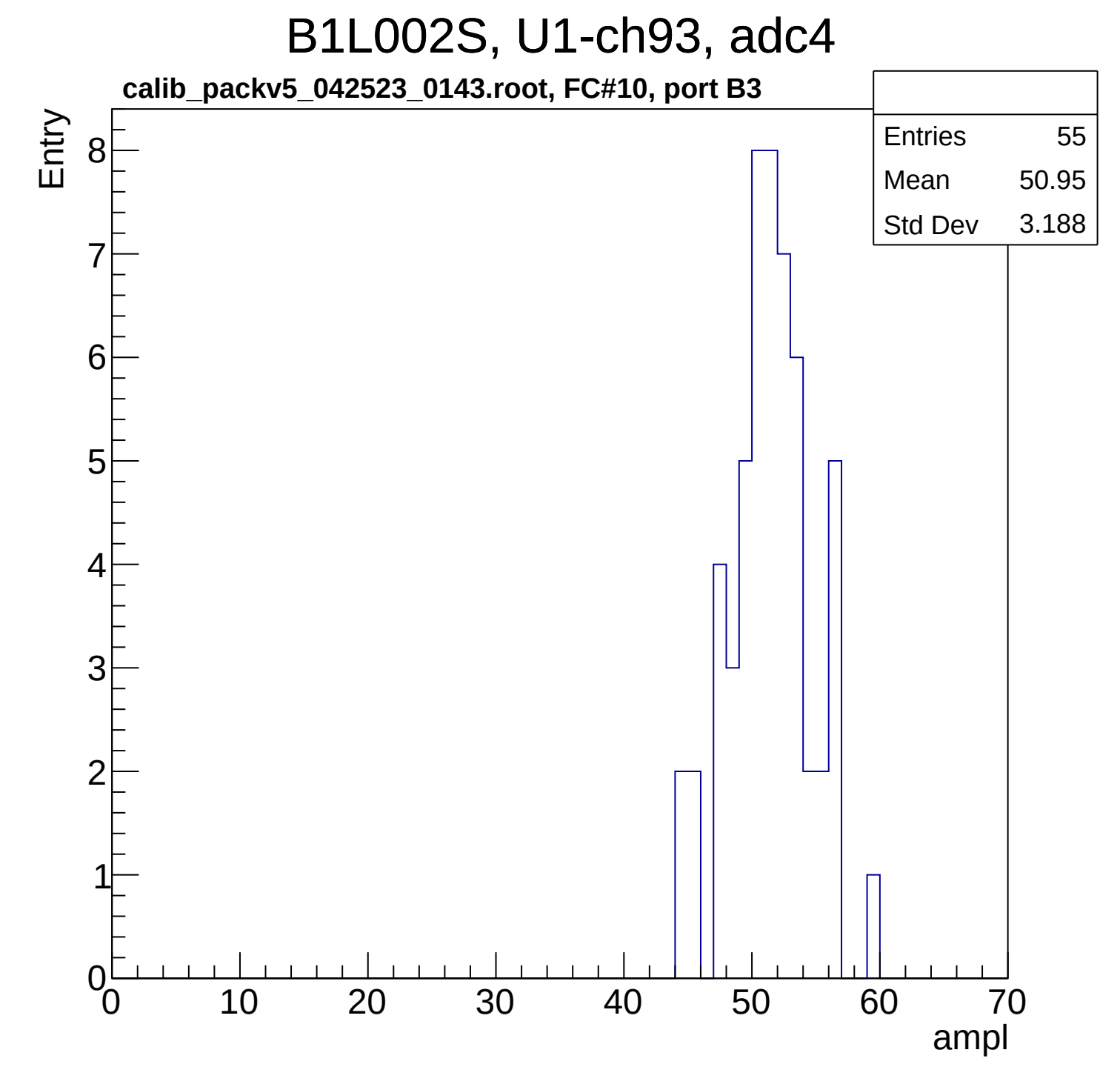

B1L002S, U1-ch93, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 45.37
Std Dev 2.928
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch93, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 50.95
Std Dev 3.188
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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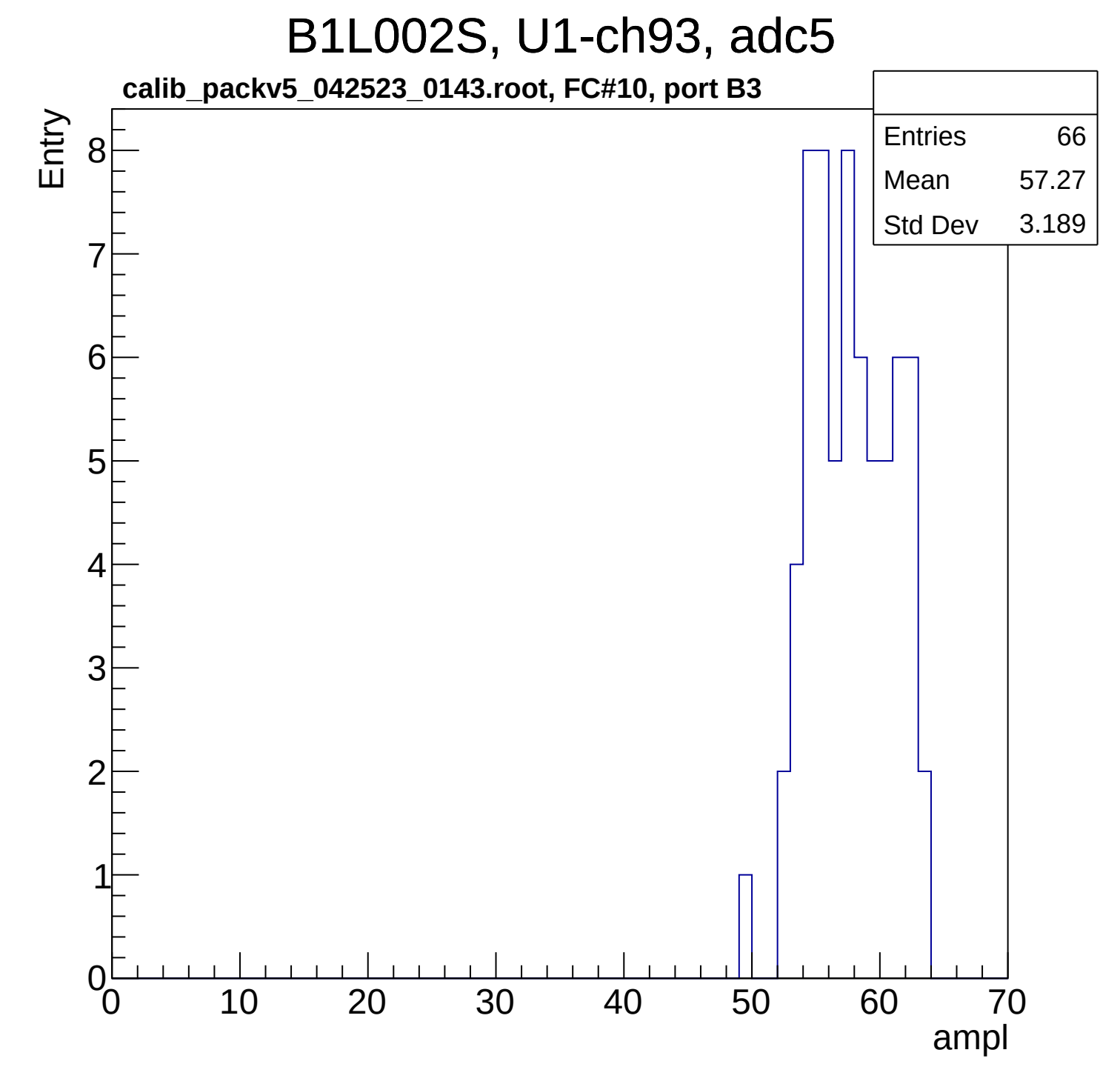
B1L002S, U1-ch93, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 57.27
Std Dev 3.189
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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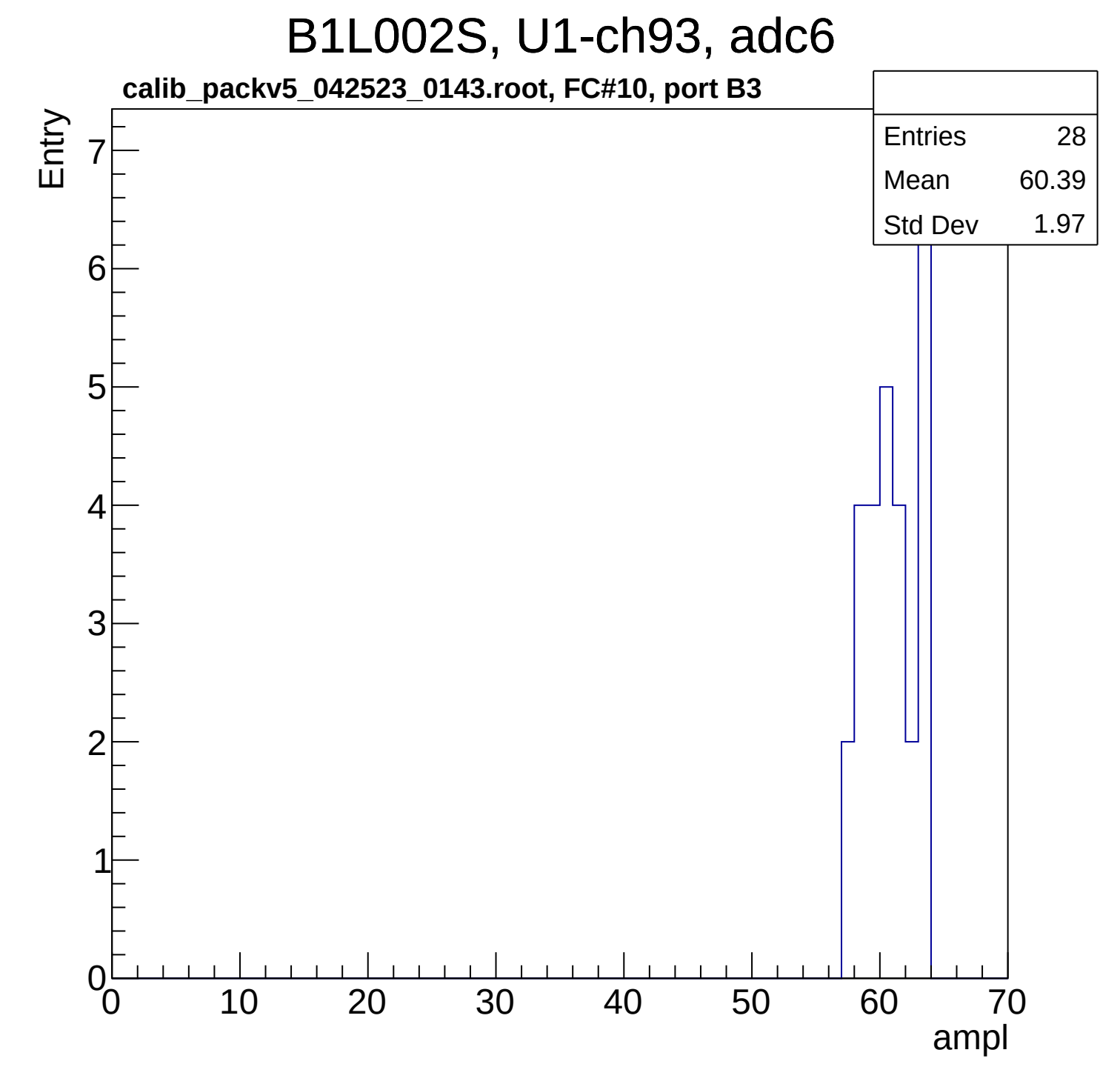
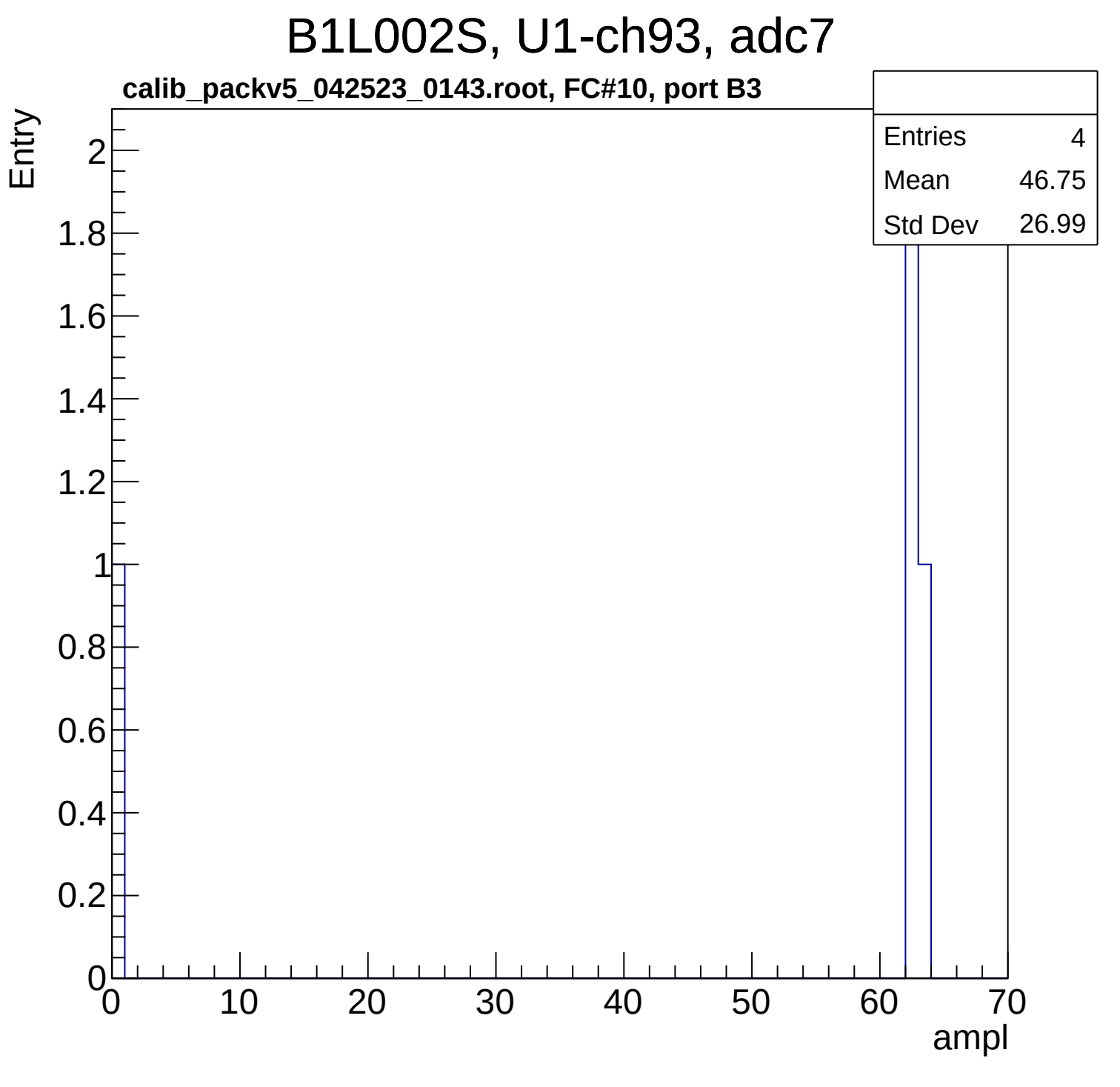

B1L002S, U1-ch93, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 60.39
Std Dev 1.97
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch93, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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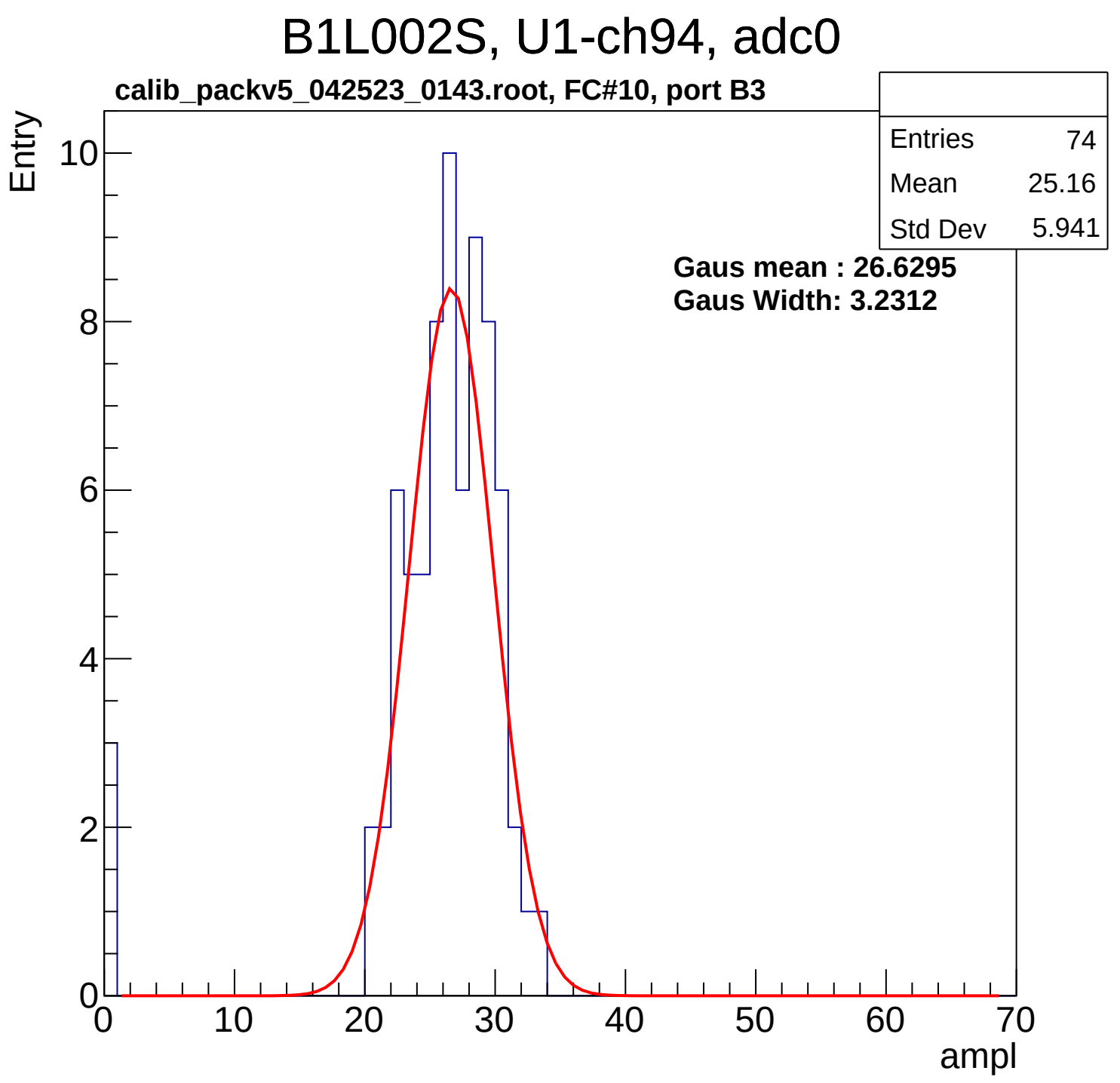
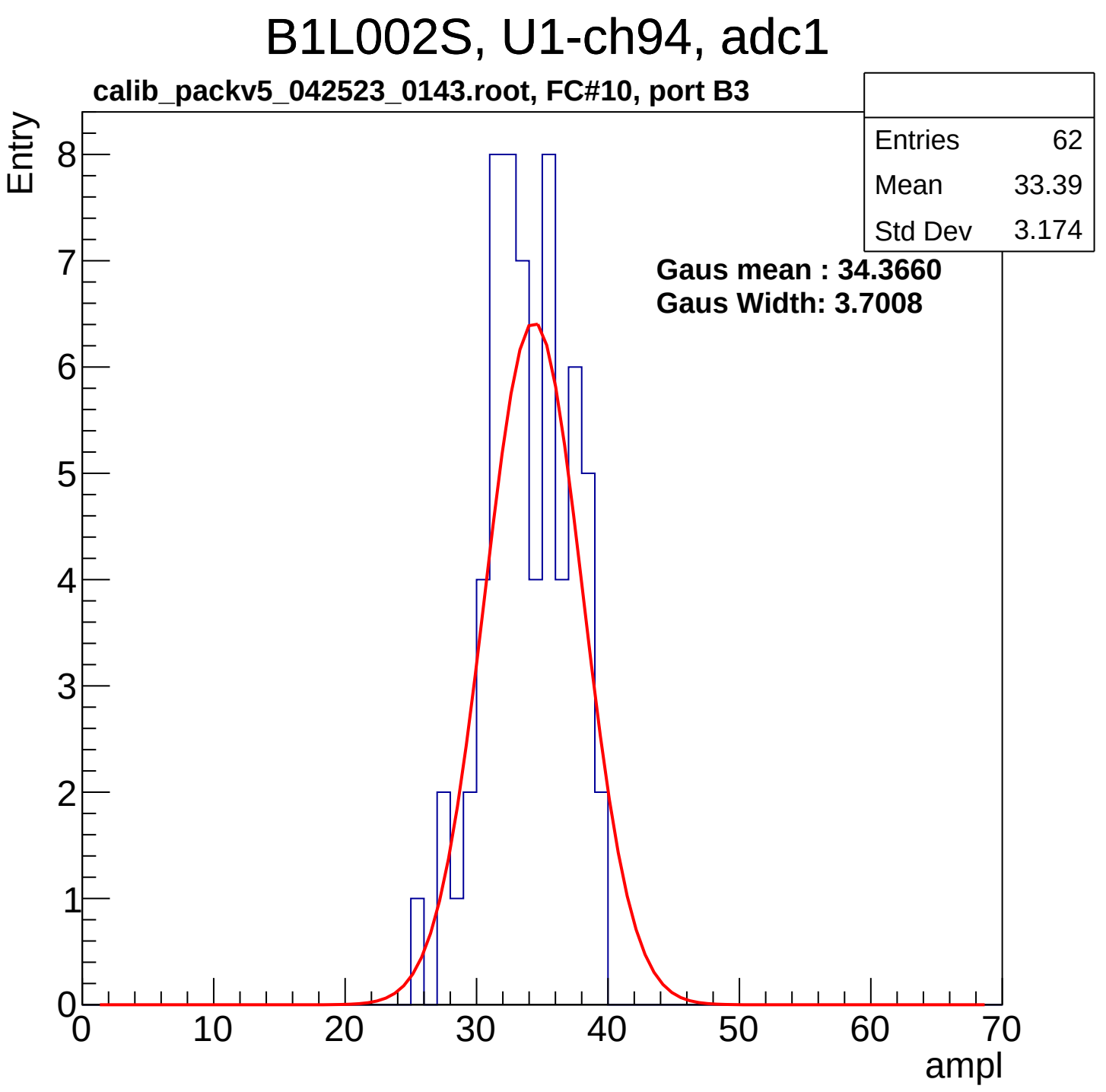

B1L002S, U1-ch94, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 25.16
Std Dev 5.941
Gaus mean : 26.6295
Gaus Width: 3.2312
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch94, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.39
Std Dev 3.174
Gaus mean : 34.3660
Gaus Width: 3.7008
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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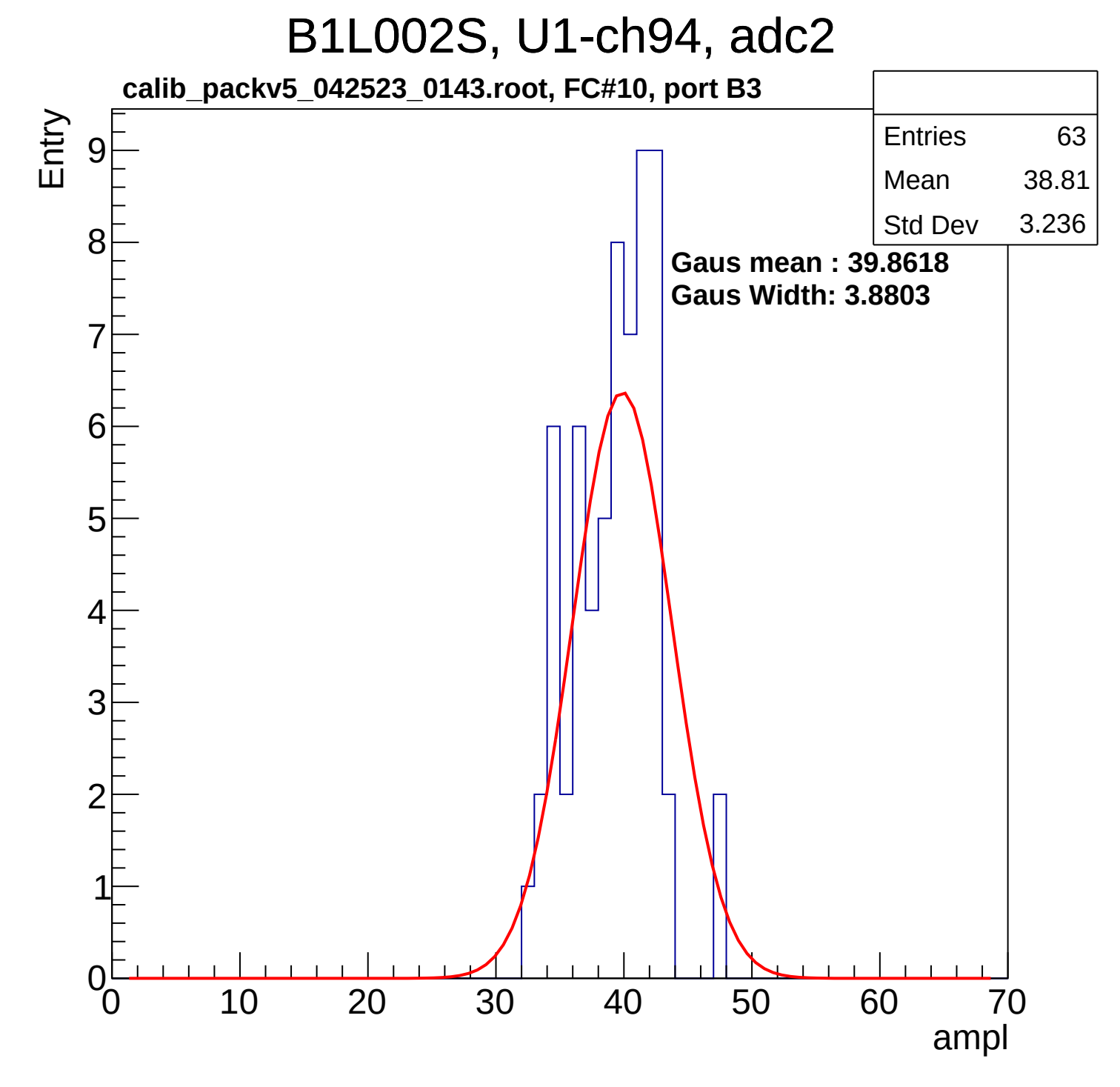
B1L002S, U1-ch94, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 38.81
Std Dev 3.236
Gaus mean : 39.8618
Gaus Width: 3.8803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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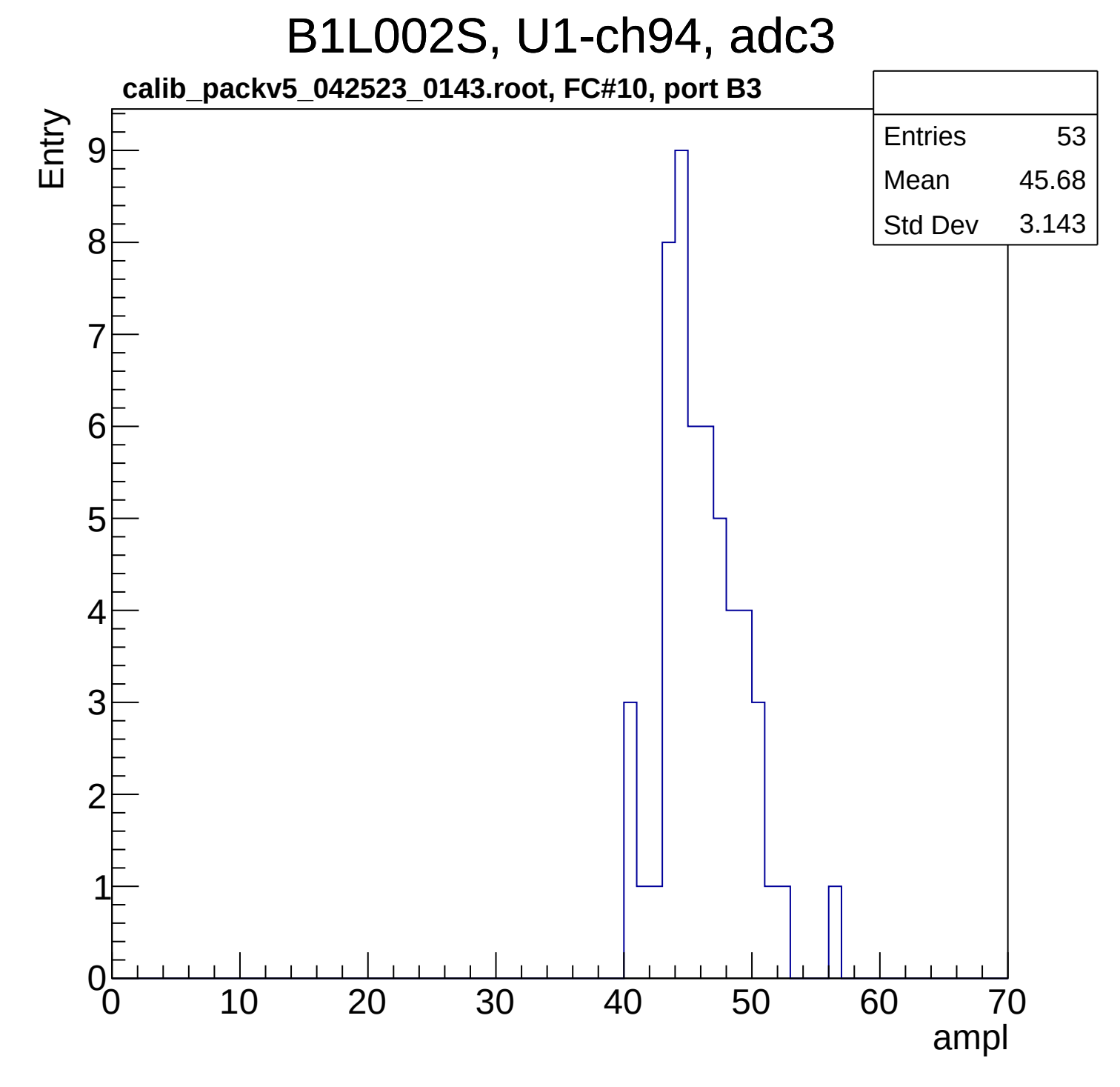
B1L002S, U1-ch94, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 45.68
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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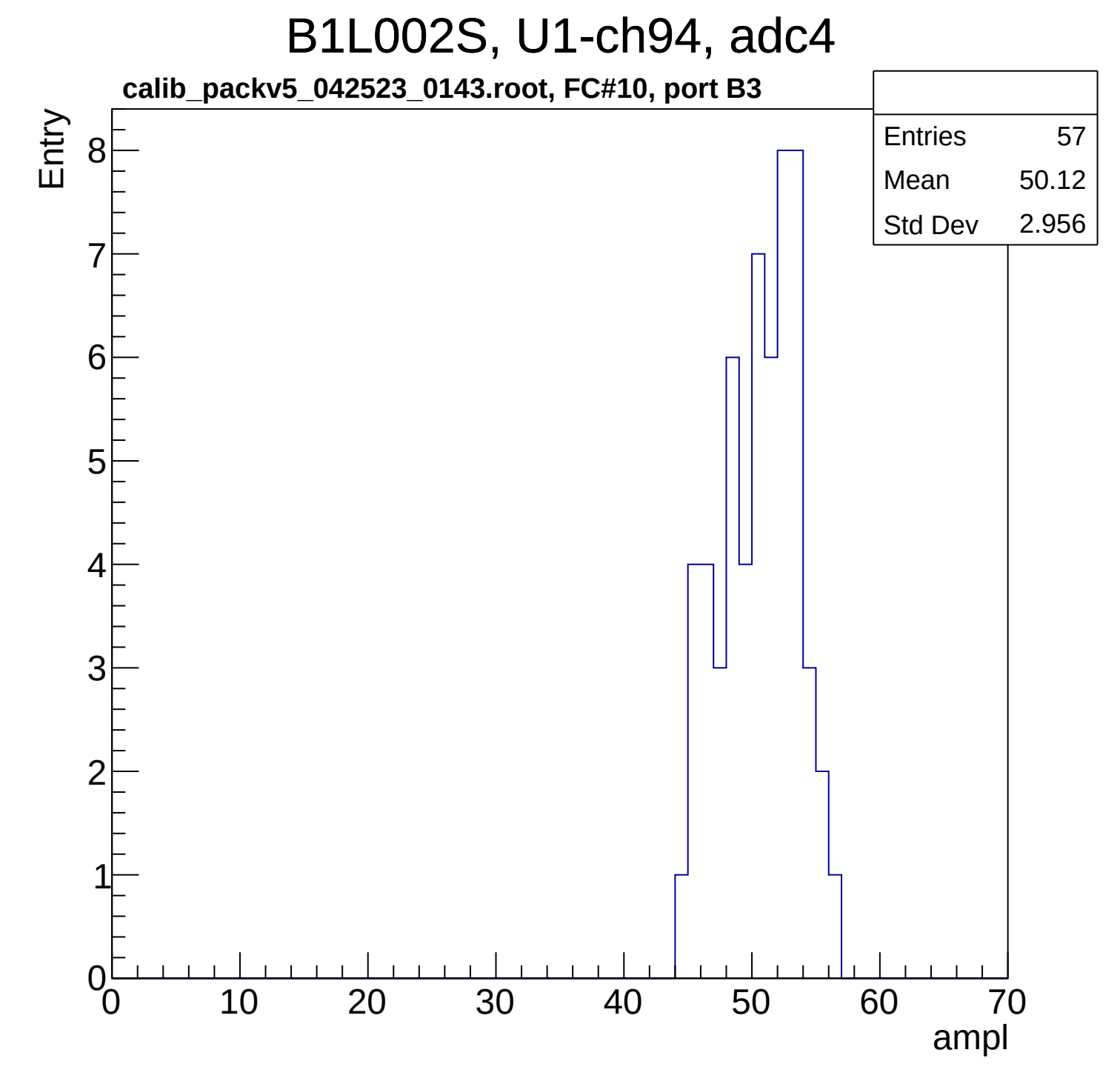
B1L002S, U1-ch94, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 50.12
Std Dev 2.956
Entry
ampl
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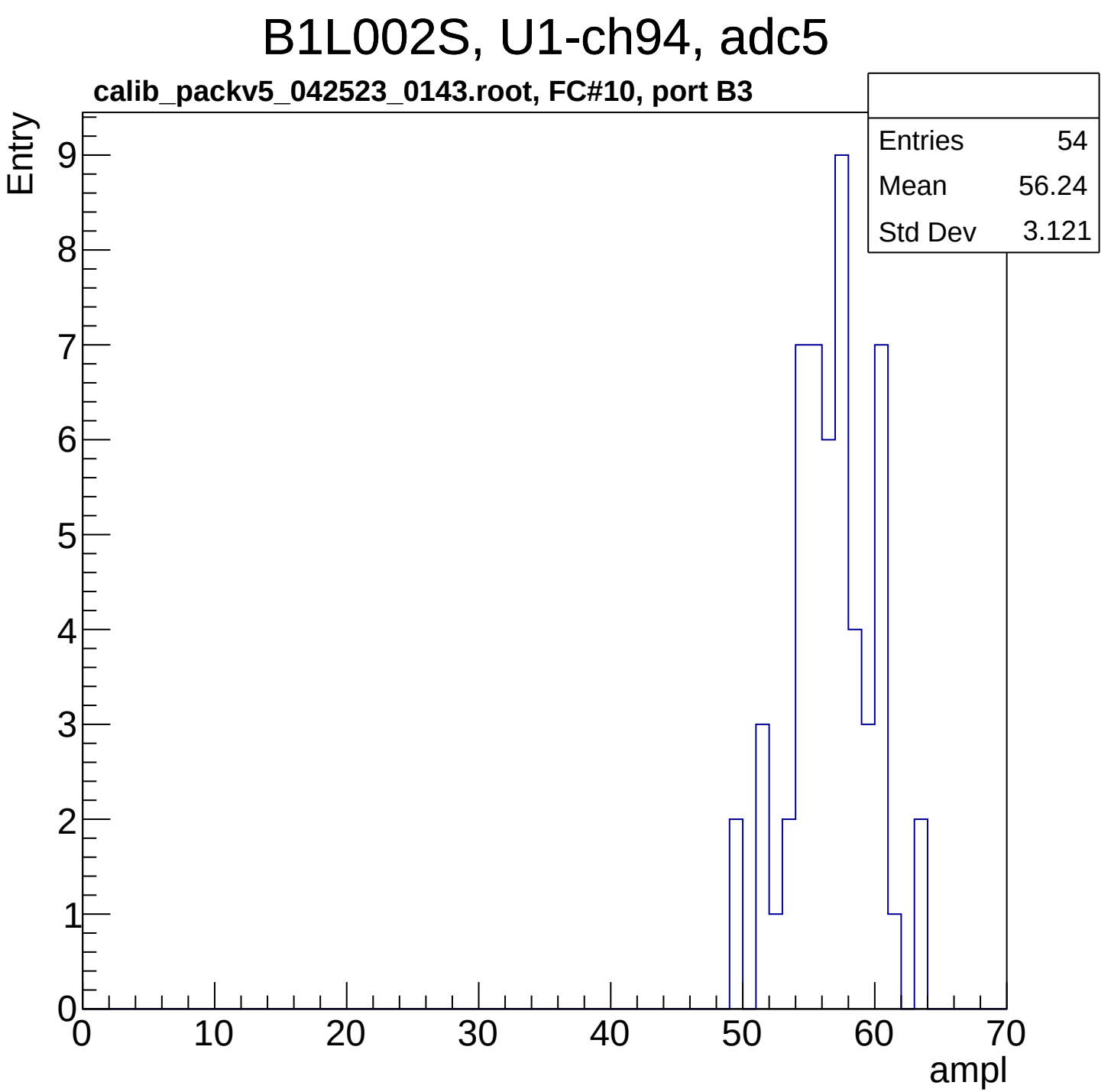
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch94, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 56.24
Std Dev 3.121
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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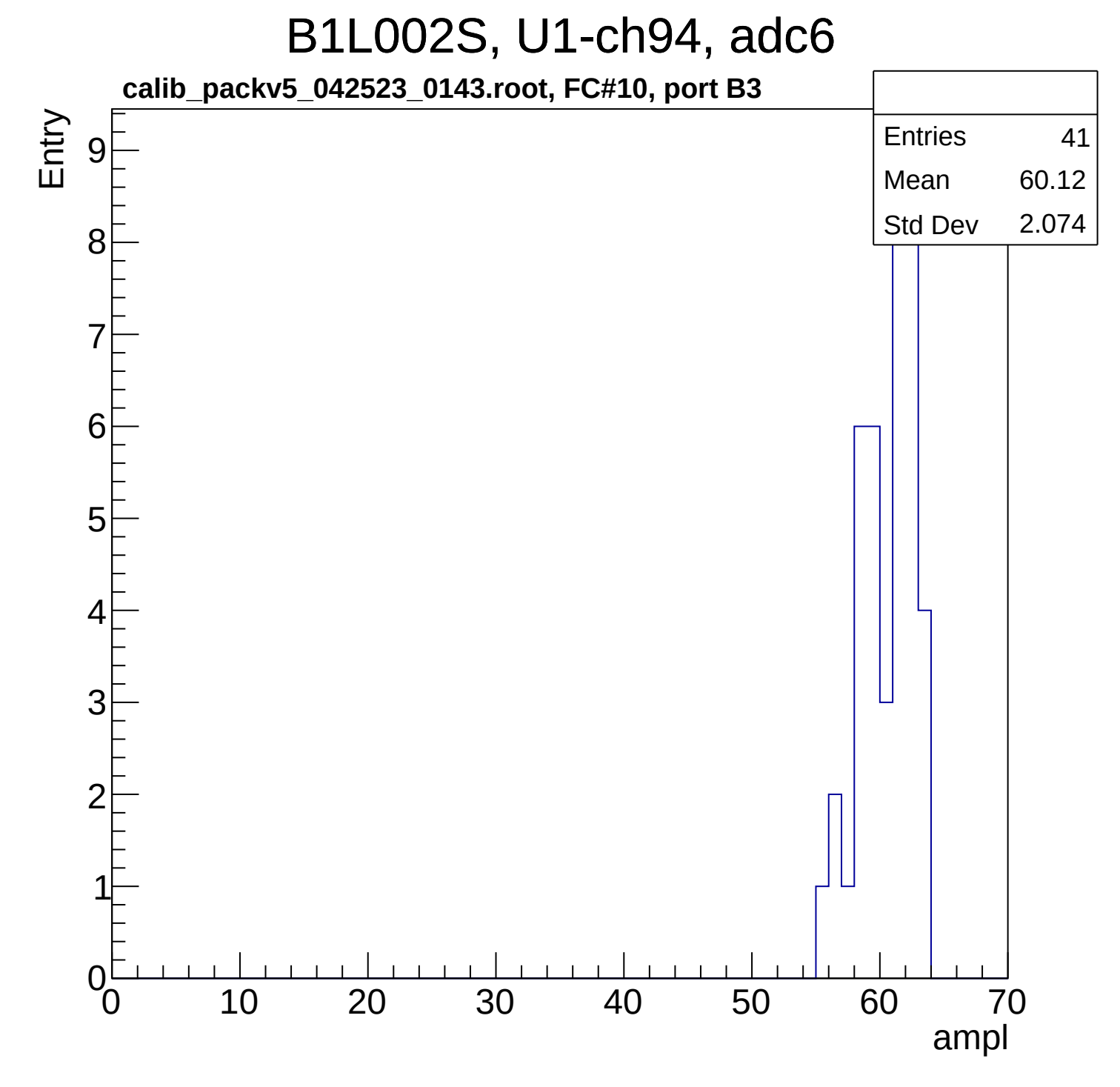
B1L002S, U1-ch94, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 60.12
Std Dev 2.074
Entry
ampl
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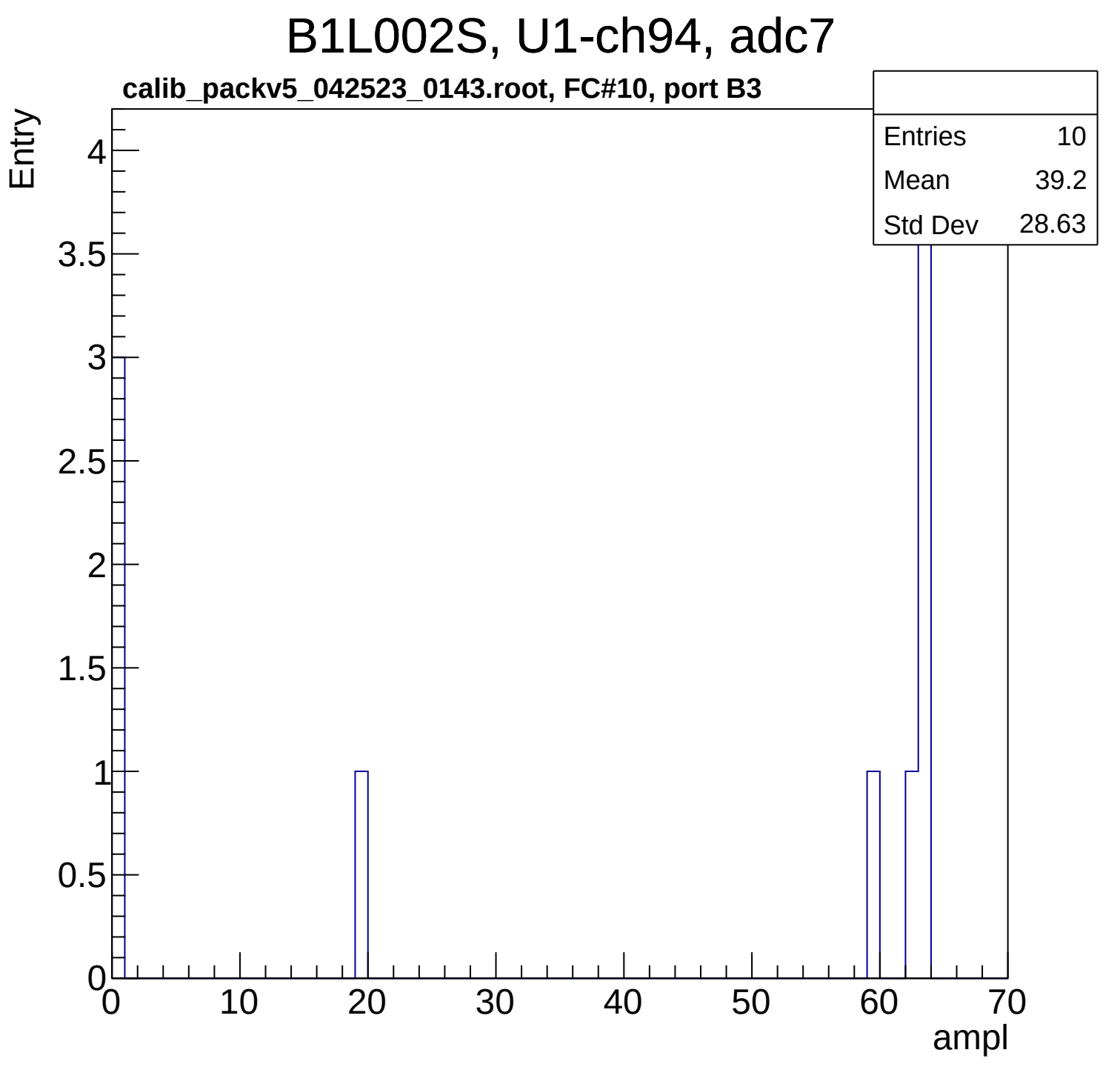
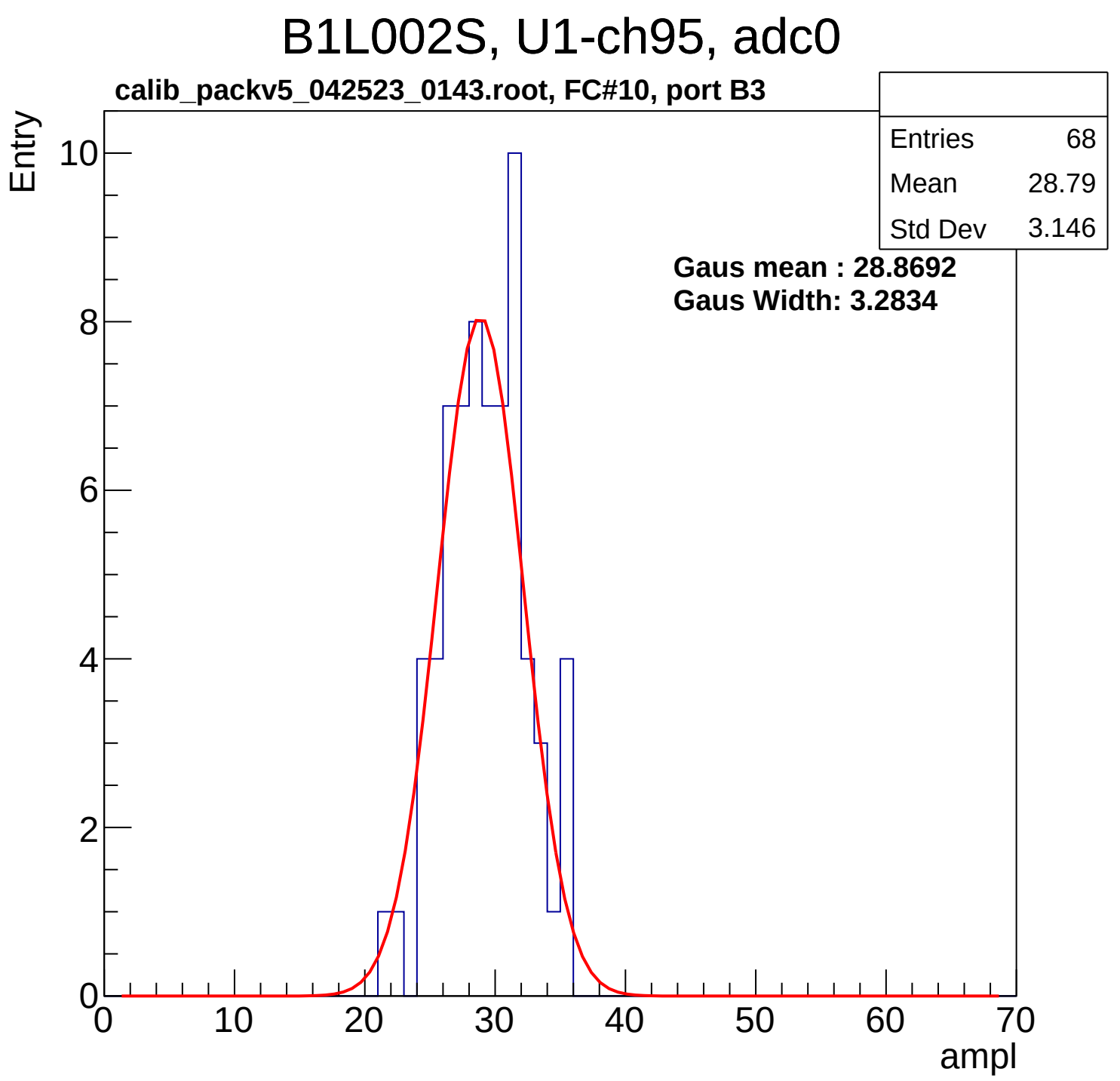
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch94, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 39.2
Std Dev 28.63
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U1-ch95, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 28.79
Std Dev 3.146
Gaus mean : 28.8692
Gaus Width: 3.2834
Entry
ampl
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2
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6
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10
0
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60
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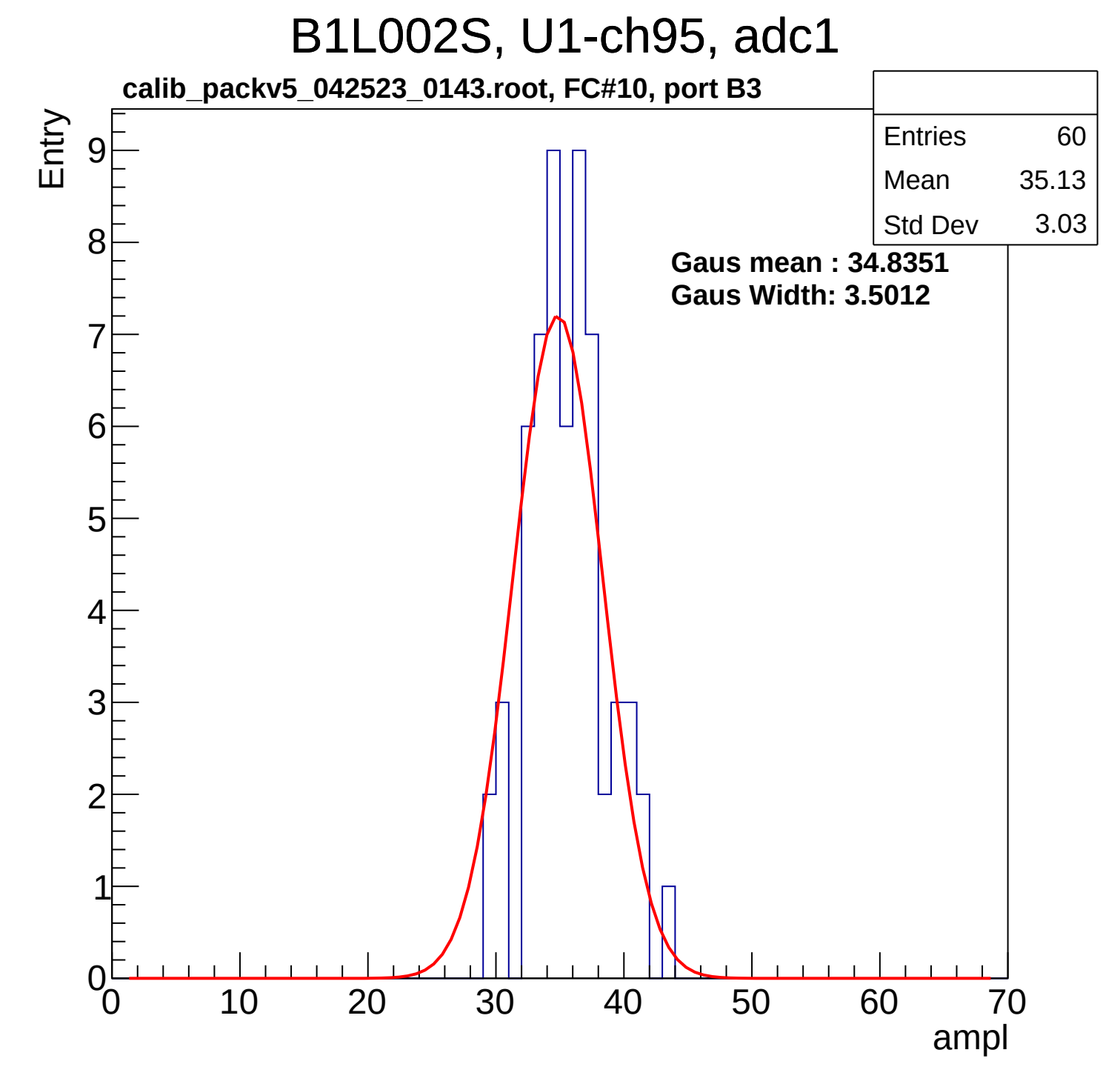
B1L002S, U1-ch95, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.13
Std Dev 3.03
Gaus mean : 34.8351
Gaus Width: 3.5012
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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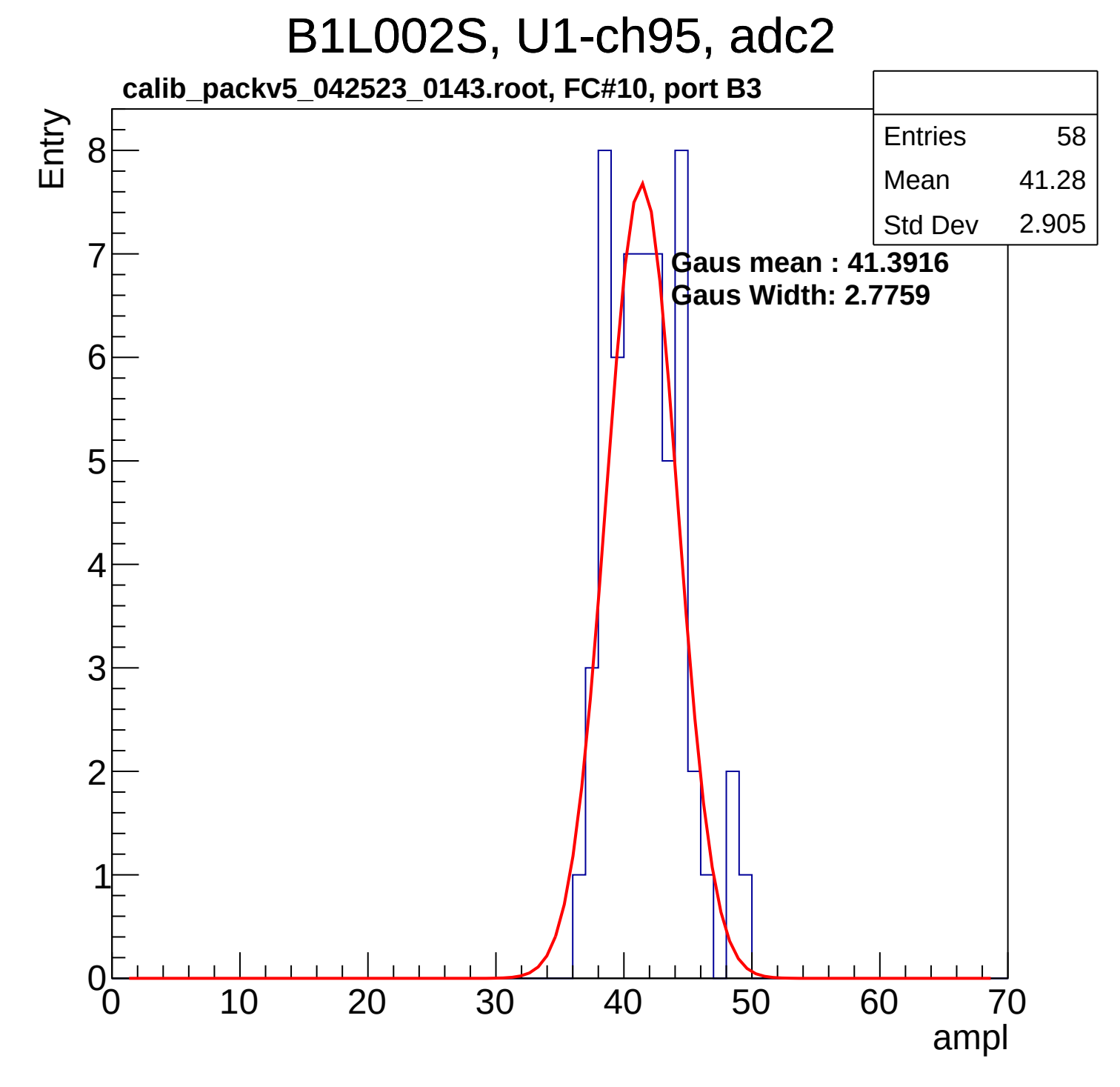
B1L002S, U1-ch95, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.28
Std Dev 2.905
Gaus mean : 41.3916
Gaus Width: 2.7759
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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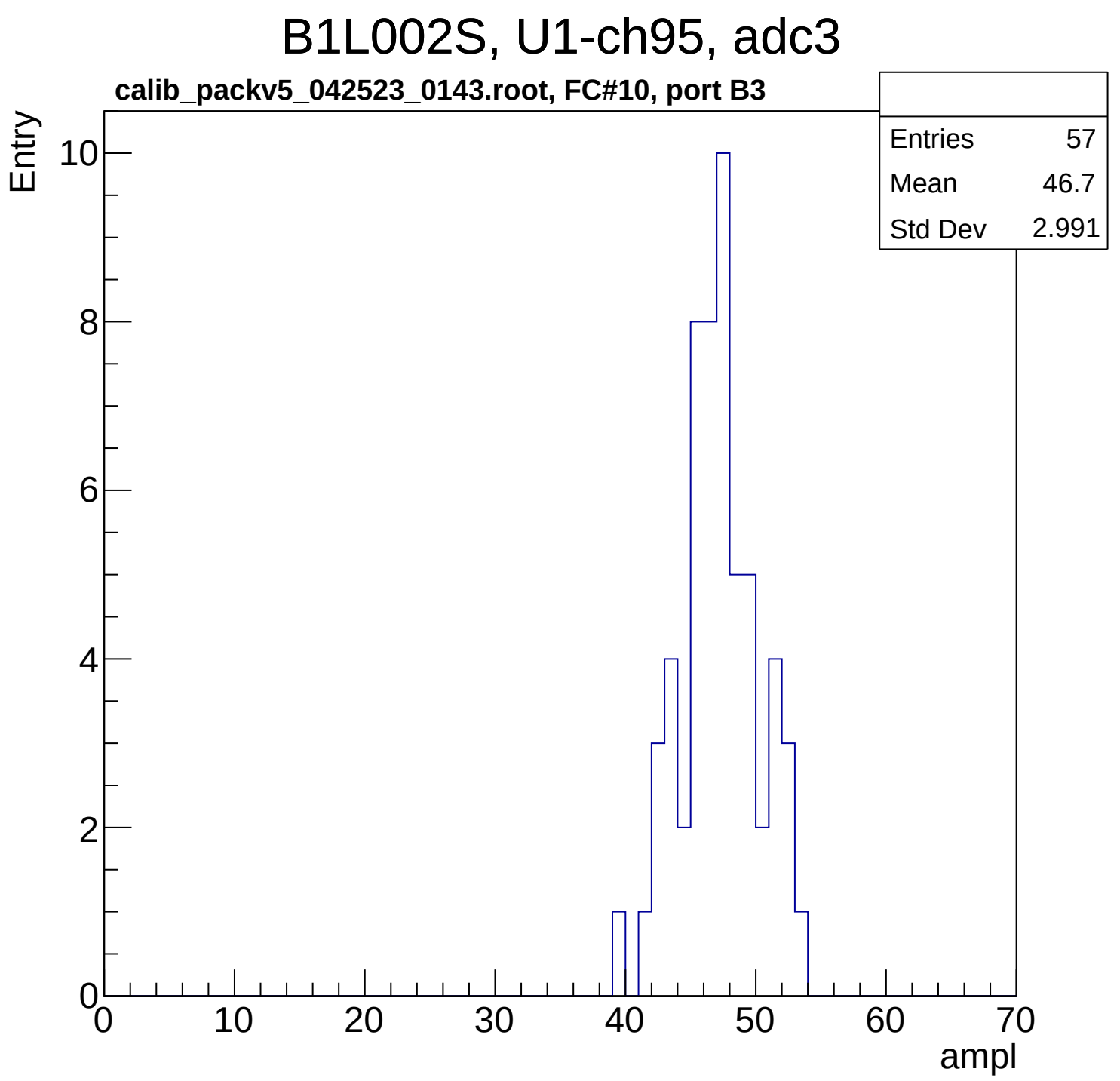
B1L002S, U1-ch95, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 46.7
Std Dev 2.991
Entry
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2
4
6
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10
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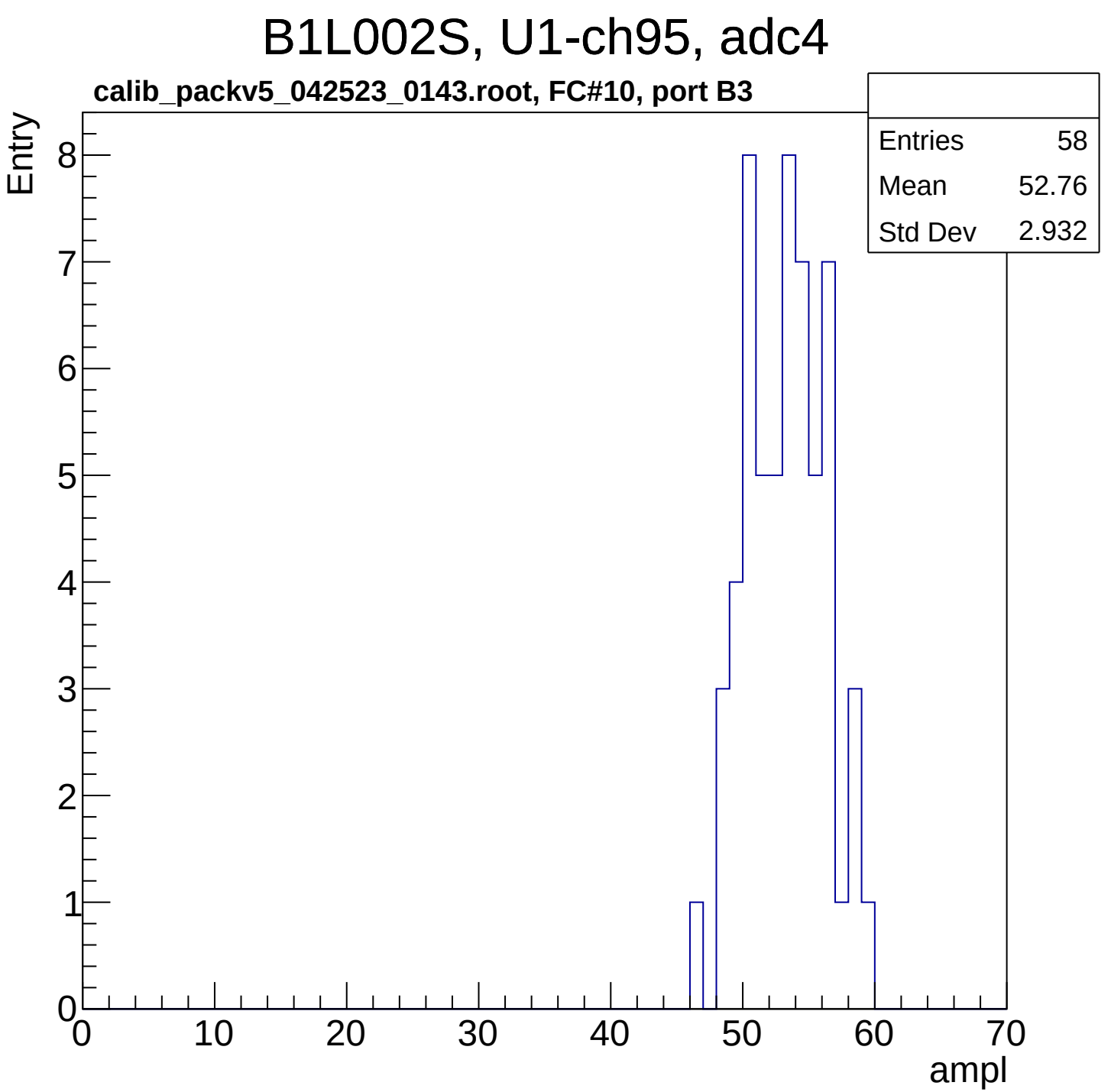
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch95, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 52.76
Std Dev 2.932
Entry
ampl
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2
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4
5
6
7
8
0
10
20
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40
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60
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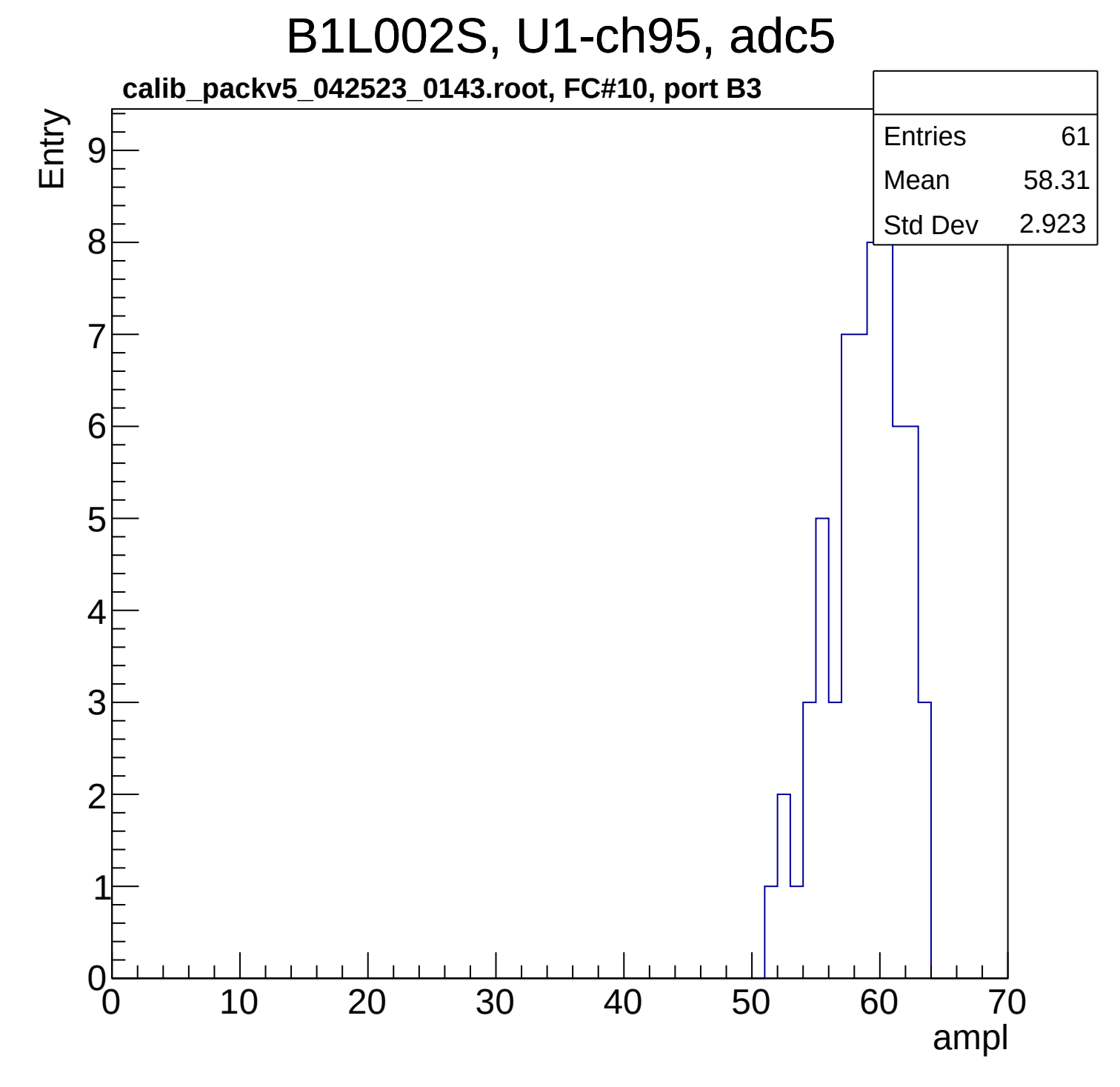
B1L002S, U1-ch95, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.31
Std Dev 2.923
Entry
ampl
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4
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6
7
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60
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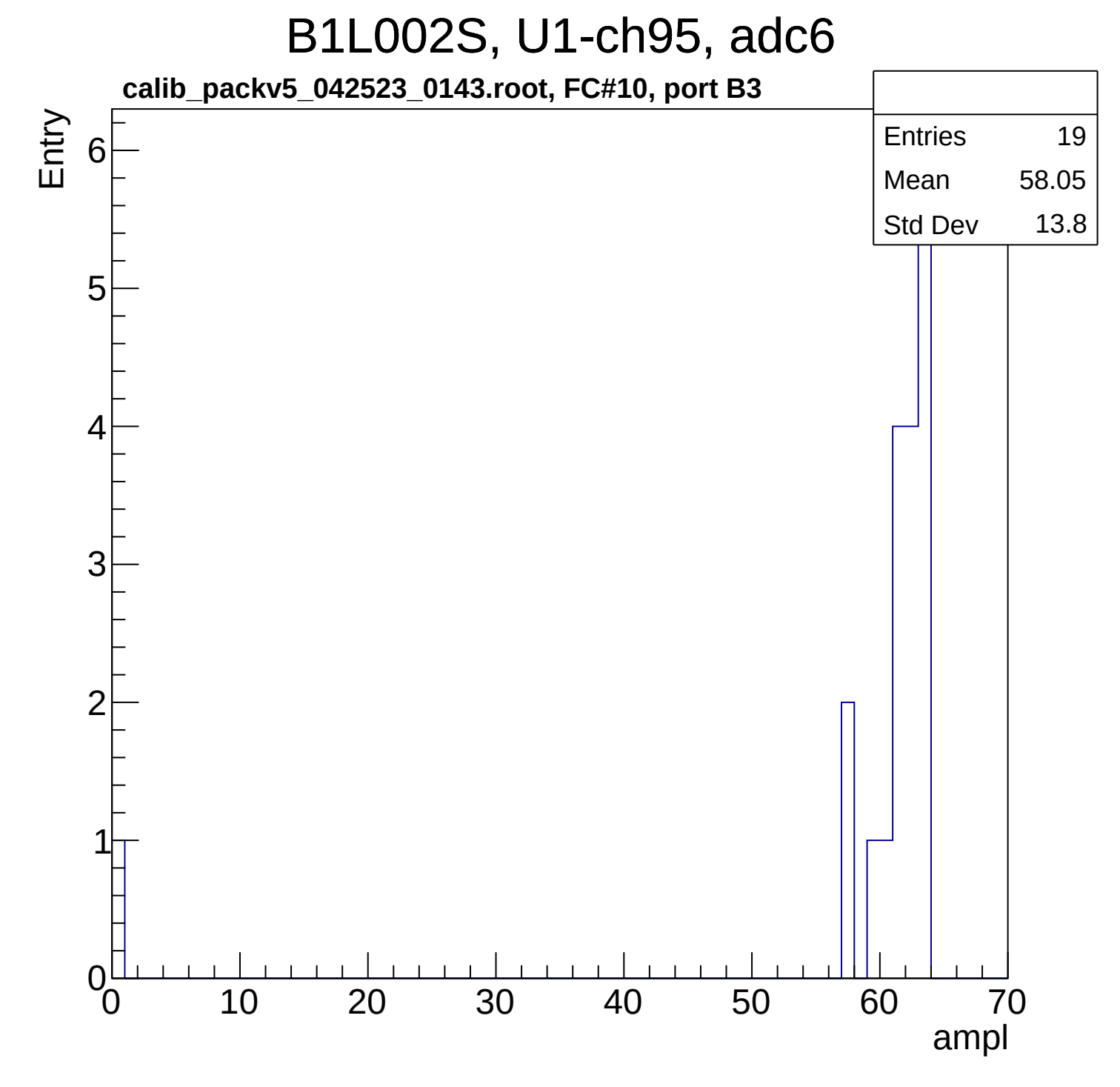
B1L002S, U1-ch95, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.05
Std Dev 13.8
Entry
0
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2
3
4
5
6
0
10
20
30
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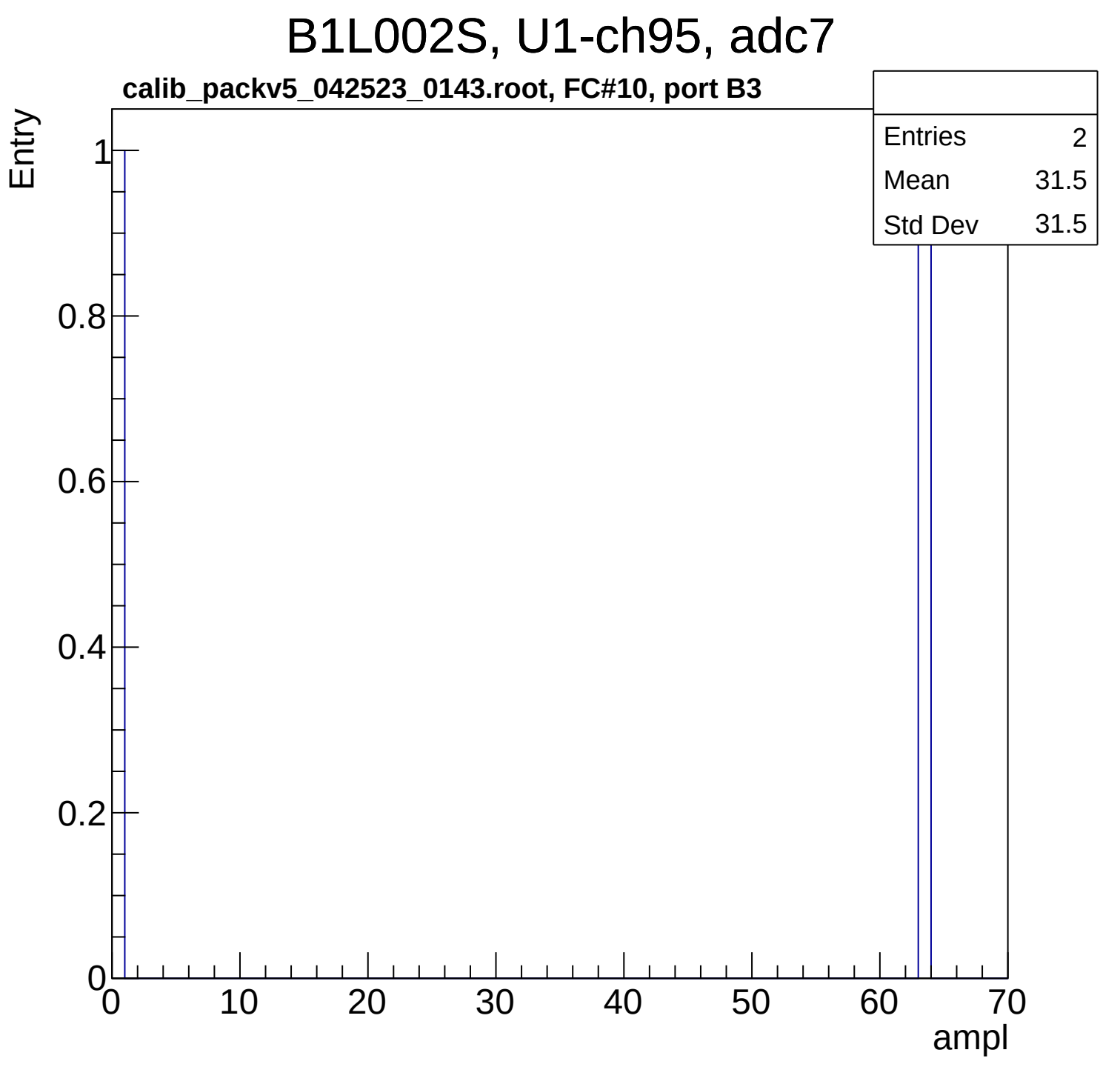
50
60
70
ampl


B1L002S, U1-ch95, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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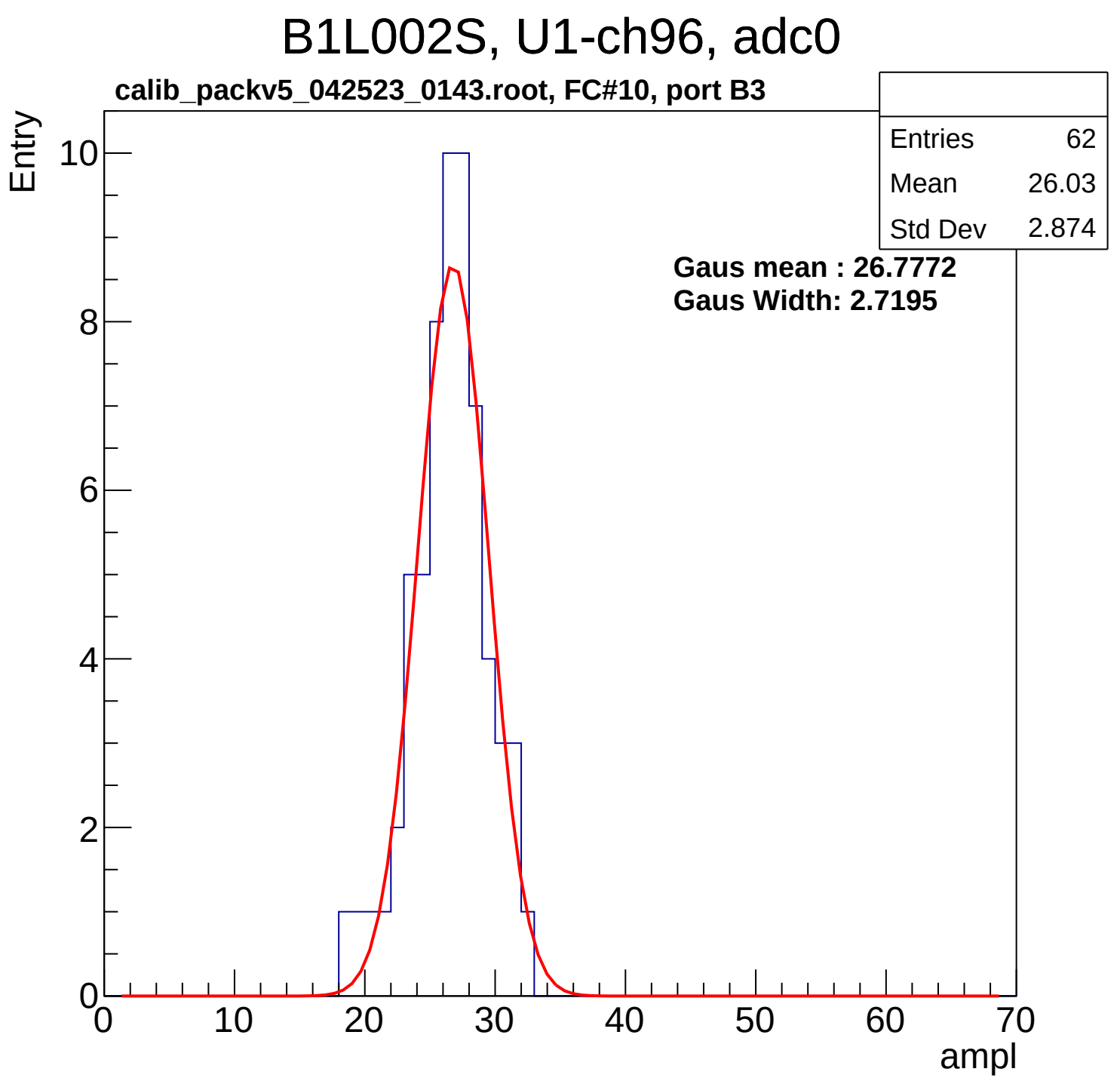
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch96, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.03
Std Dev 2.874
Gaus mean : 26.7772
Gaus Width: 2.7195
Entry
ampl
0
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4
6
8
10
0
10
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40
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60
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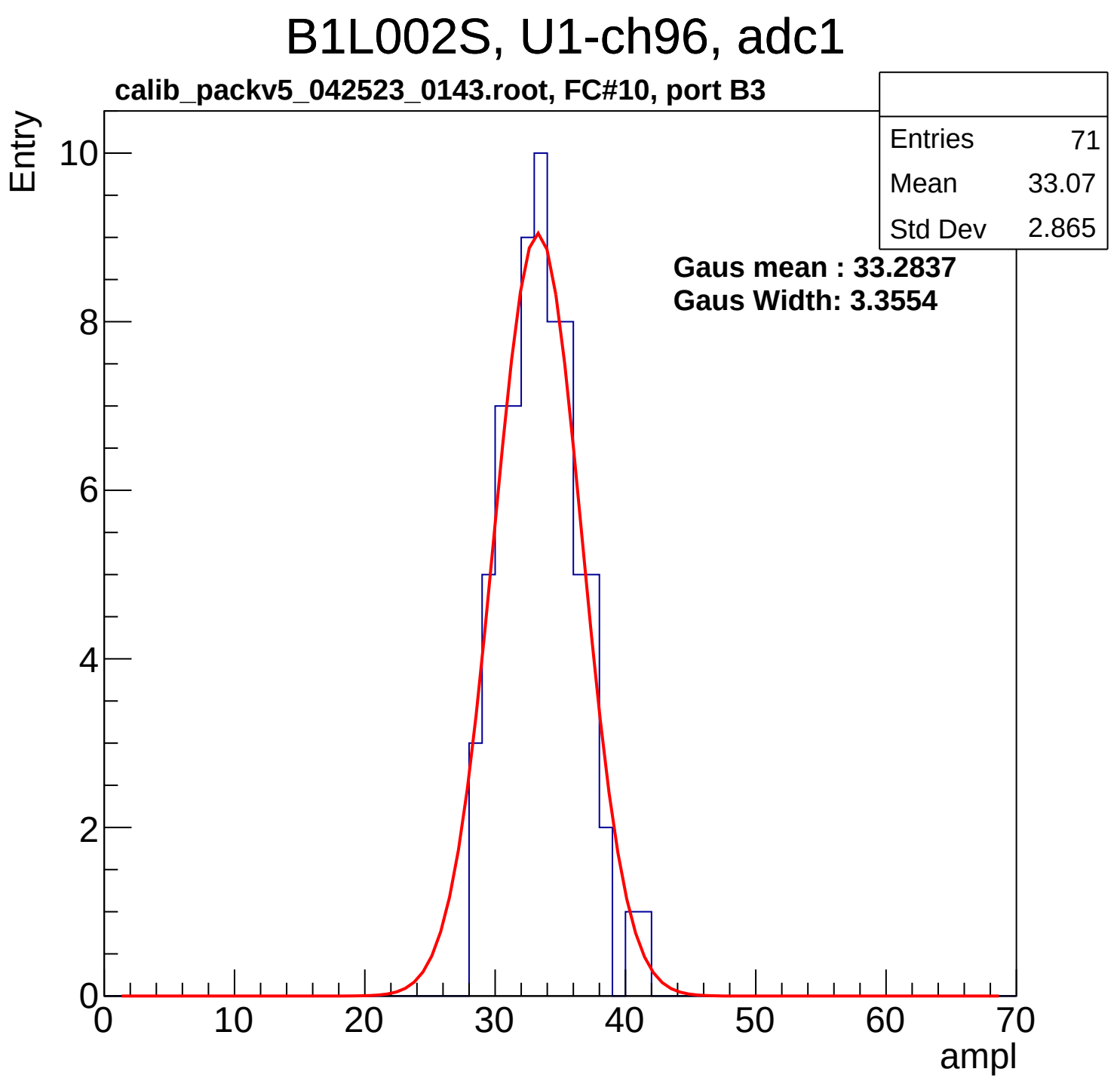
B1L002S, U1-ch96, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 33.07
Std Dev 2.865
Gaus mean : 33.2837
Gaus Width: 3.3554
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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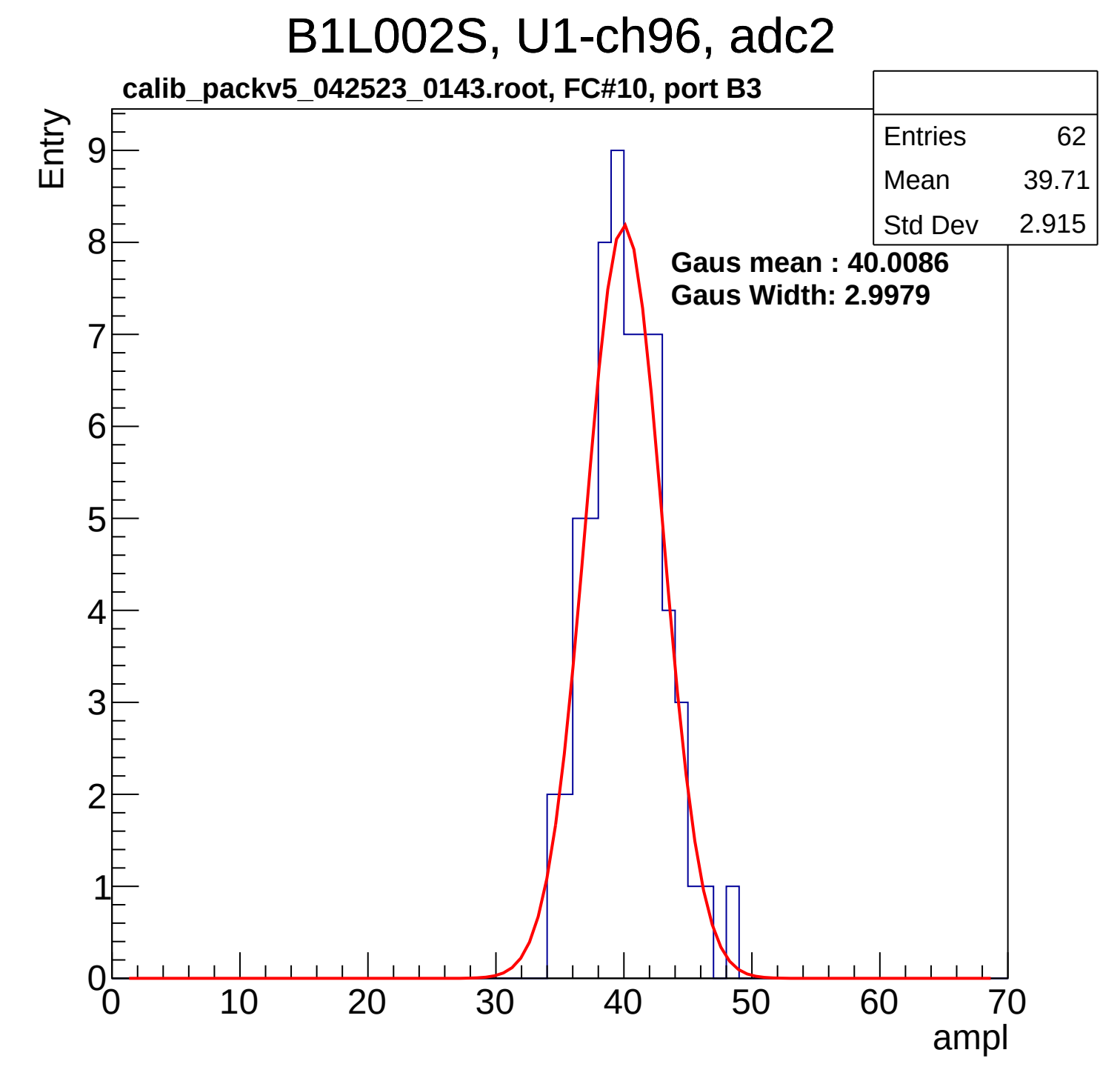
B1L002S, U1-ch96, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 39.71
Std Dev 2.915
Gaus mean : 40.0086
Gaus Width: 2.9979
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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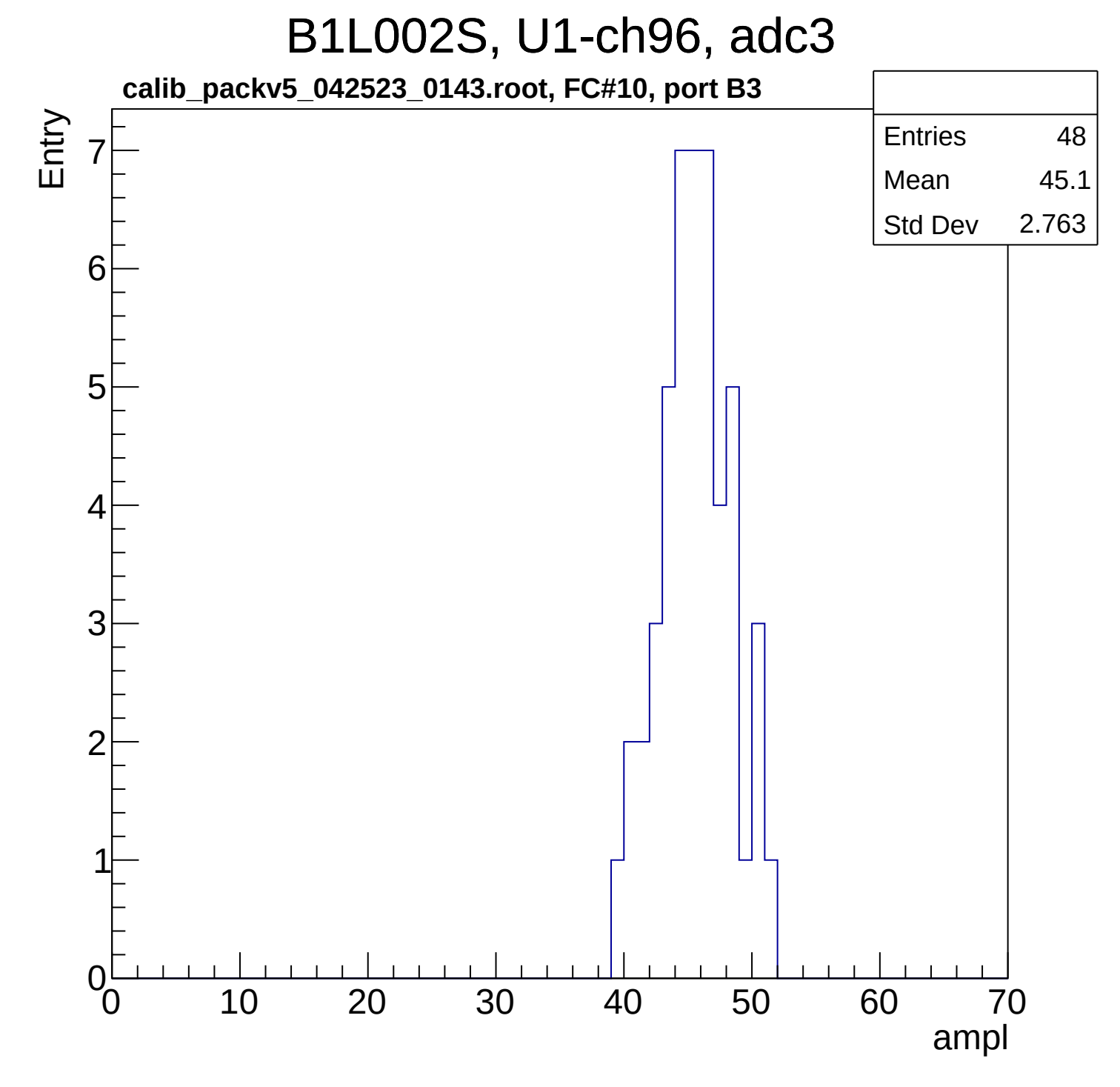
B1L002S, U1-ch96, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 45.1
Std Dev 2.763
Entry
ampl
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3
4
5
6
7
0
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60
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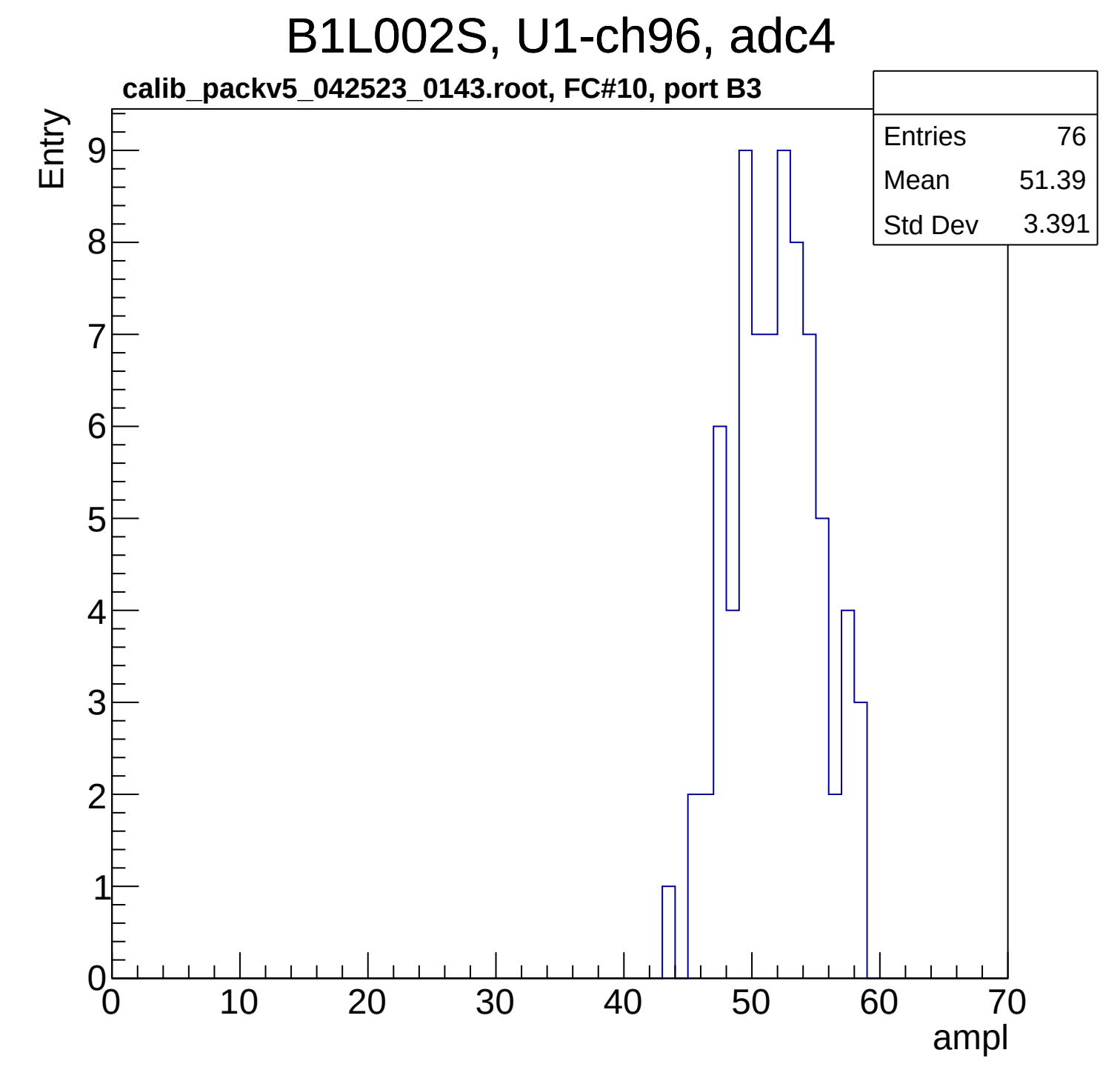
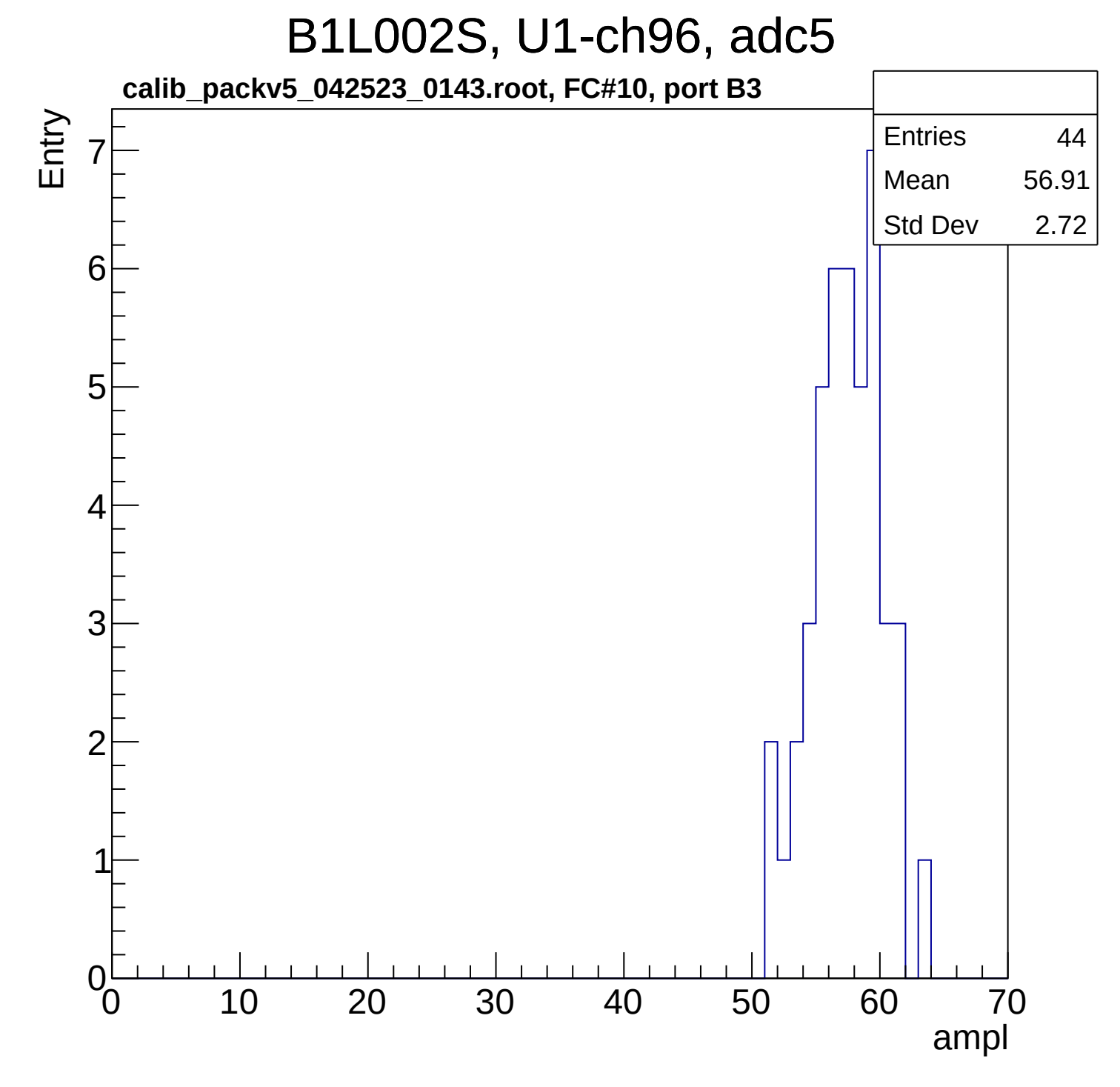

B1L002S, U1-ch96, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 51.39
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch96, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 56.91
Std Dev 2.72
Entry
ampl
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2
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4
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60
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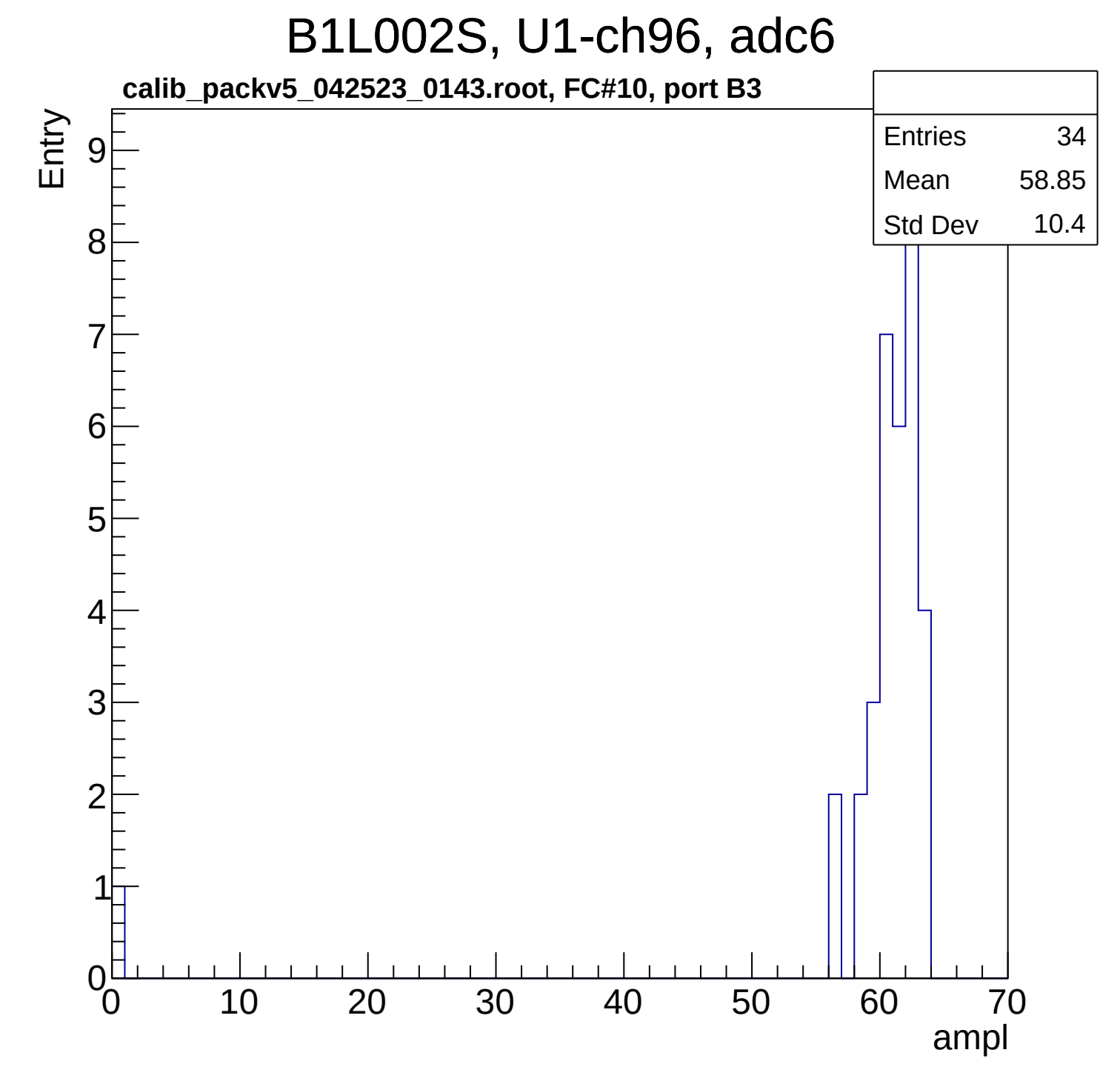
B1L002S, U1-ch96, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.85
Std Dev 10.4
Entry
ampl
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2
3
4
5
6
7
8
9
0
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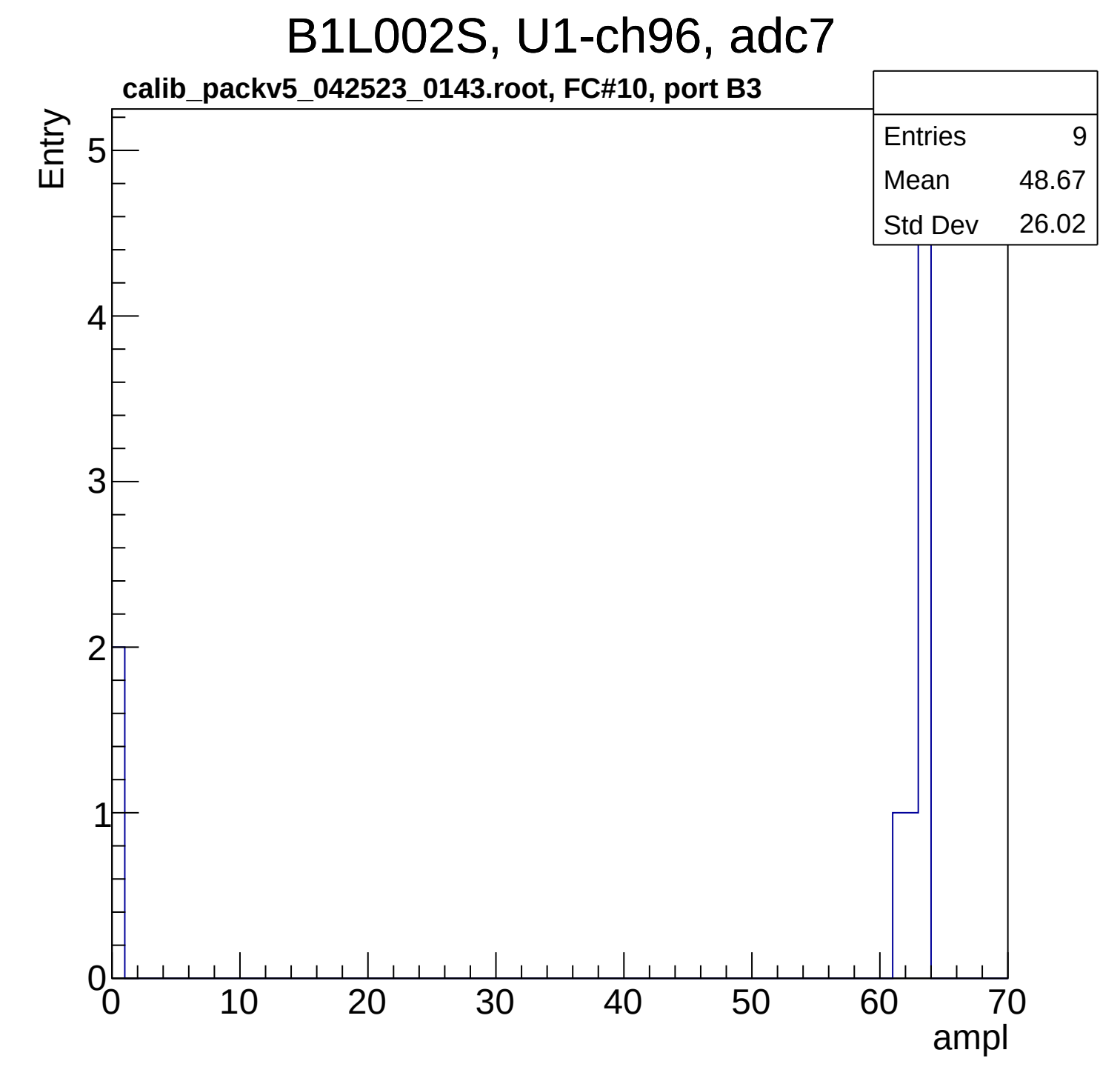
B1L002S, U1-ch96, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 48.67
Std Dev 26.02
Entry
0
1
2
3
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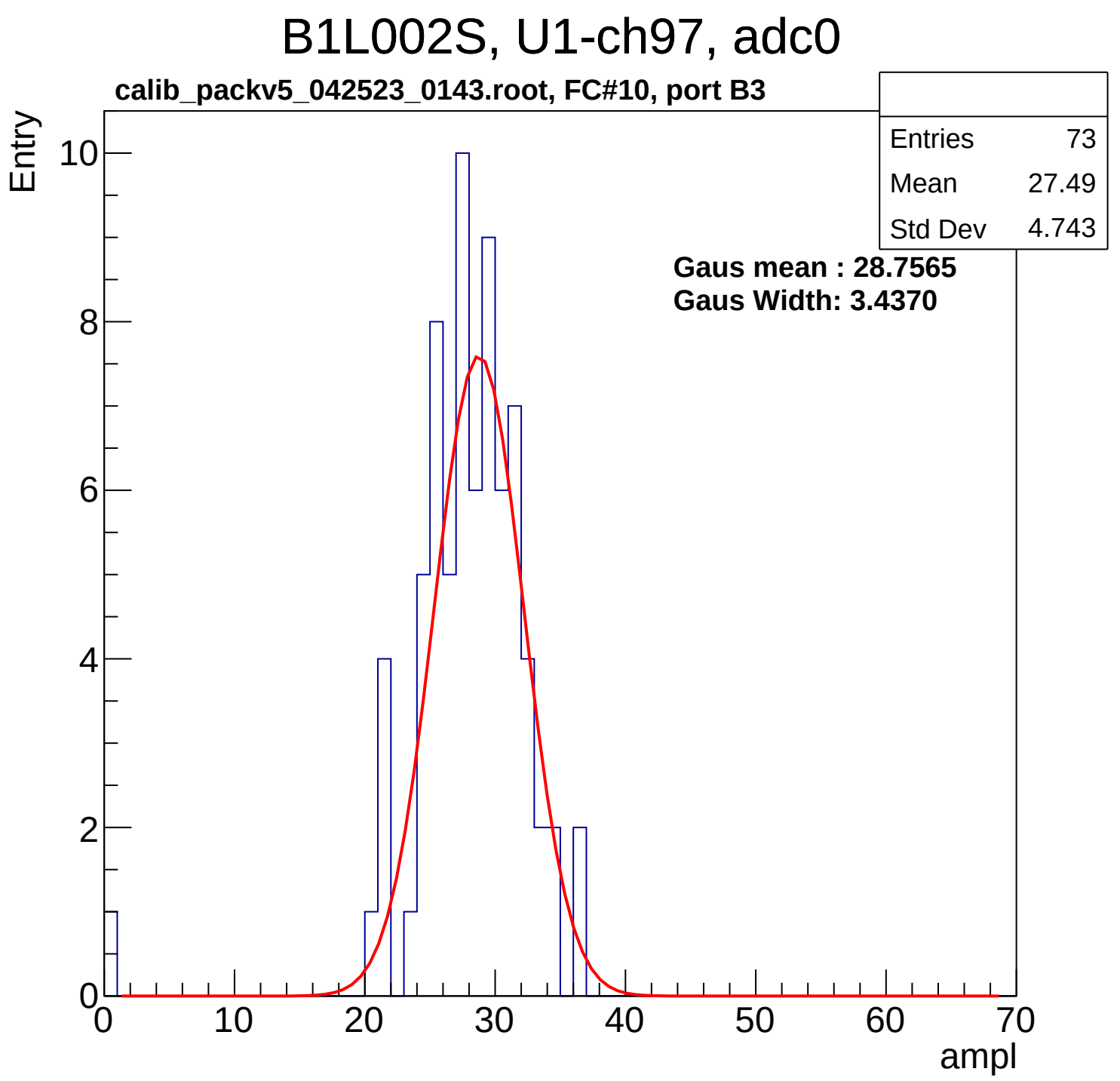
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch97, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 27.49
Std Dev 4.743
Gaus mean : 28.7565
Gaus Width: 3.4370
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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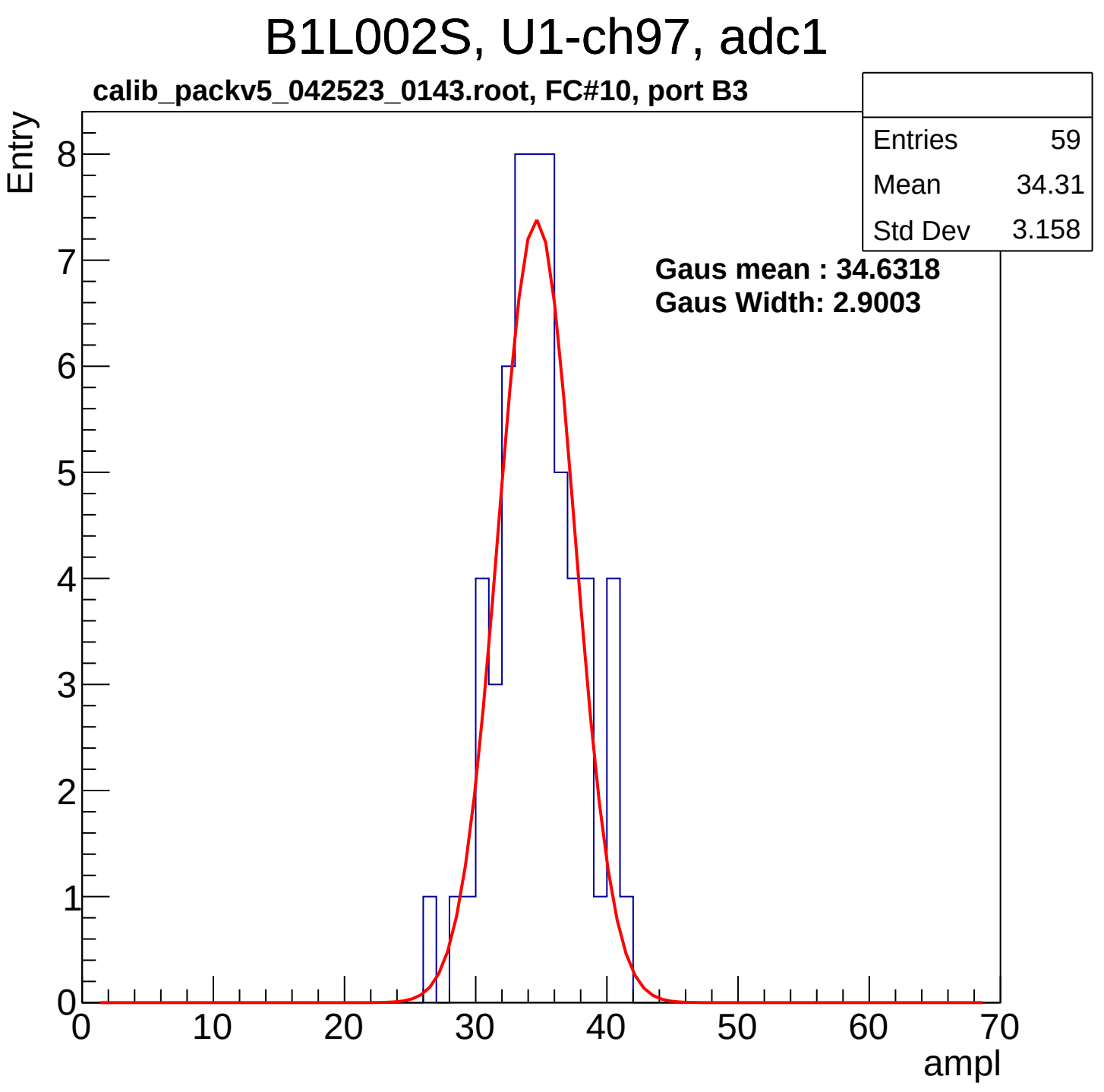
B1L002S, U1-ch97, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.31
Std Dev 3.158
Gaus mean : 34.6318
Gaus Width: 2.9003
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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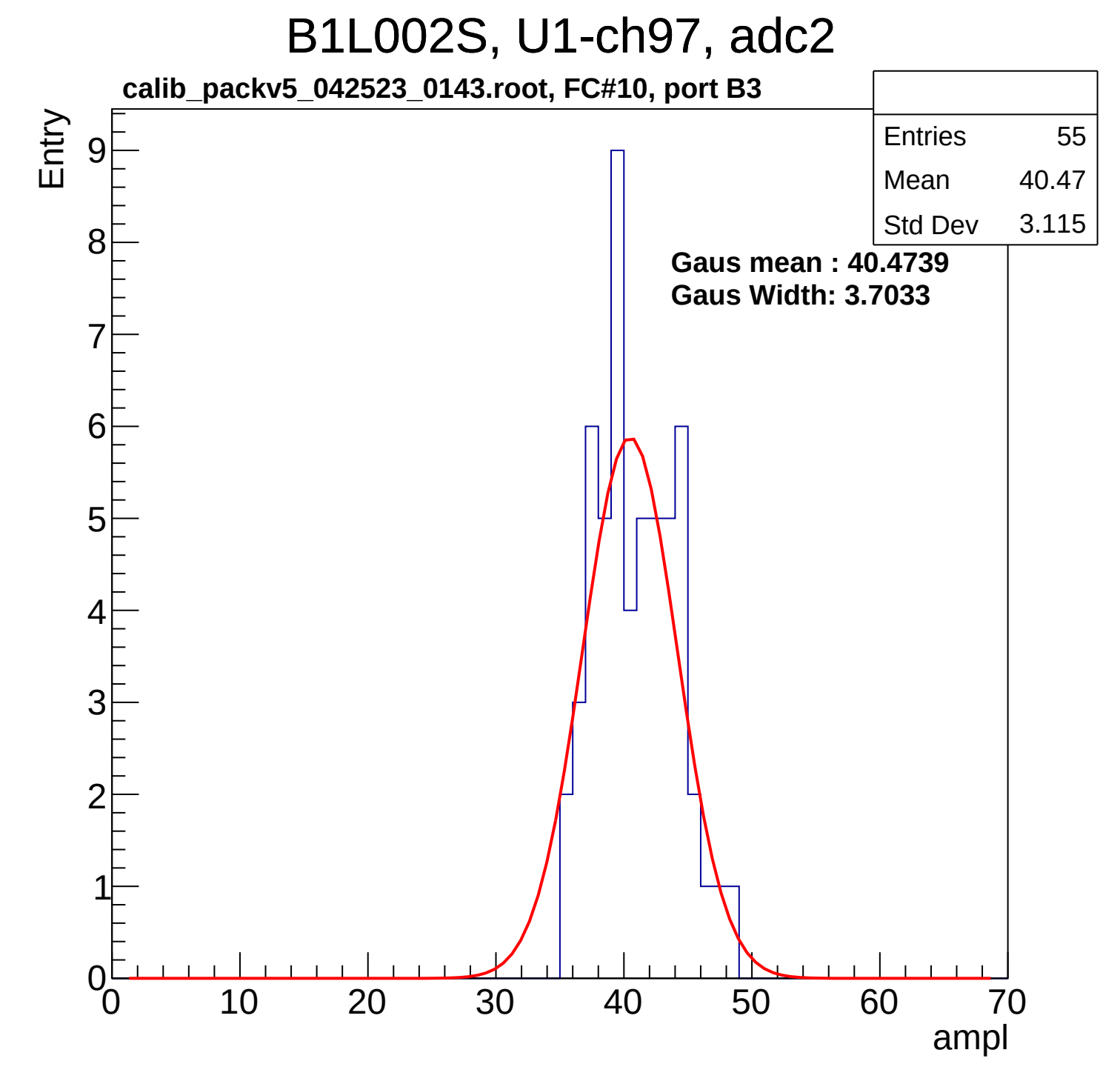
B1L002S, U1-ch97, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.47
Std Dev 3.115
Gaus mean : 40.4739
Gaus Width: 3.7033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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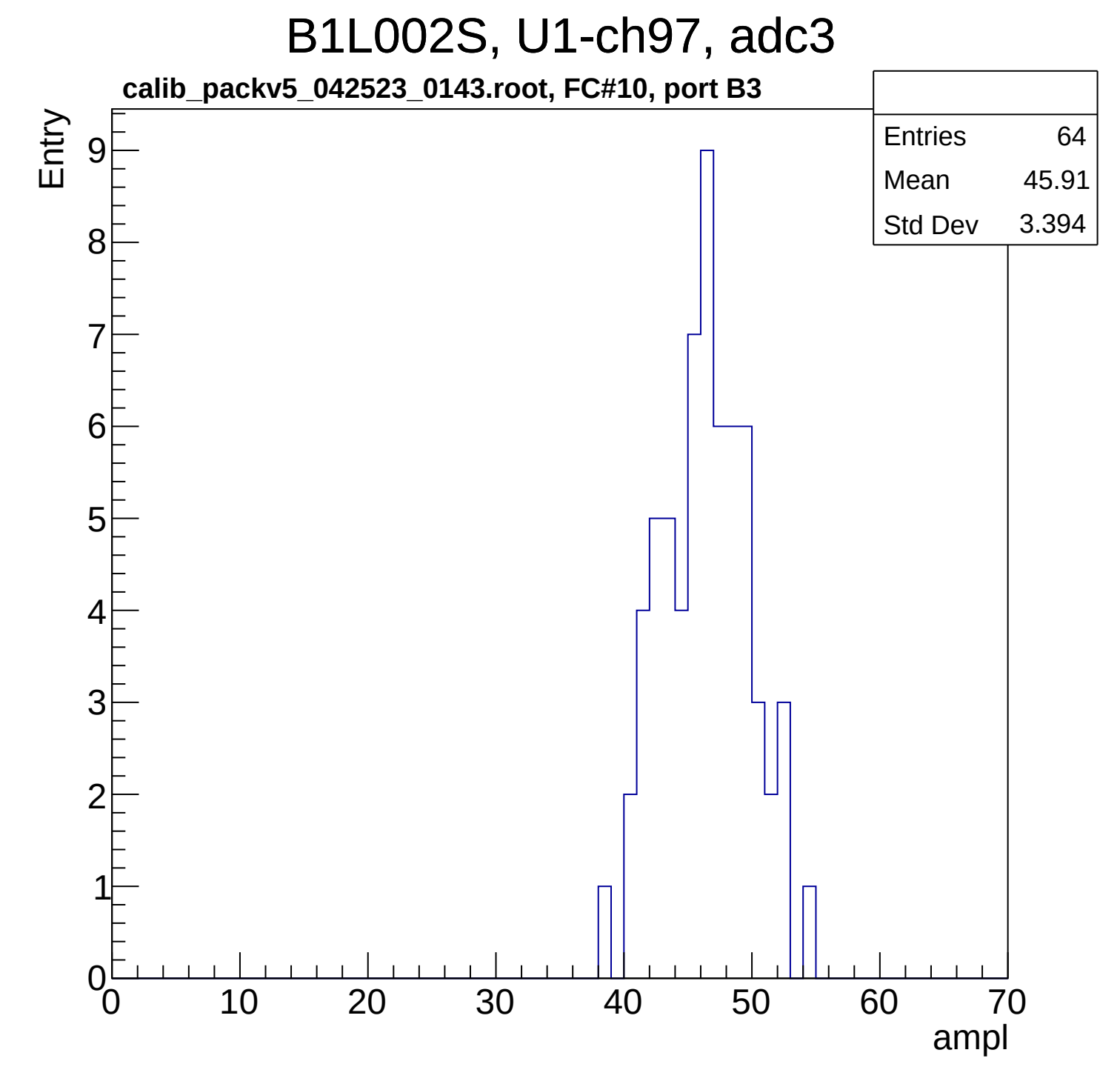
B1L002S, U1-ch97, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 45.91
Std Dev 3.394
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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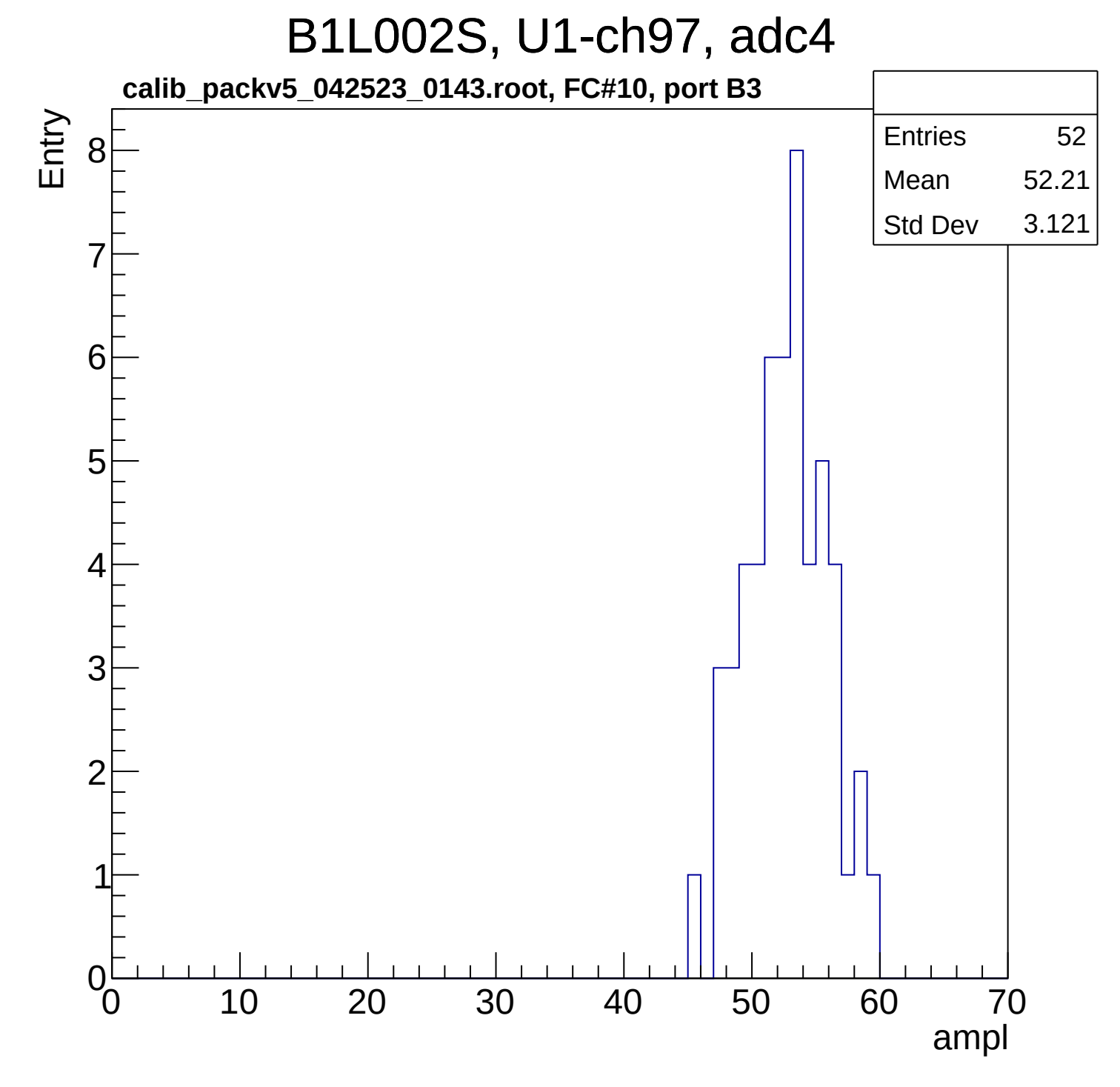
B1L002S, U1-ch97, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.21
Std Dev 3.121
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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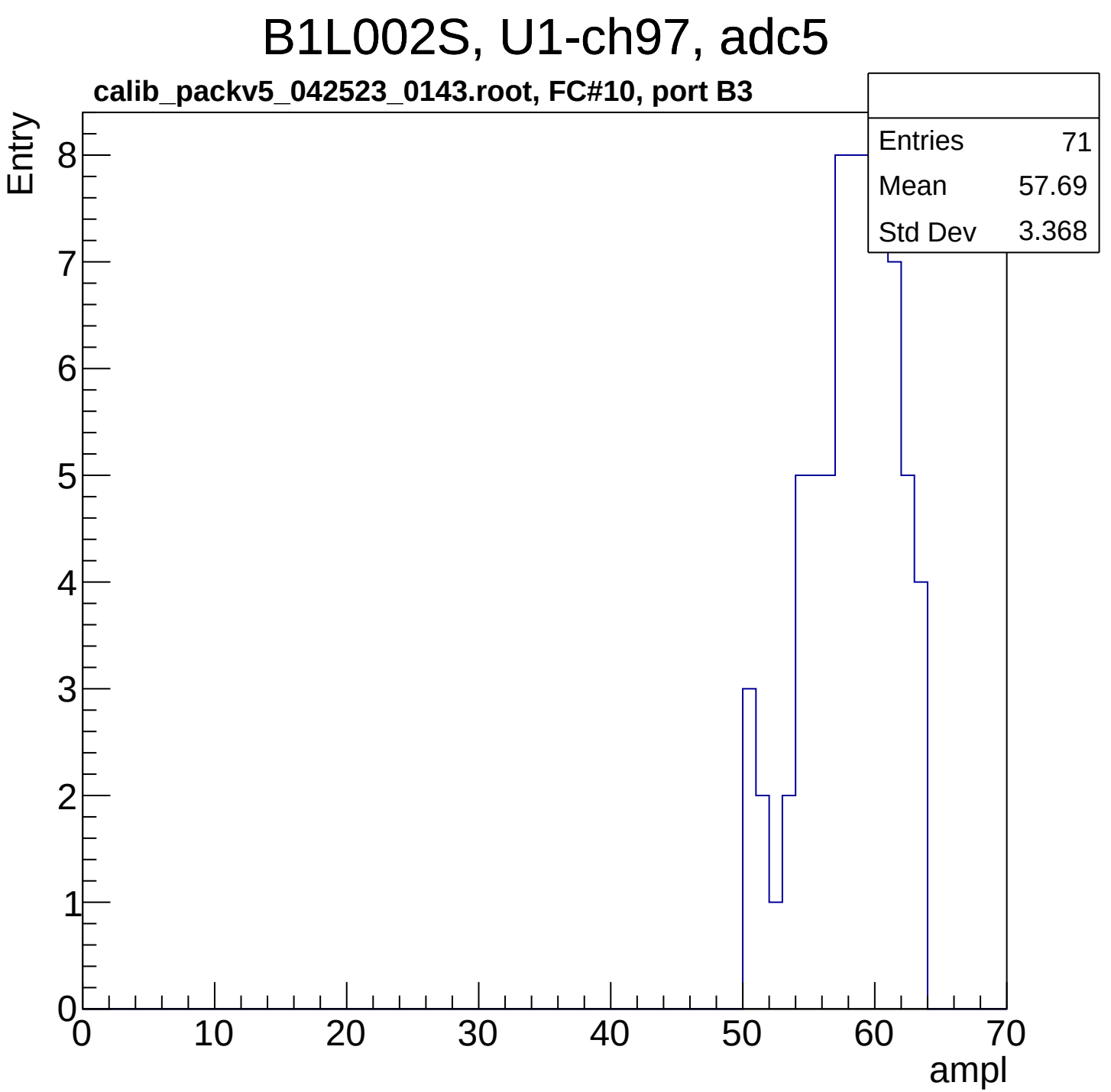
B1L002S, U1-ch97, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57.69
Std Dev 3.368
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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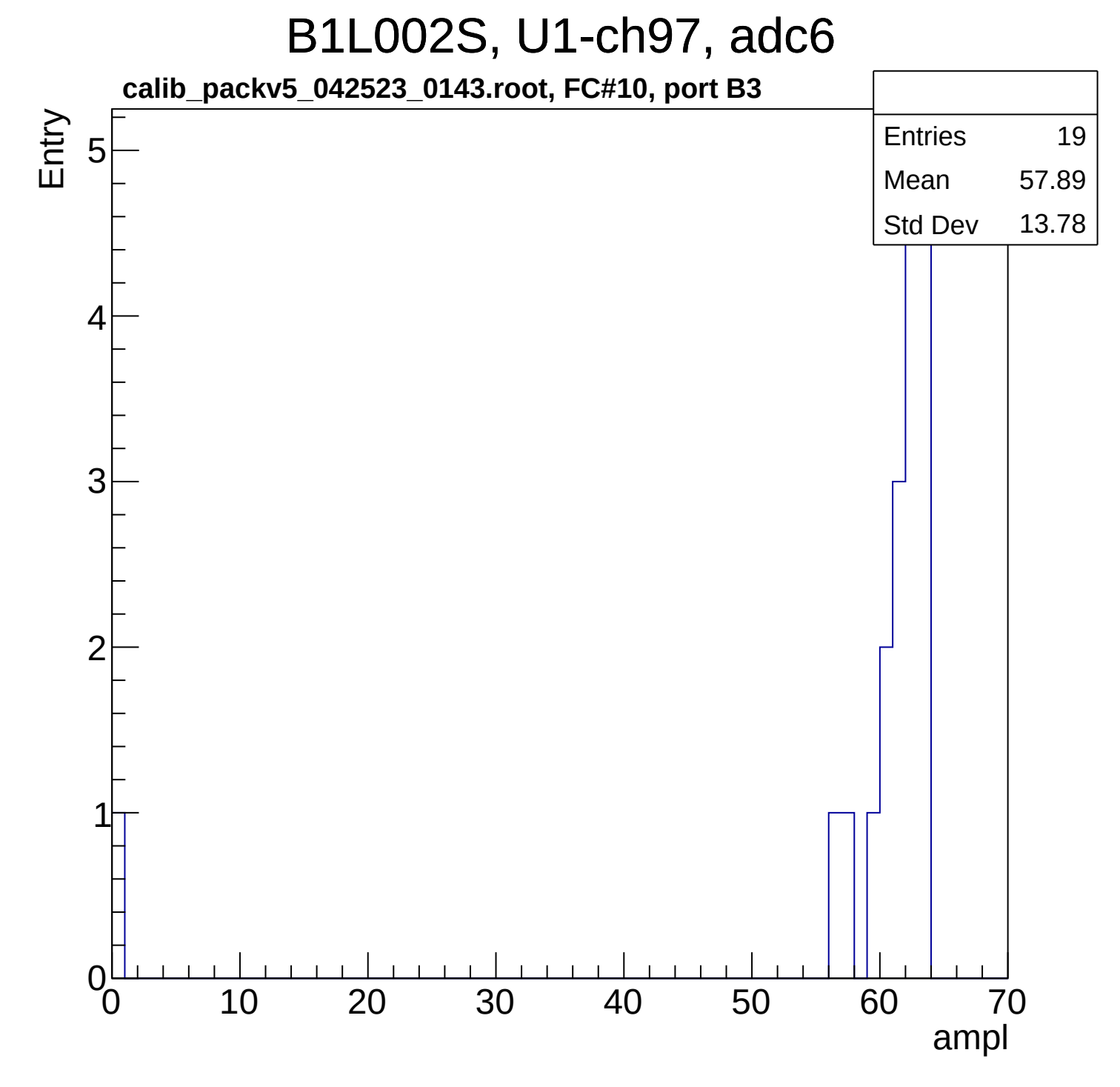
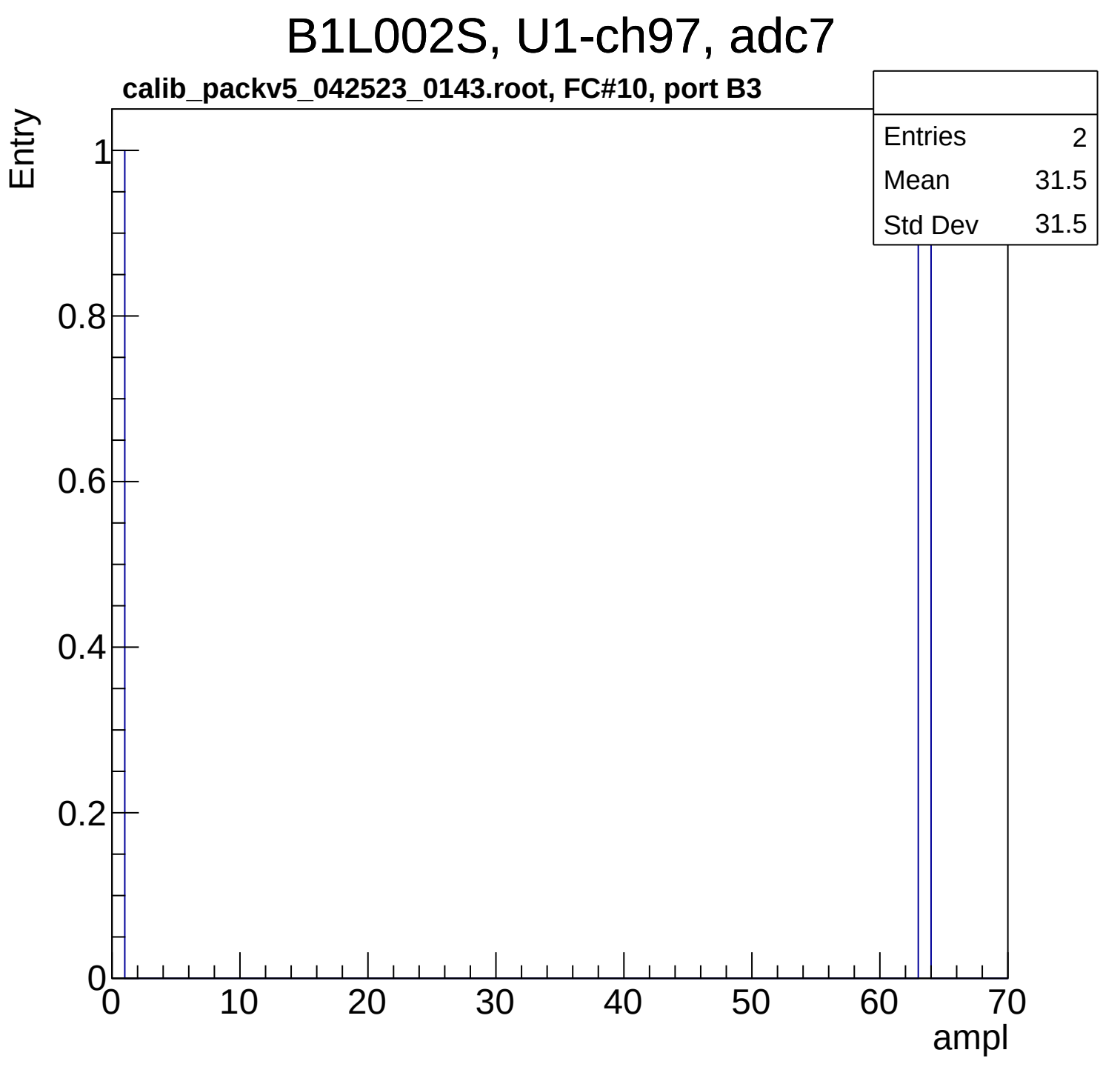

B1L002S, U1-ch97, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 57.89
Std Dev 13.78
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch97, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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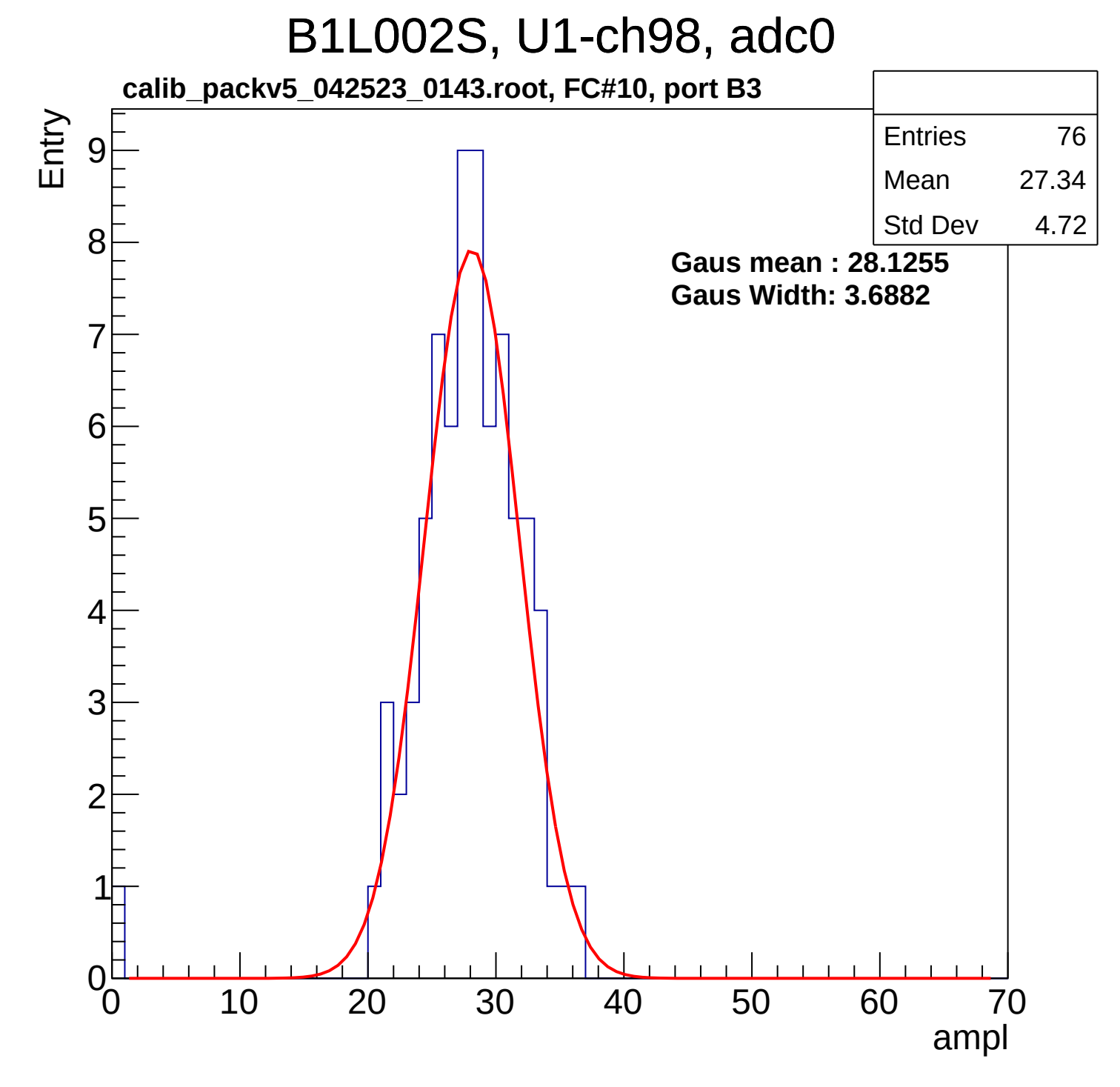
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch98, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 27.34
Std Dev 4.72
Gaus mean : 28.1255
Gaus Width: 3.6882
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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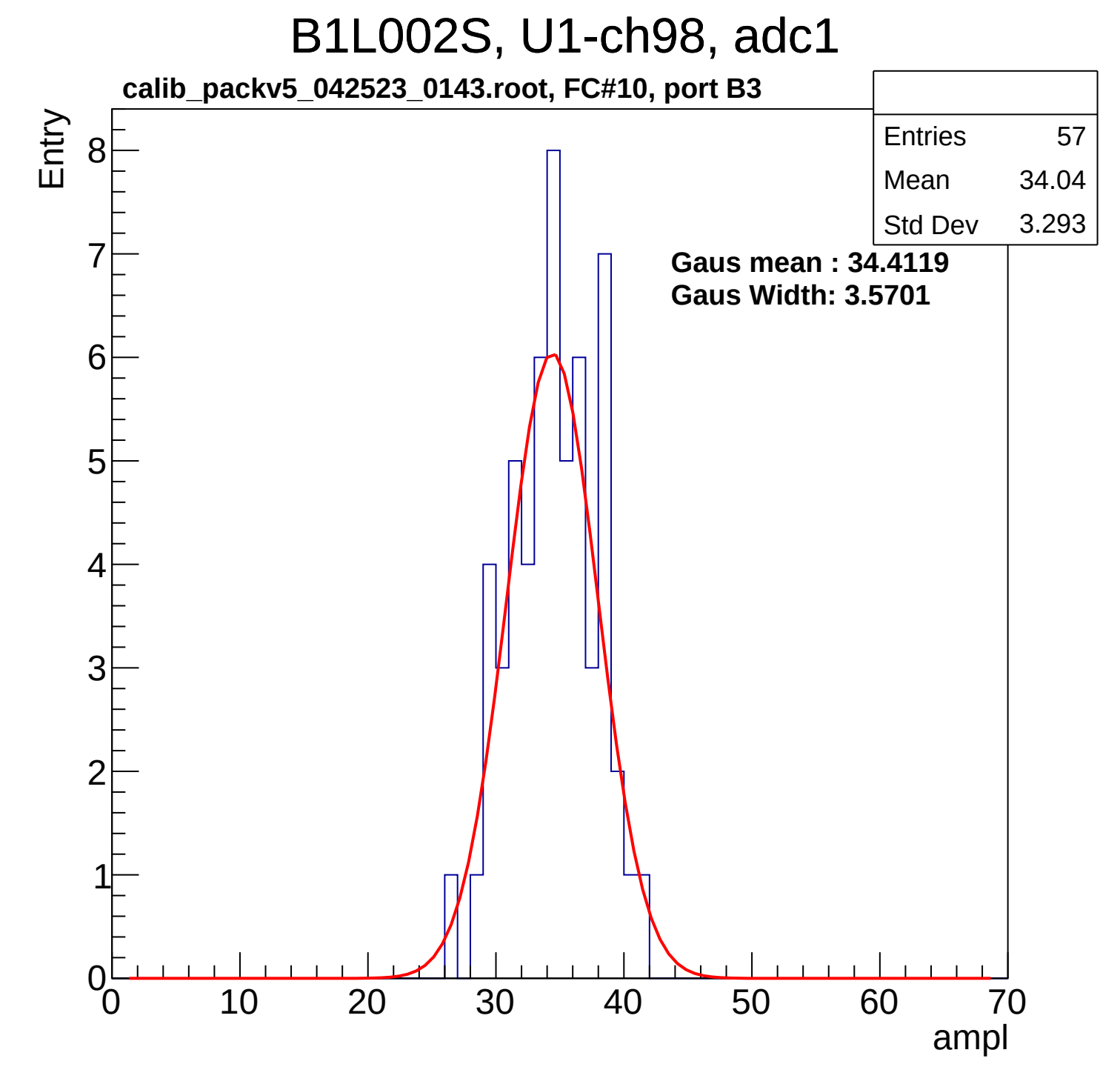
B1L002S, U1-ch98, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.04
Std Dev 3.293
Gaus mean : 34.4119
Gaus Width: 3.5701
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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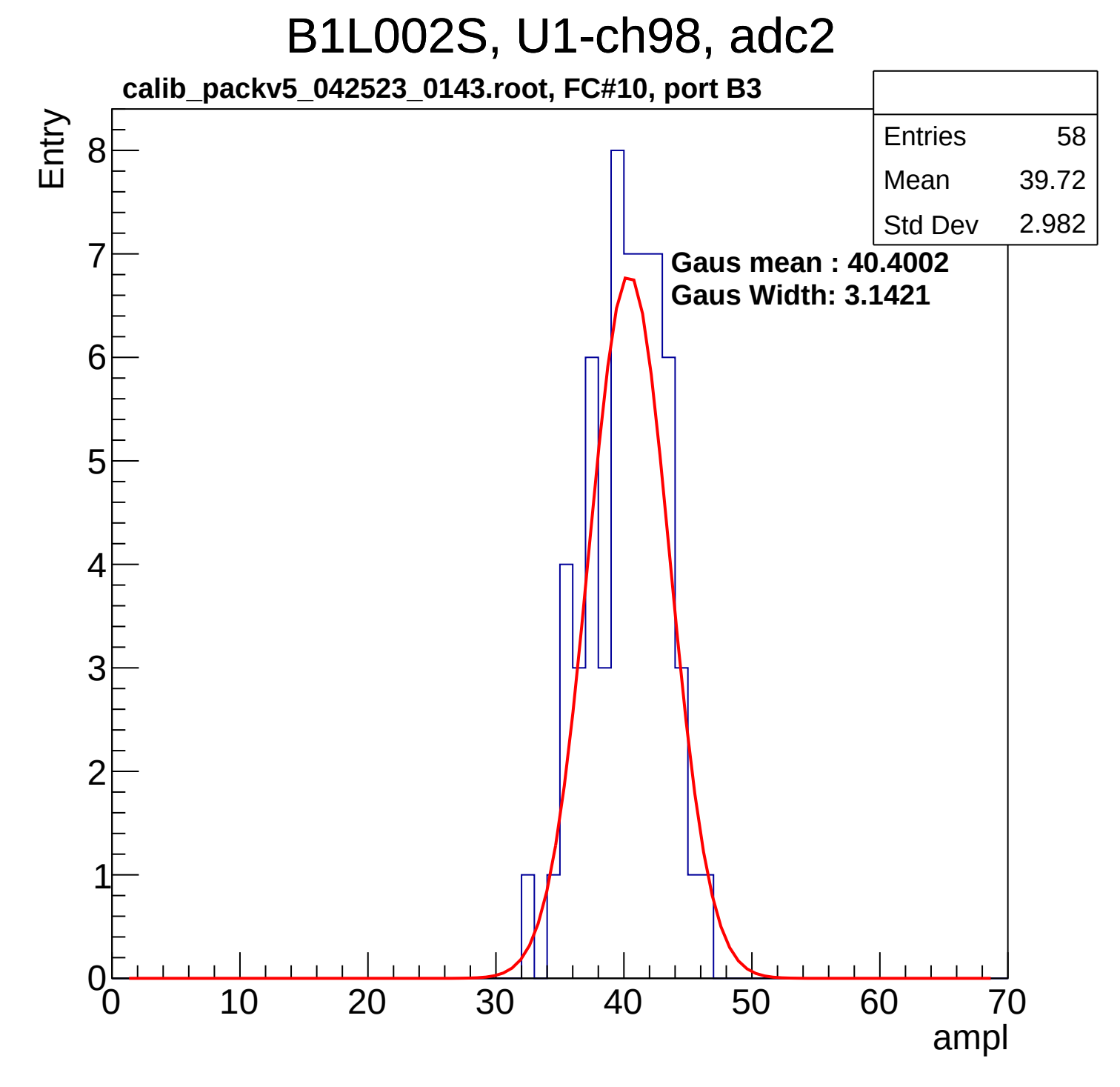
B1L002S, U1-ch98, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 39.72
Std Dev 2.982
Gaus mean : 40.4002
Gaus Width: 3.1421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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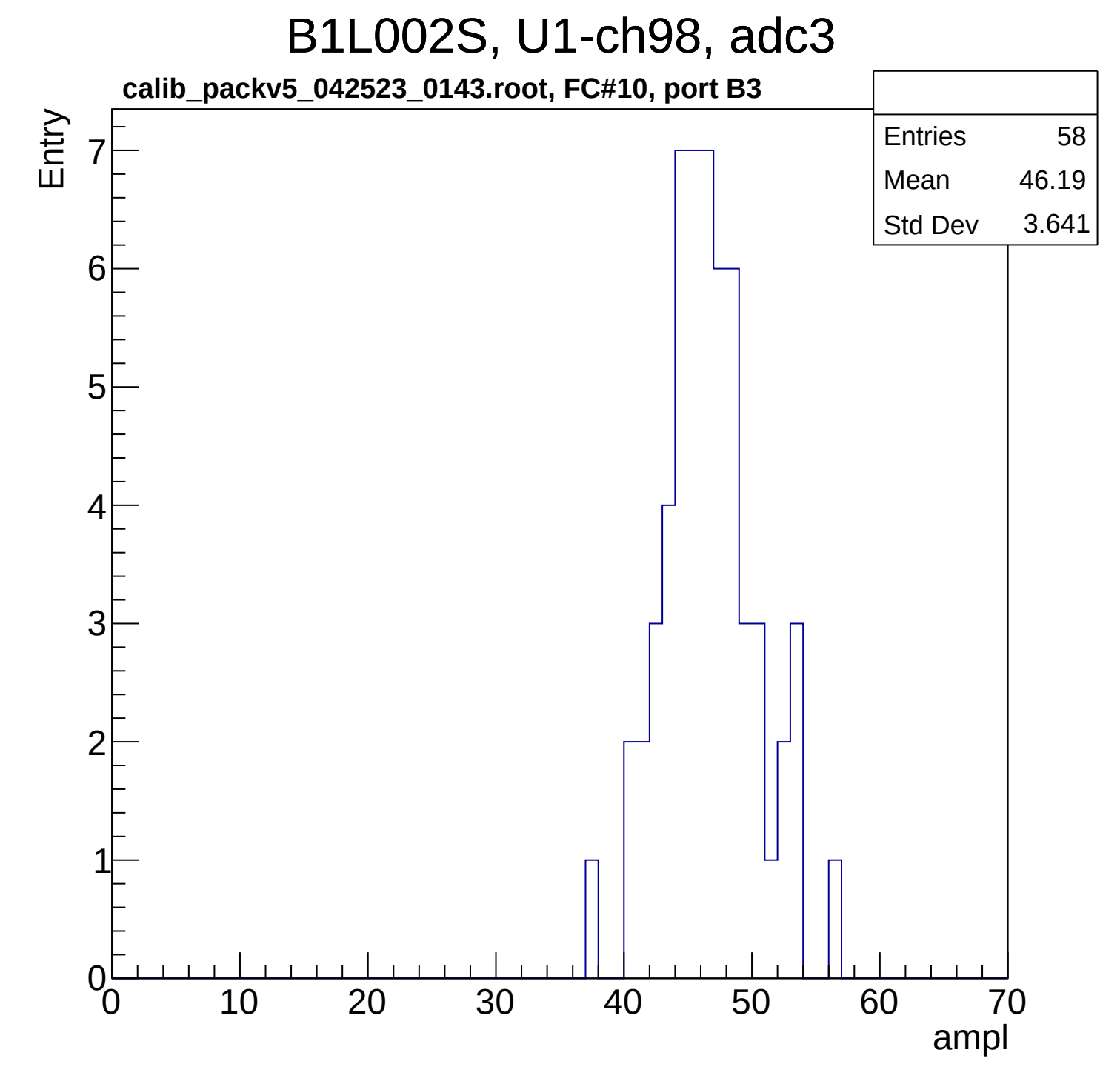
B1L002S, U1-ch98, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.19
Std Dev 3.641
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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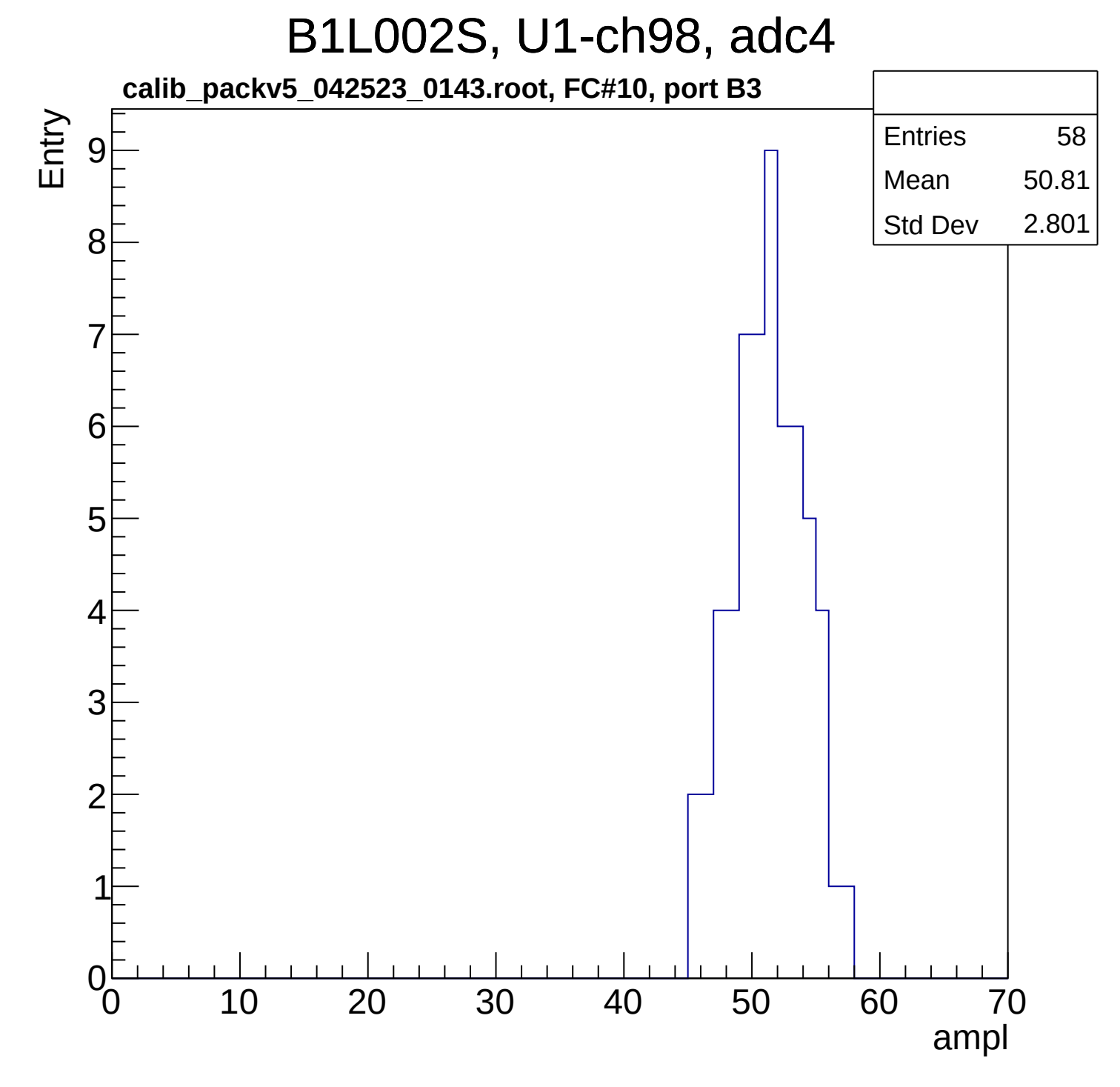
B1L002S, U1-ch98, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 50.81
Std Dev 2.801
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
50
60
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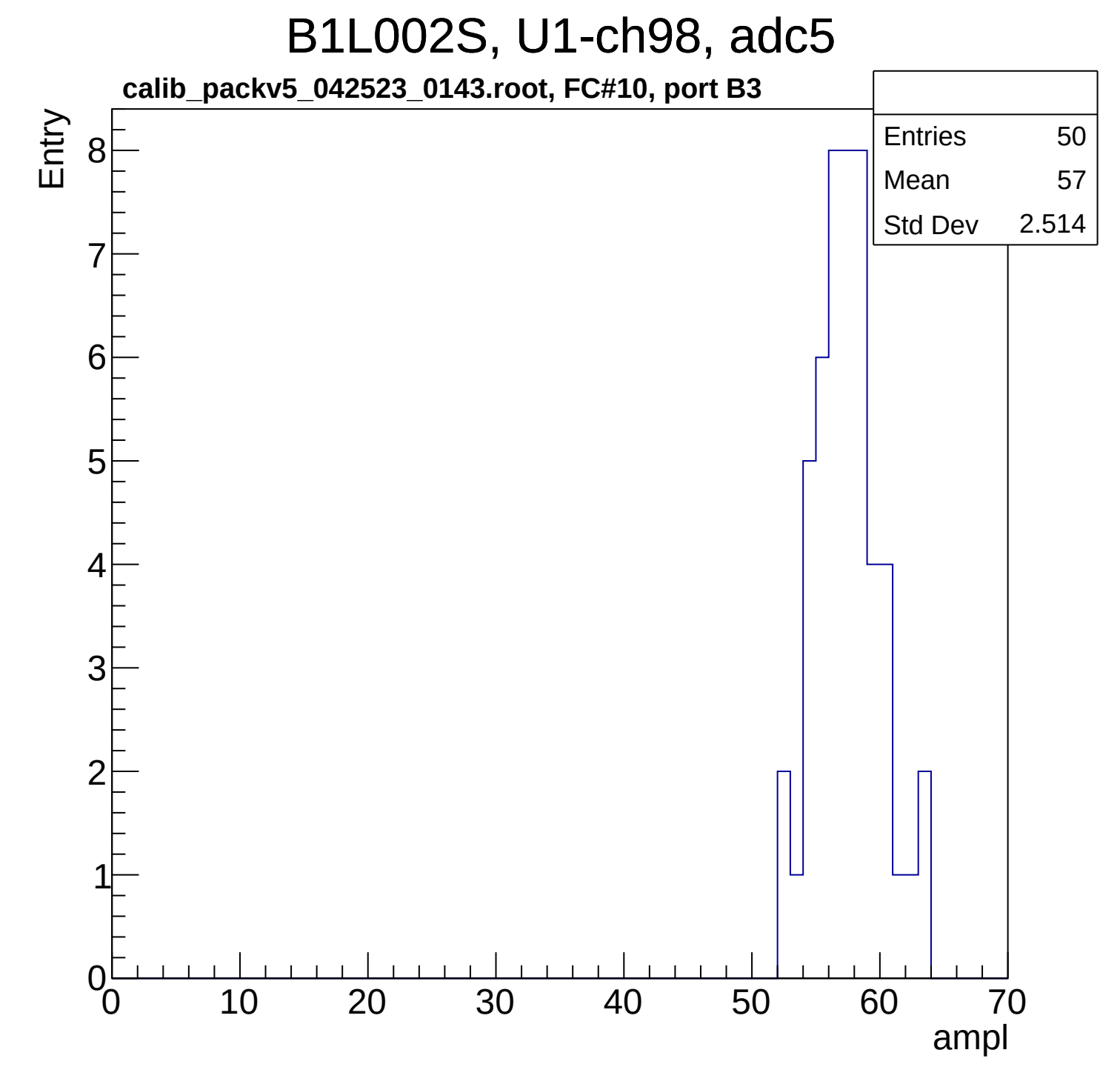
B1L002S, U1-ch98, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57
Std Dev 2.514
Entry
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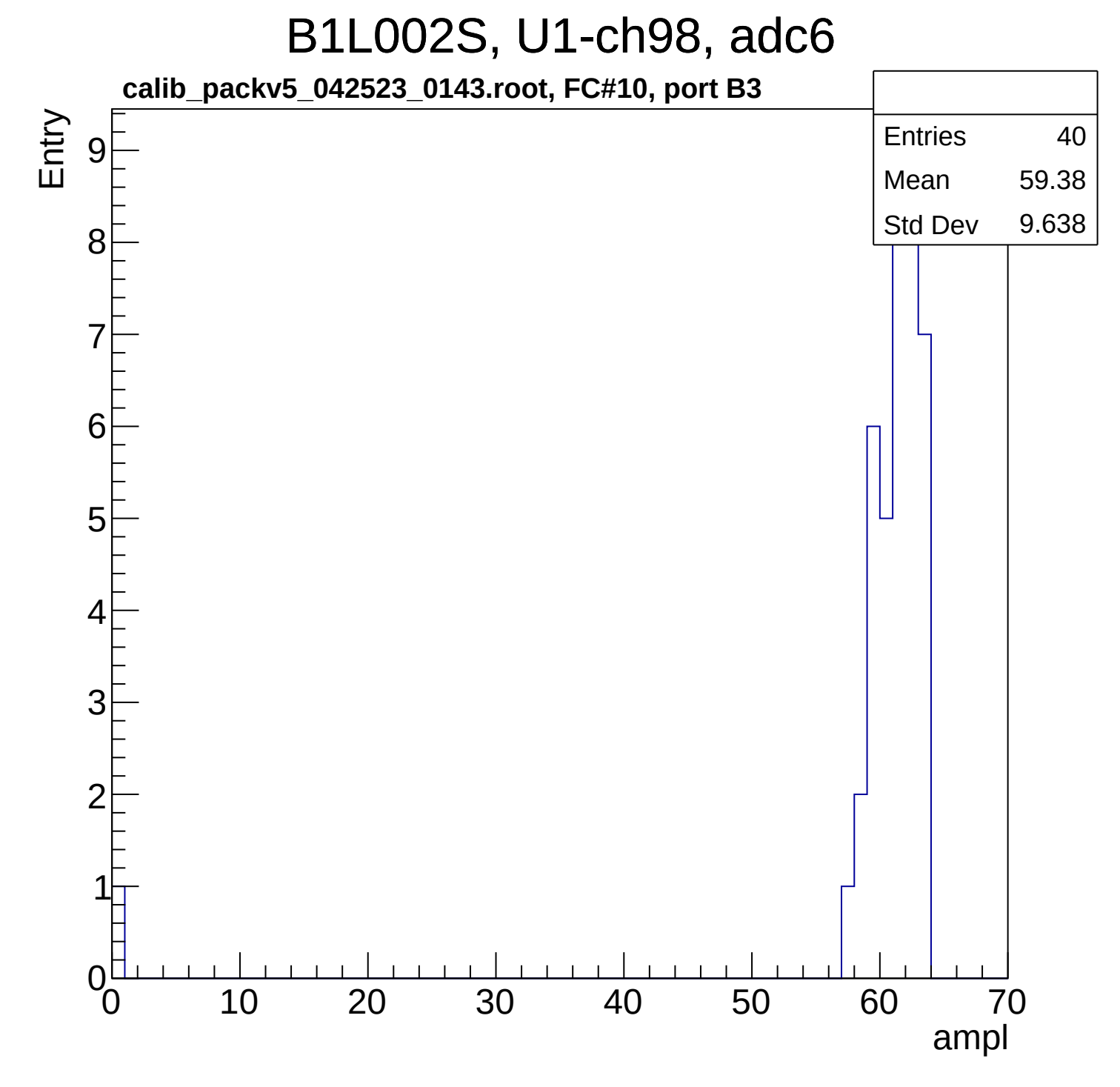
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch98, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 59.38
Std Dev 9.638
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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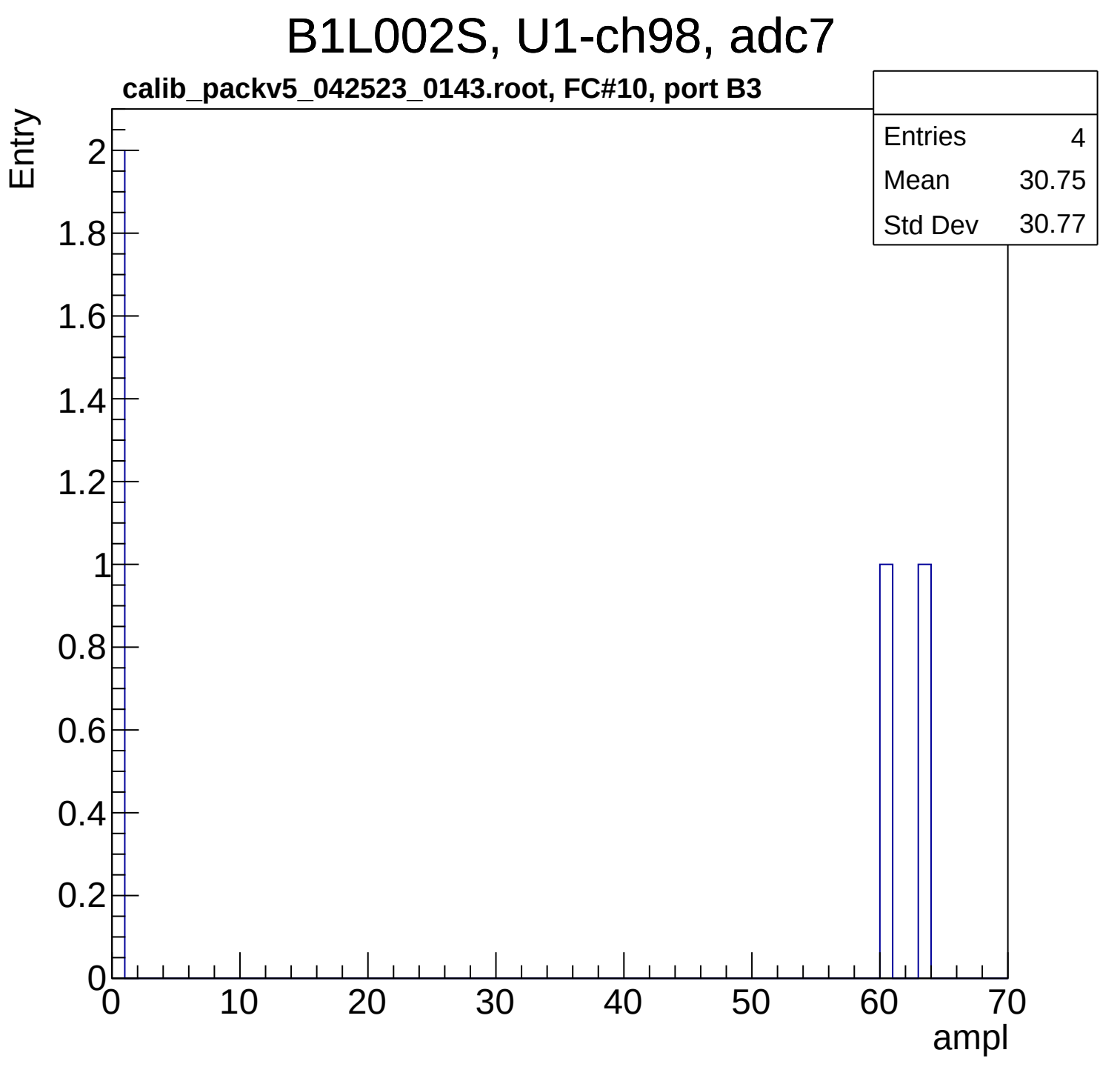
B1L002S, U1-ch98, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 30.75
Std Dev 30.77
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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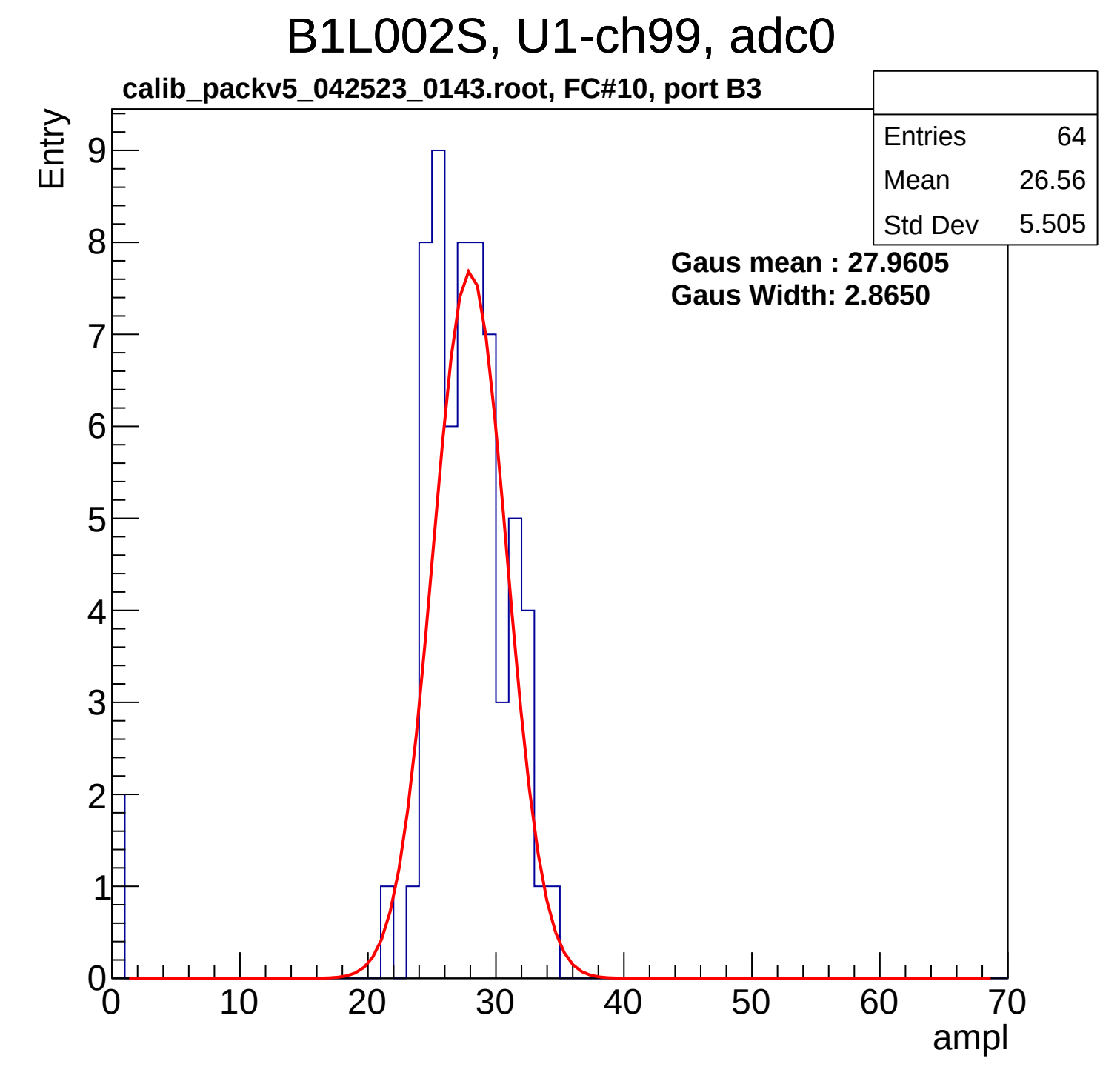
B1L002S, U1-ch99, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 26.56
Std Dev 5.505
Gaus mean : 27.9605
Gaus Width: 2.8650
Entry
ampl
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5
6
7
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0
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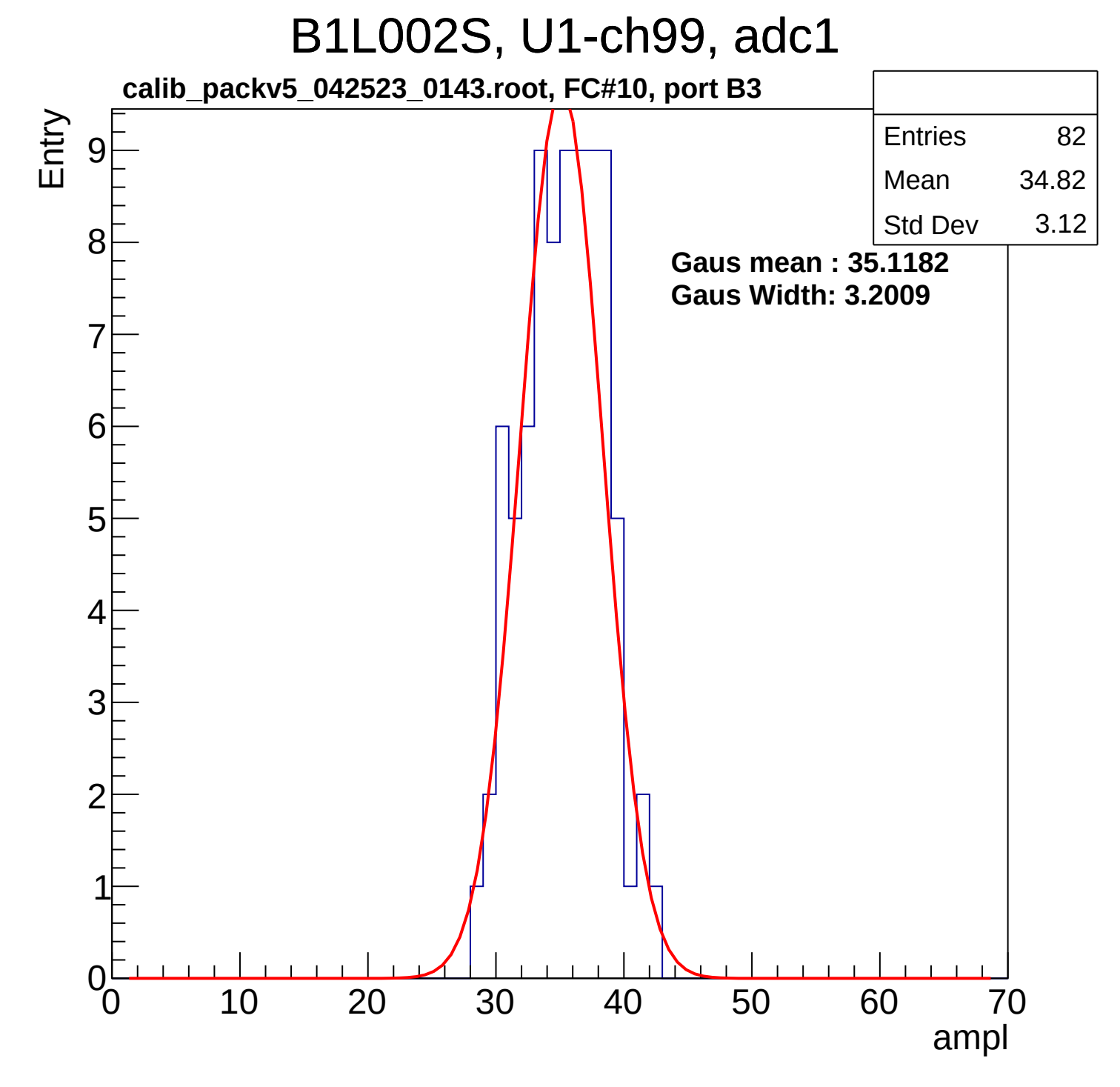
B1L002S, U1-ch99, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 34.82
Std Dev 3.12
Gaus mean : 35.1182
Gaus Width: 3.2009
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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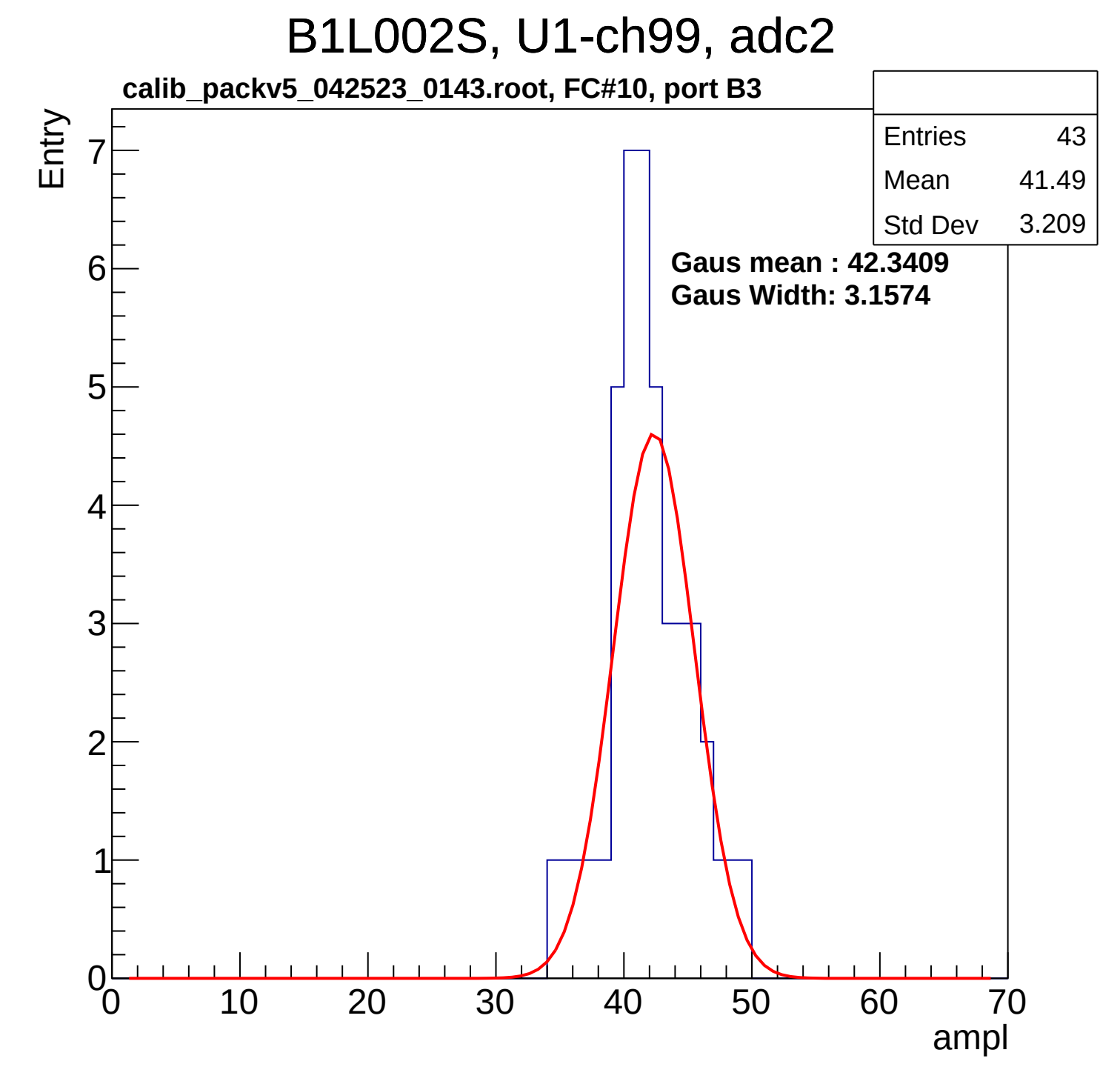
B1L002S, U1-ch99, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 41.49
Std Dev 3.209
Gaus mean : 42.3409
Gaus Width: 3.1574
Entry
ampl
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4
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6
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0
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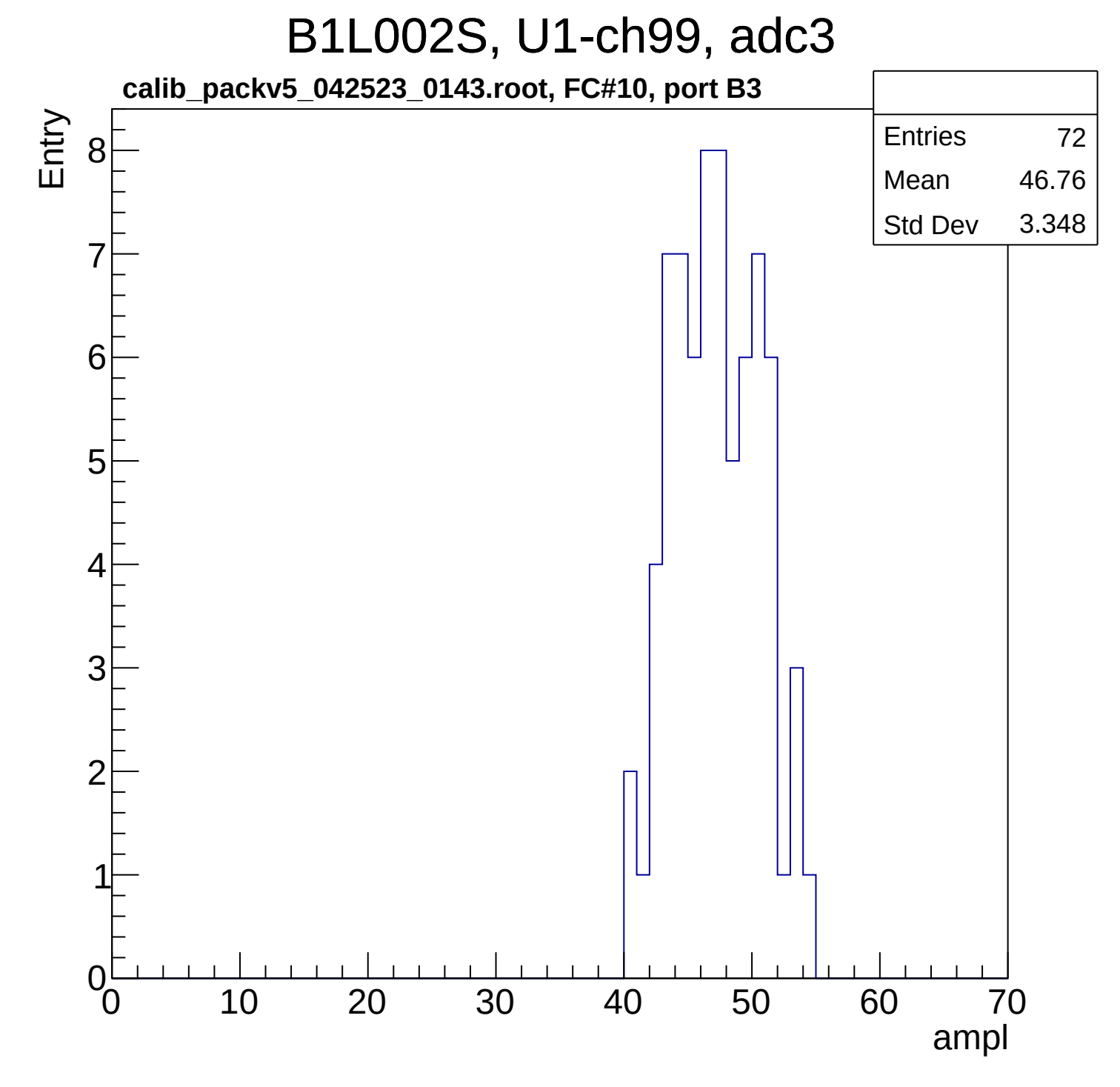
B1L002S, U1-ch99, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.76
Std Dev 3.348
Entry
ampl
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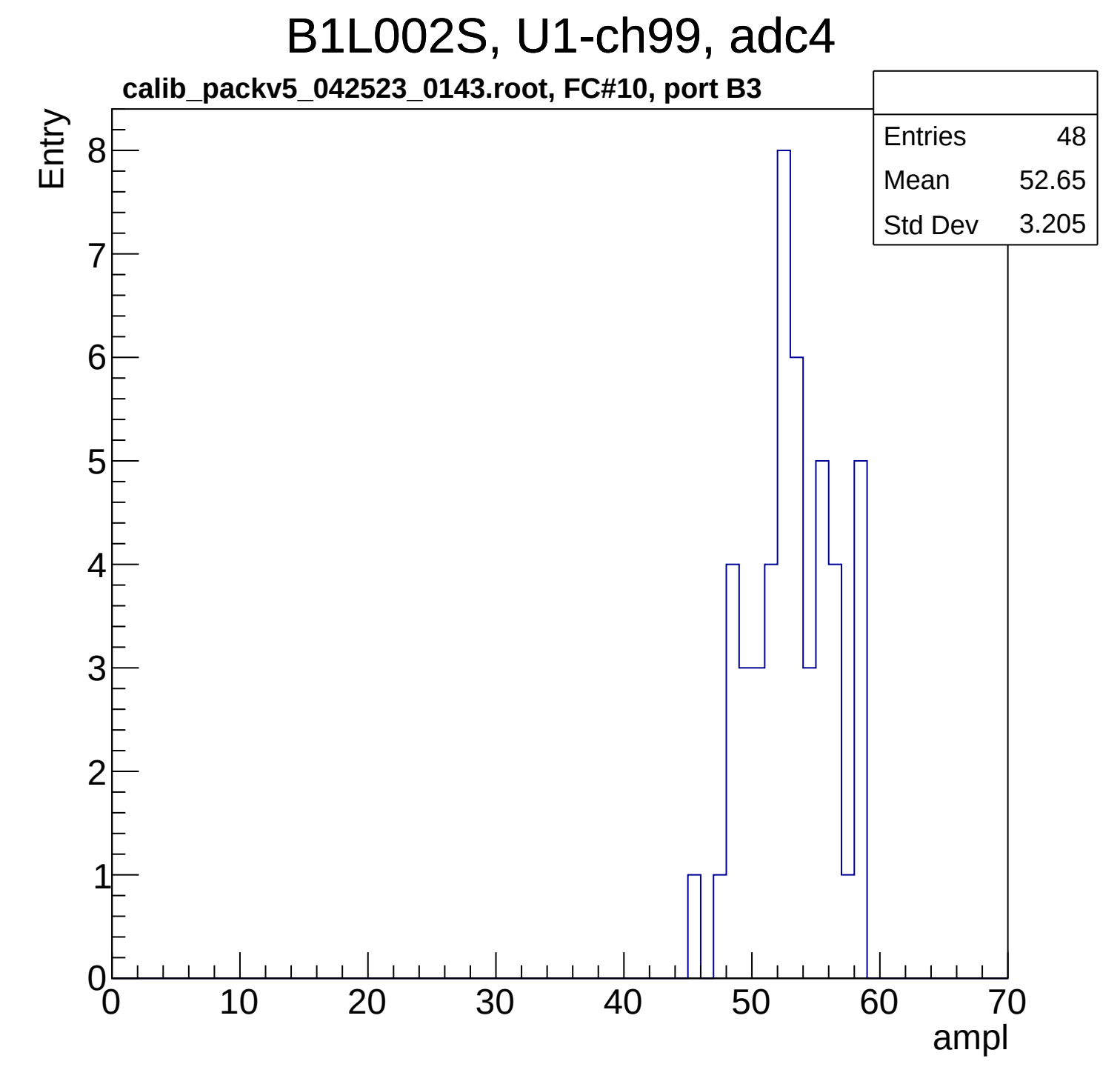
B1L002S, U1-ch99, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 52.65
Std Dev 3.205
Entry
ampl
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0
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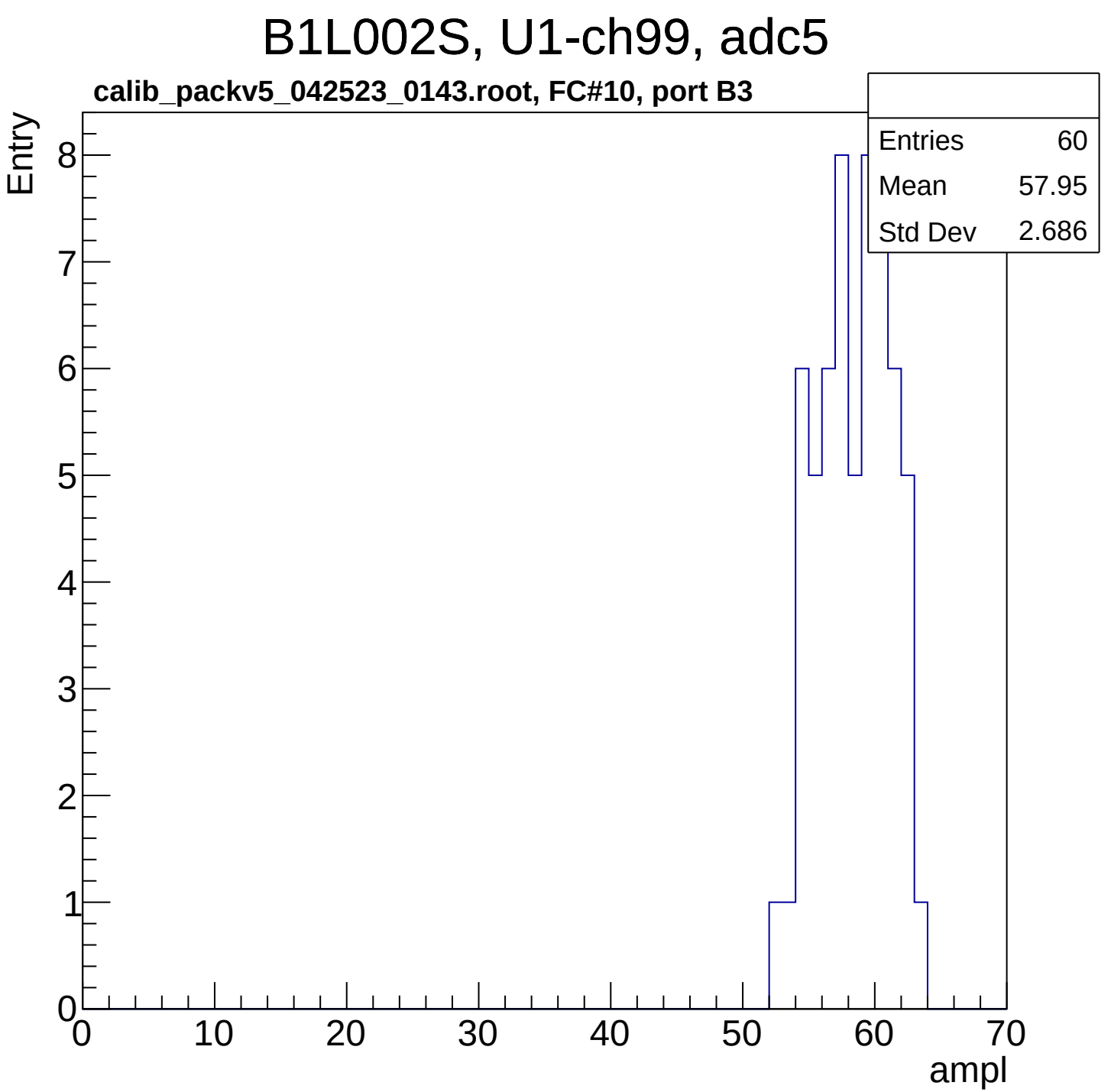
B1L002S, U1-ch99, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.95
Std Dev 2.686
Entry
ampl
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1
2
3
4
5
6
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0
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60
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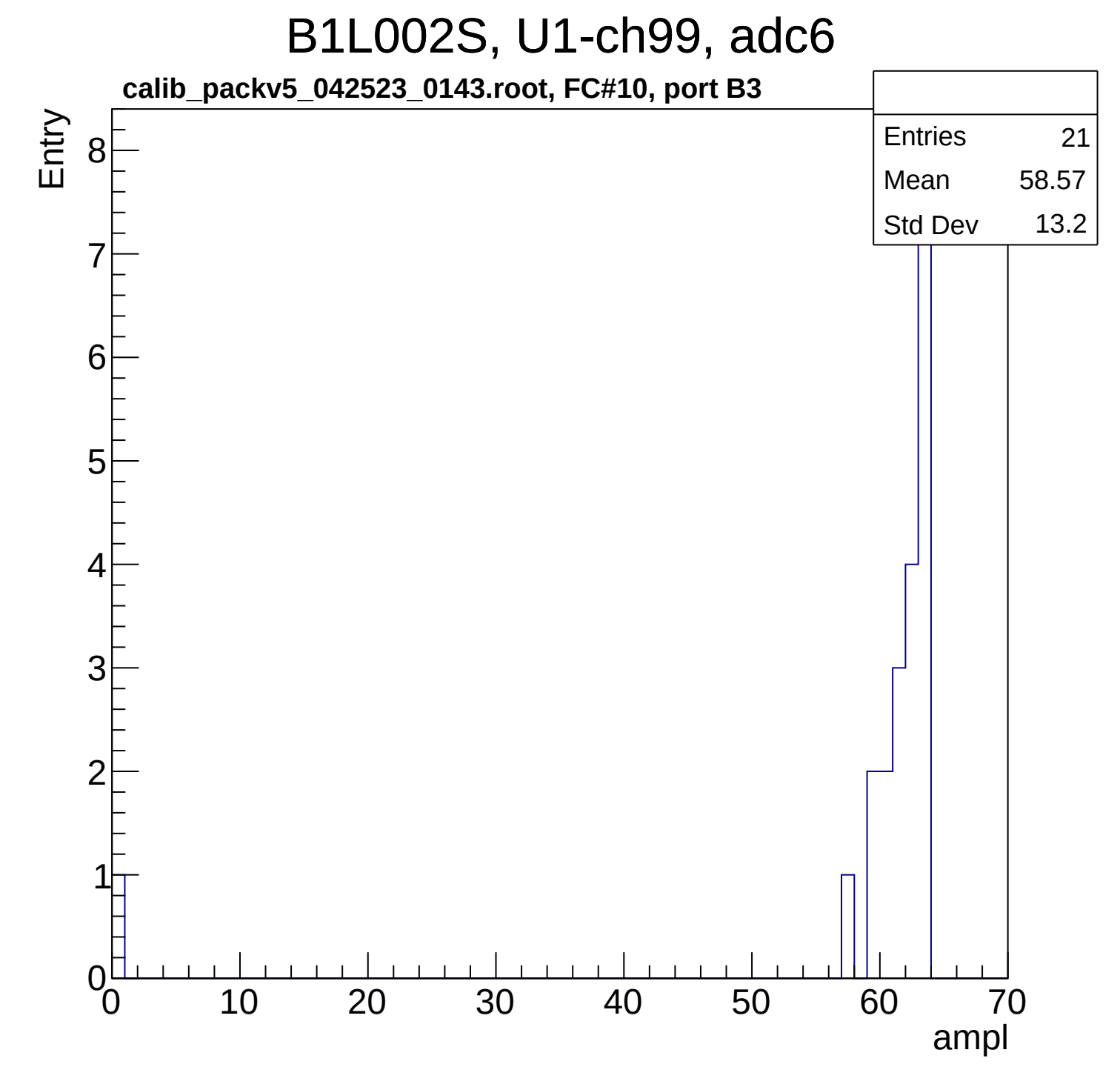
B1L002S, U1-ch99, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.57
Std Dev 13.2
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
50
60
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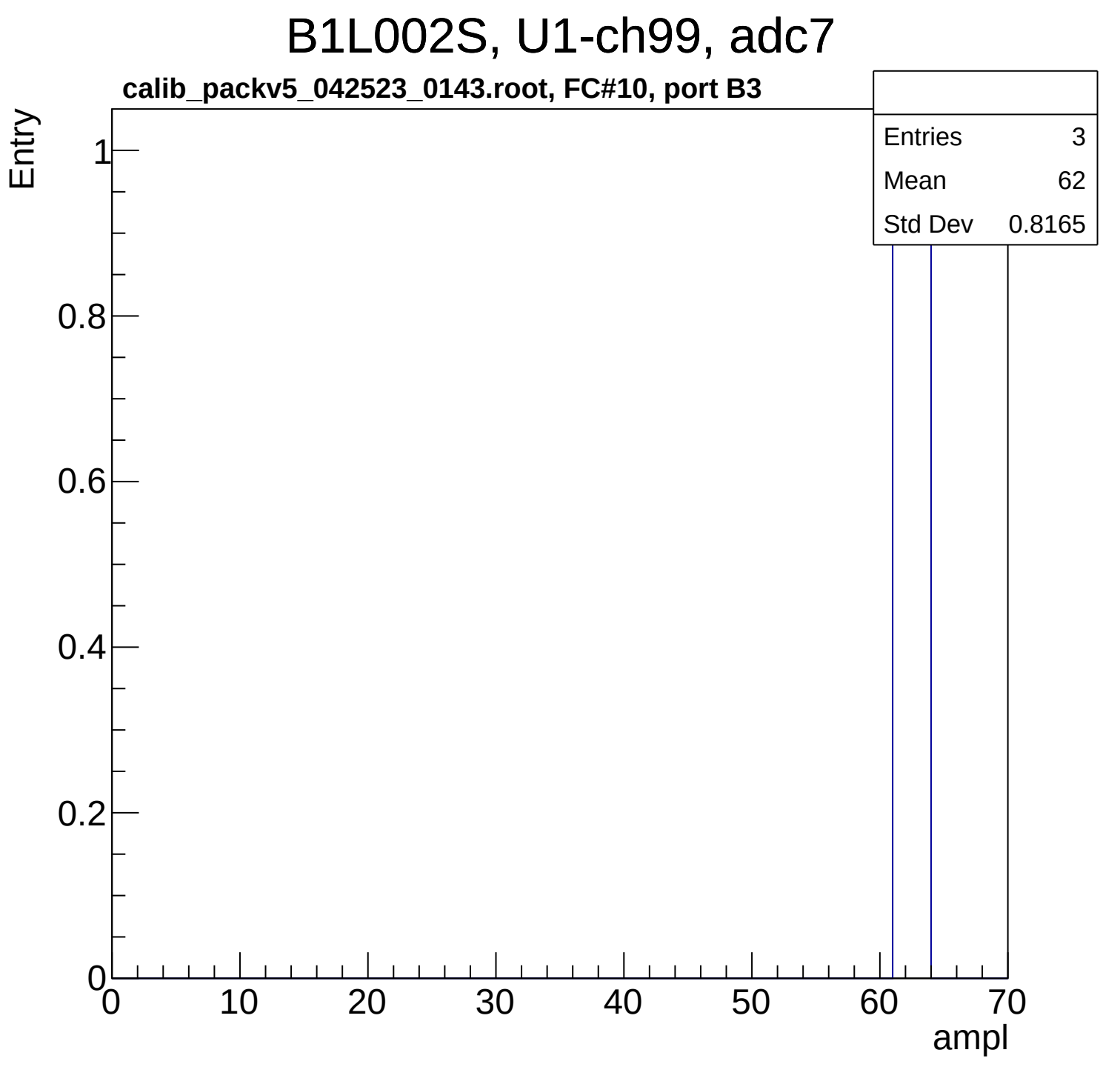
B1L002S, U1-ch99, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 62
Std Dev 0.8165
Entry
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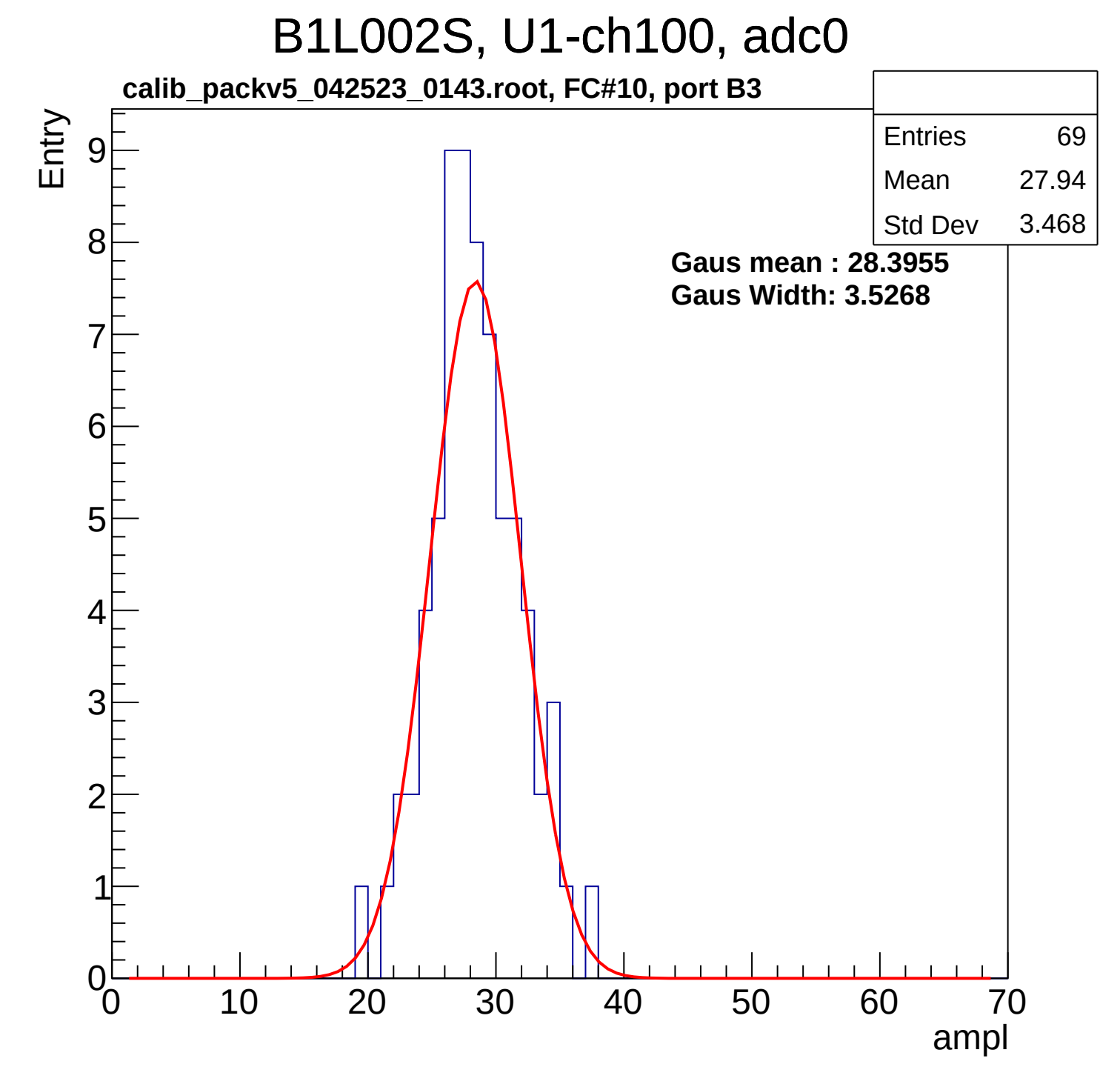
40
50
60
70
ampl


B1L002S, U1-ch100, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27.94
Std Dev 3.468
Gaus mean : 28.3955
Gaus Width: 3.5268
Entry
ampl
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2
3
4
5
6
7
8
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0
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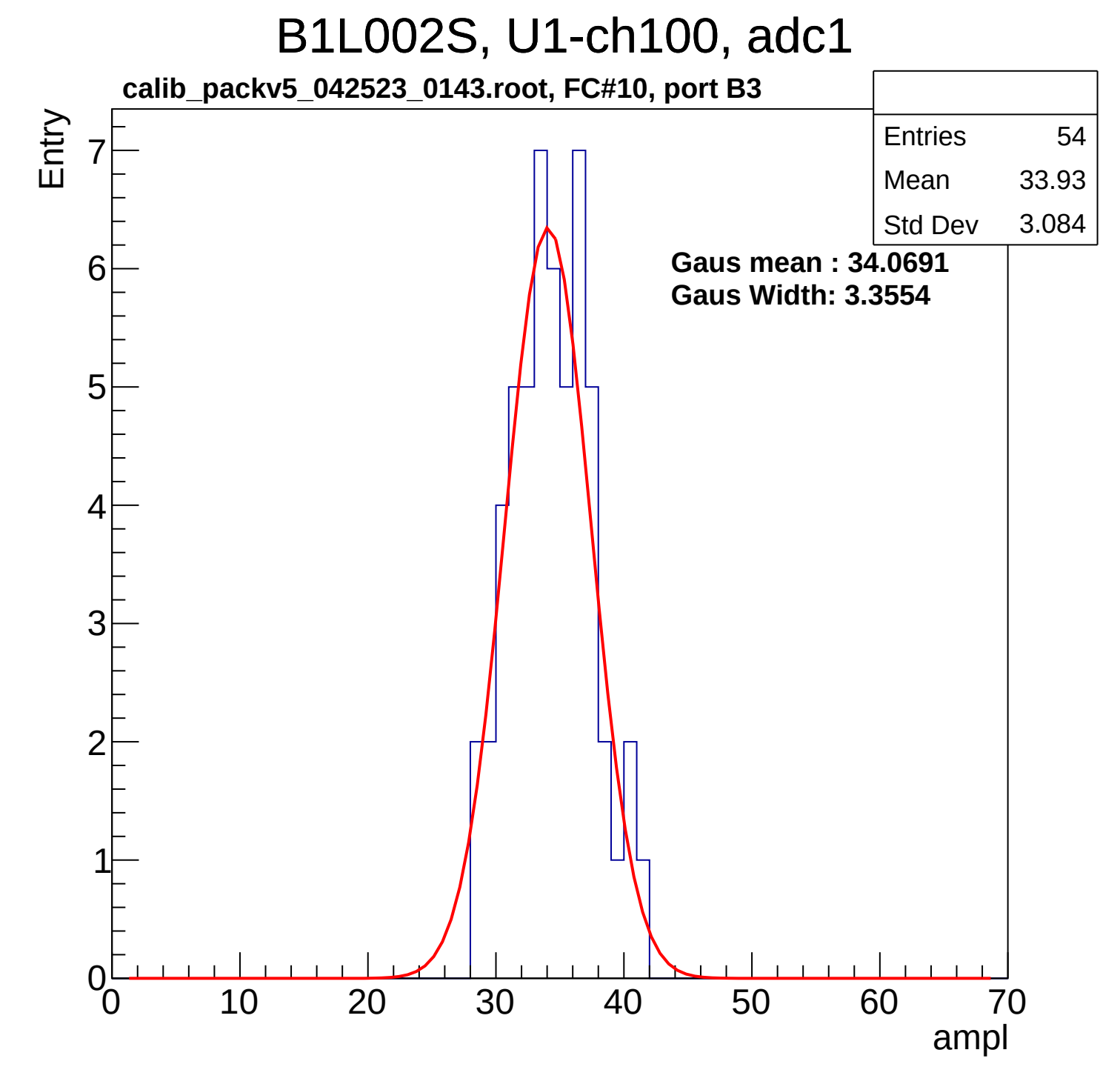
B1L002S, U1-ch100, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 33.93
Std Dev 3.084
Gaus mean : 34.0691
Gaus Width: 3.3554
Entry
ampl
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1
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4
5
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0
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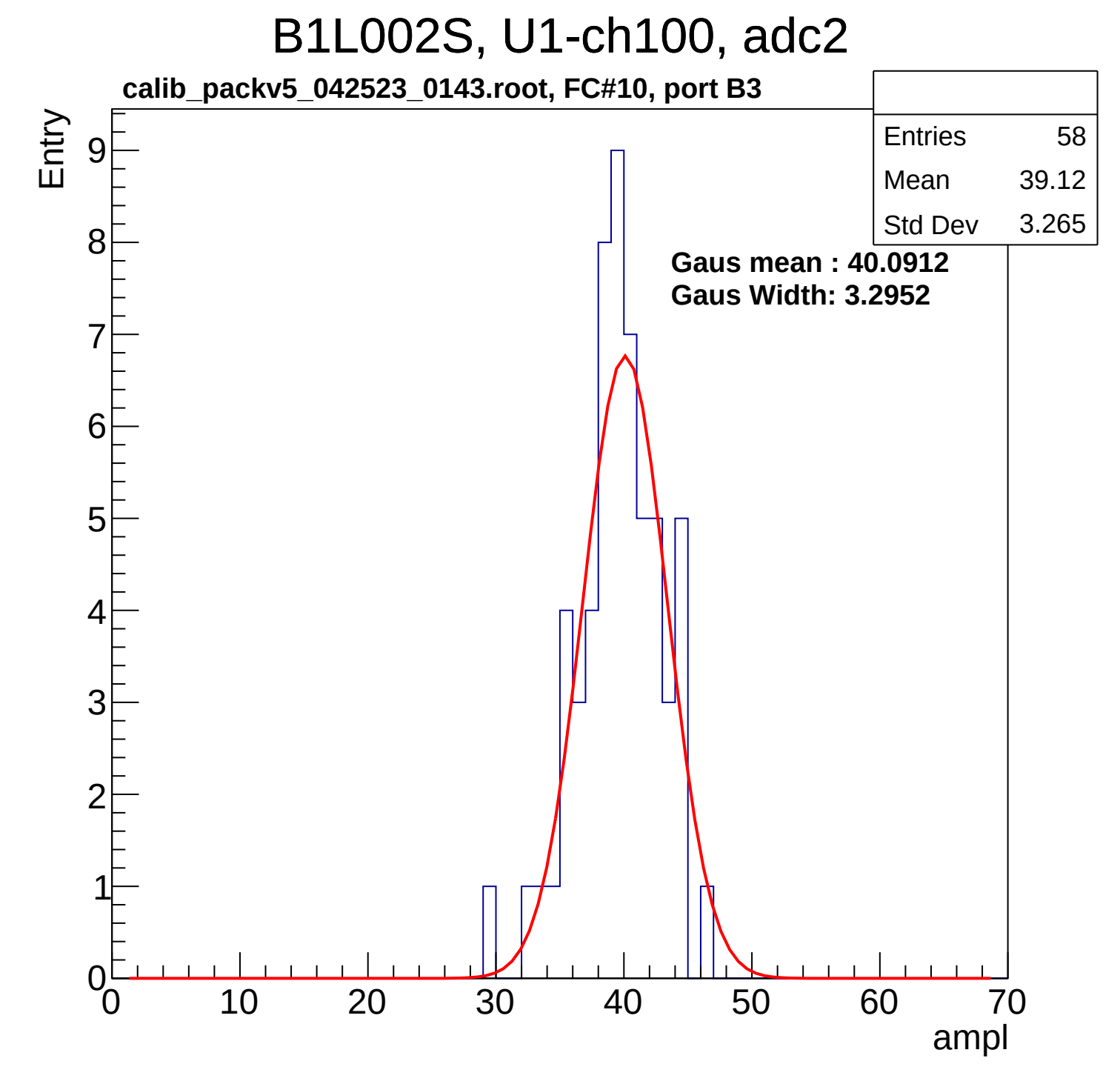
B1L002S, U1-ch100, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 39.12
Std Dev 3.265
Gaus mean : 40.0912
Gaus Width: 3.2952
Entry
ampl
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3
4
5
6
7
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0
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60
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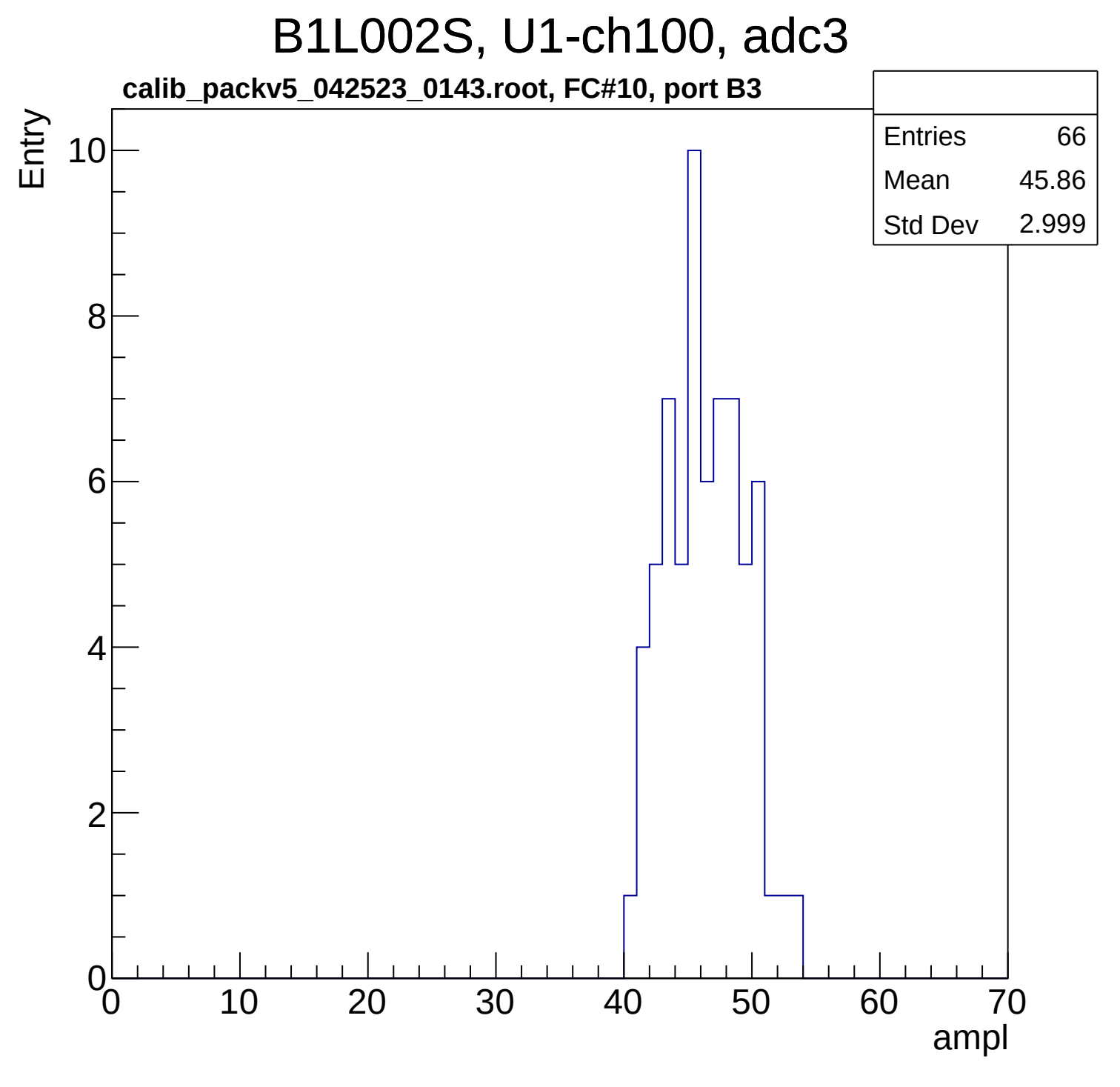
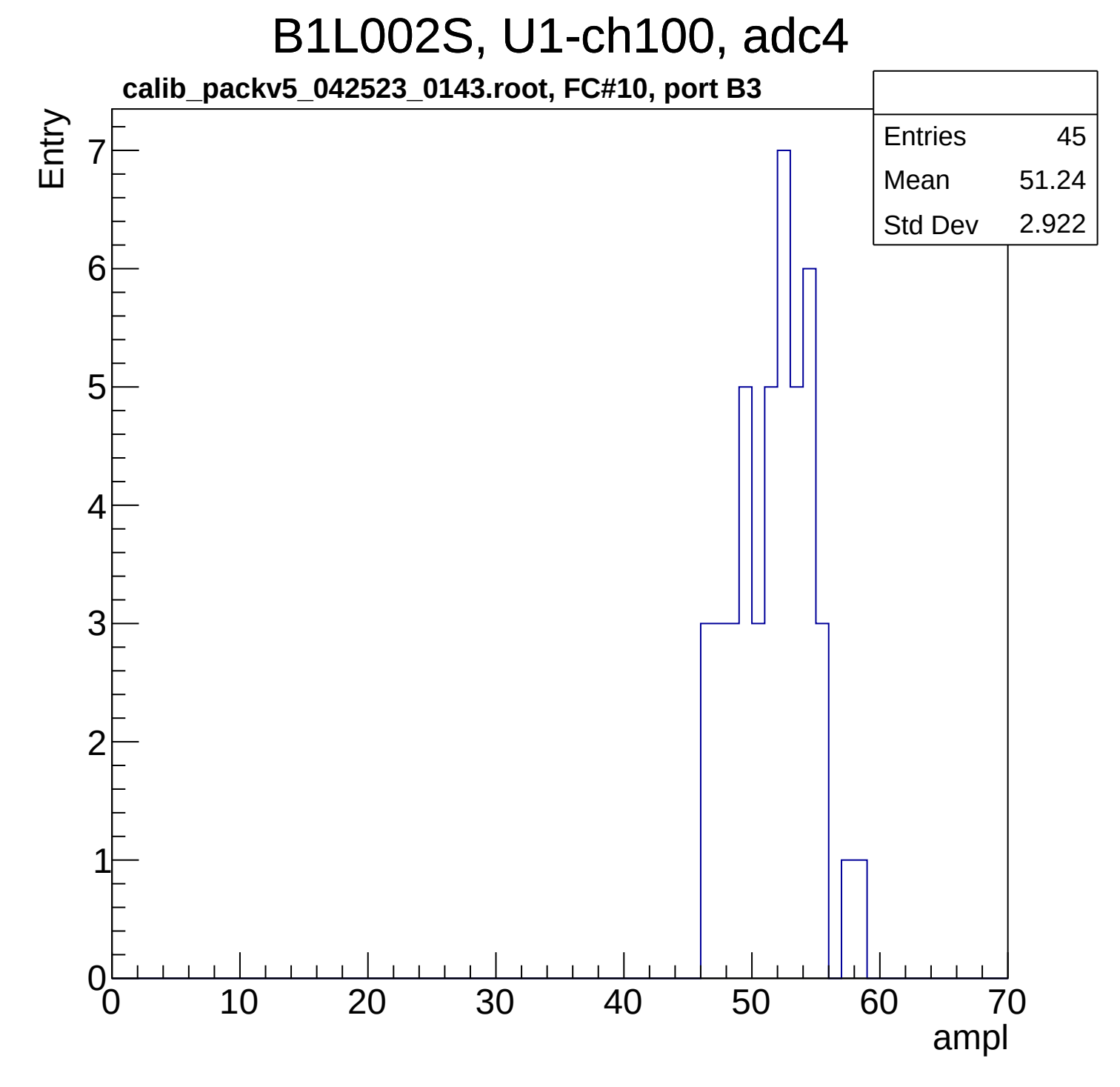

B1L002S, U1-ch100, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 45.86
Std Dev 2.999
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch100, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 51.24
Std Dev 2.922
Entry
0
1
2
3
4
5
6
7
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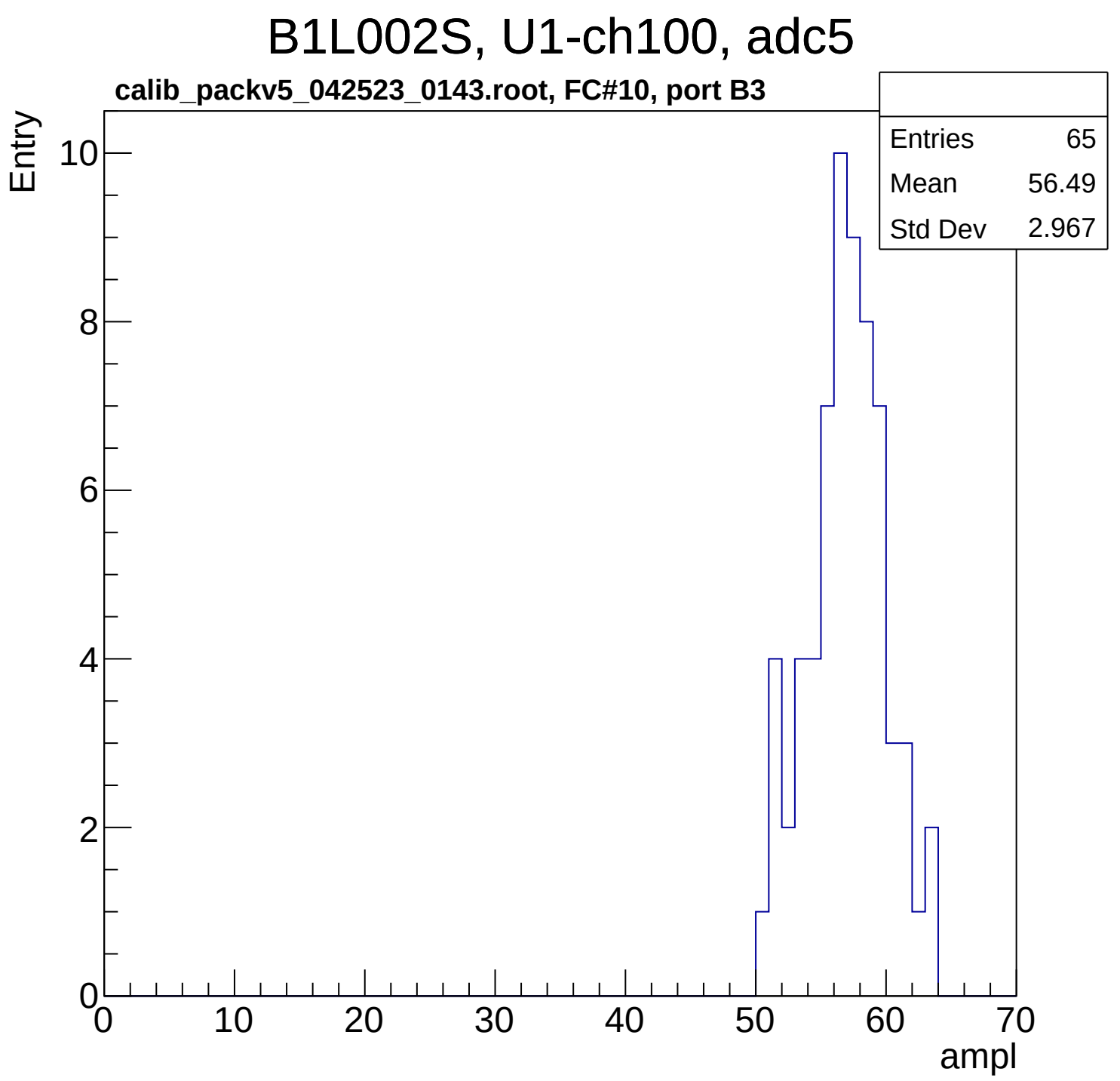
70
ampl


B1L002S, U1-ch100, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 56.49
Std Dev 2.967
Entry
0
2
4
6
8
10
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10
20
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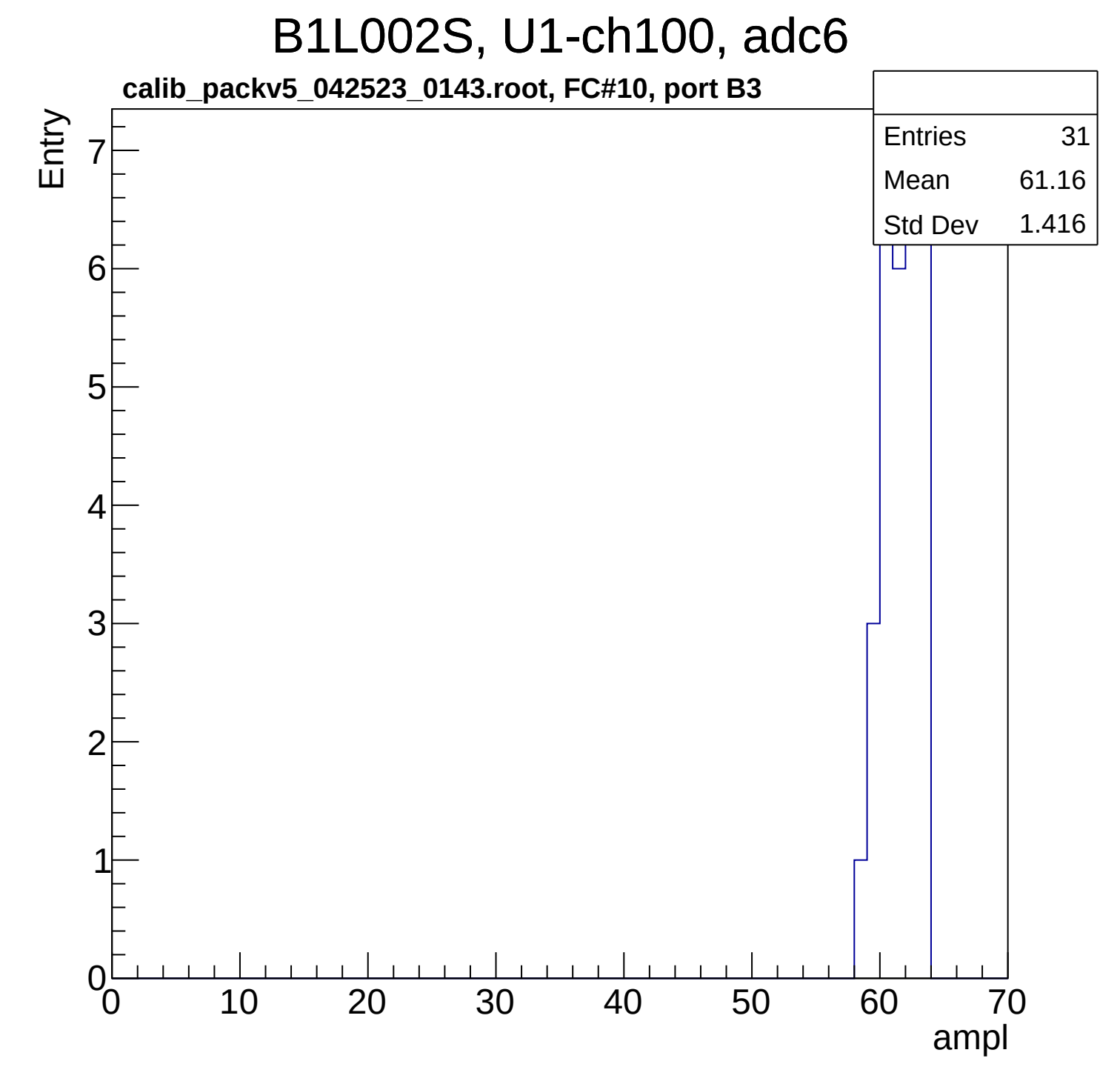
50
60
70
ampl


B1L002S, U1-ch100, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 61.16
Std Dev 1.416
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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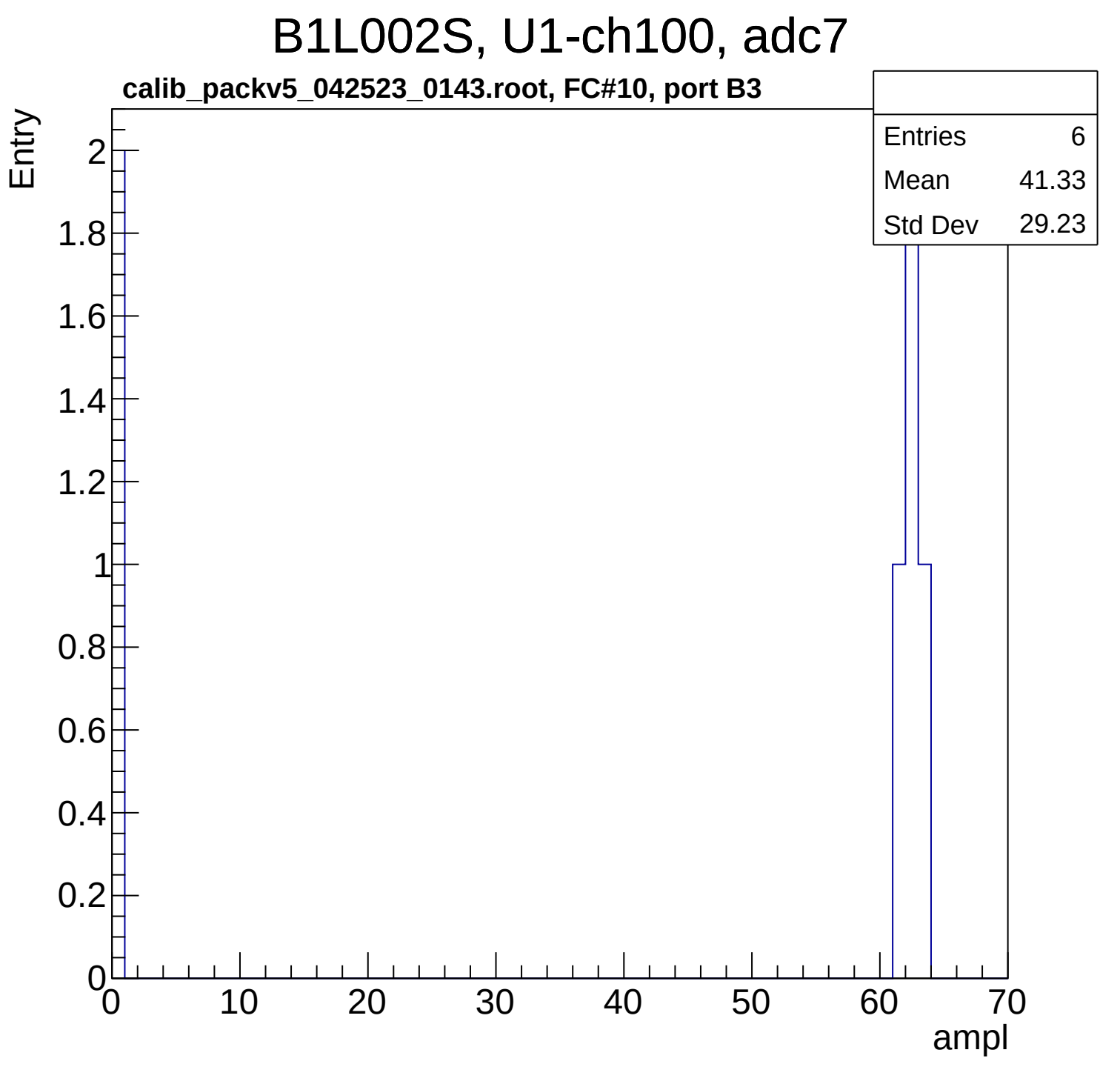
B1L002S, U1-ch100, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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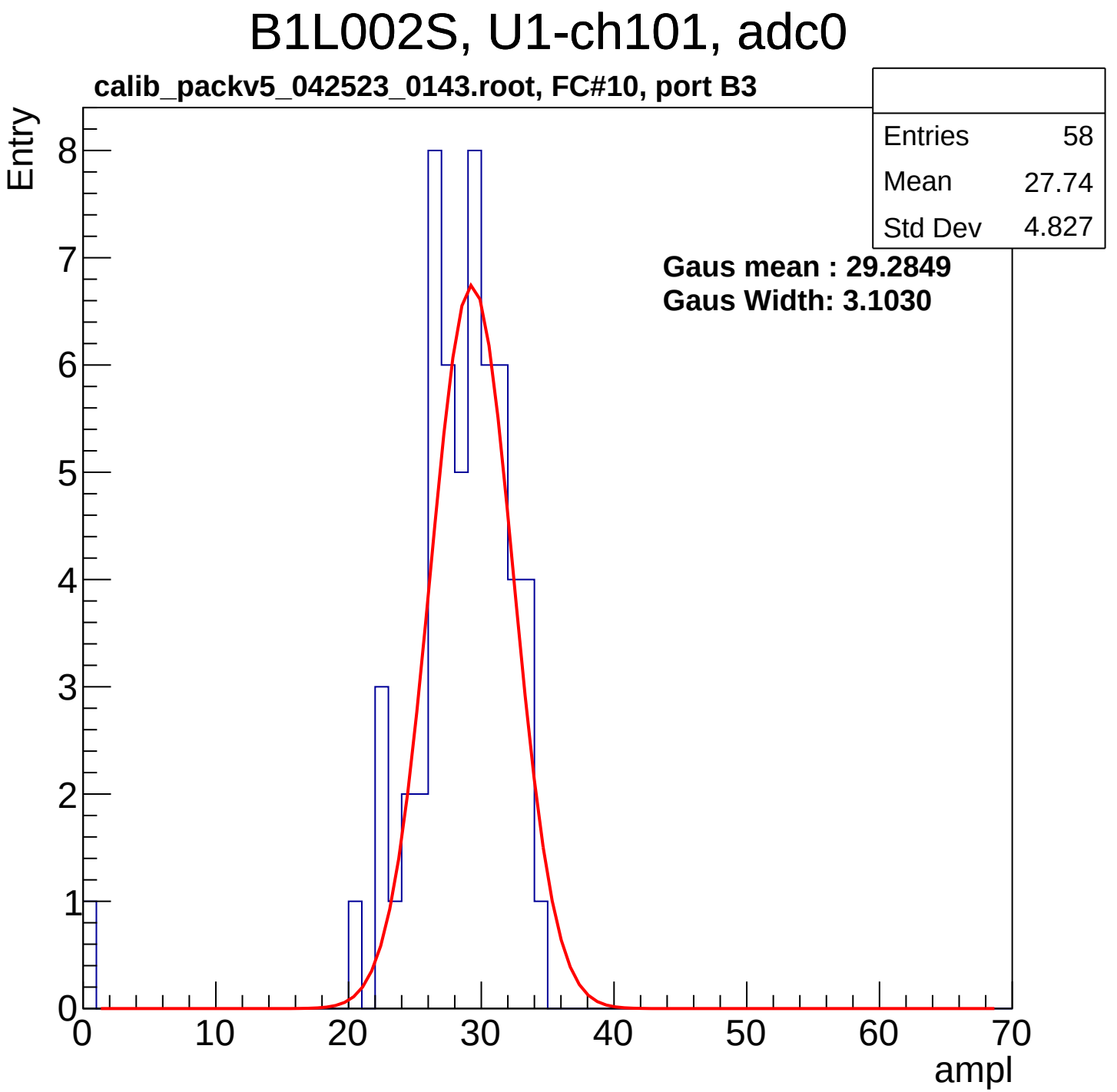
B1L002S, U1-ch101, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.74
Std Dev 4.827
Gaus mean : 29.2849
Gaus Width: 3.1030
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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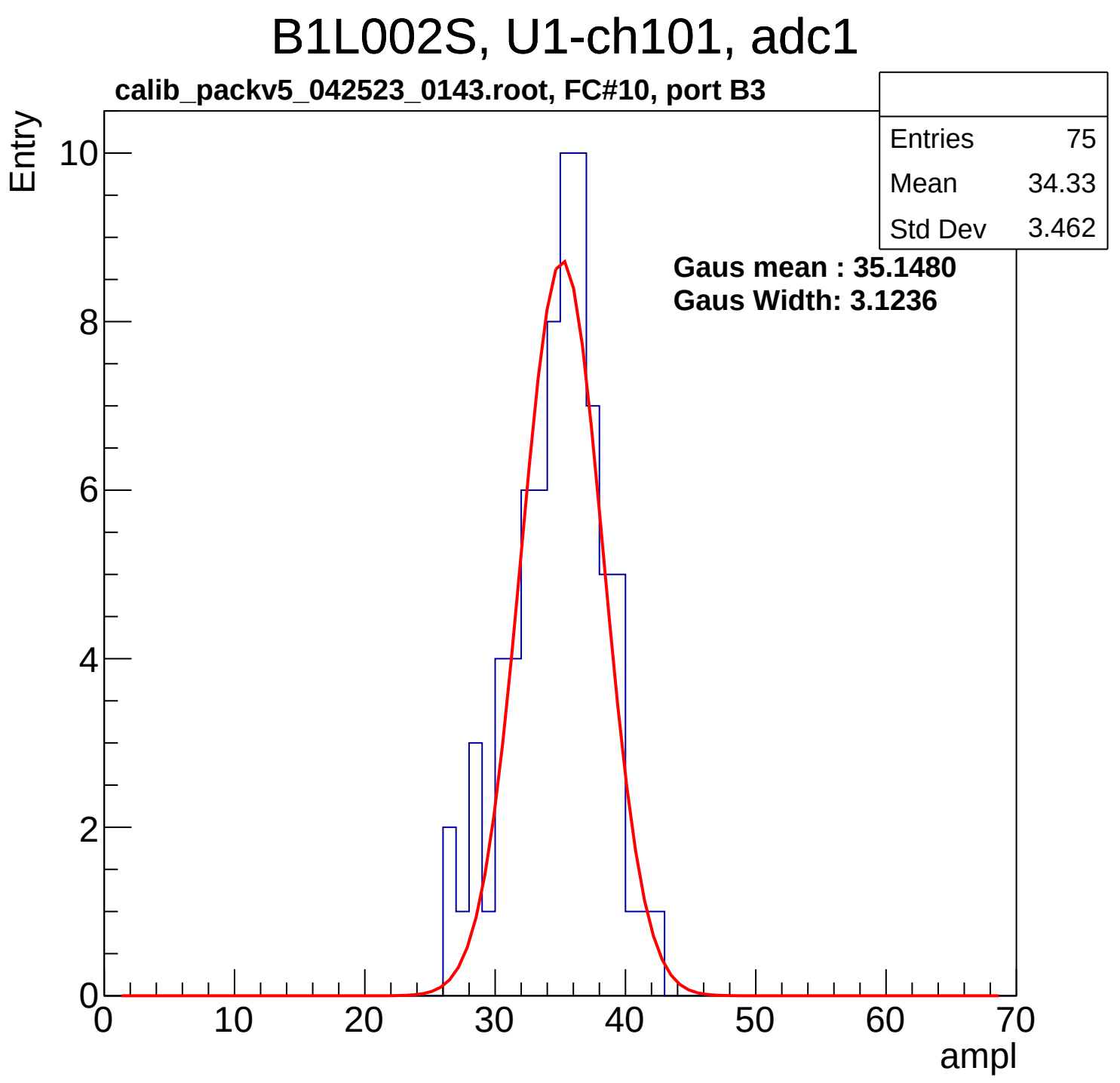
B1L002S, U1-ch101, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.33
Std Dev 3.462
Gaus mean : 35.1480
Gaus Width: 3.1236
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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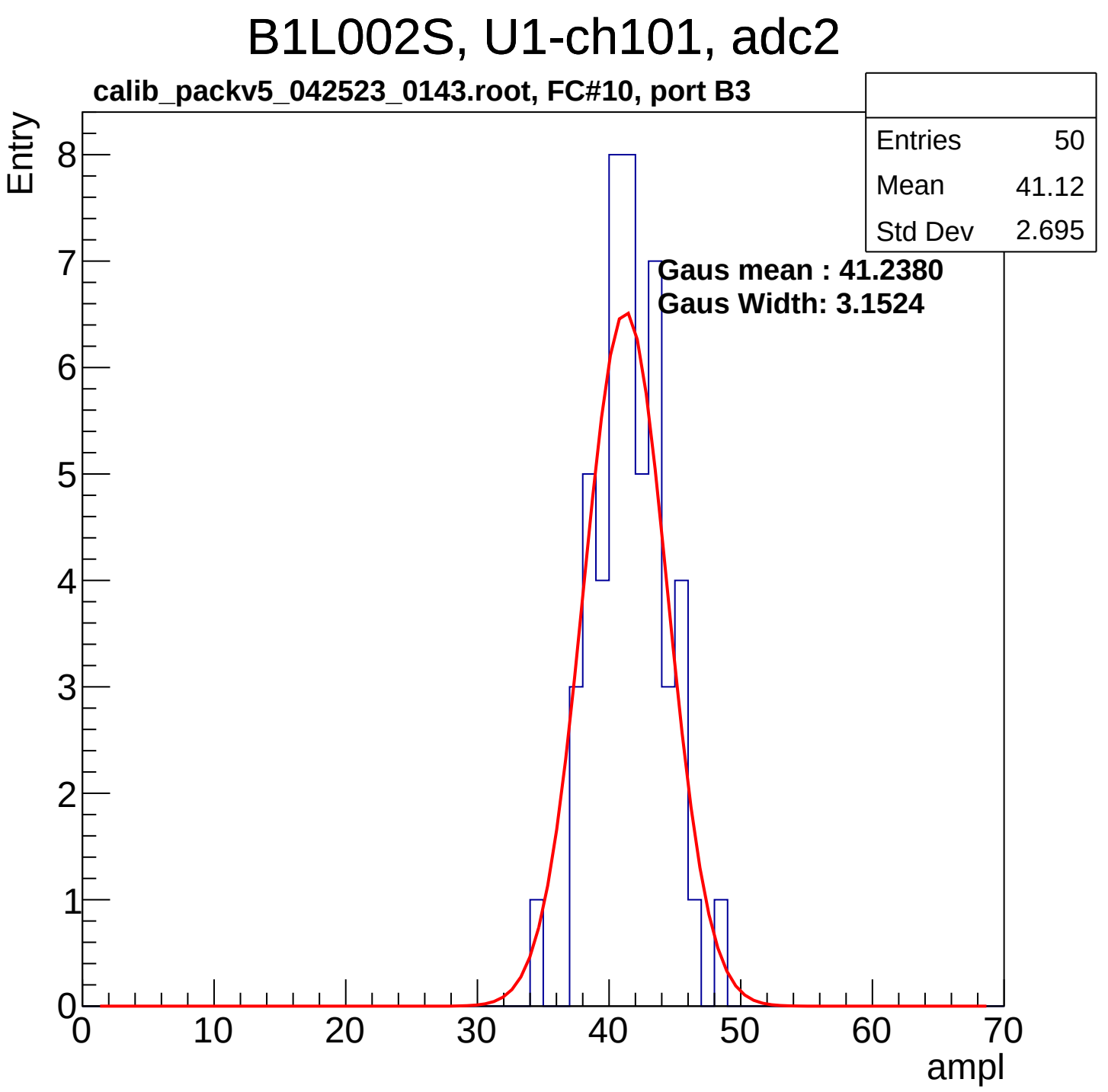
B1L002S, U1-ch101, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.12
Std Dev 2.695
Gaus mean : 41.2380
Gaus Width: 3.1524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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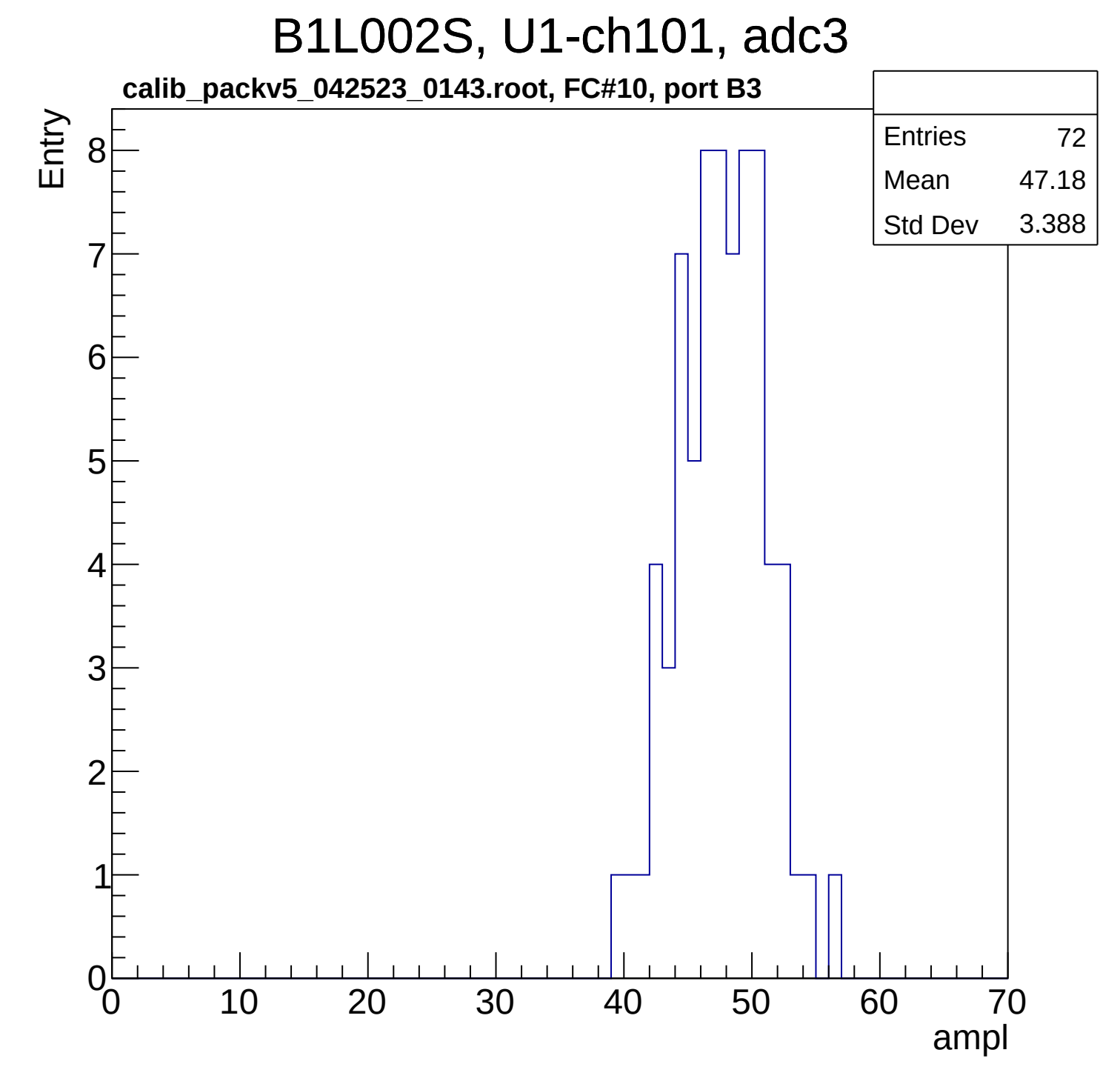
B1L002S, U1-ch101, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 47.18
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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60
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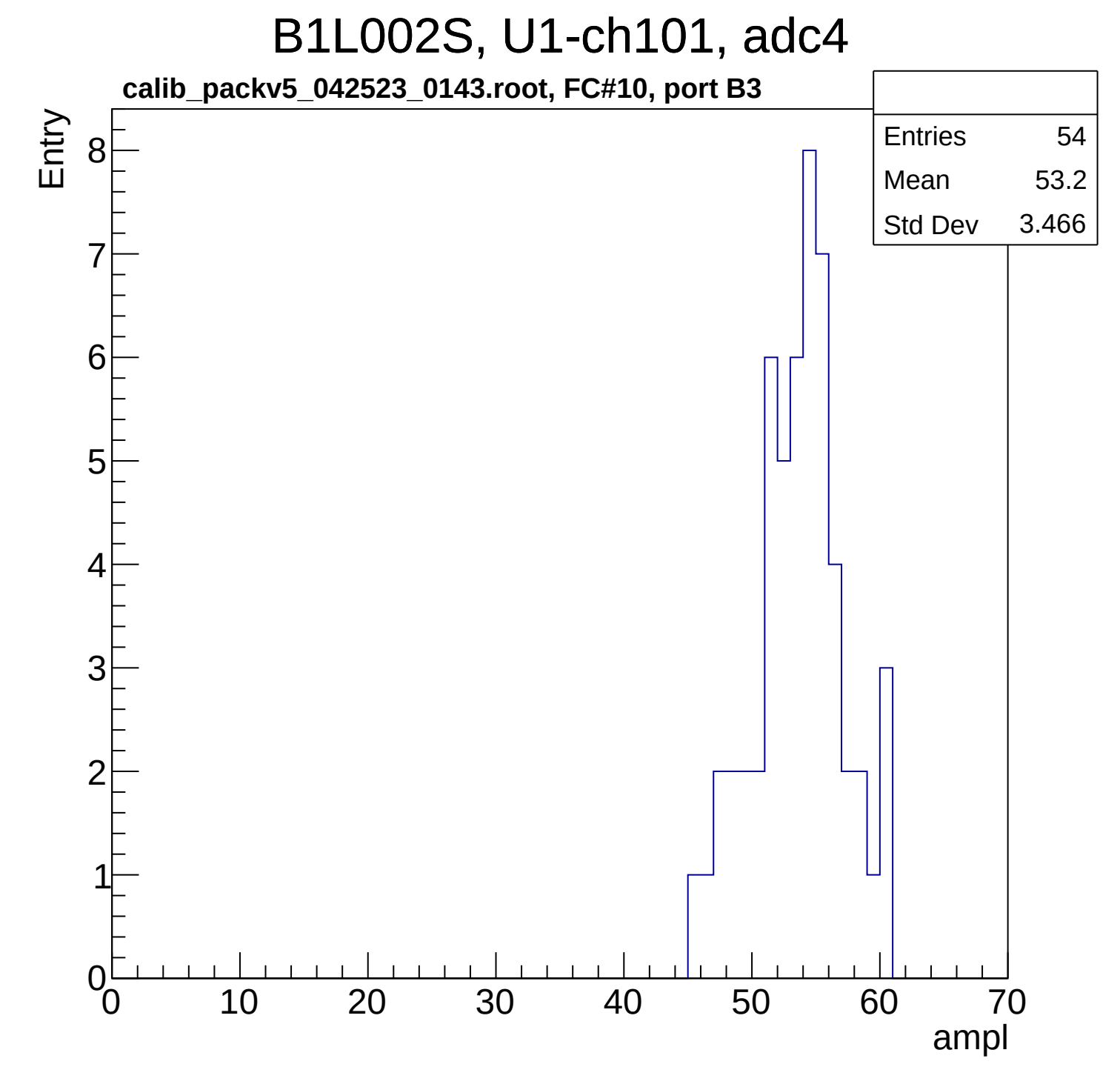
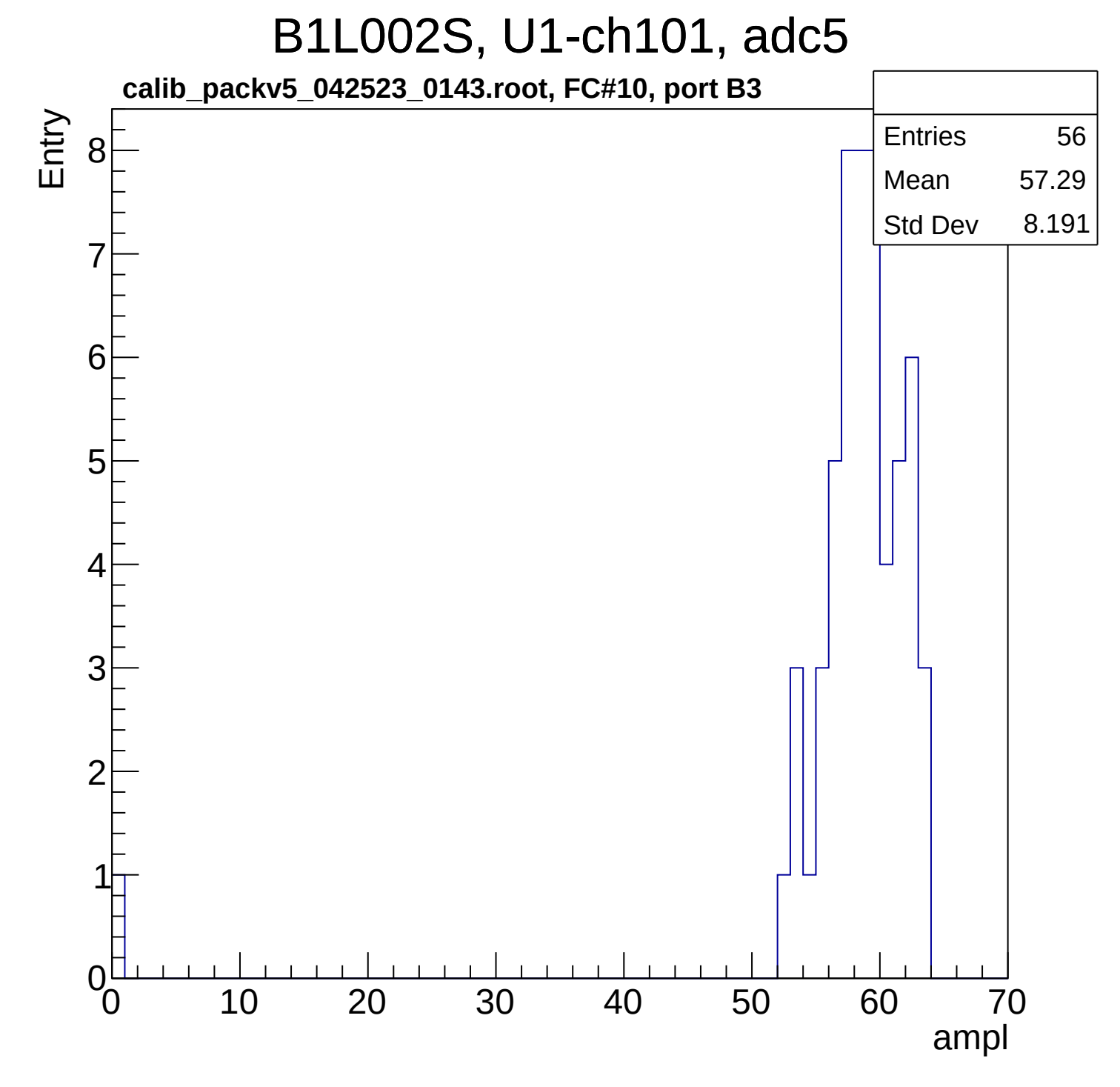

B1L002S, U1-ch101, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.2
Std Dev 3.466
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch101, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 57.29
Std Dev 8.191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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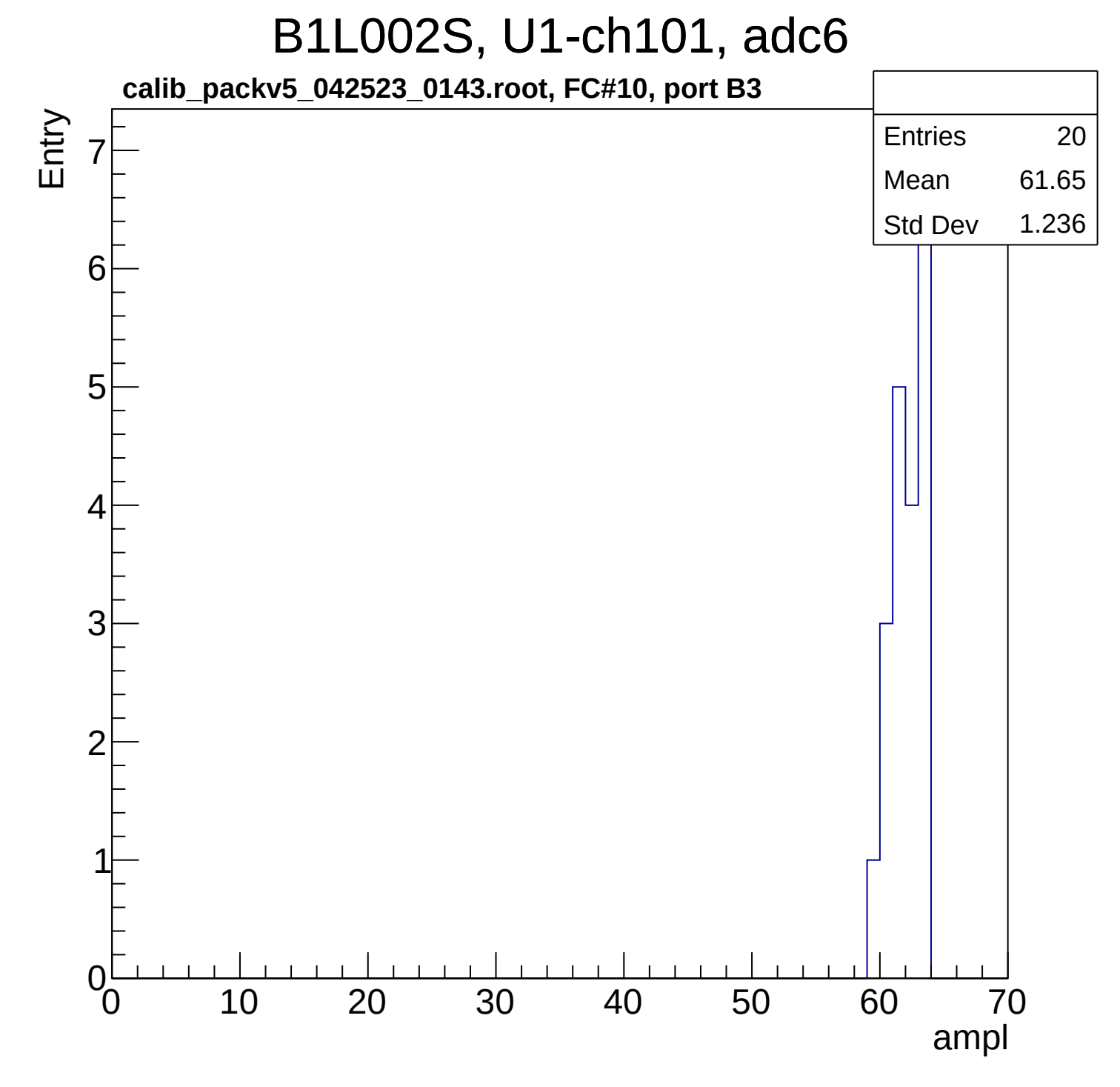
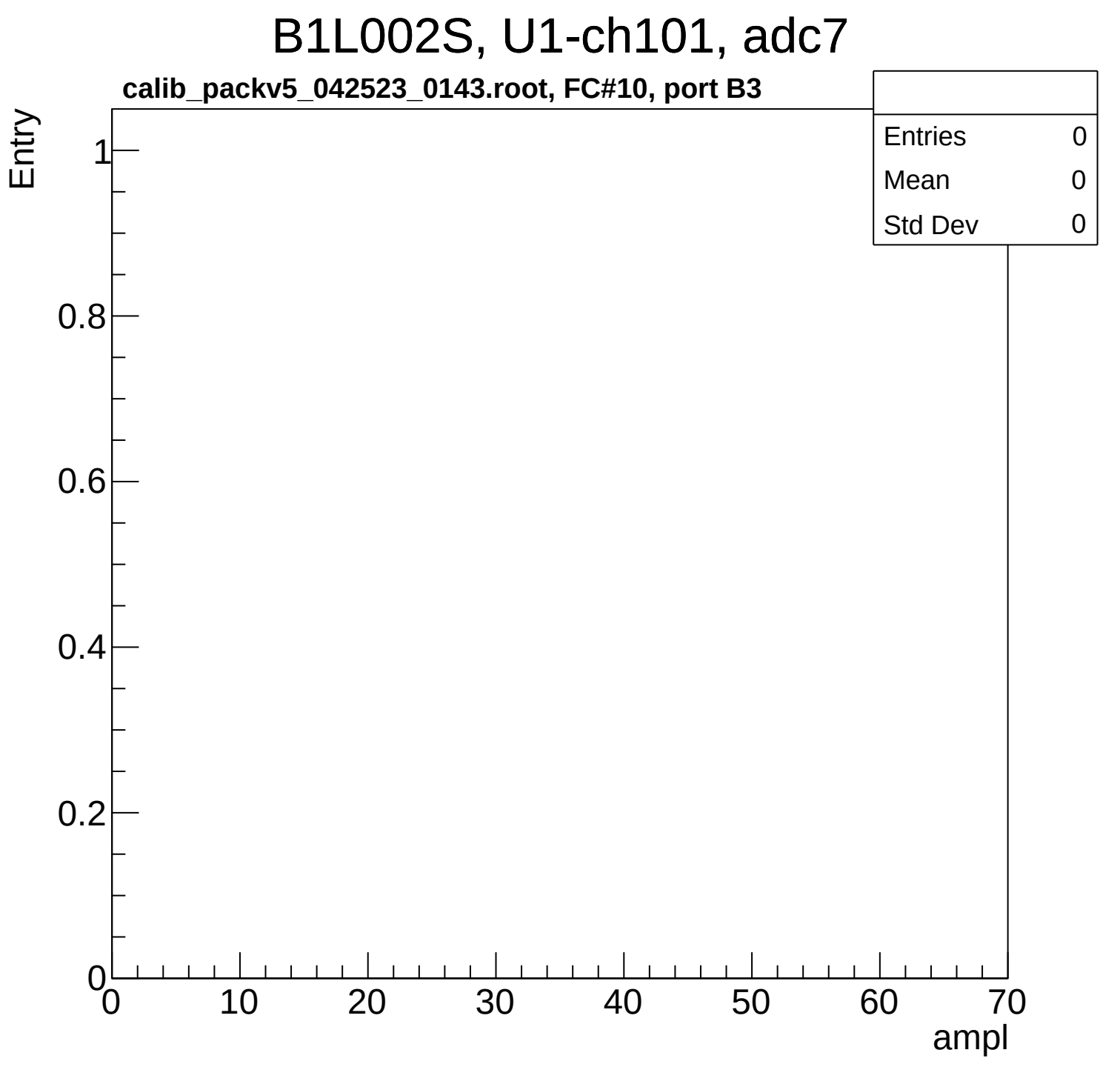

B1L002S, U1-ch101, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61.65
Std Dev 1.236
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch101, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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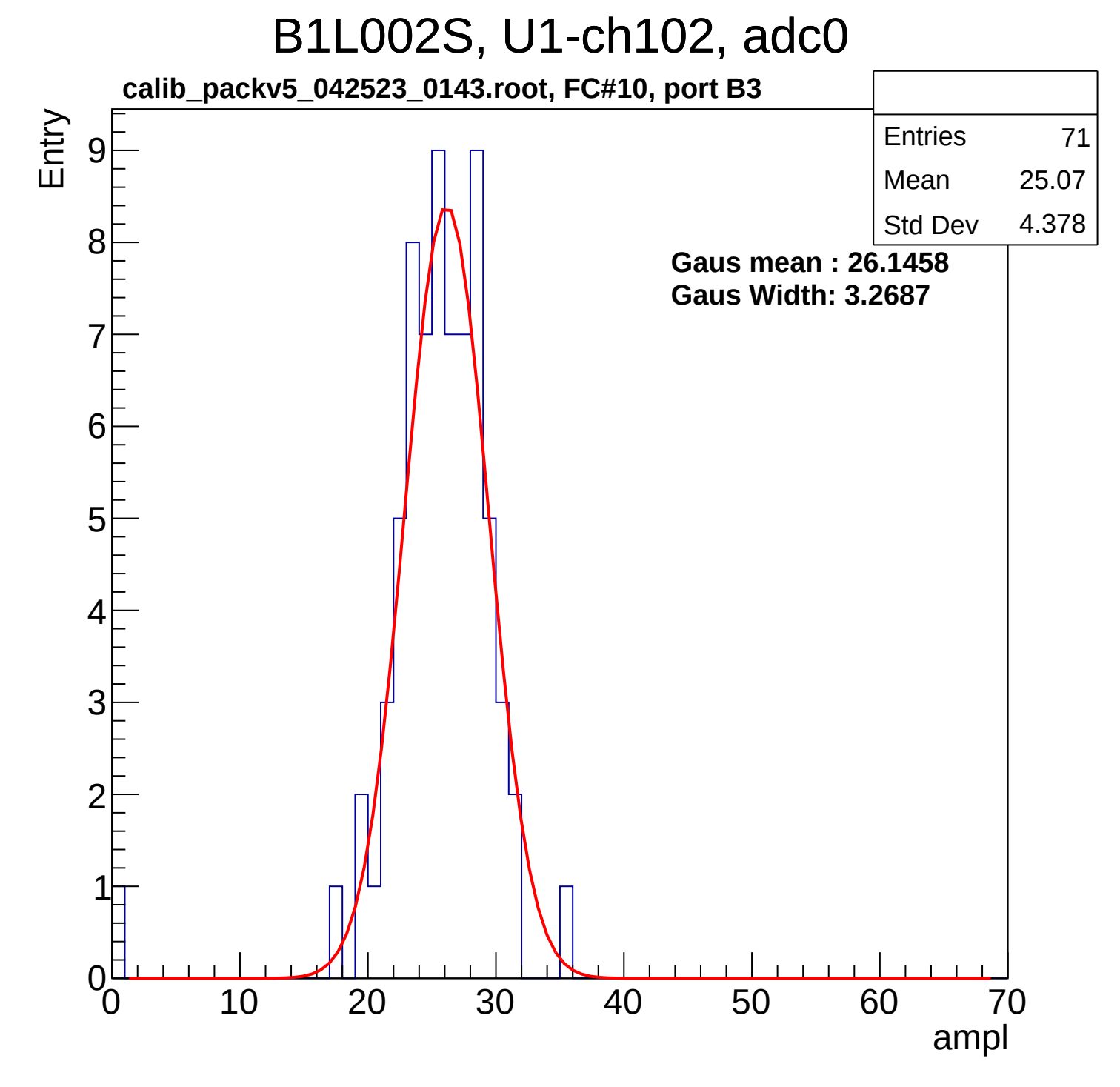
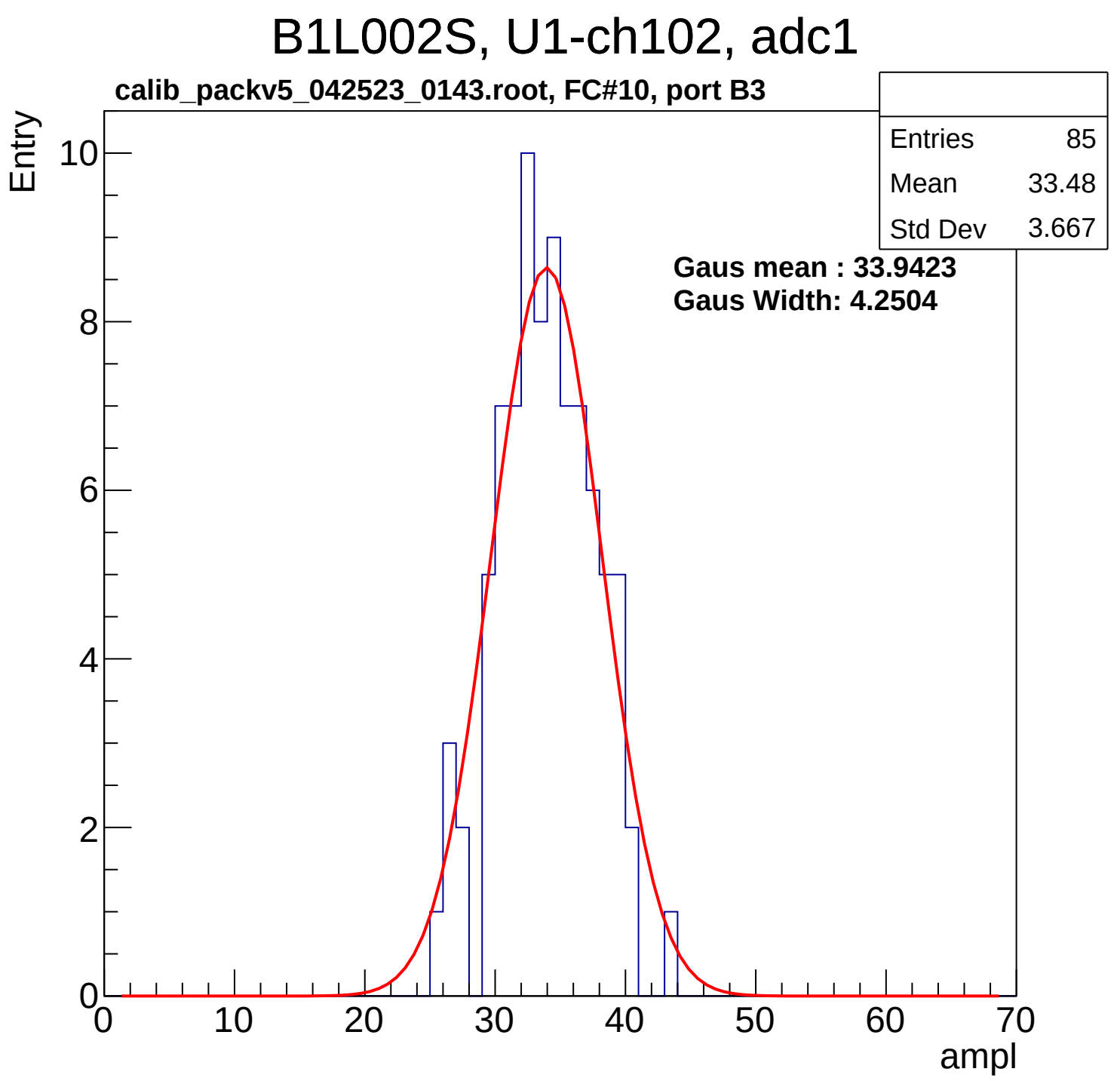
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch102, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 25.07
Std Dev 4.378
Gaus mean : 26.1458
Gaus Width: 3.2687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch102, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 33.48
Std Dev 3.667
Gaus mean : 33.9423
Gaus Width: 4.2504
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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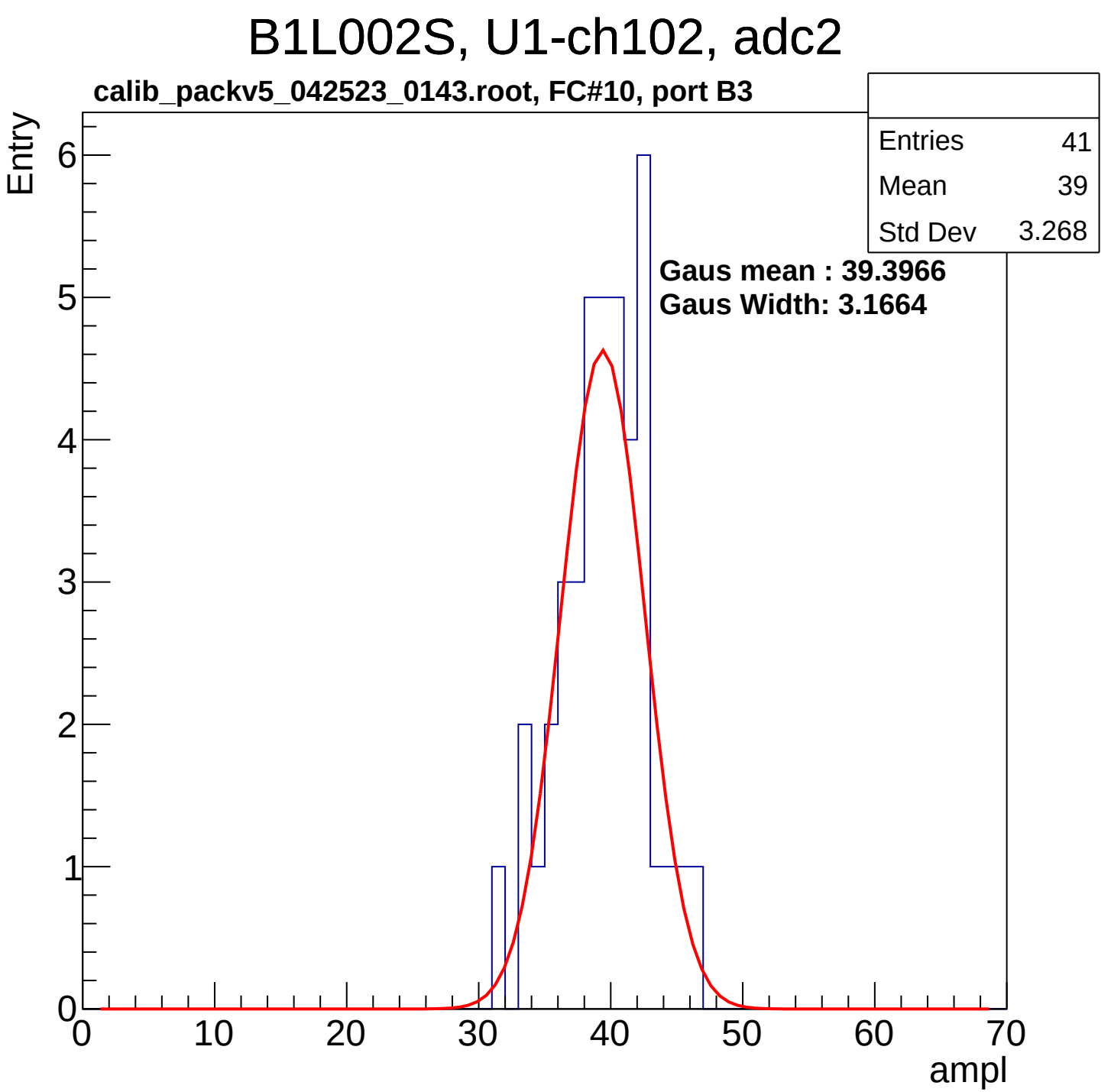
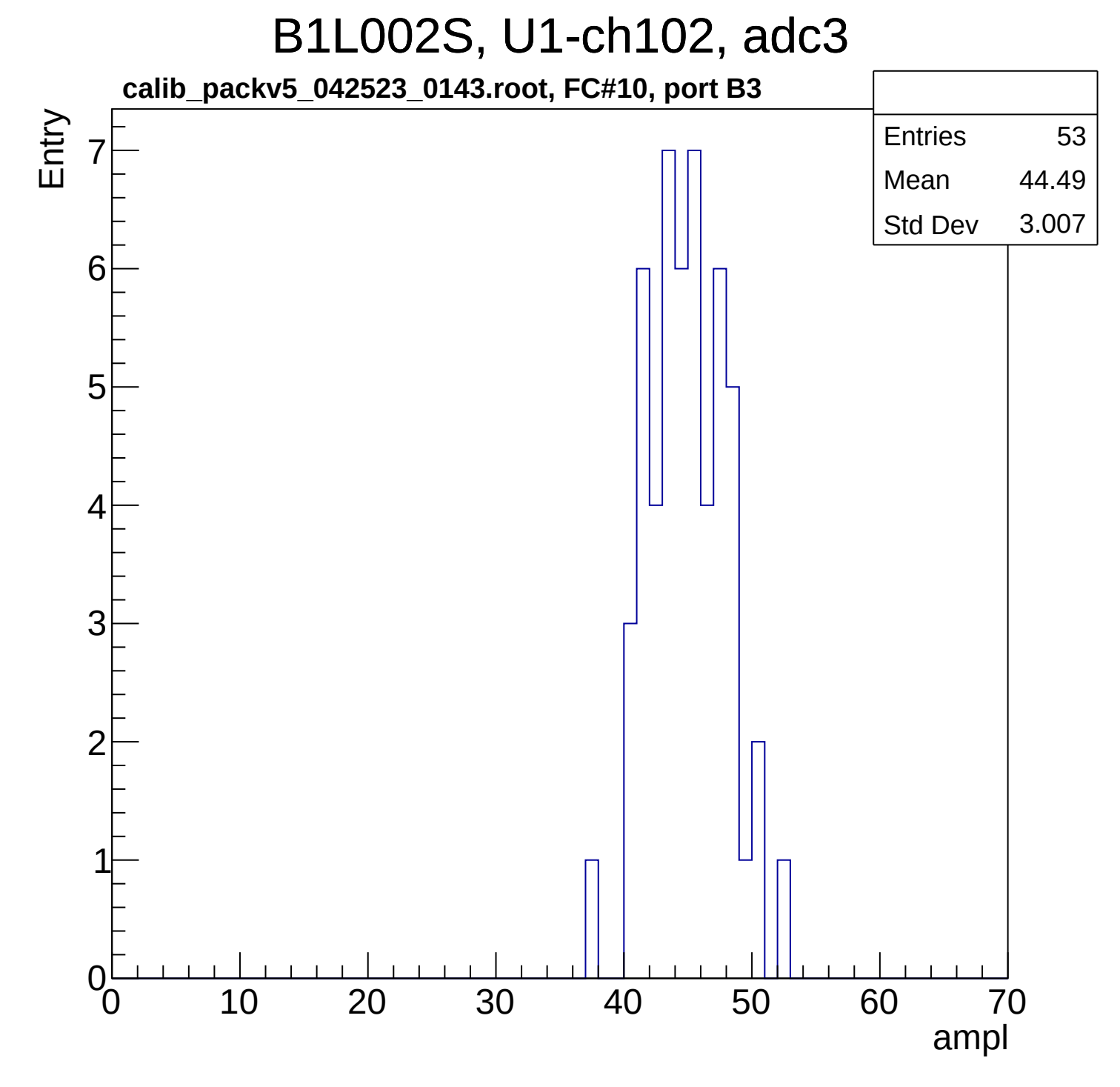

B1L002S, U1-ch102, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 39
Std Dev 3.268
Gaus mean : 39.3966
Gaus Width: 3.1664
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U1-ch102, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 44.49
Std Dev 3.007
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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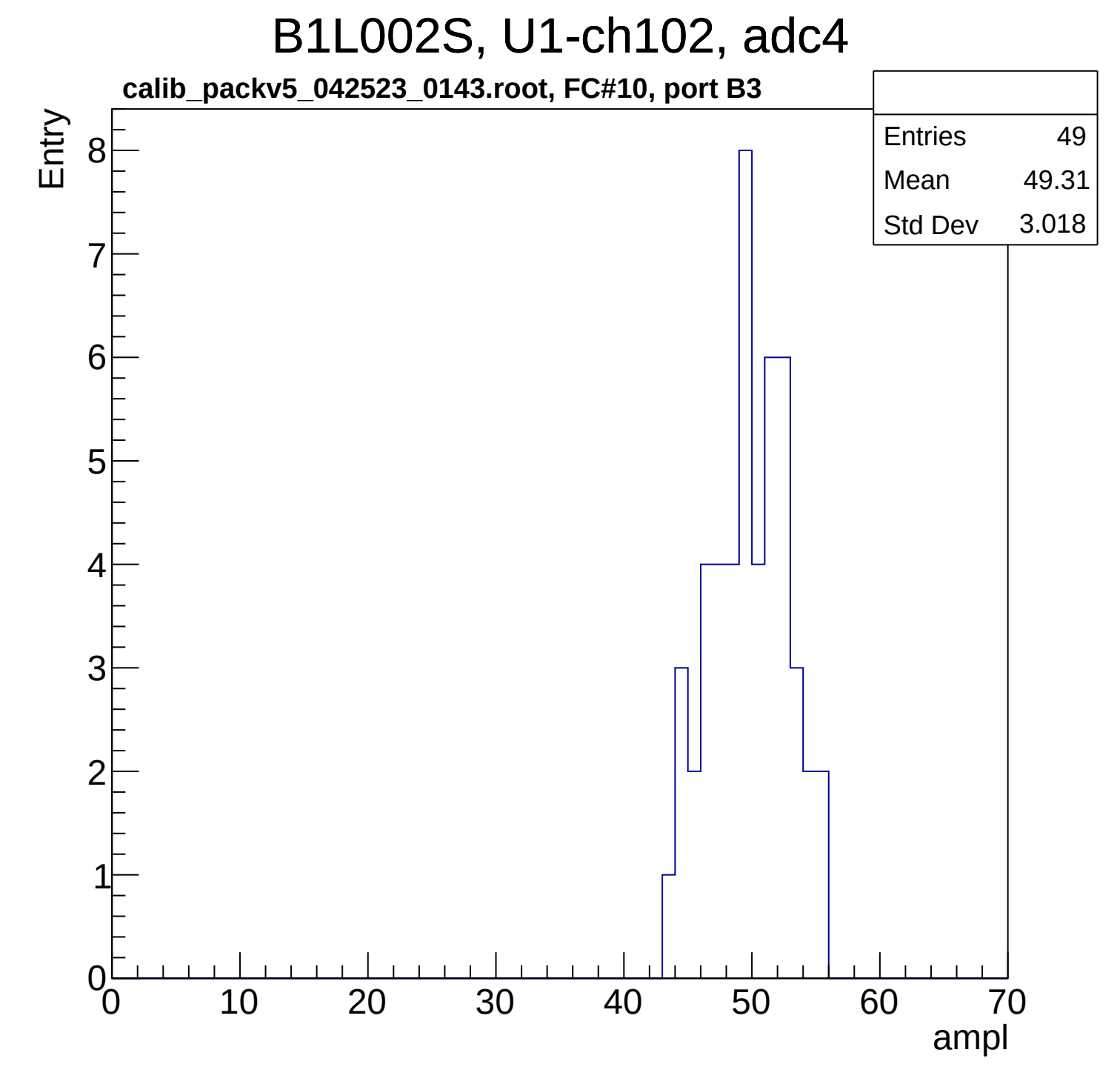
40
50
60
70


B1L002S, U1-ch102, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 49.31
Std Dev 3.018
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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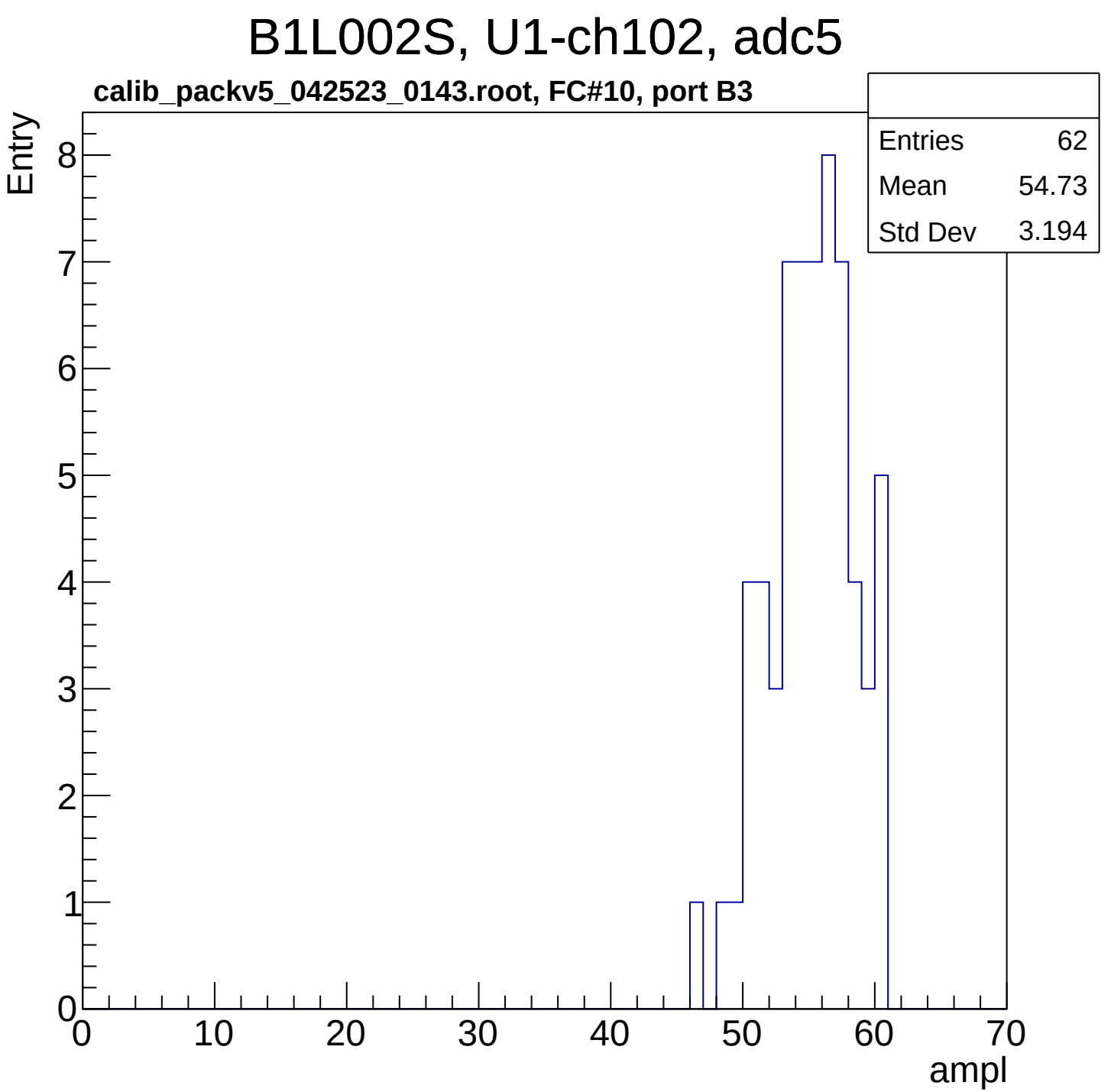
B1L002S, U1-ch102, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 54.73
Std Dev 3.194
Entry
ampl
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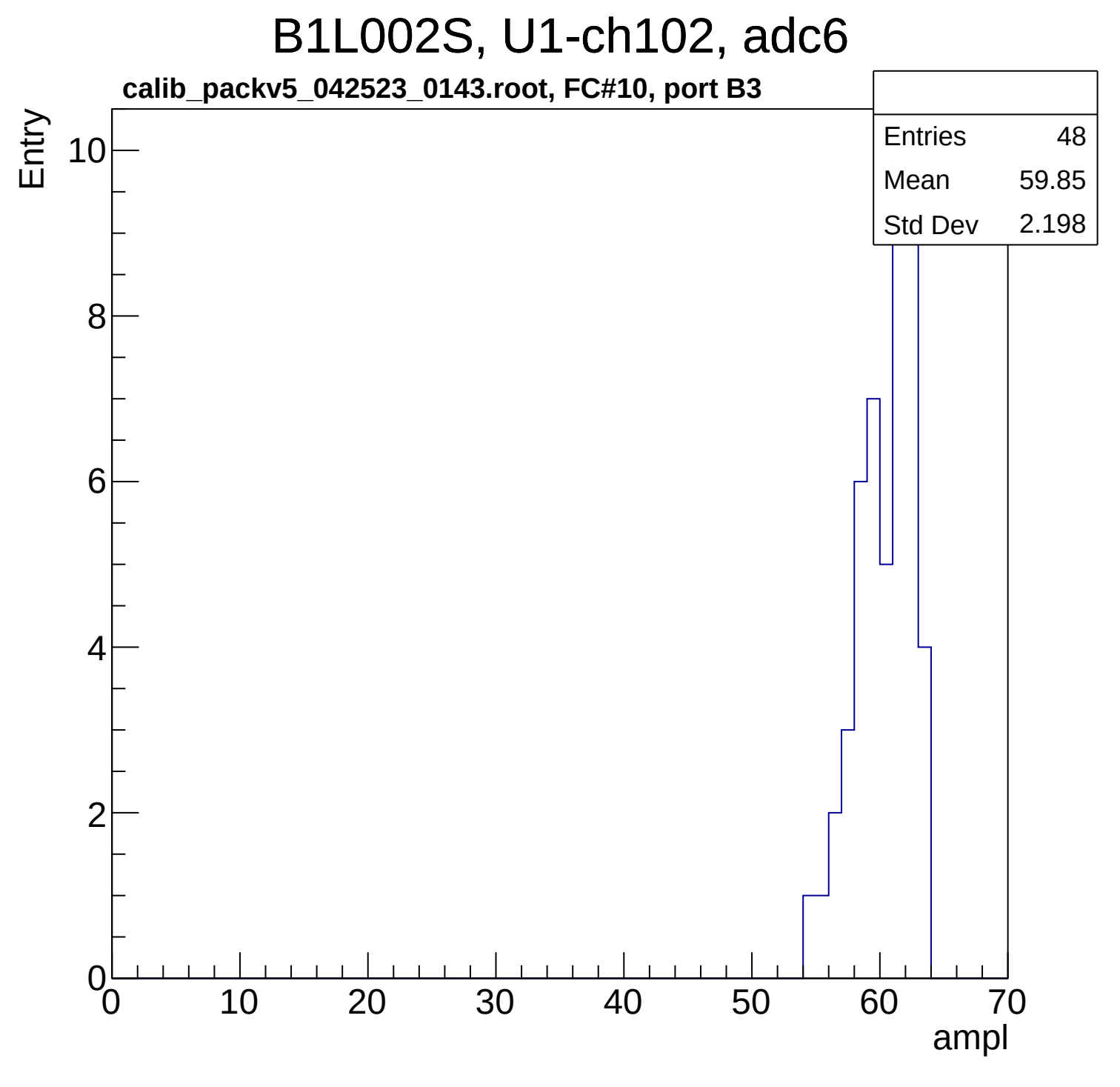
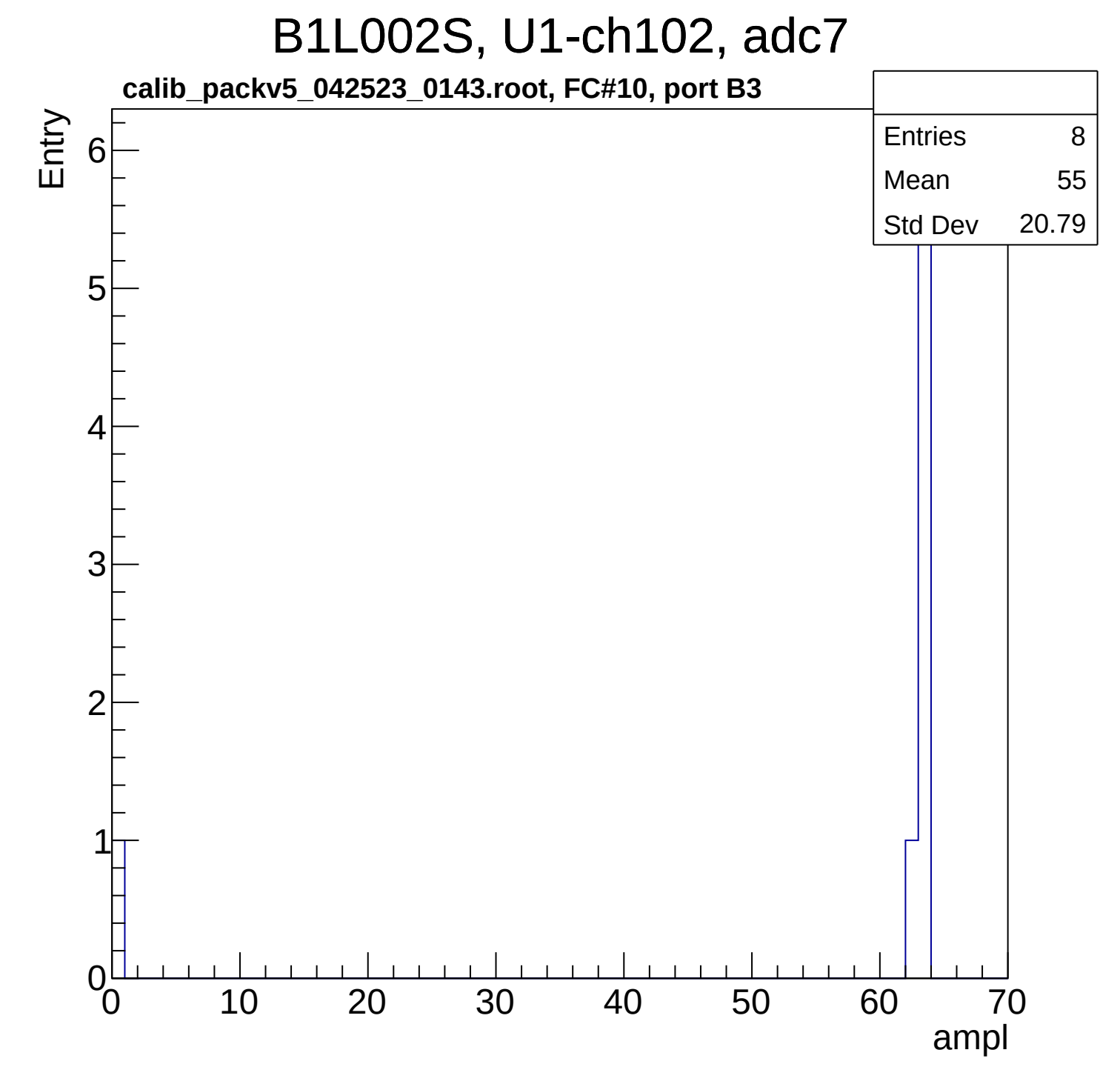
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch102, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 59.85
Std Dev 2.198
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch102, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 55
Std Dev 20.79
Entry
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2
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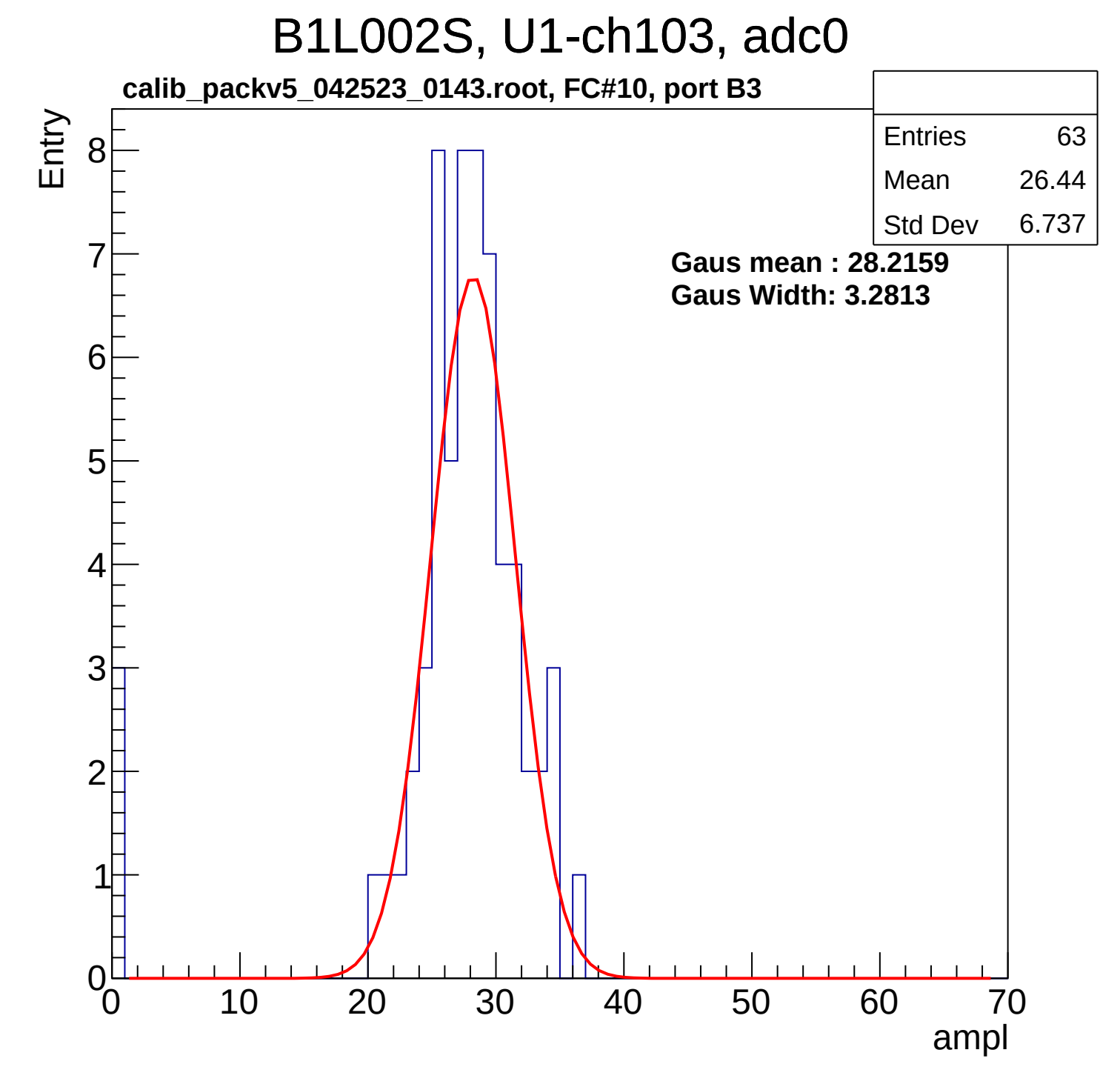
70
ampl


B1L002S, U1-ch103, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 26.44
Std Dev 6.737
Gaus mean : 28.2159
Gaus Width: 3.2813
Entry
ampl
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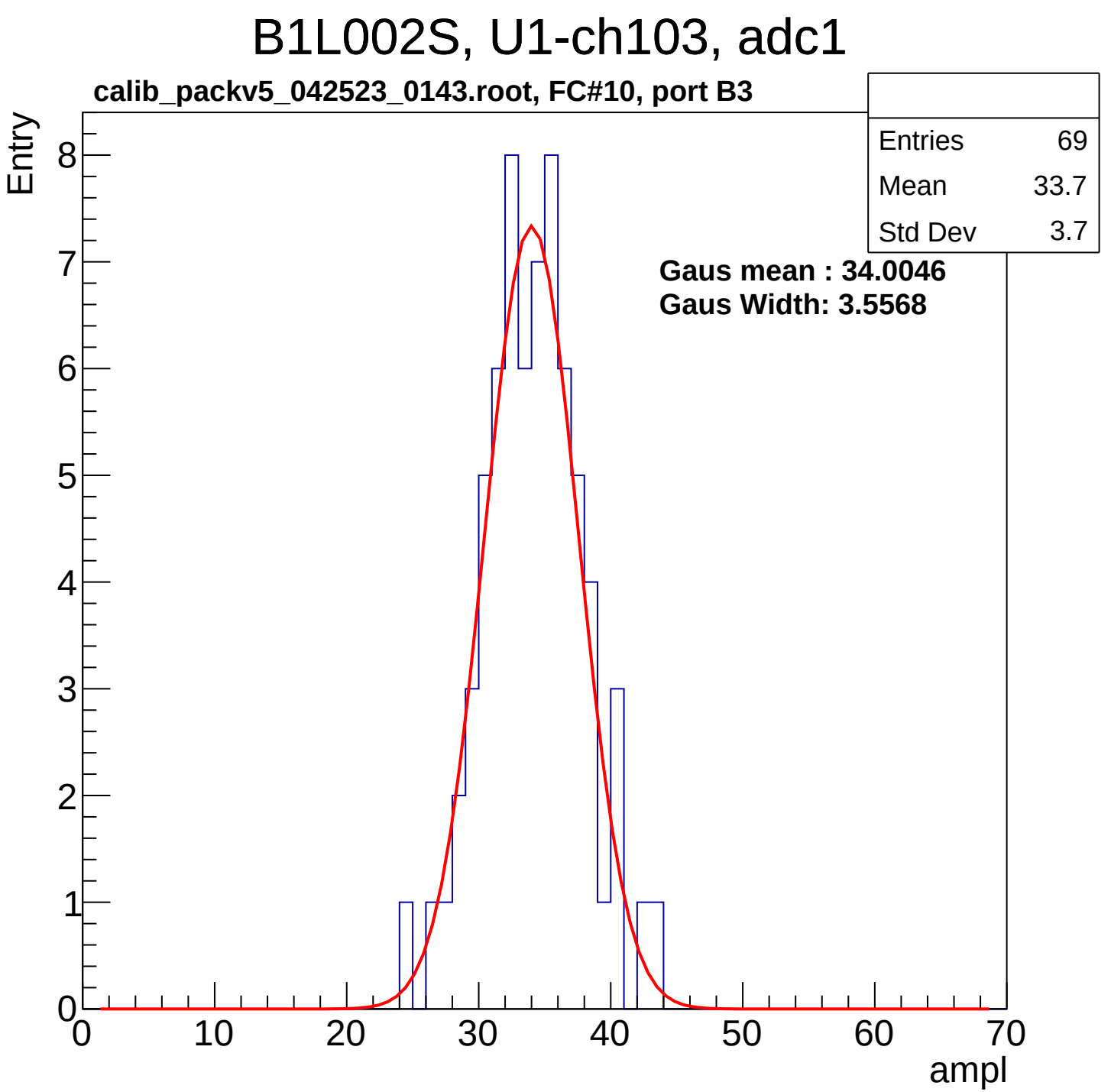
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch103, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 33.7
Std Dev 3.7
Gaus mean : 34.0046
Gaus Width: 3.5568
Entry
ampl
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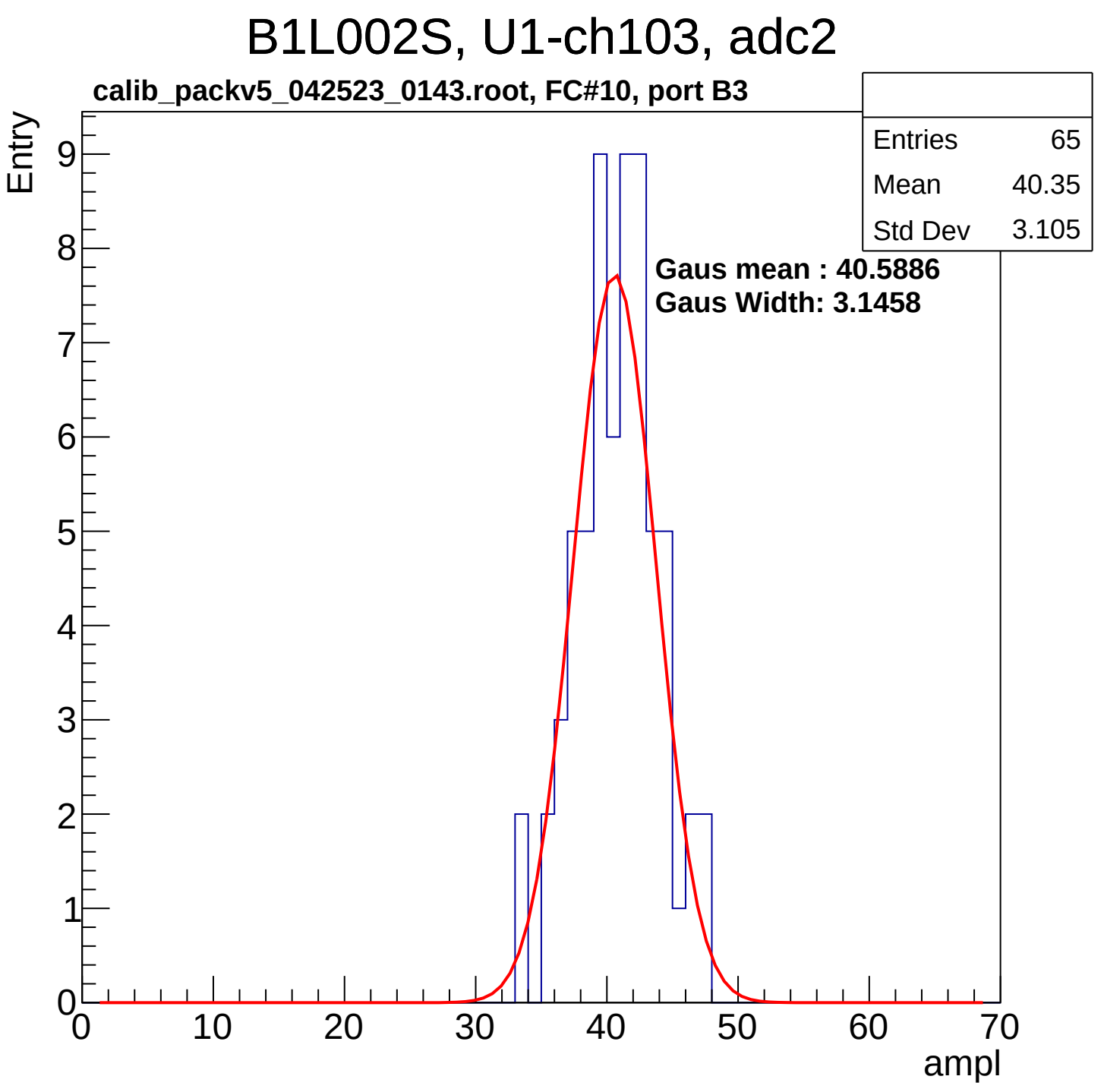
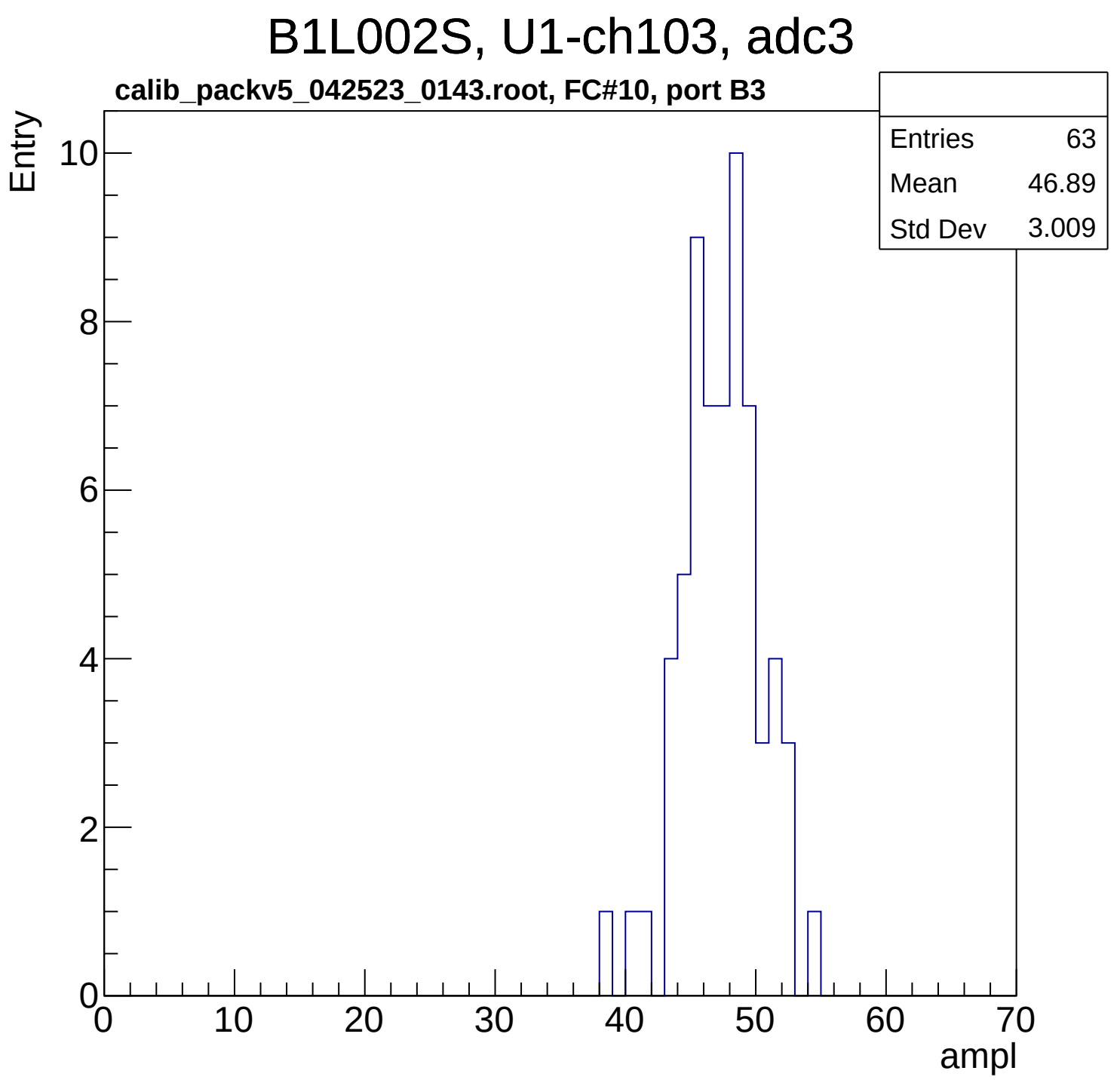
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch103, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.35
Std Dev 3.105
Gaus mean : 40.5886
Gaus Width: 3.1458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch103, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.89
Std Dev 3.009
Entry
ampl
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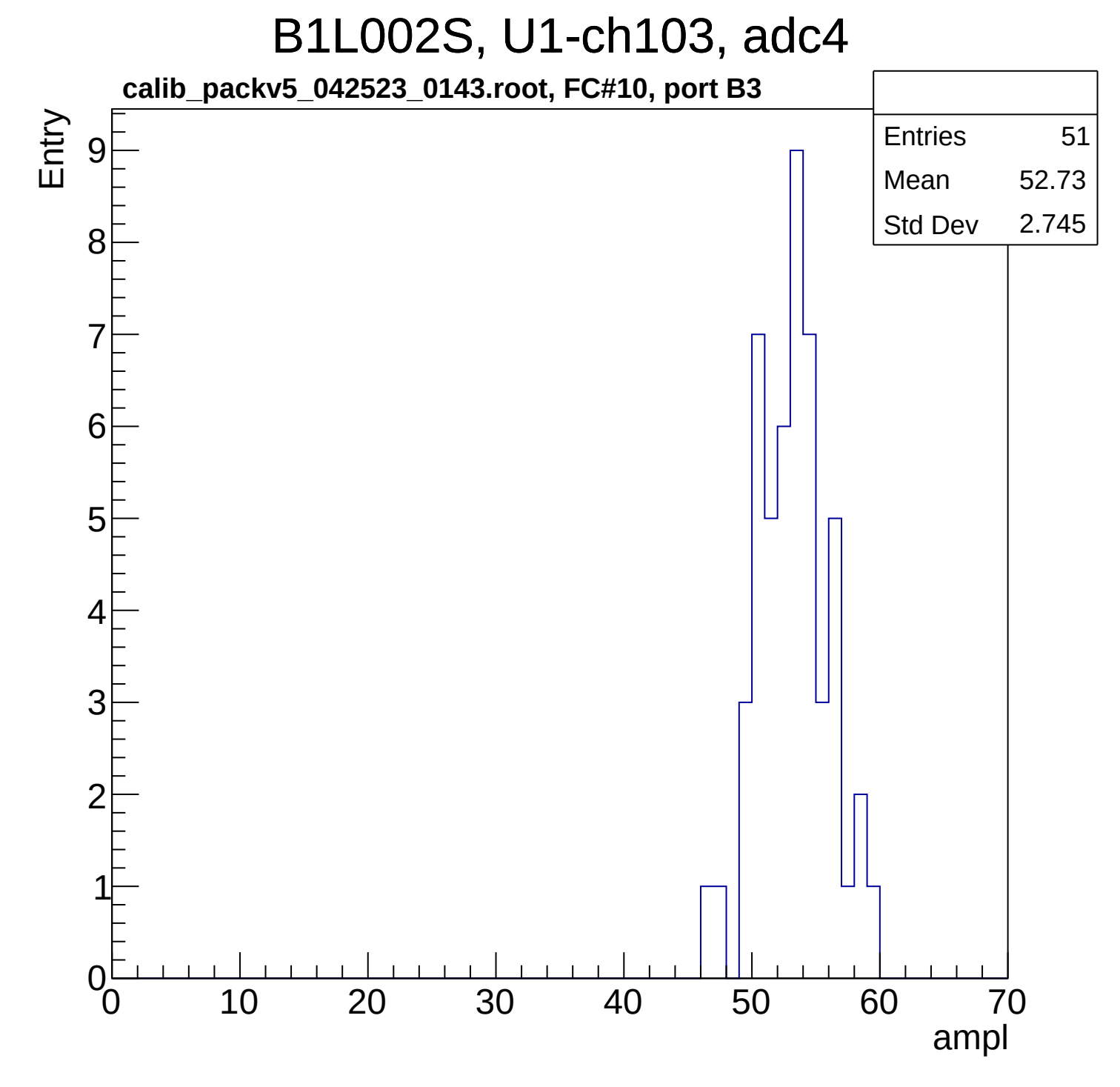
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch103, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 52.73
Std Dev 2.745
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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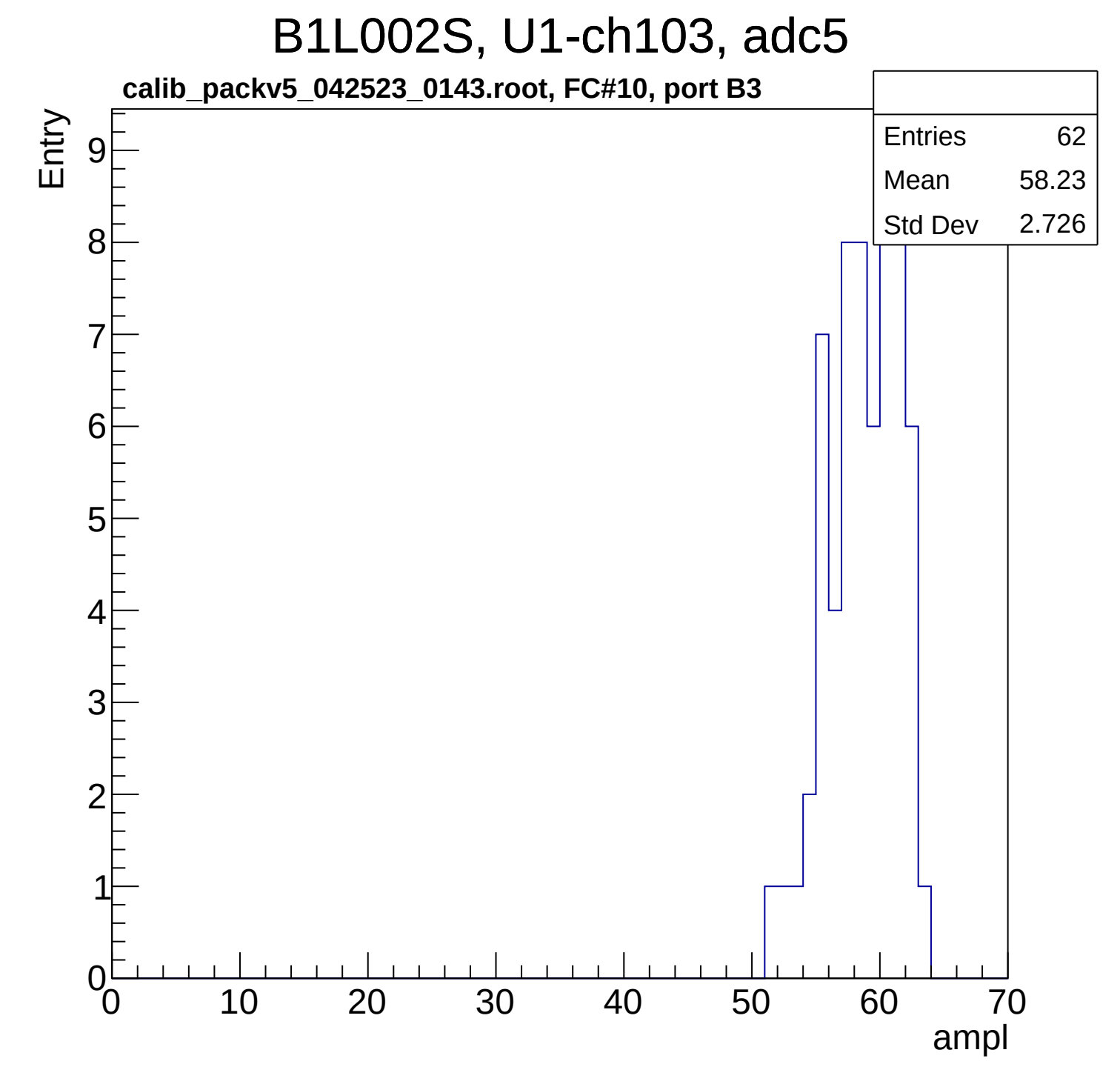
B1L002S, U1-ch103, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.23
Std Dev 2.726
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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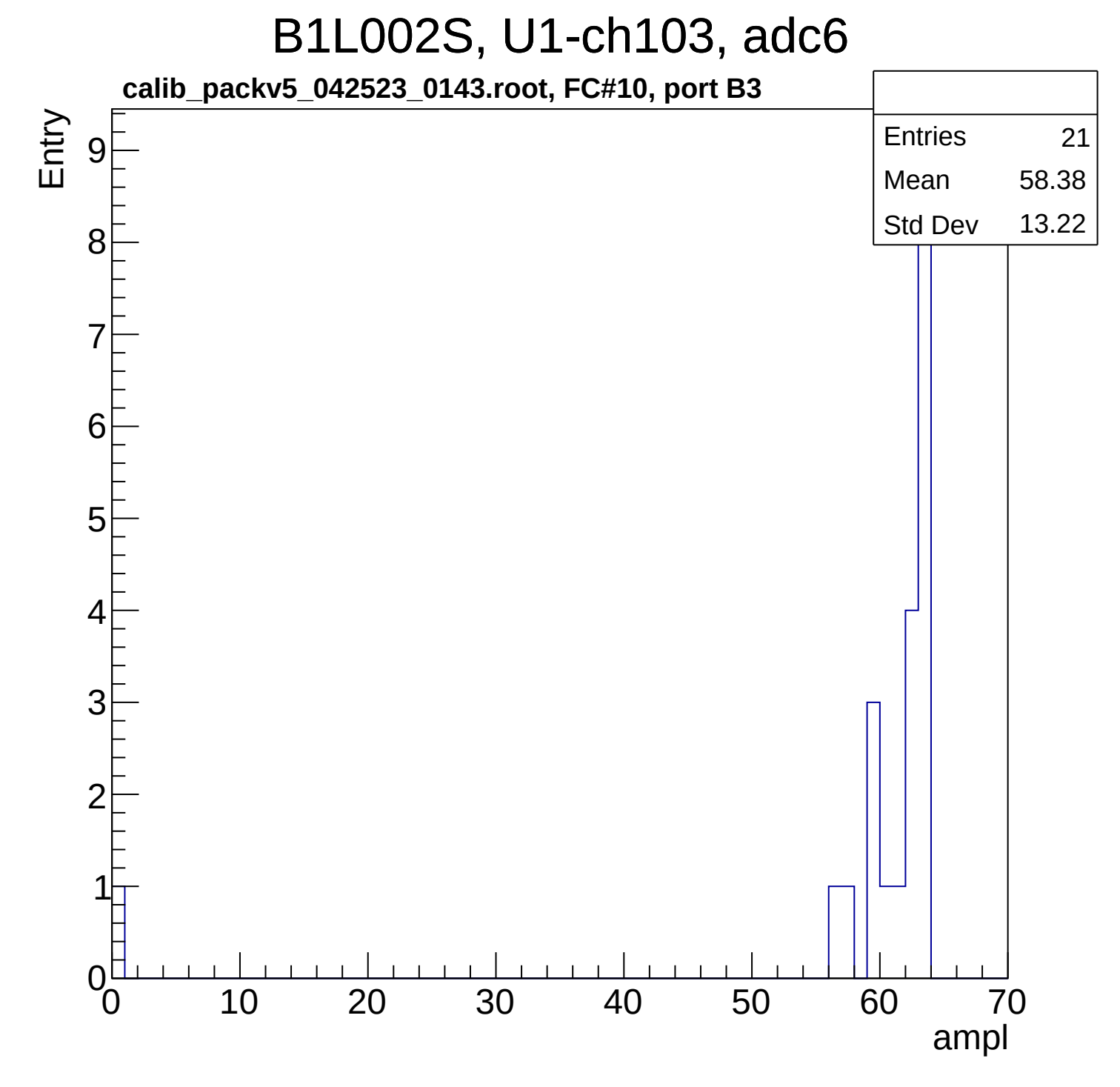
B1L002S, U1-ch103, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.38
Std Dev 13.22
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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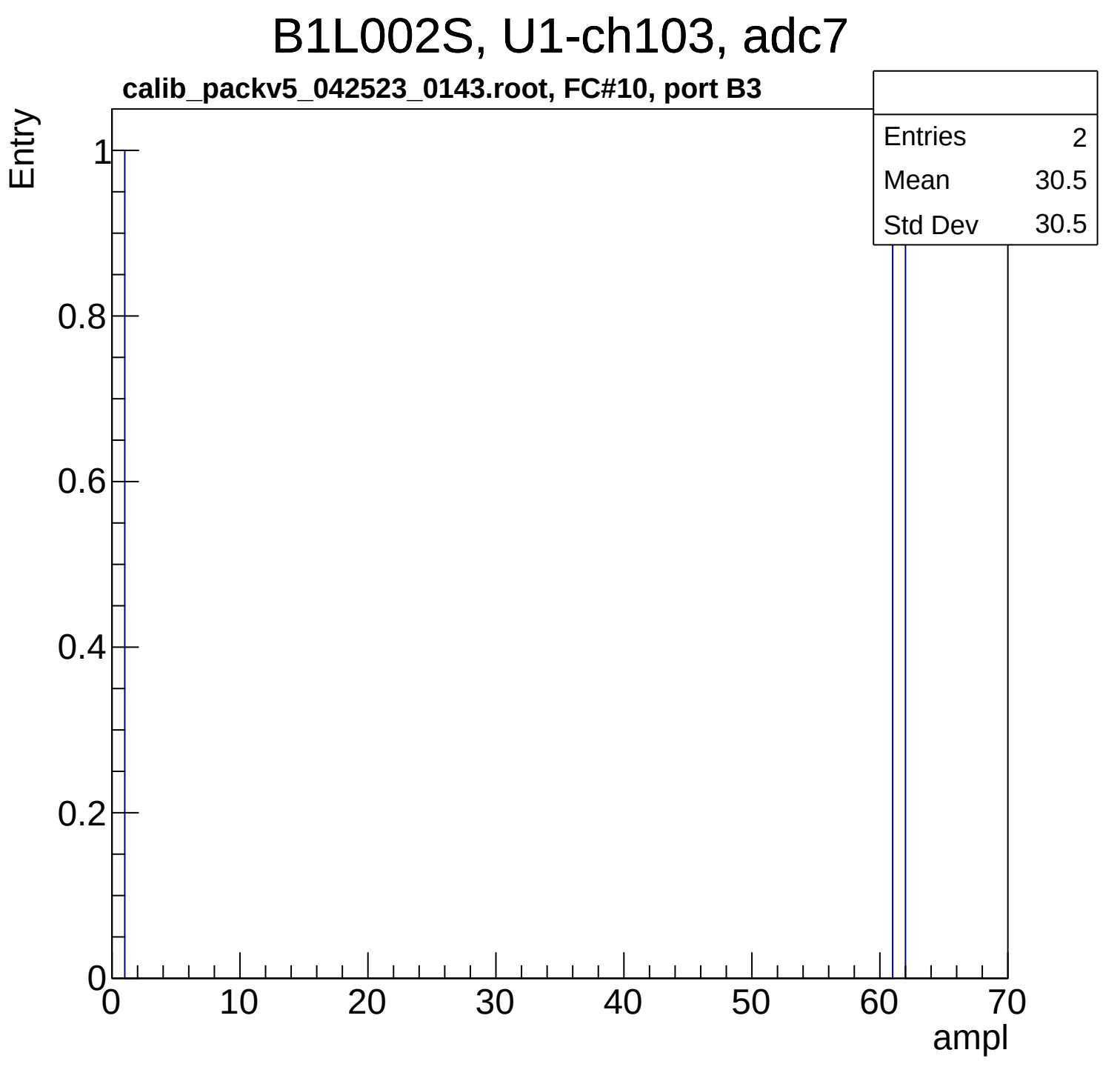
B1L002S, U1-ch103, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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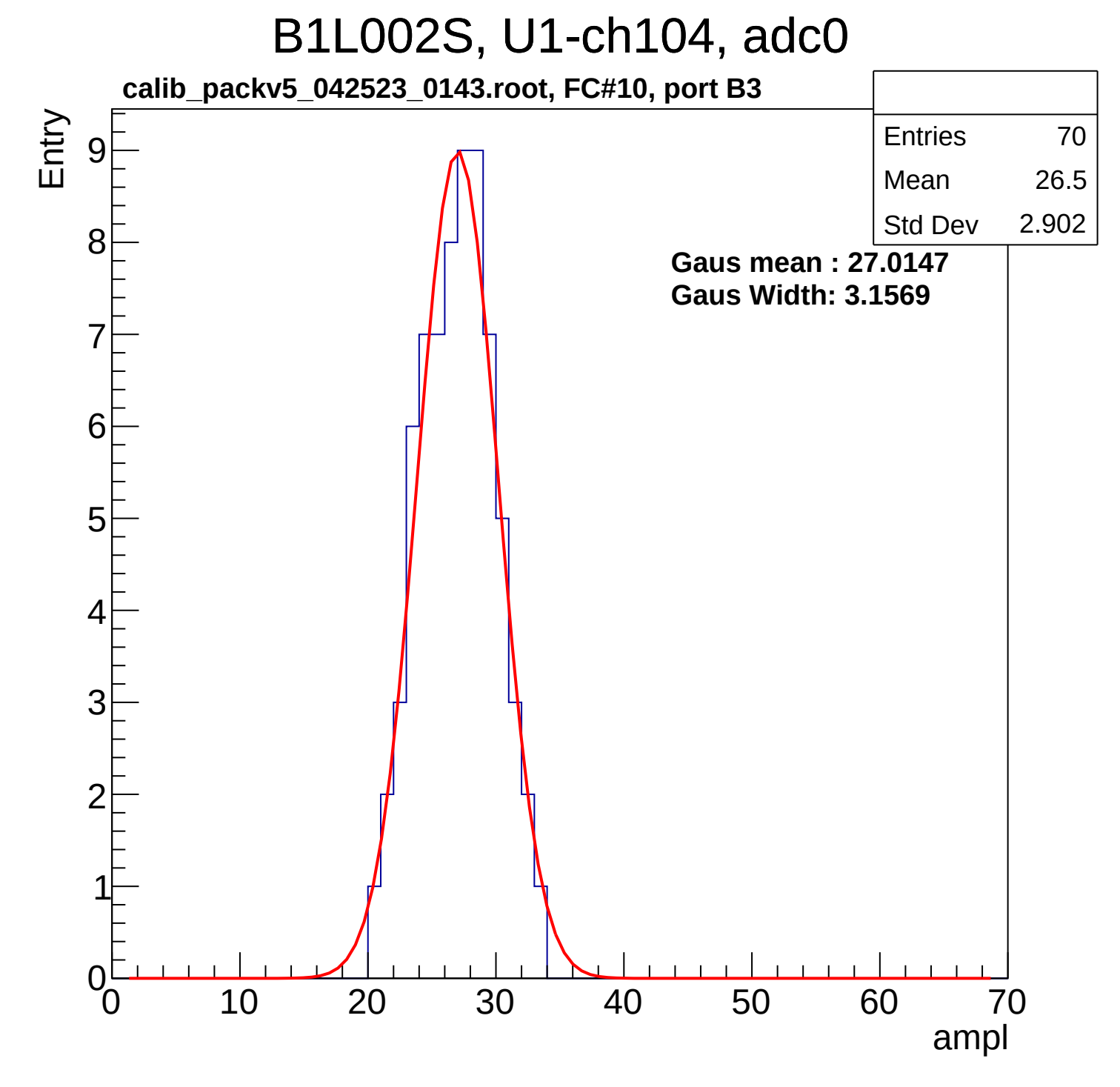
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch104, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.5
Std Dev 2.902
Gaus mean : 27.0147
Gaus Width: 3.1569
Entry
ampl
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2
3
4
5
6
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8
9
0
10
20
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40
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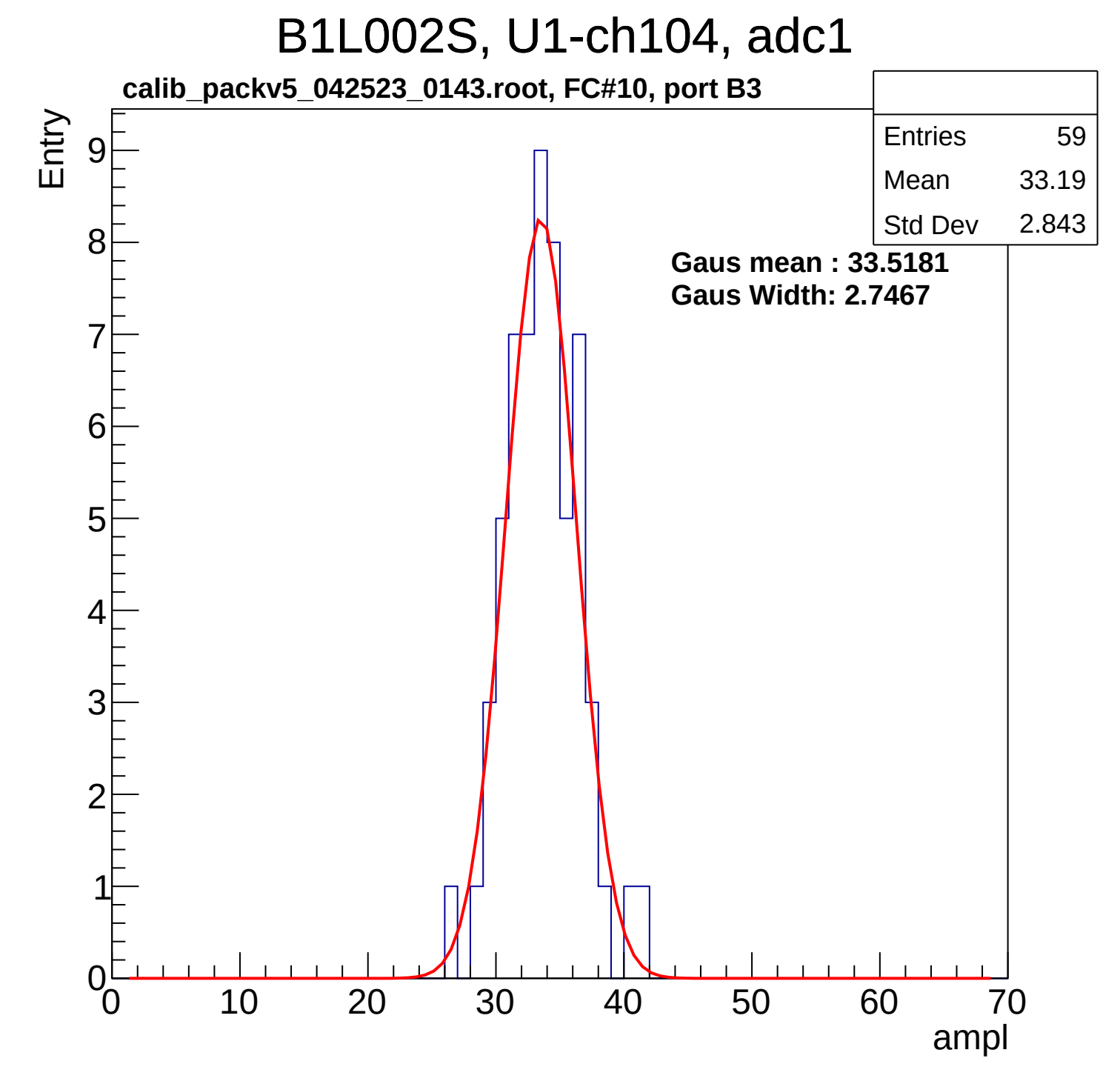
B1L002S, U1-ch104, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 33.19
Std Dev 2.843
Gaus mean : 33.5181
Gaus Width: 2.7467
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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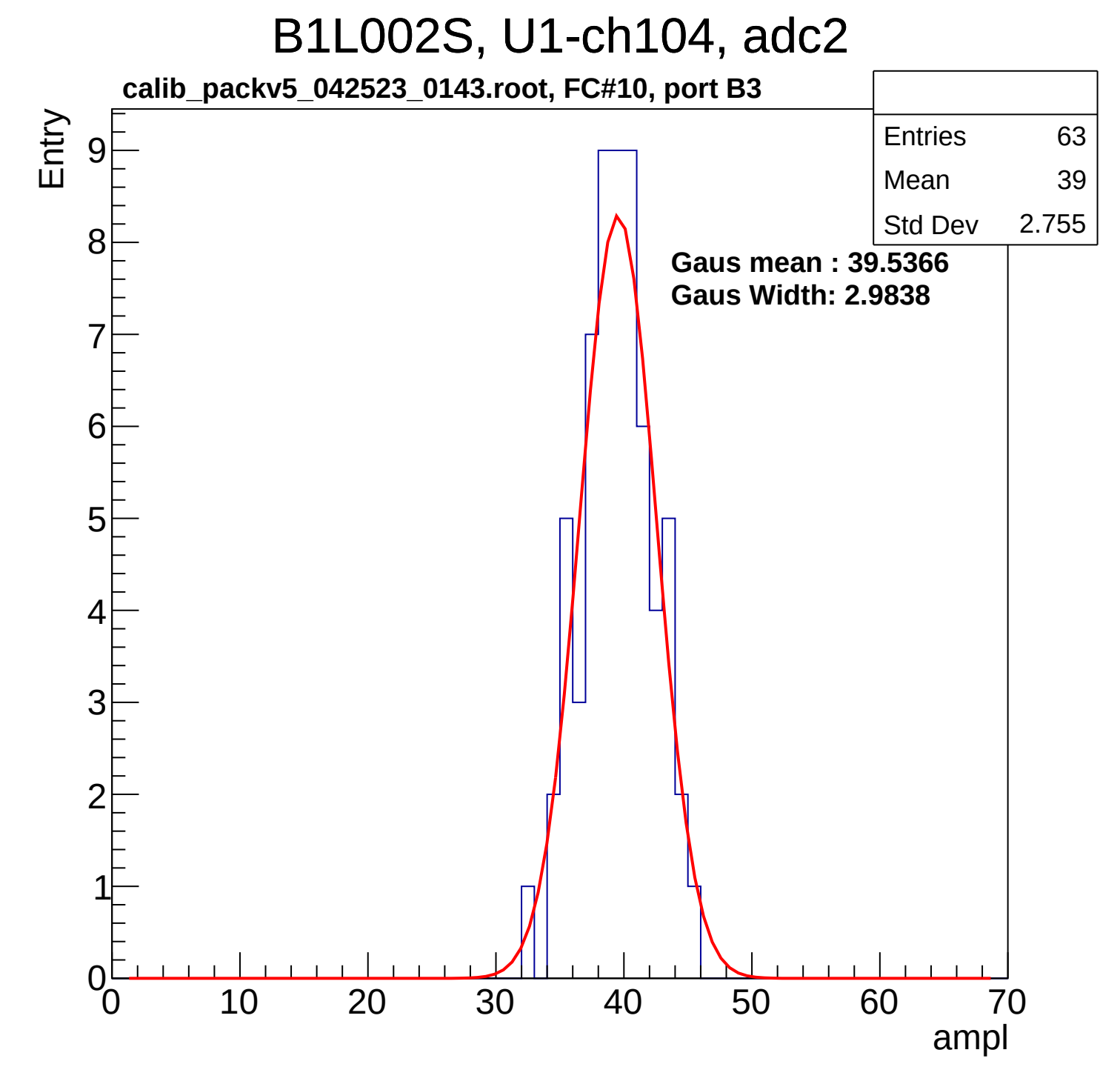
B1L002S, U1-ch104, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39
Std Dev 2.755
Gaus mean : 39.5366
Gaus Width: 2.9838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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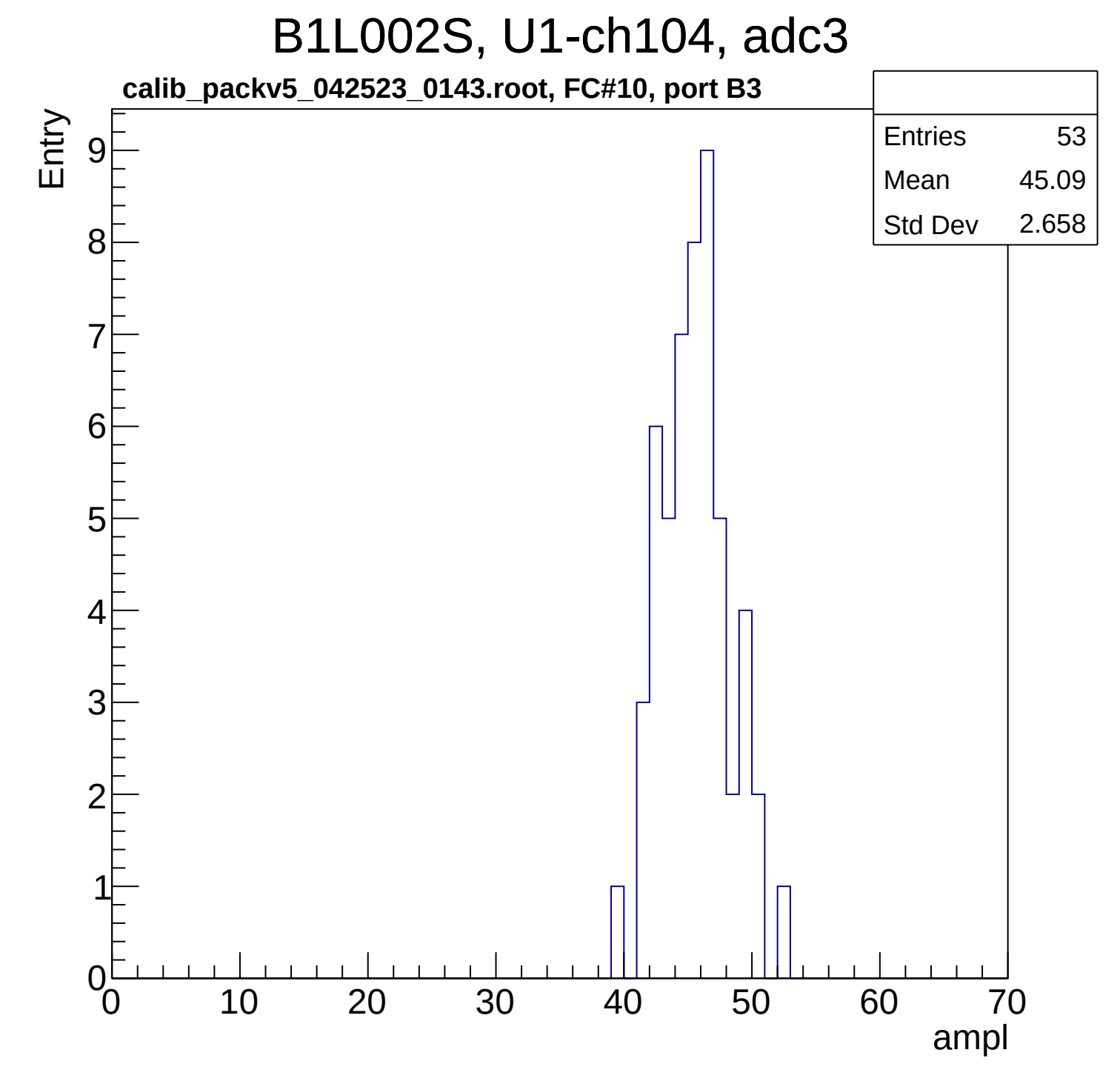
B1L002S, U1-ch104, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 45.09
Std Dev 2.658
Entry
ampl
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2
3
4
5
6
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8
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0
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60
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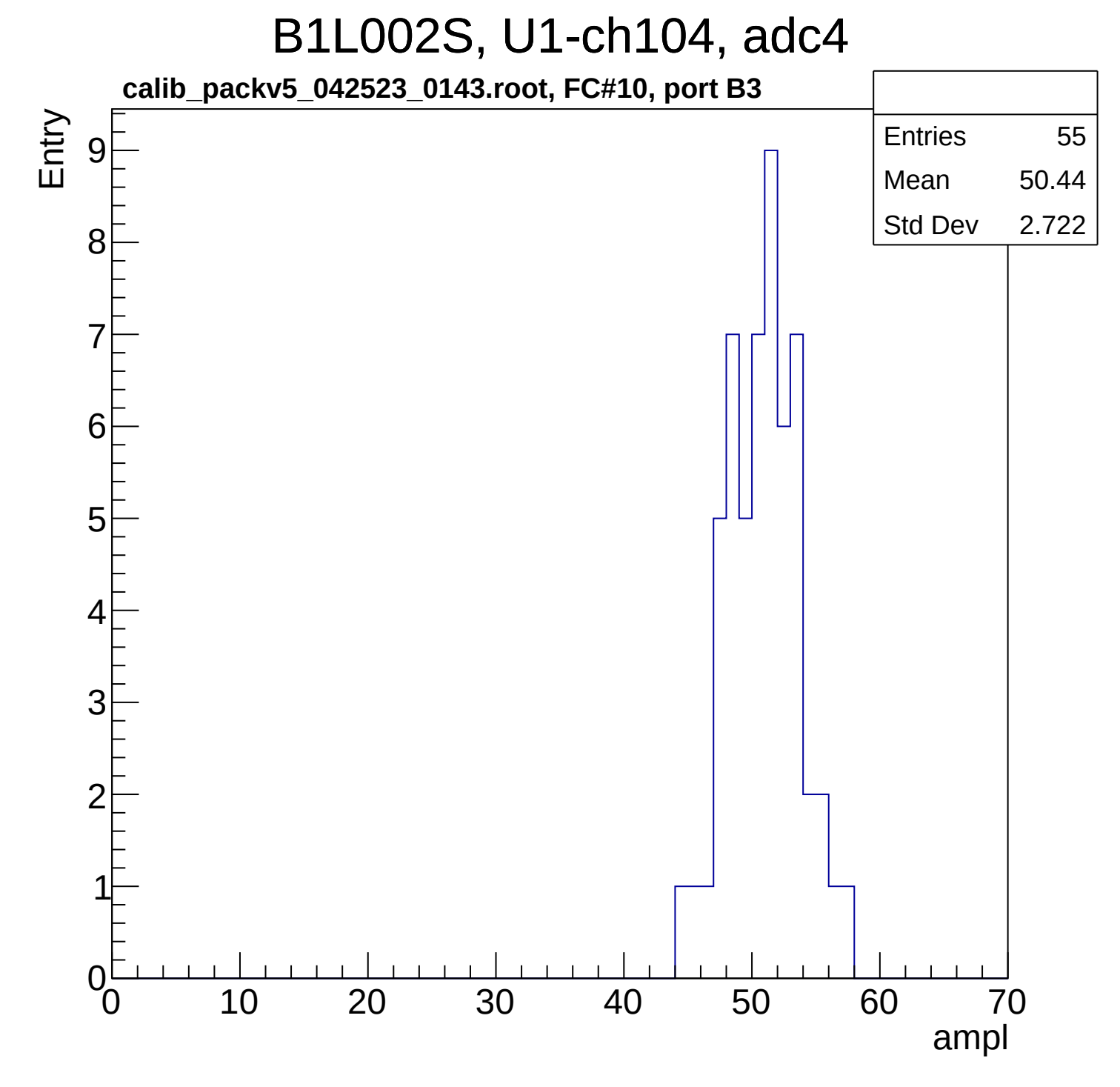
B1L002S, U1-ch104, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 50.44
Std Dev 2.722
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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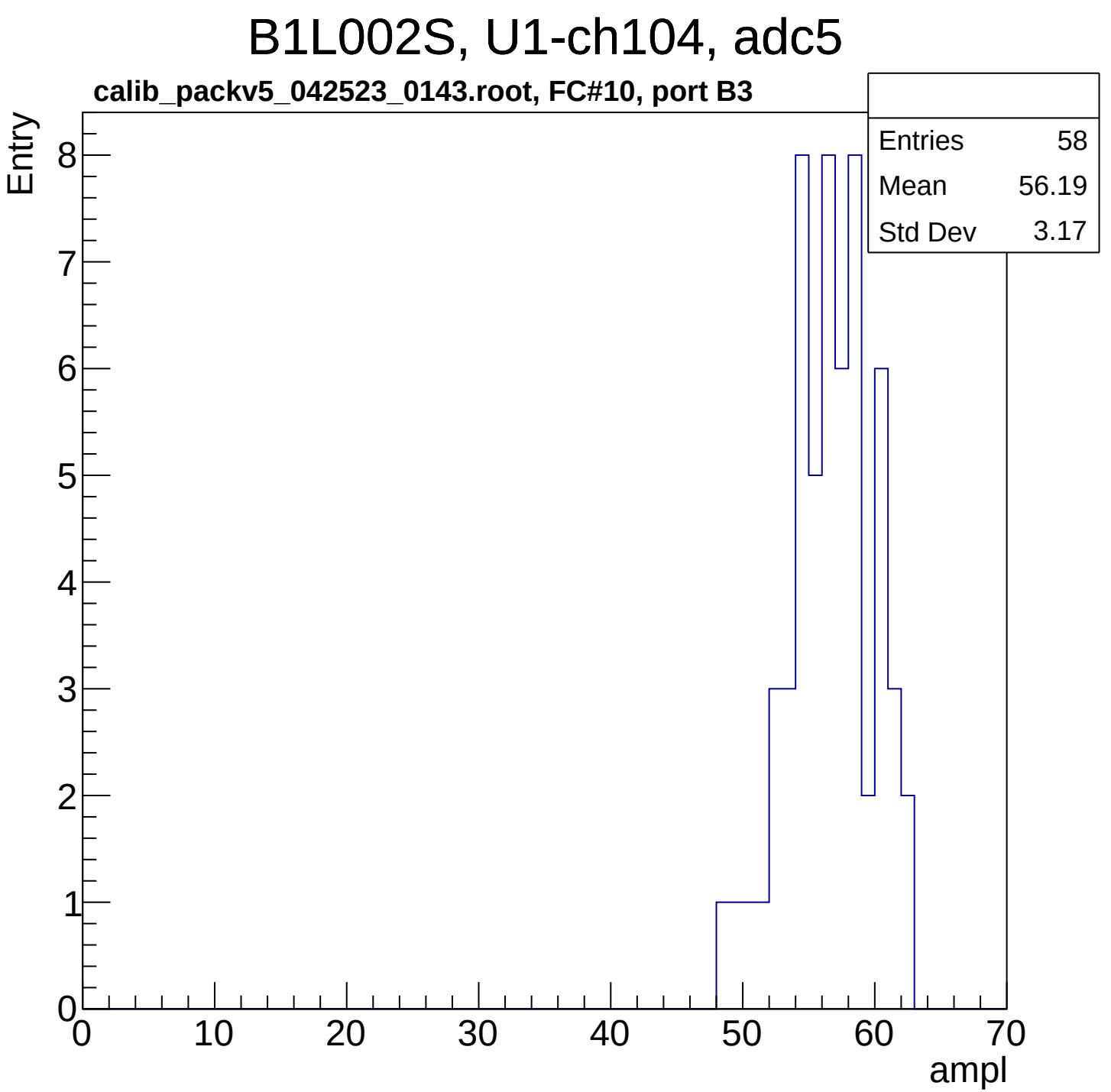
B1L002S, U1-ch104, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 56.19
Std Dev 3.17
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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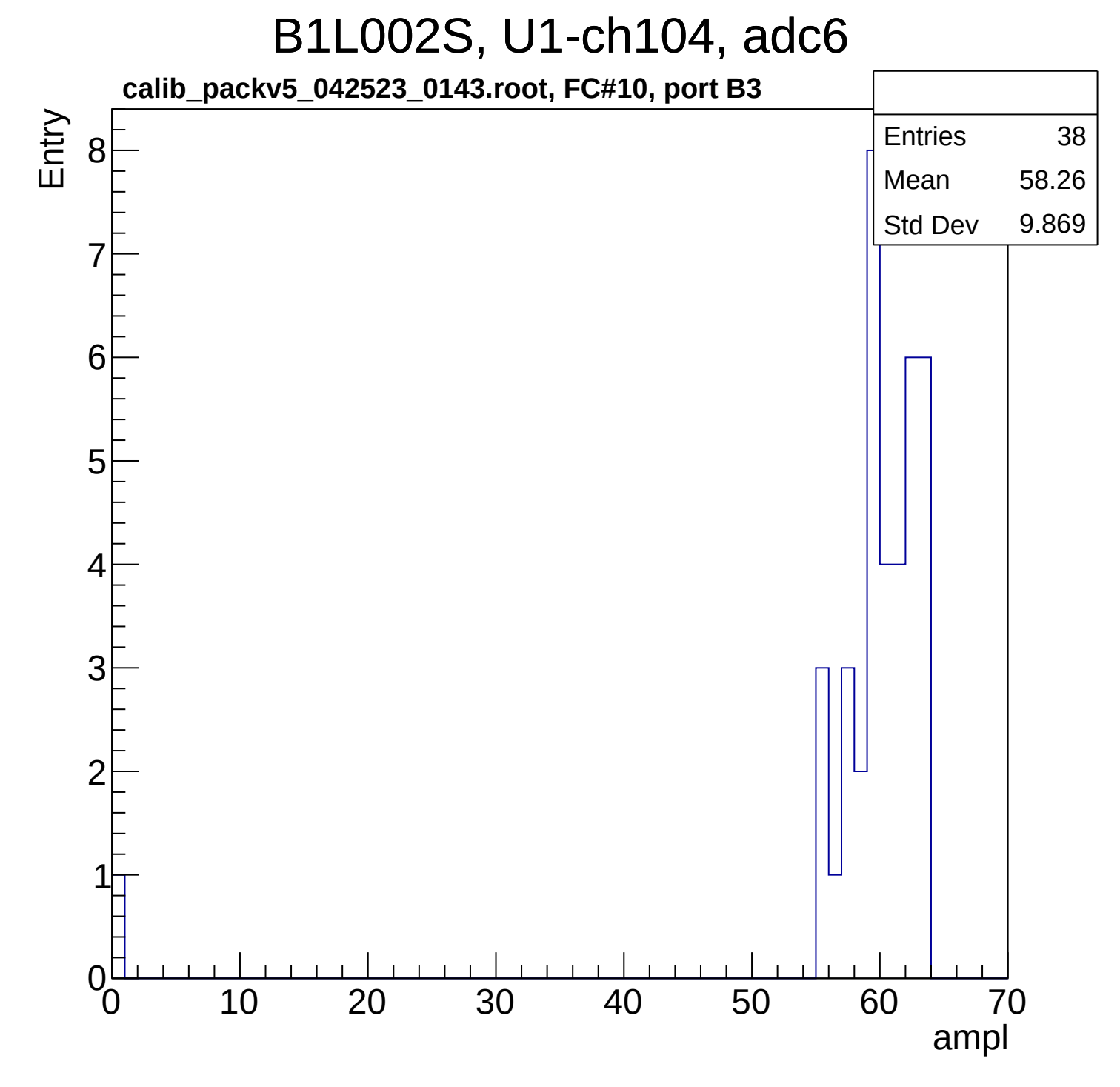
B1L002S, U1-ch104, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 38
Mean 58.26
Std Dev 9.869
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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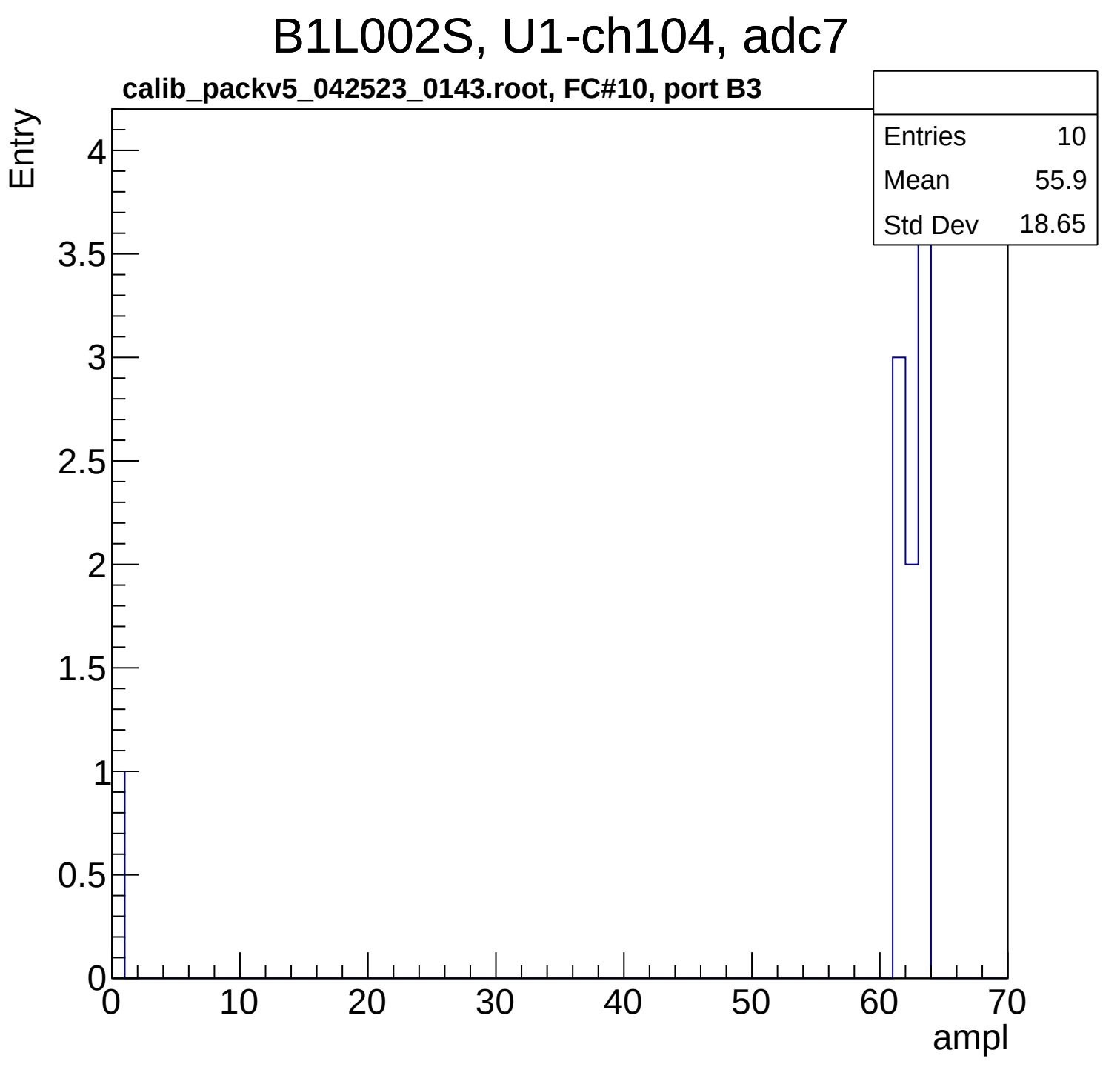
B1L002S, U1-ch104, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.9
Std Dev 18.65
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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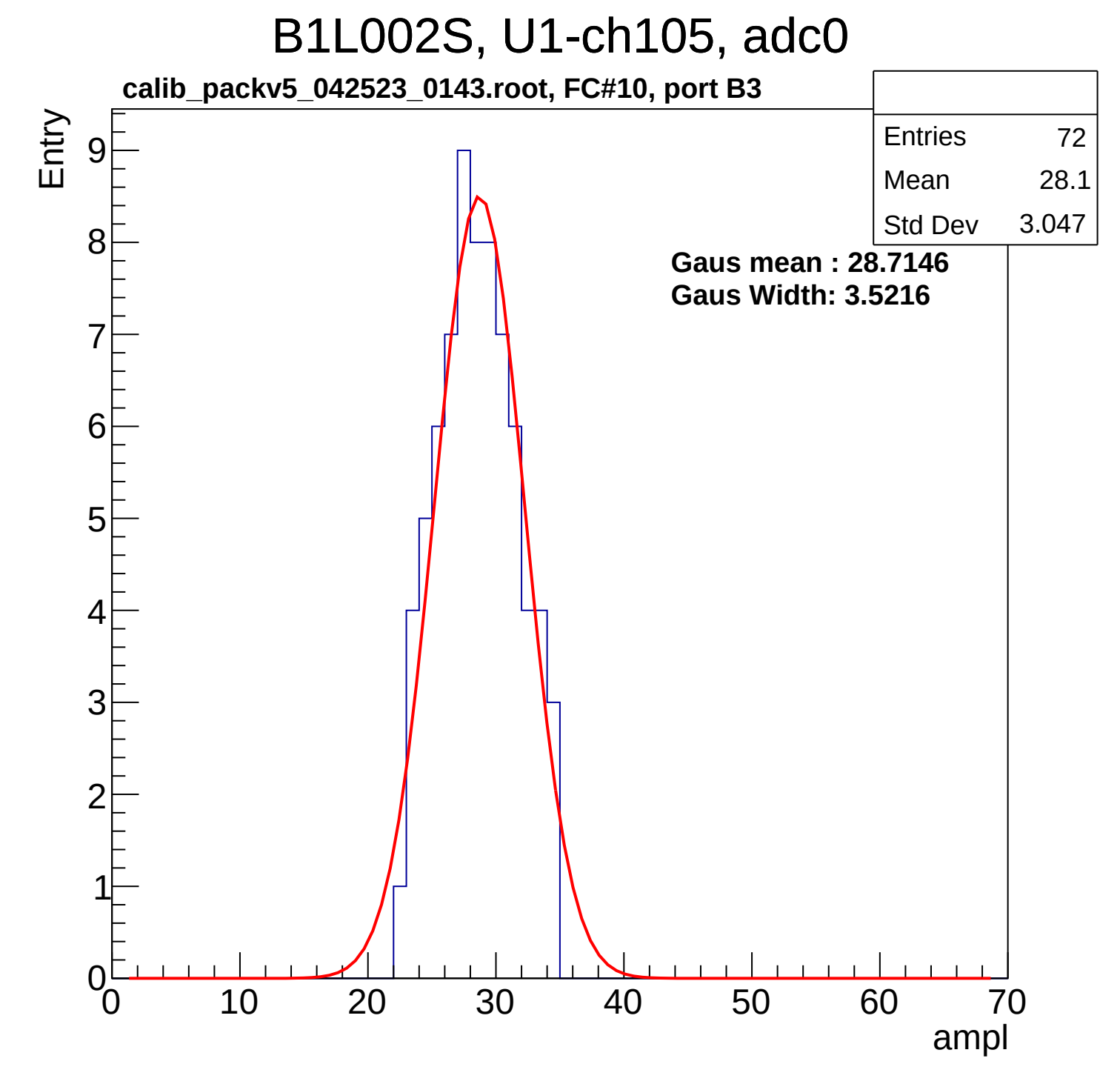
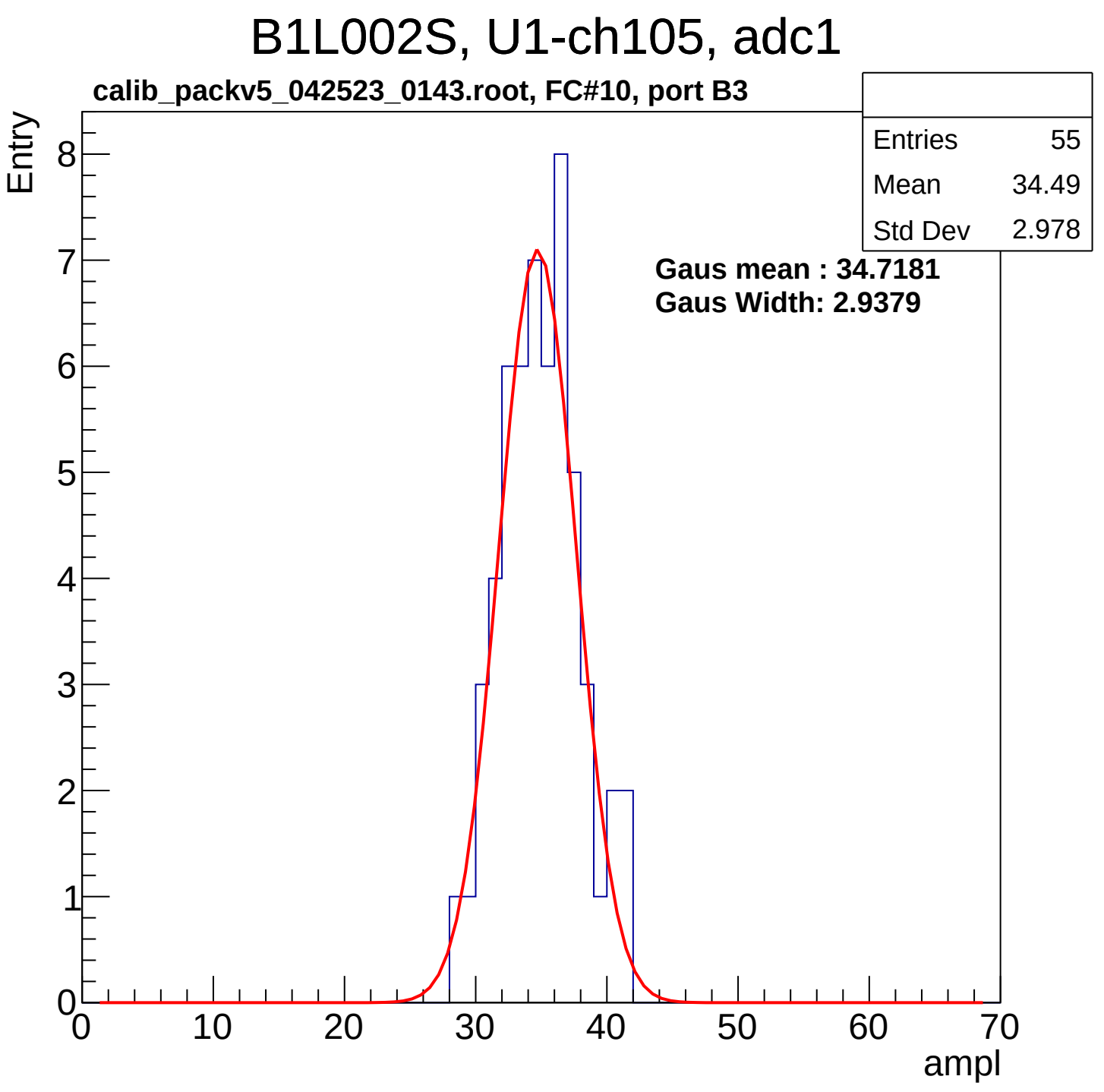

B1L002S, U1-ch105, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.1
Std Dev 3.047
Gaus mean : 28.7146
Gaus Width: 3.5216
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch105, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 34.49
Std Dev 2.978
Gaus mean : 34.7181
Gaus Width: 2.9379
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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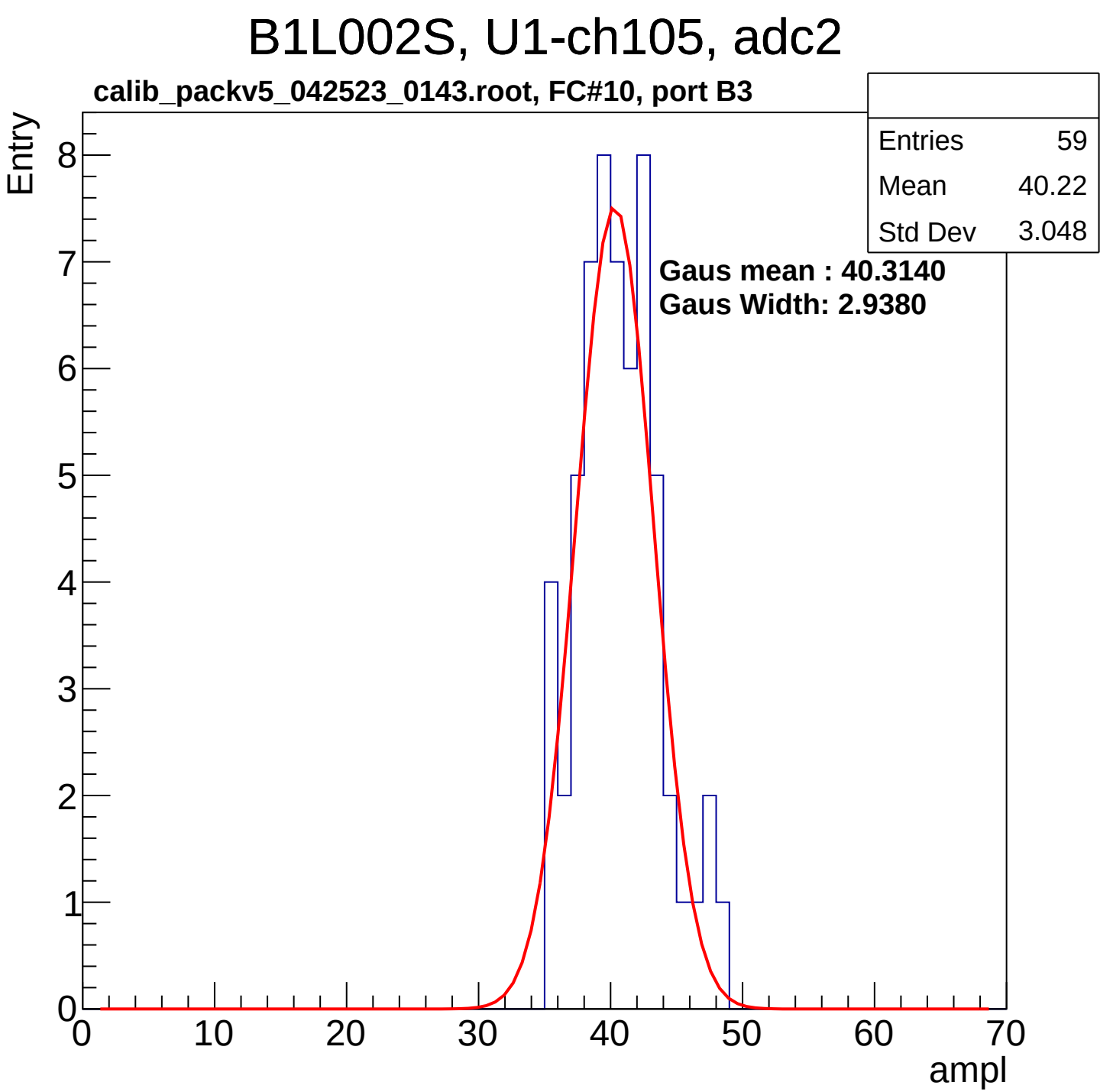
B1L002S, U1-ch105, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.22
Std Dev 3.048
Gaus mean : 40.3140
Gaus Width: 2.9380
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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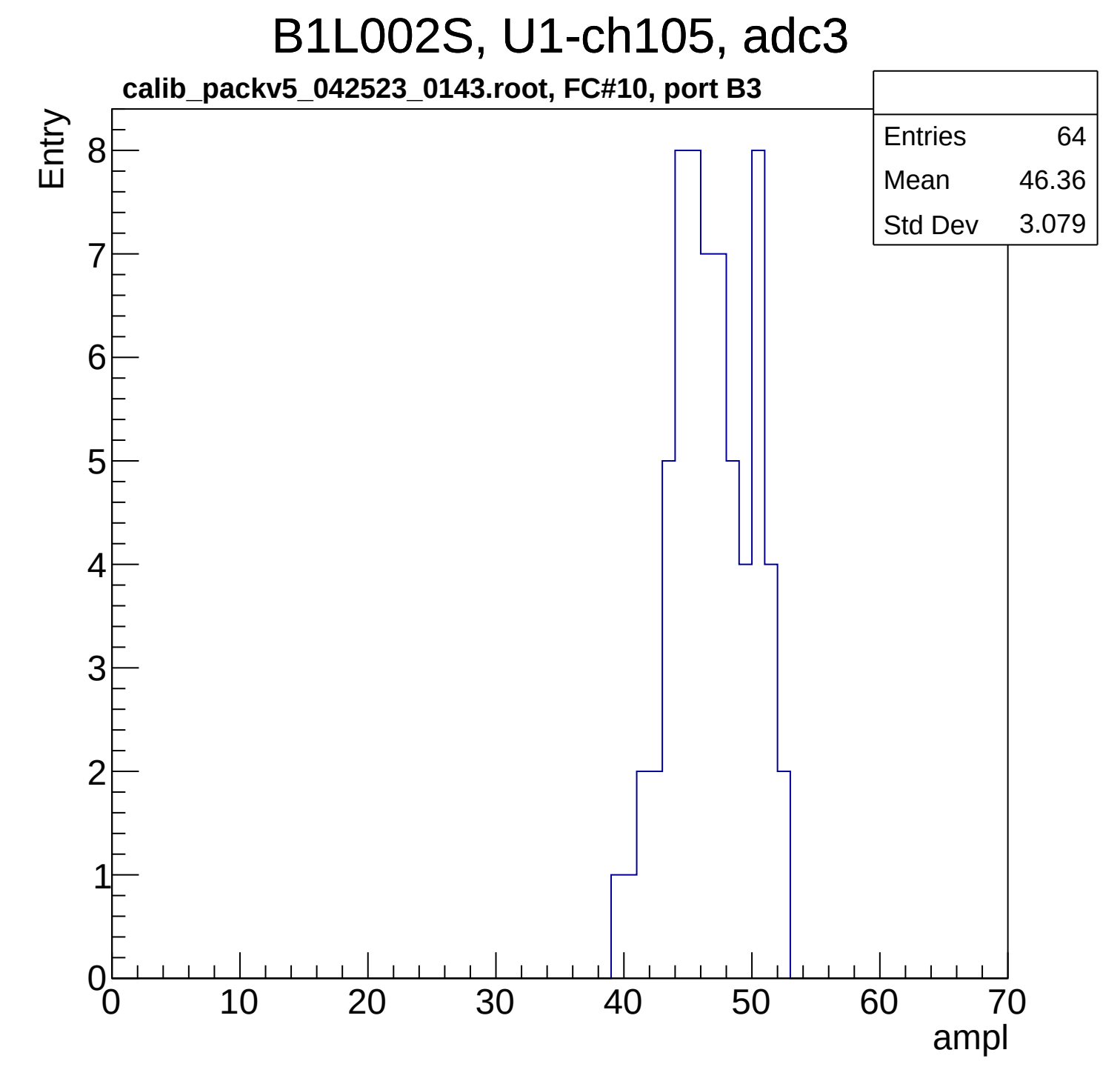
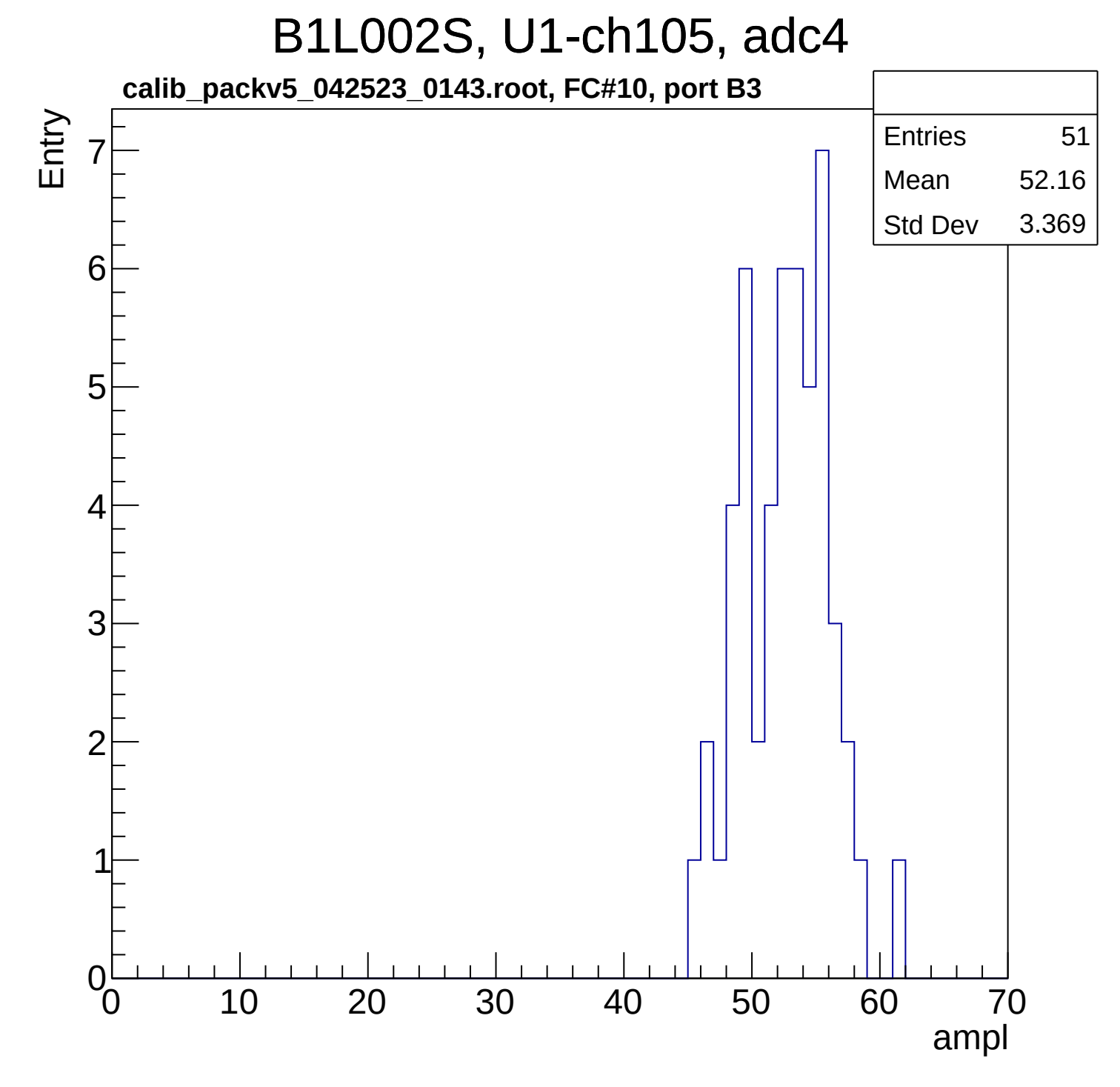

B1L002S, U1-ch105, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.36
Std Dev 3.079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch105, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 52.16
Std Dev 3.369
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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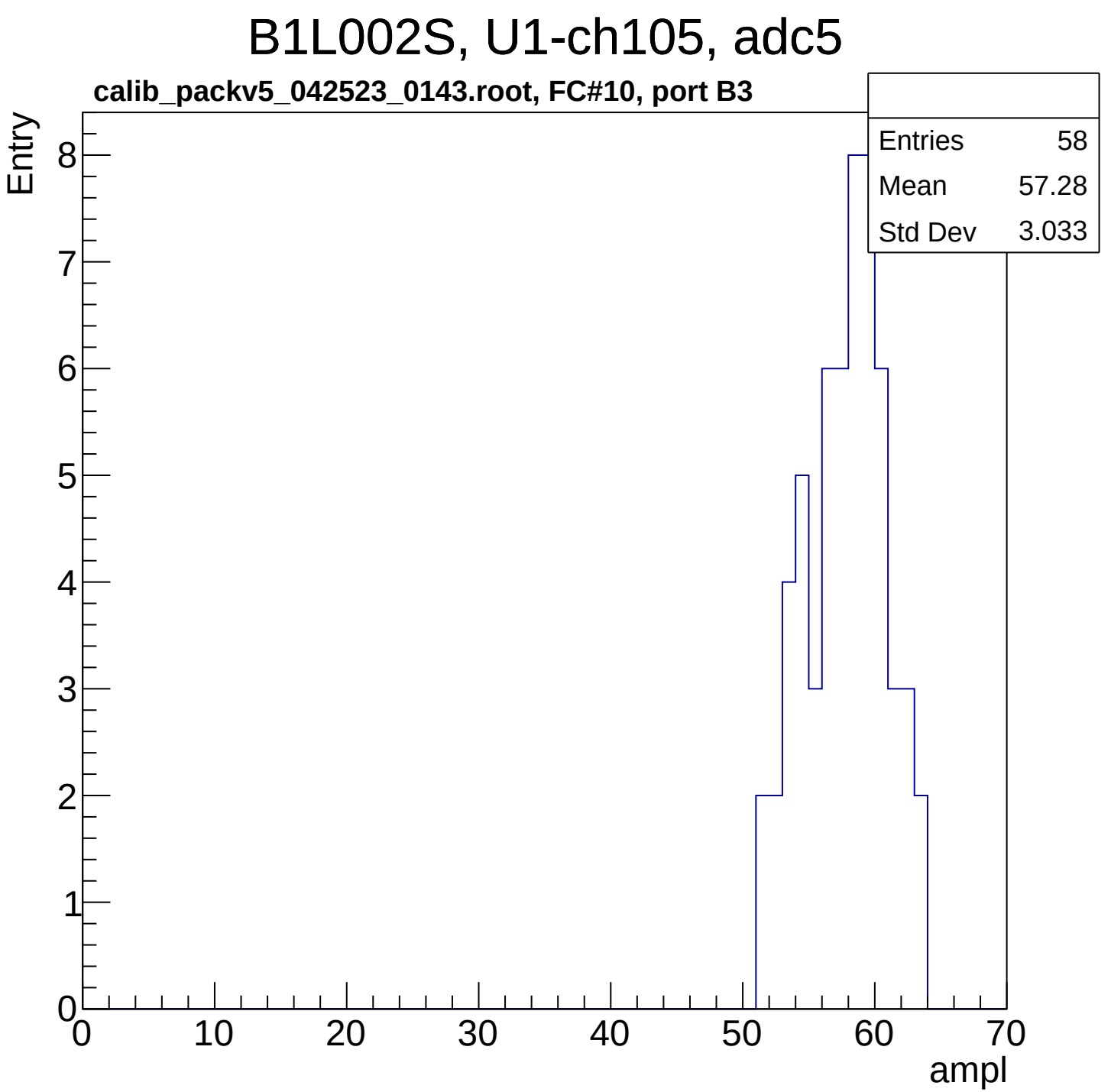
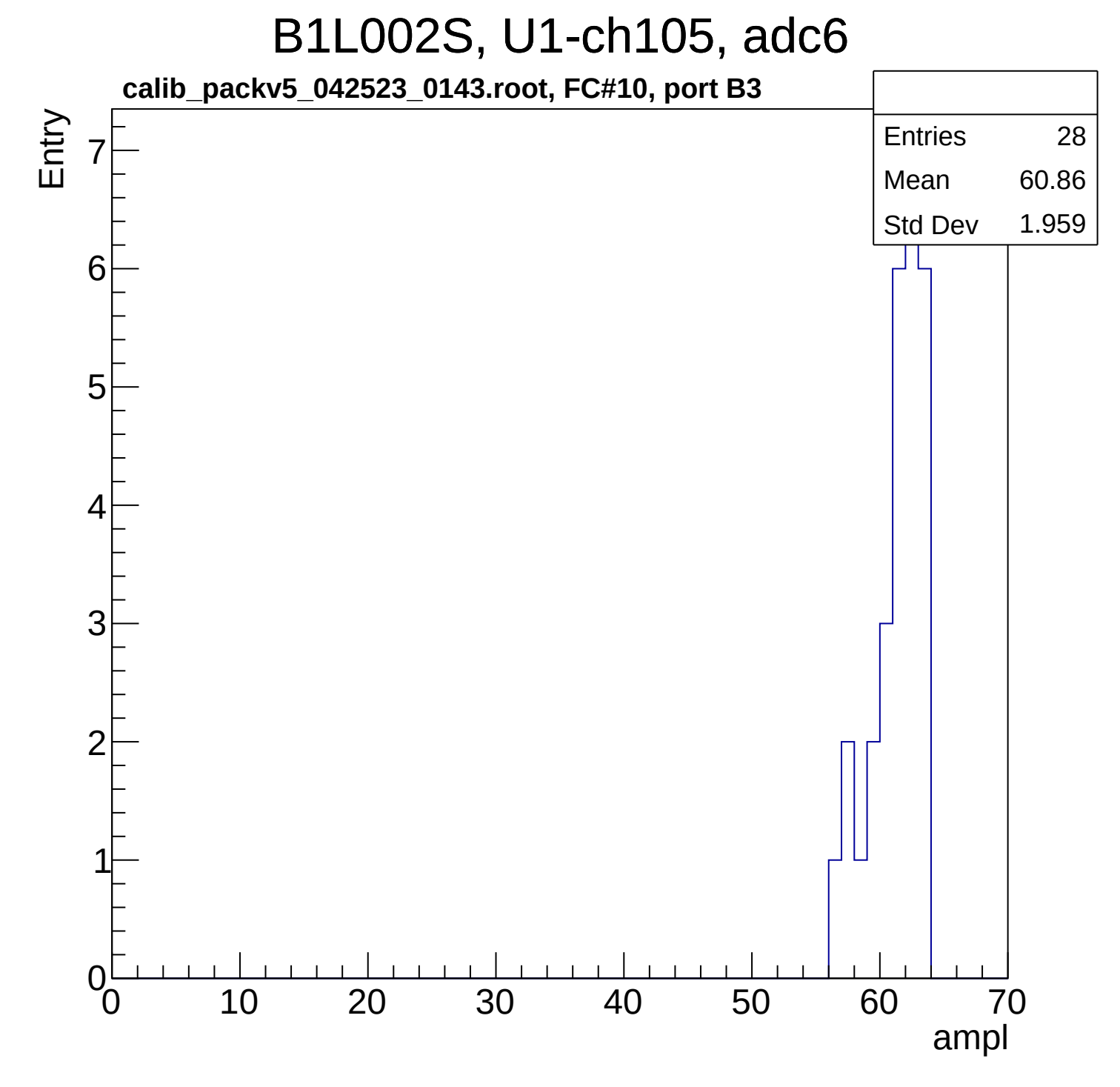
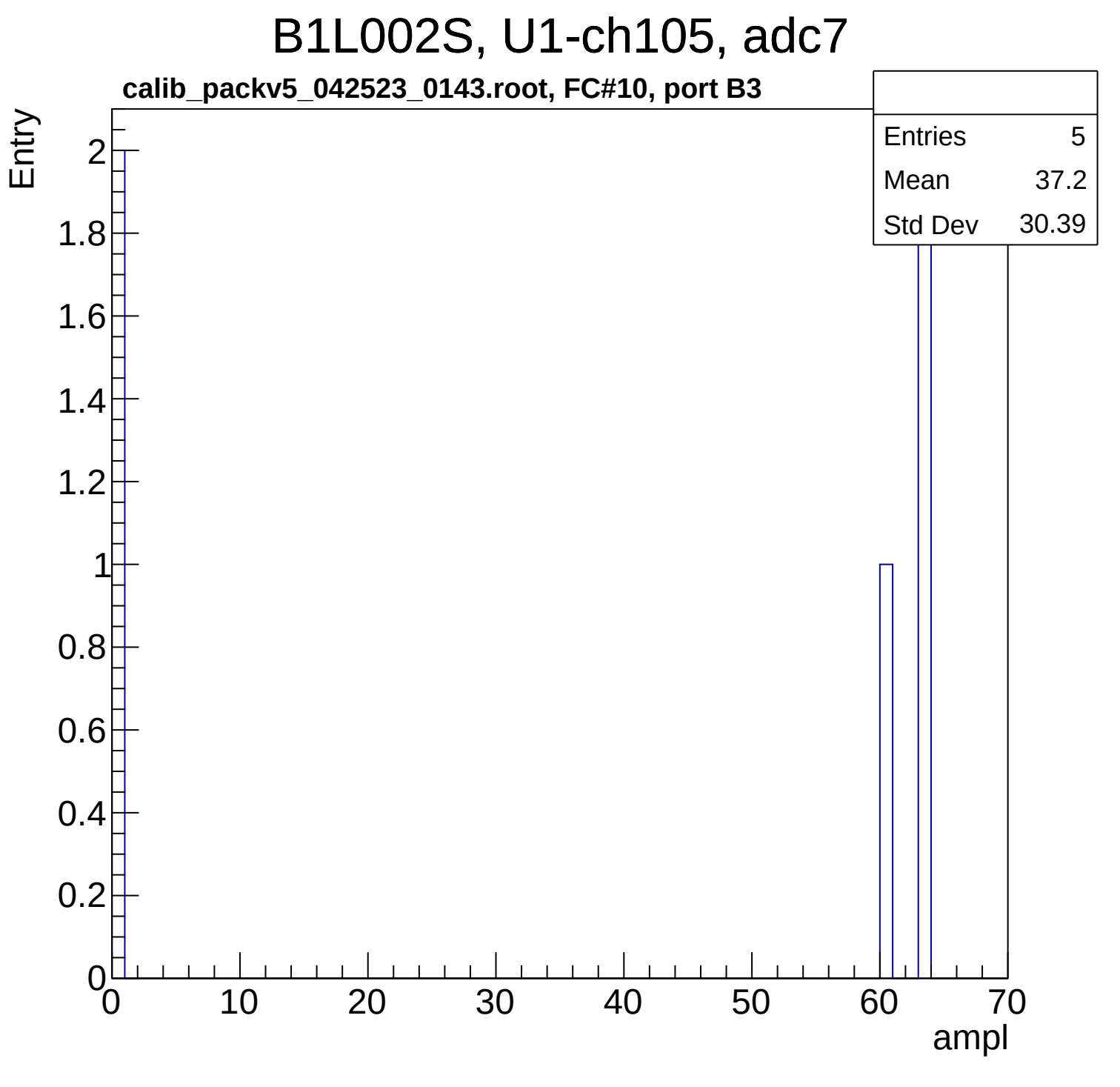

B1L002S, U1-ch105, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.28
Std Dev 3.033
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch105, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 60.86
Std Dev 1.959
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch105, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 37.2
Std Dev 30.39
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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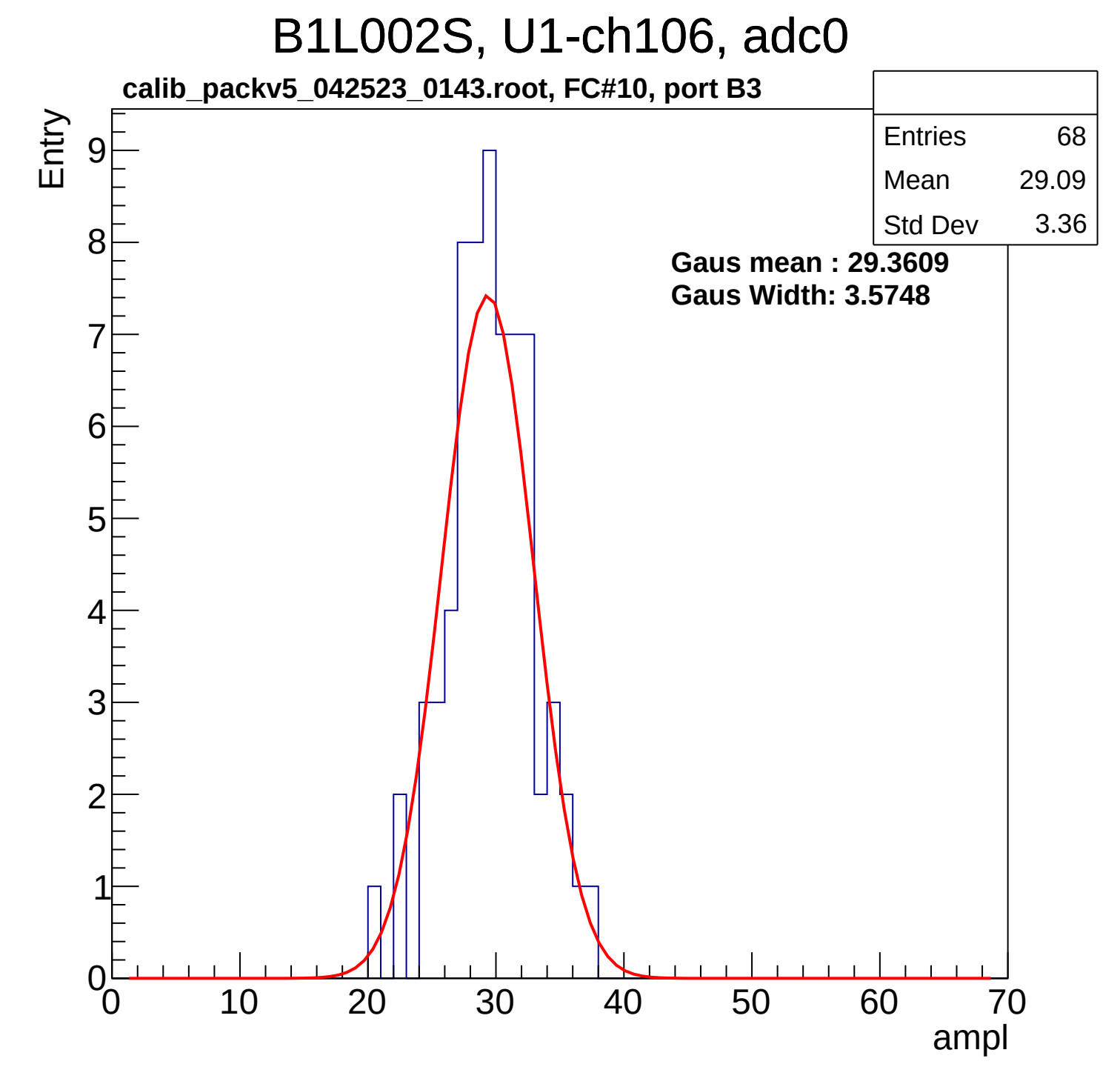
B1L002S, U1-ch106, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 29.09
Std Dev 3.36
Gaus mean : 29.3609
Gaus Width: 3.5748
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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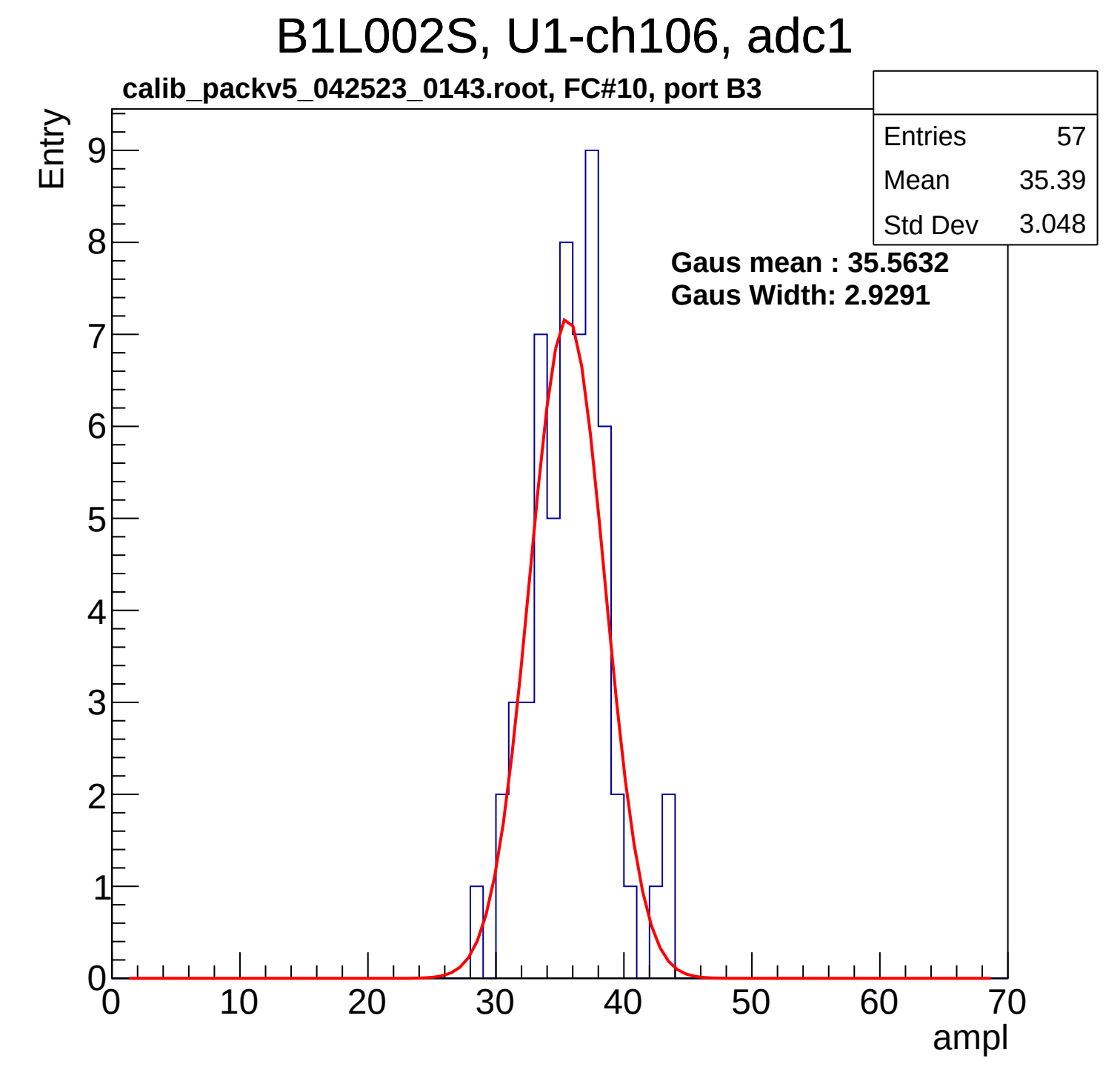
B1L002S, U1-ch106, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.39
Std Dev 3.048
Gaus mean : 35.5632
Gaus Width: 2.9291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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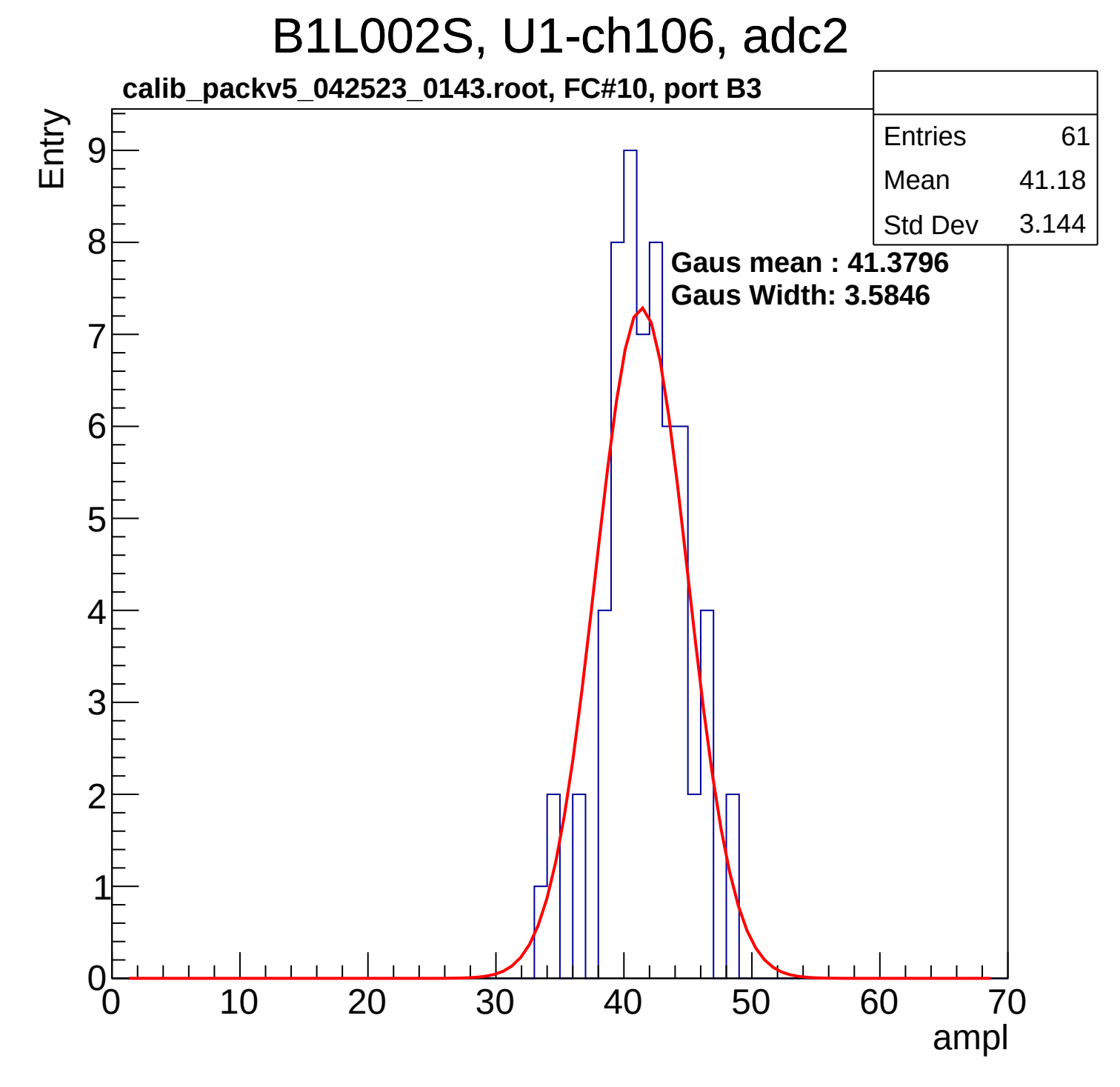
B1L002S, U1-ch106, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.18
Std Dev 3.144
Gaus mean : 41.3796
Gaus Width: 3.5846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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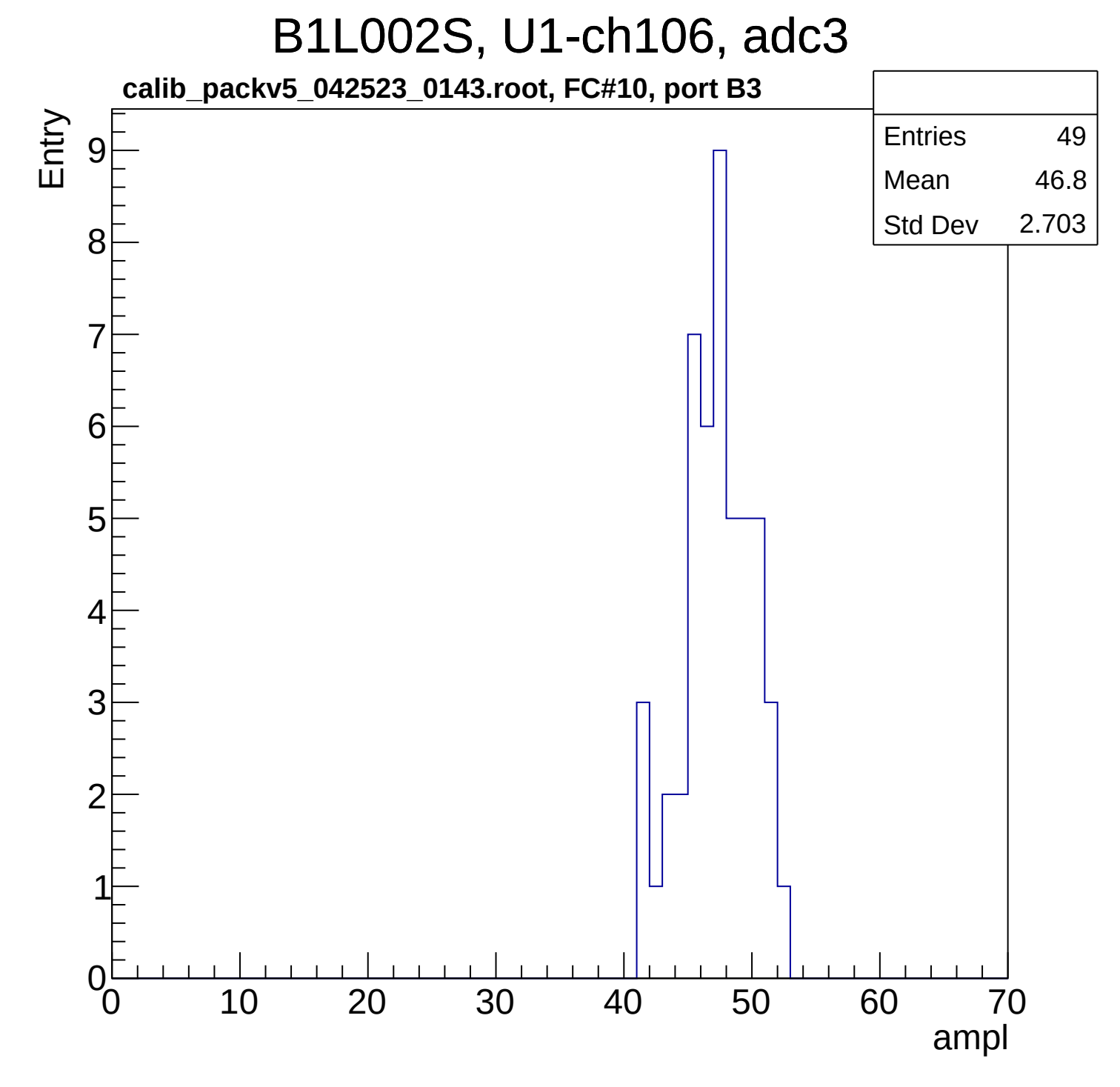
B1L002S, U1-ch106, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 46.8
Std Dev 2.703
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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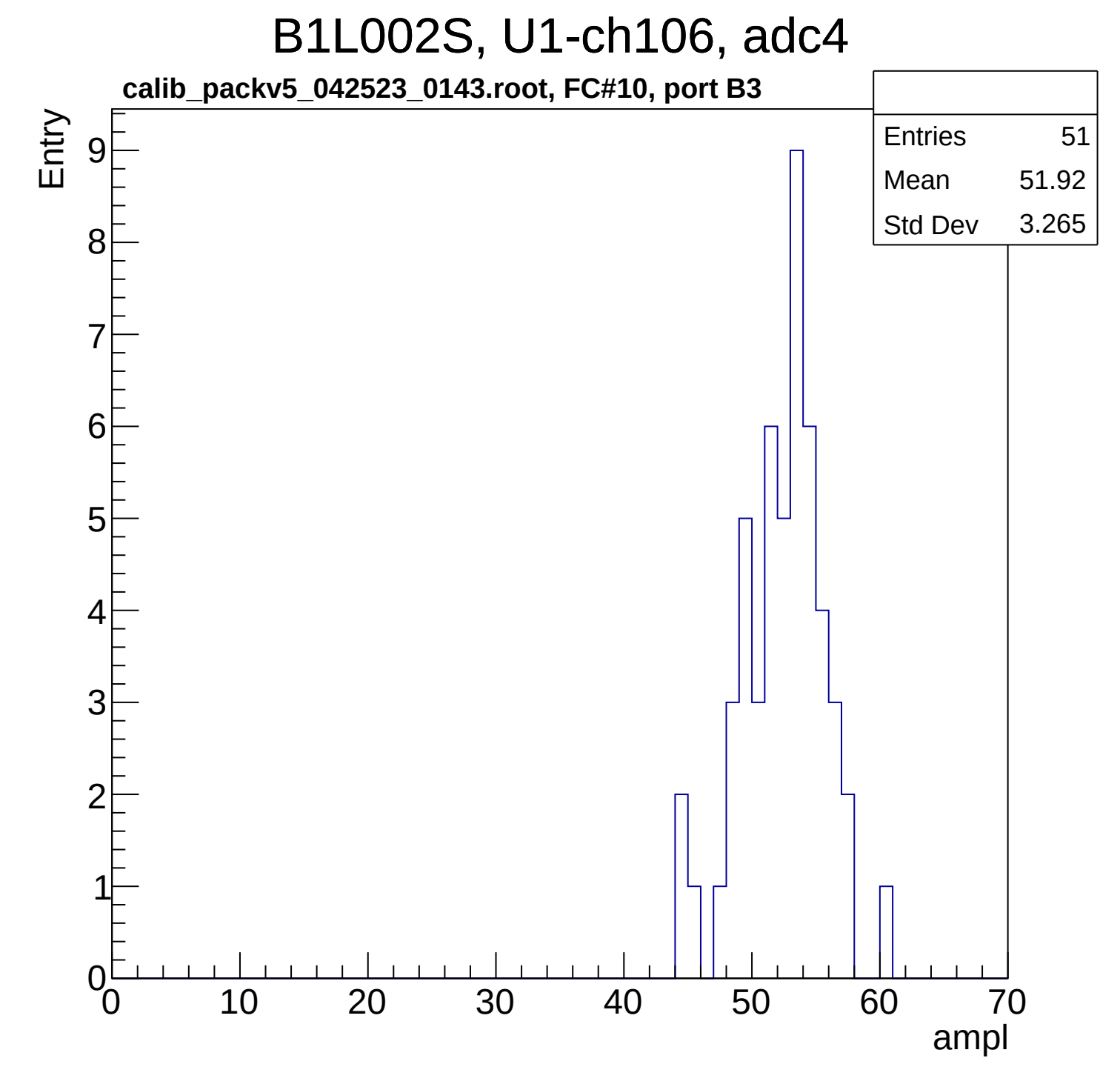
B1L002S, U1-ch106, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 51.92
Std Dev 3.265
Entry
ampl
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2
3
4
5
6
7
8
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0
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30
40
50
60
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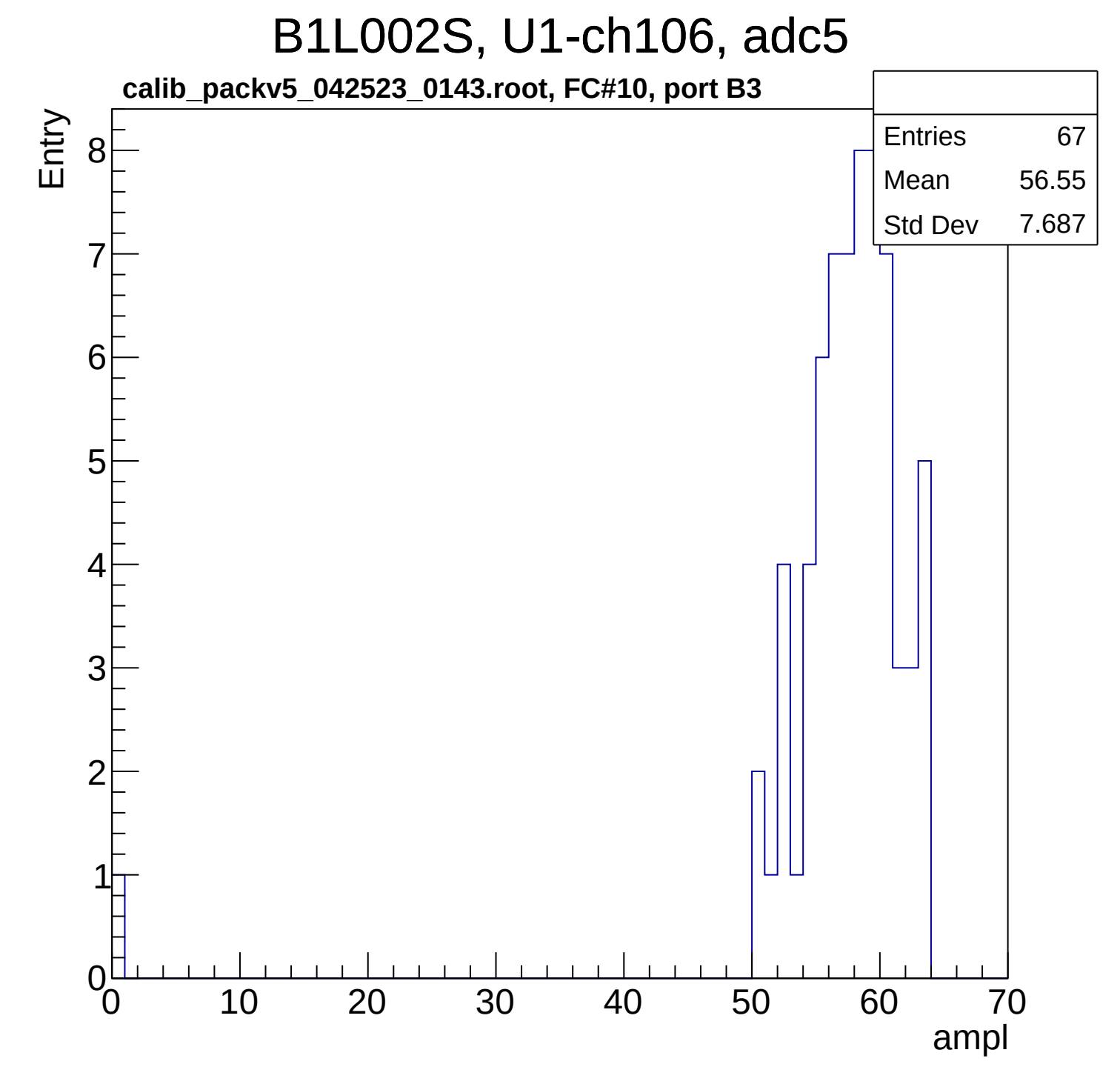
B1L002S, U1-ch106, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 56.55
Std Dev 7.687
Entry
ampl
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2
3
4
5
6
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0
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20
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40
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60
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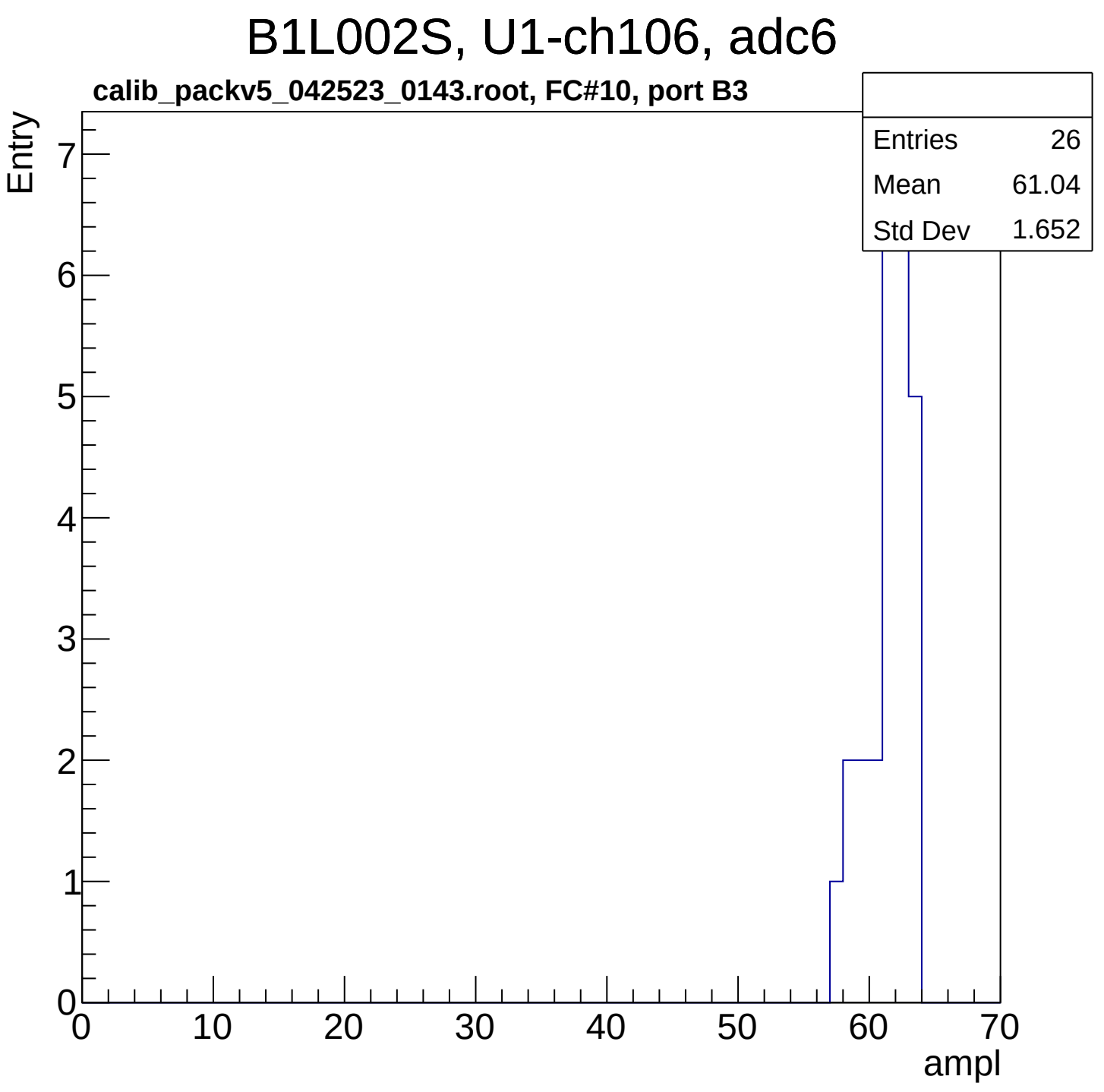
B1L002S, U1-ch106, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 61.04
Std Dev 1.652
Entry
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2
3
4
5
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7
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10
20
30
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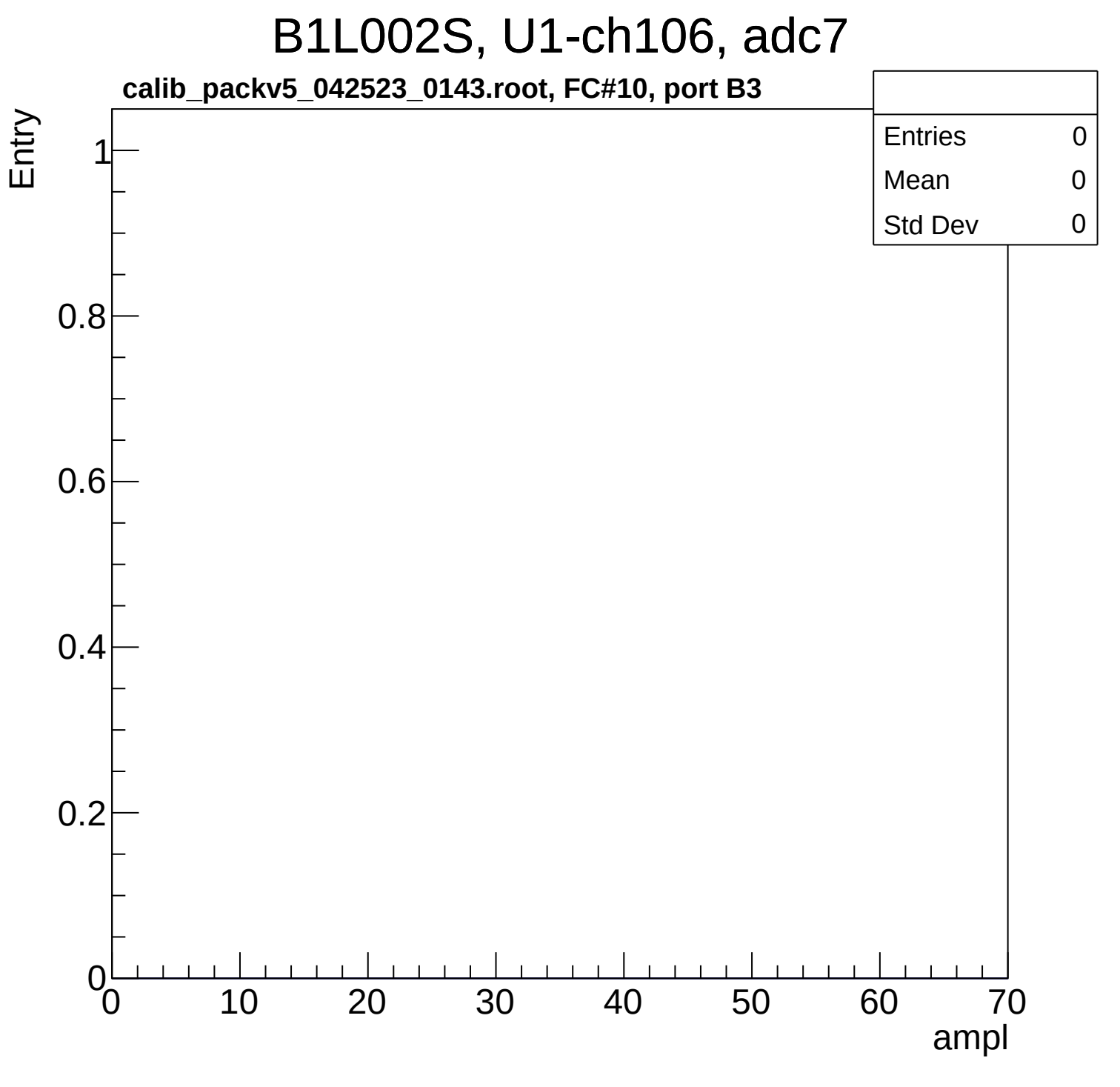
50
60
70
ampl


B1L002S, U1-ch106, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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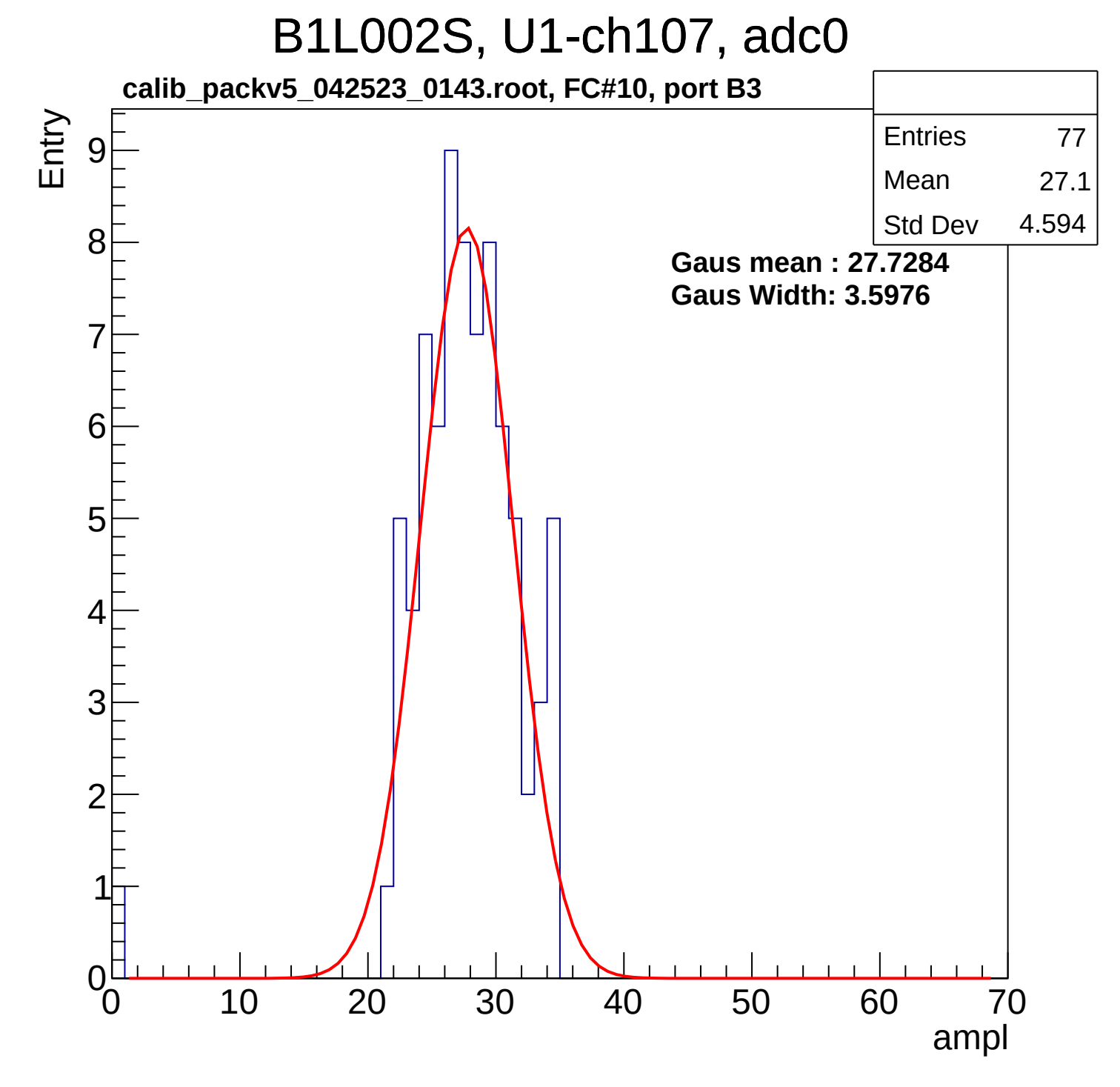
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch107, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 27.1
Std Dev 4.594
Gaus mean : 27.7284
Gaus Width: 3.5976
Entry
ampl
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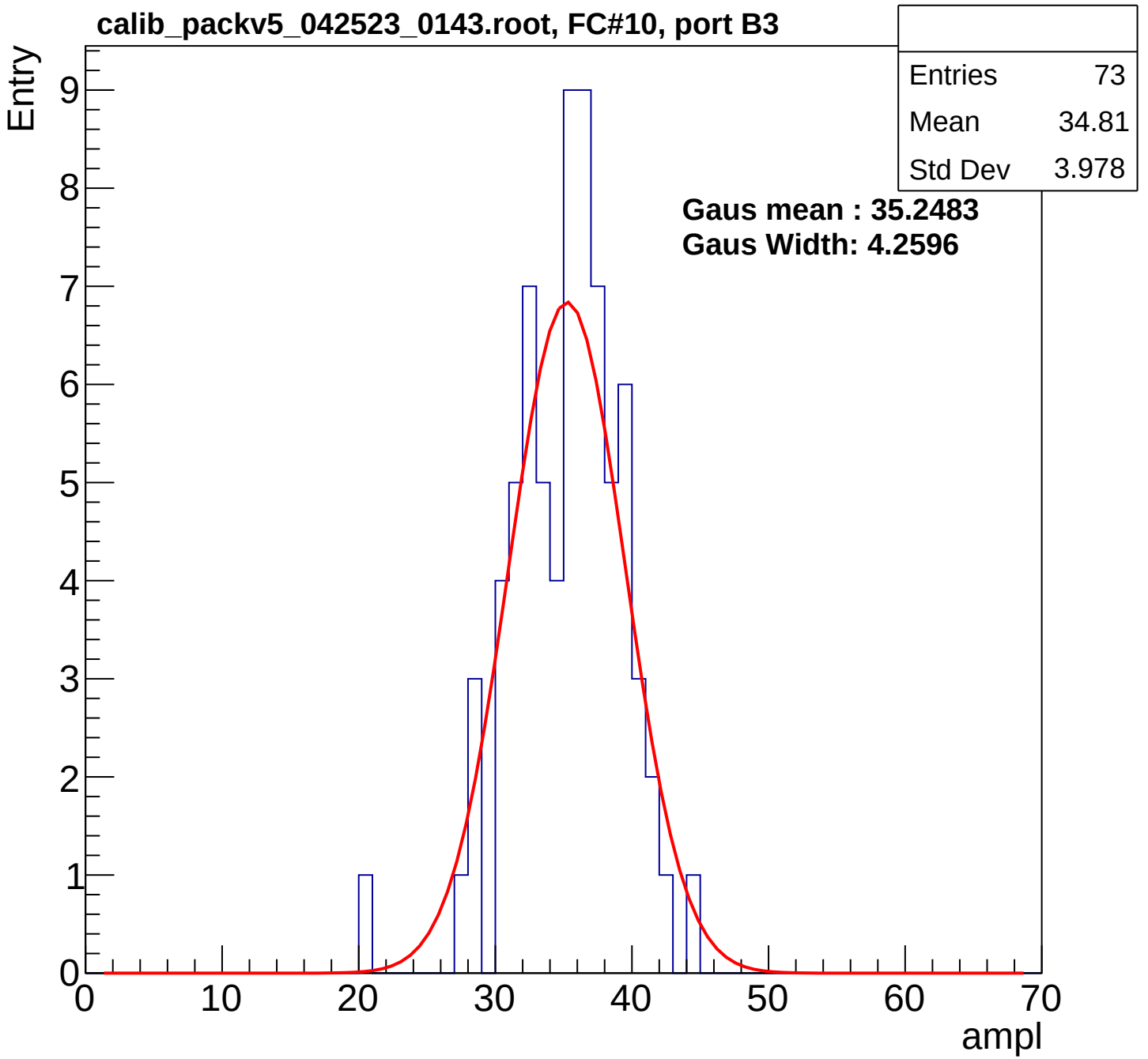
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch107, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.81
Std Dev 3.978
Gaus mean : 35.2483
Gaus Width: 4.2596
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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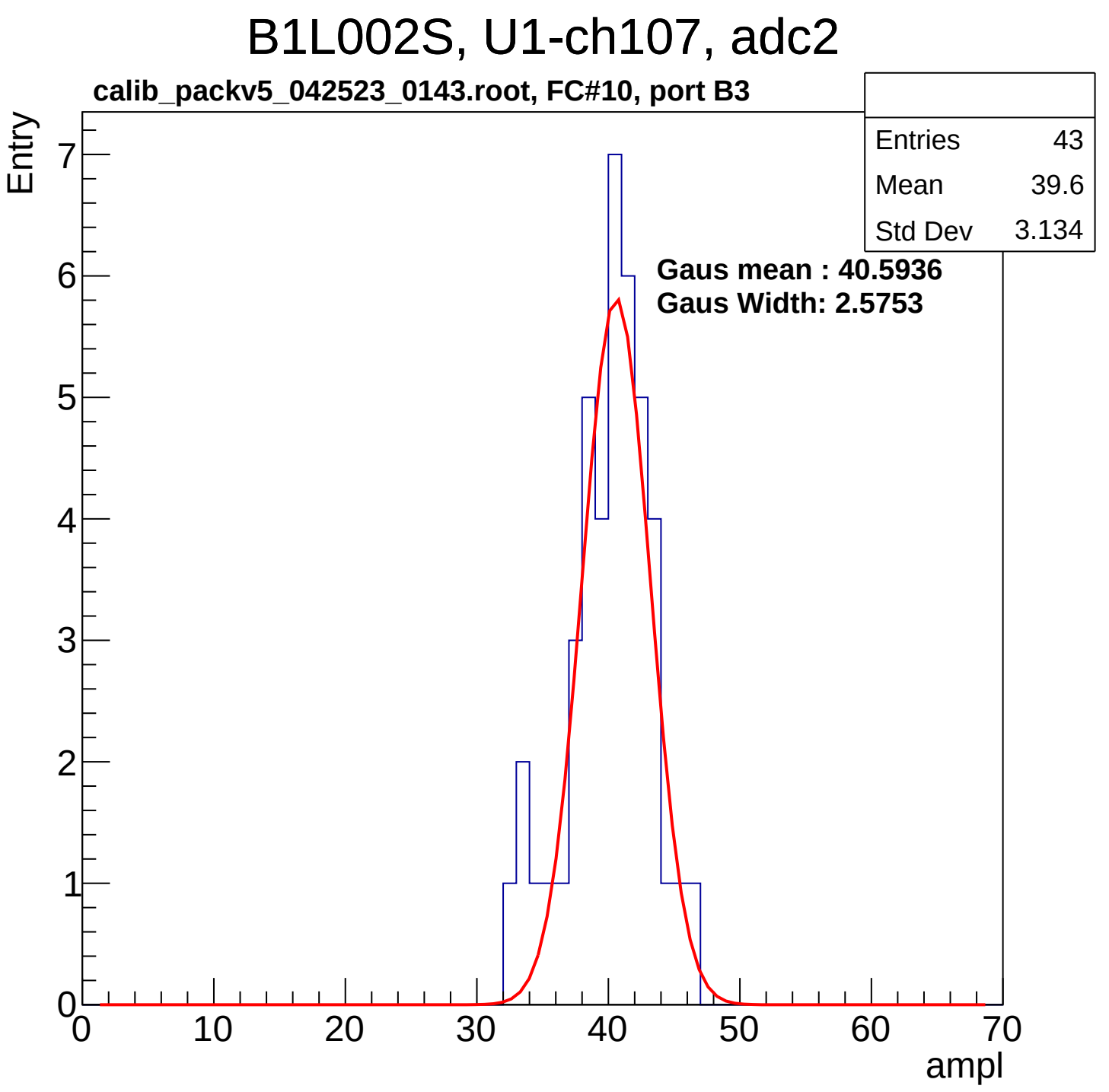
B1L002S, U1-ch107, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 39.6
Std Dev 3.134
Gaus mean : 40.5936
Gaus Width: 2.5753
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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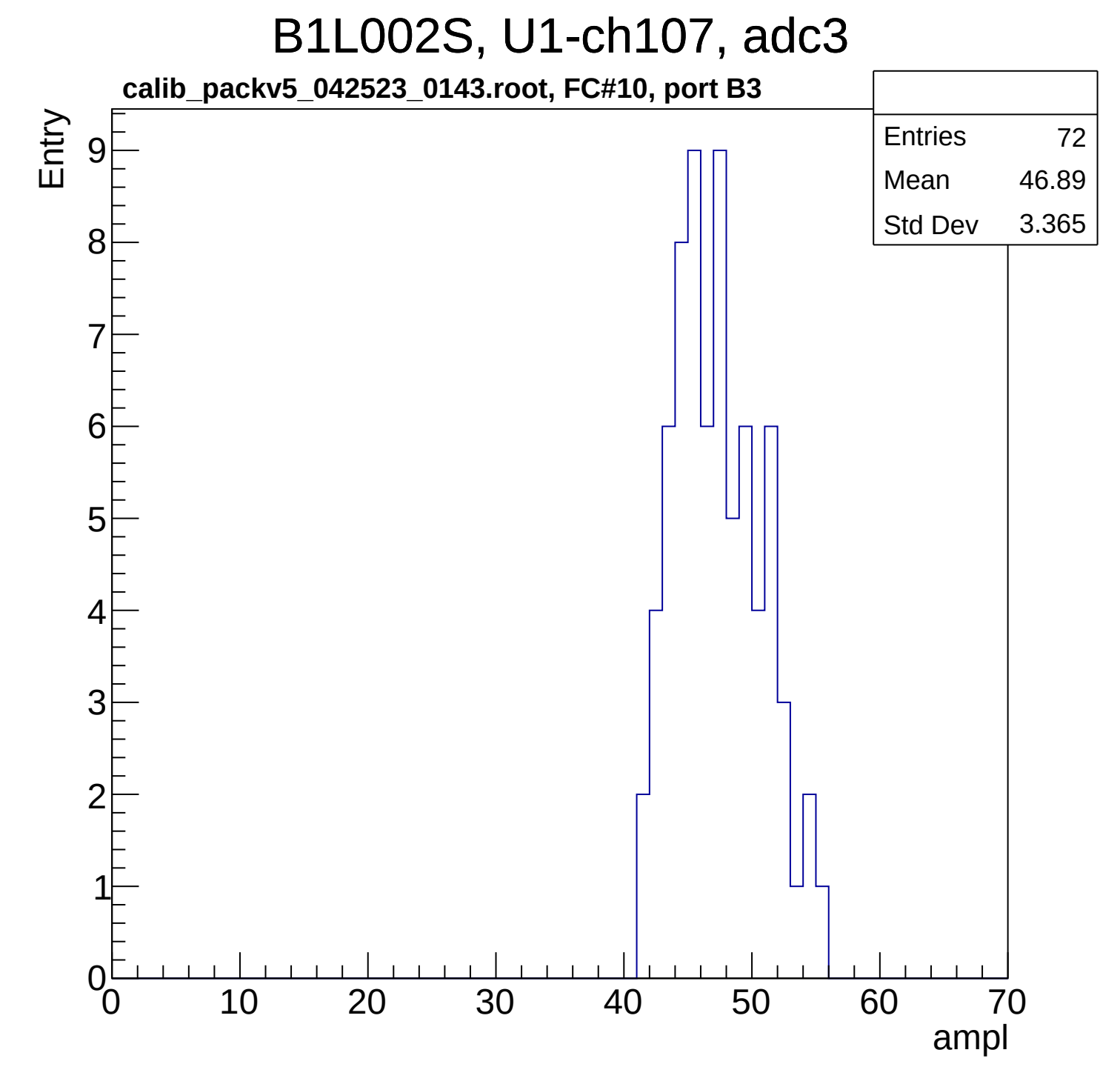
B1L002S, U1-ch107, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.89
Std Dev 3.365
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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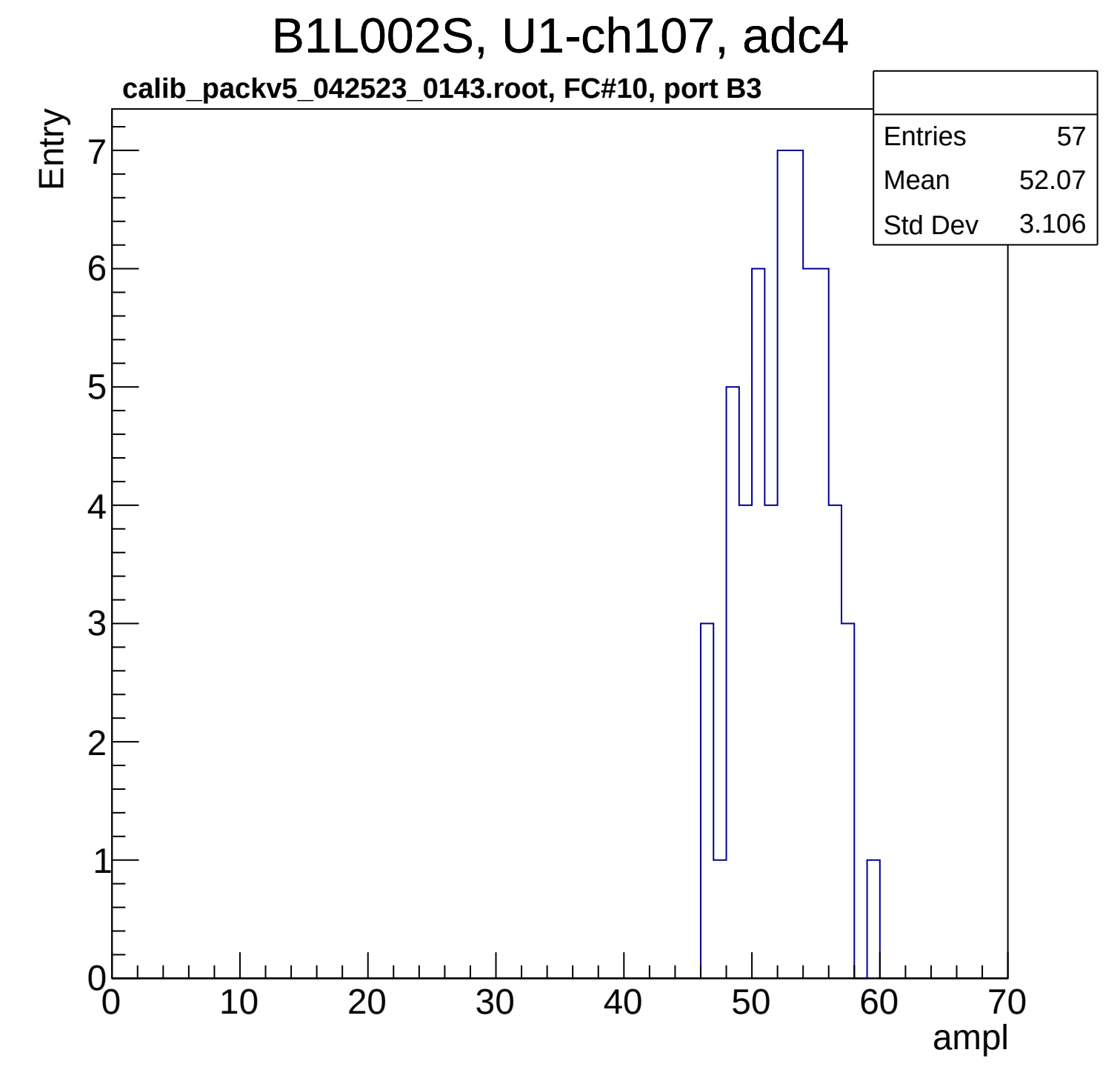
B1L002S, U1-ch107, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 52.07
Std Dev 3.106
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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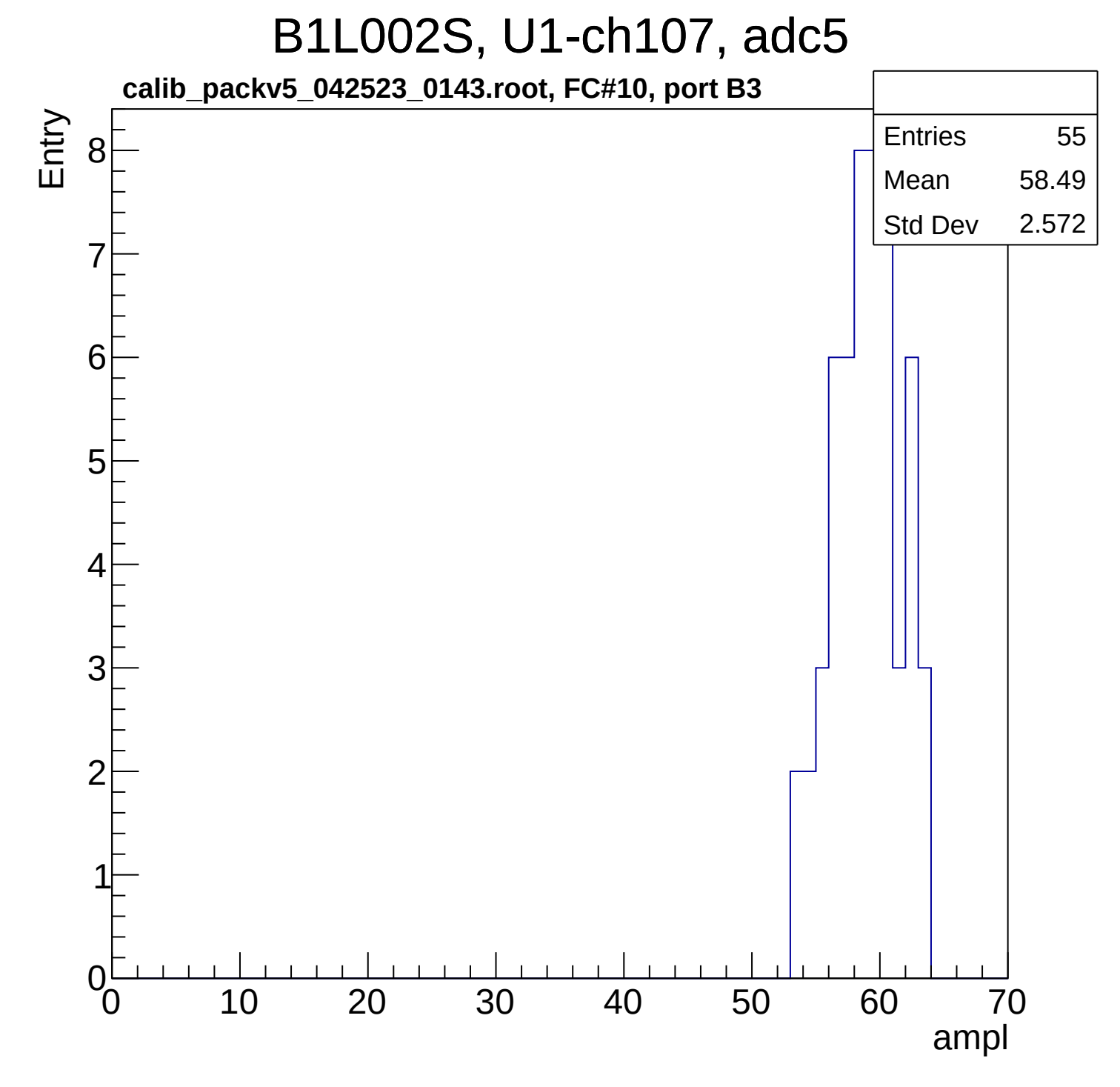
B1L002S, U1-ch107, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.49
Std Dev 2.572
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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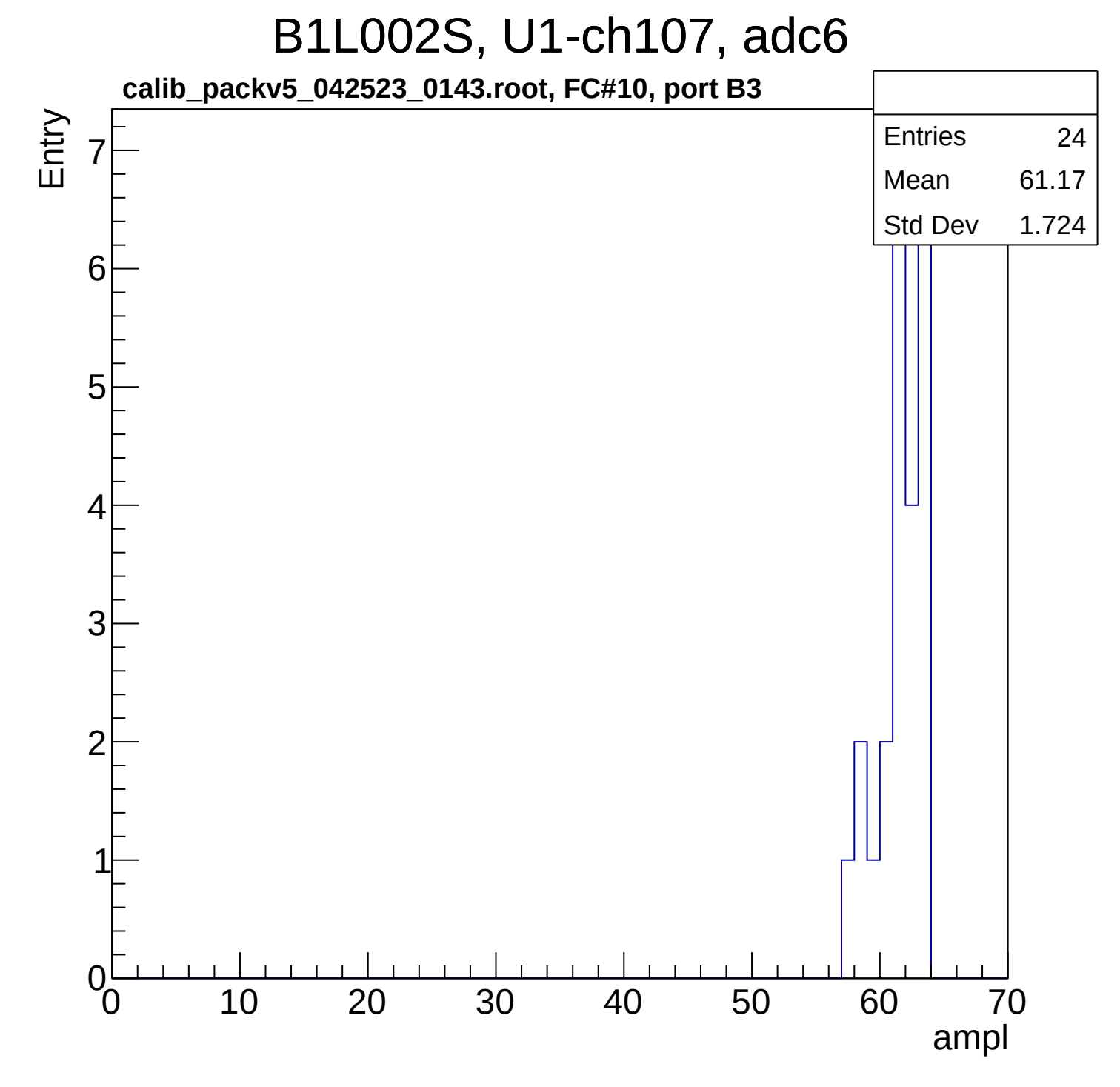
B1L002S, U1-ch107, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61.17
Std Dev 1.724
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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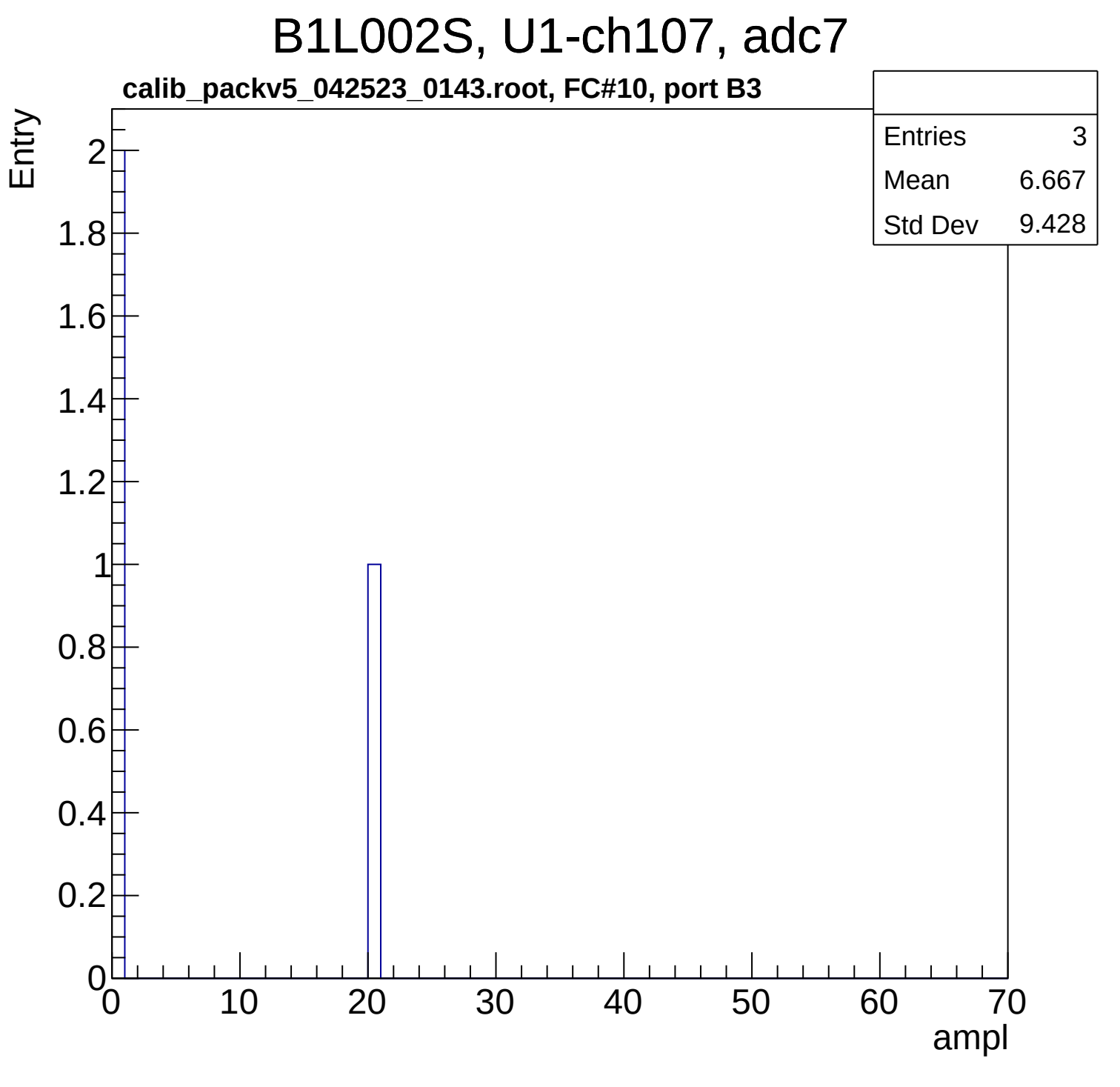
B1L002S, U1-ch107, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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30
40
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60
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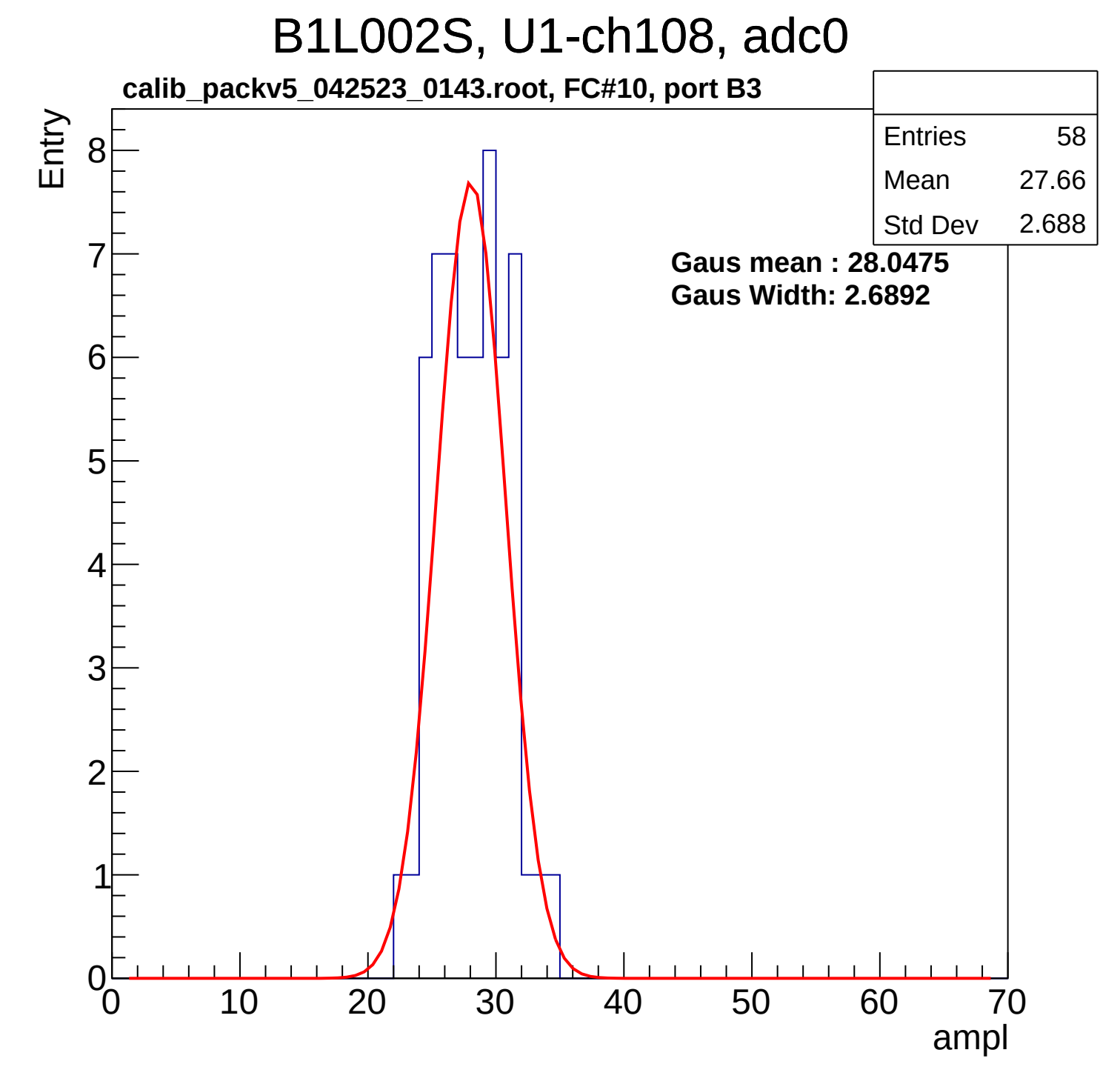
B1L002S, U1-ch108, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.66
Std Dev 2.688
Gaus mean : 28.0475
Gaus Width: 2.6892
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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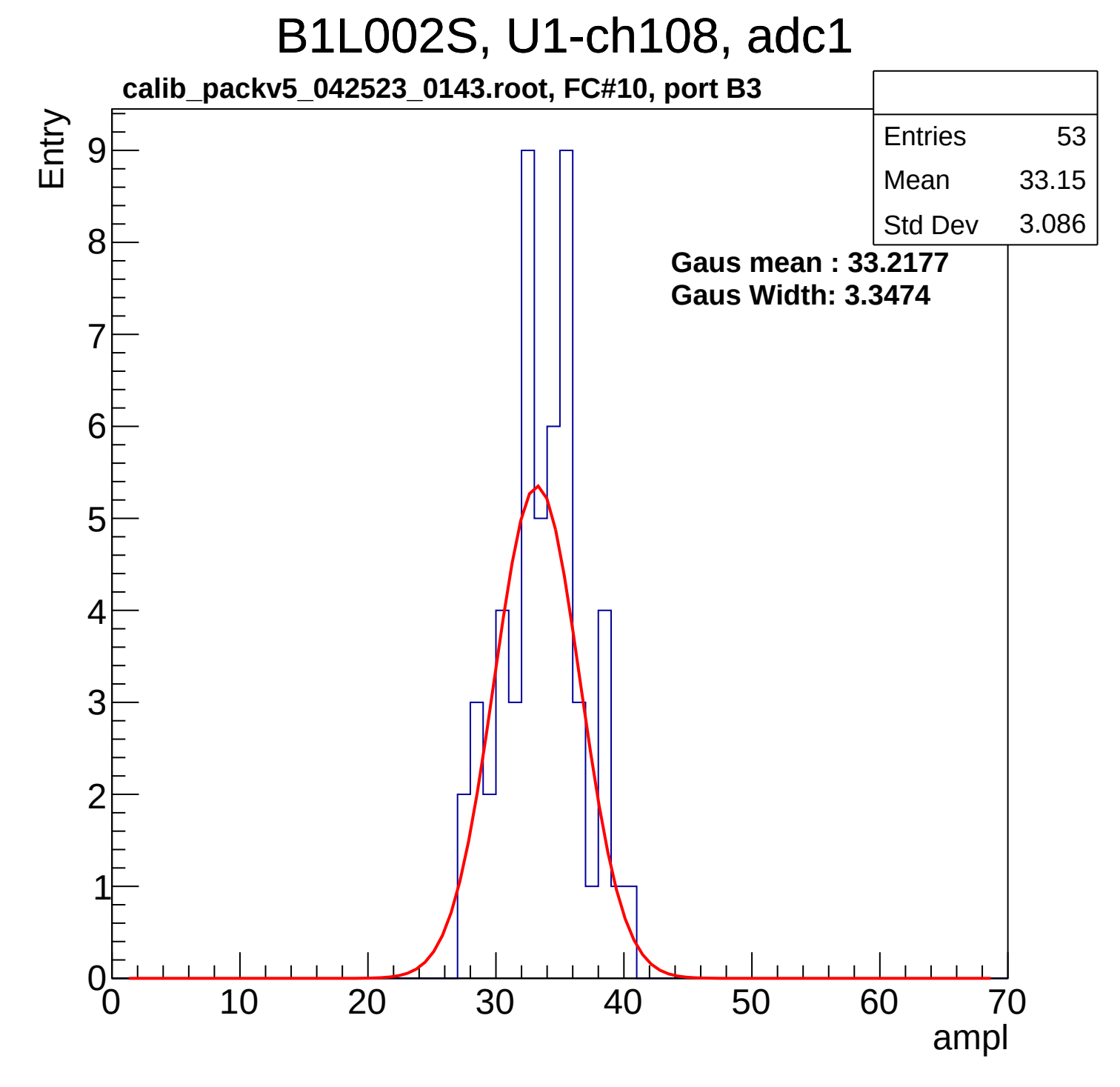
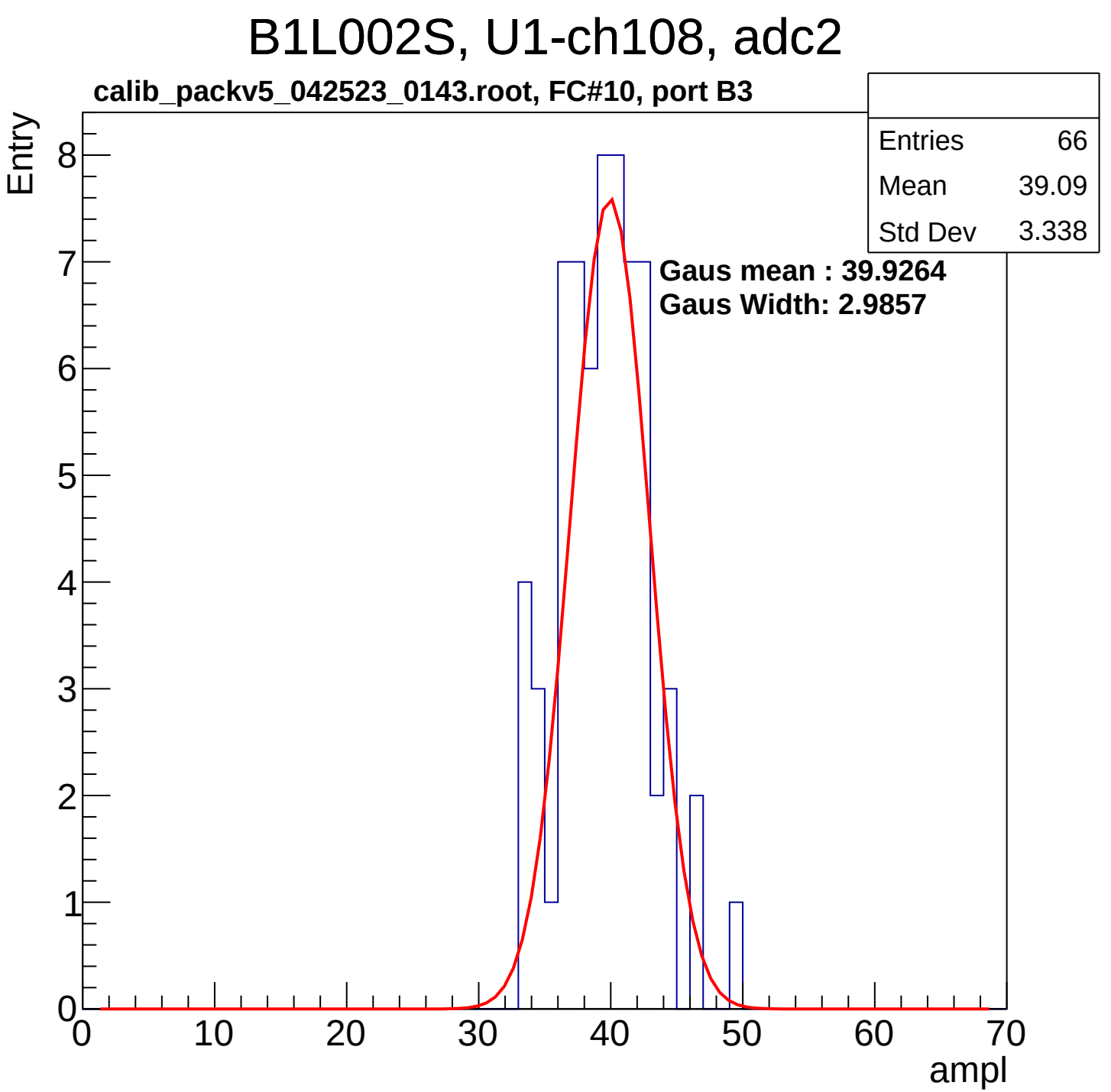
60
70


B1L002S, U1-ch108, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 33.15
Std Dev 3.086
Gaus mean : 33.2177
Gaus Width: 3.3474
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch108, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 39.09
Std Dev 3.338
Gaus mean : 39.9264
Gaus Width: 2.9857
Entry
ampl
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1
2
3
4
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6
7
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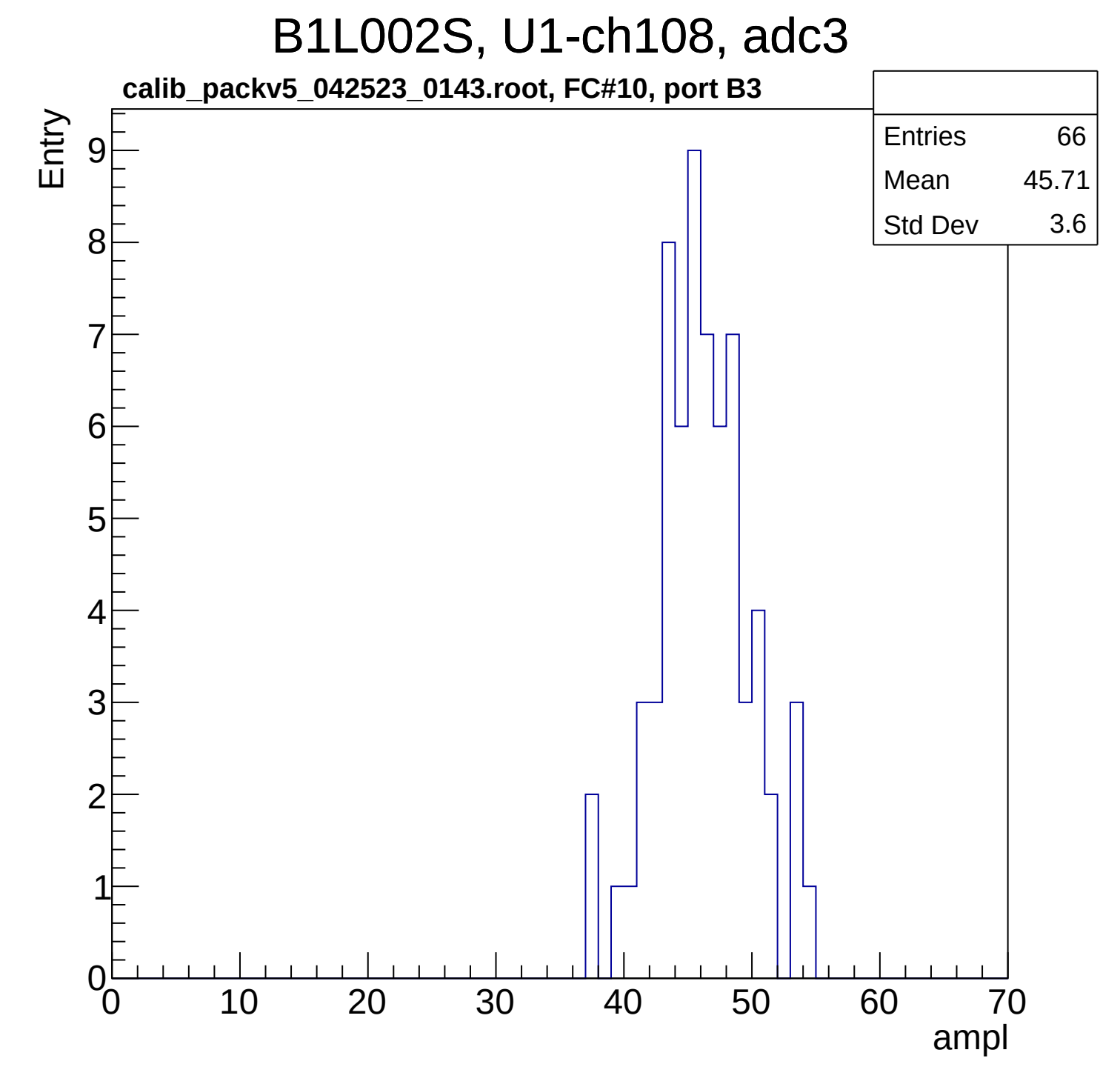
B1L002S, U1-ch108, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 45.71
Std Dev 3.6
Entry
ampl
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2
3
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6
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0
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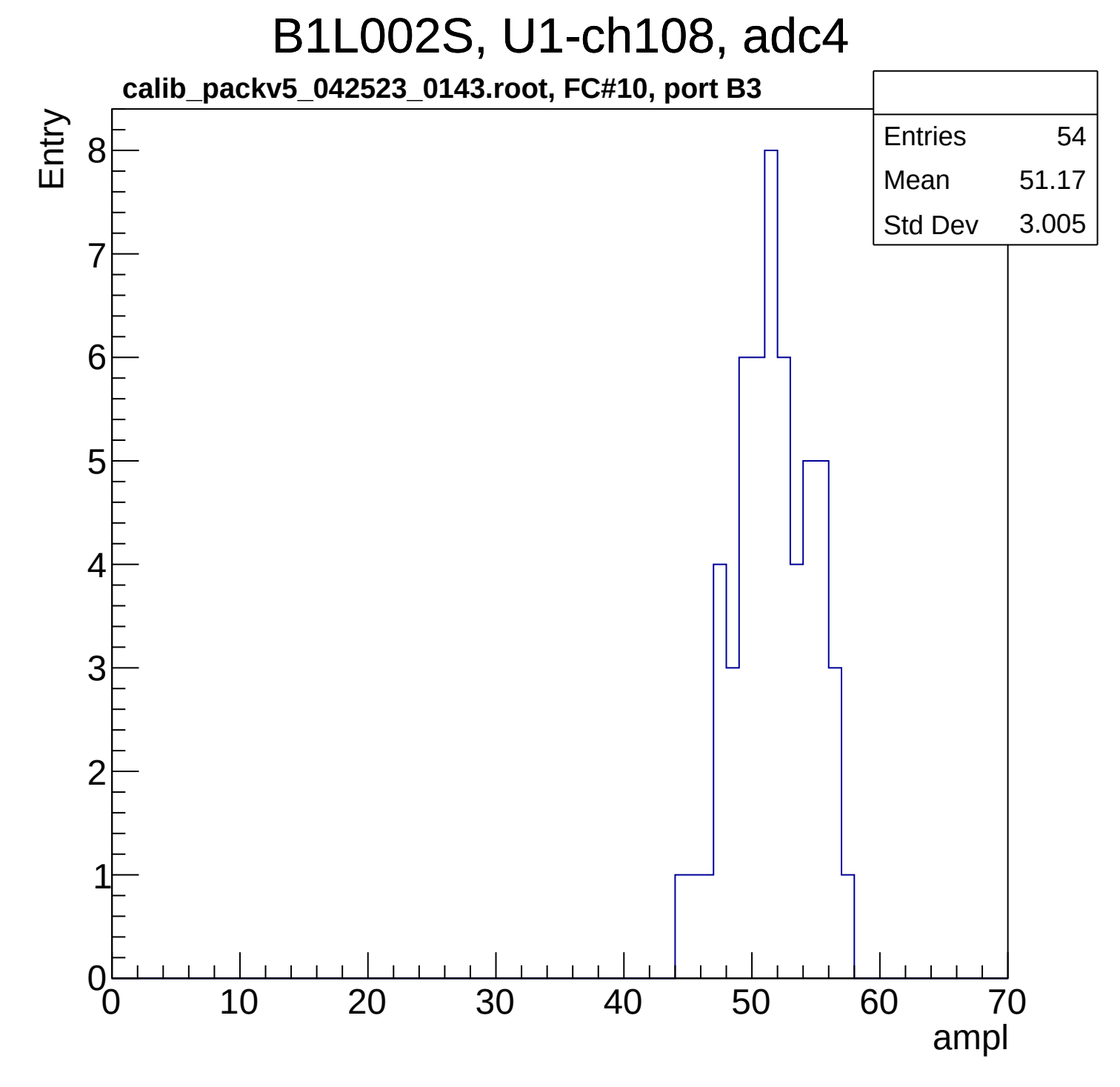
B1L002S, U1-ch108, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 51.17
Std Dev 3.005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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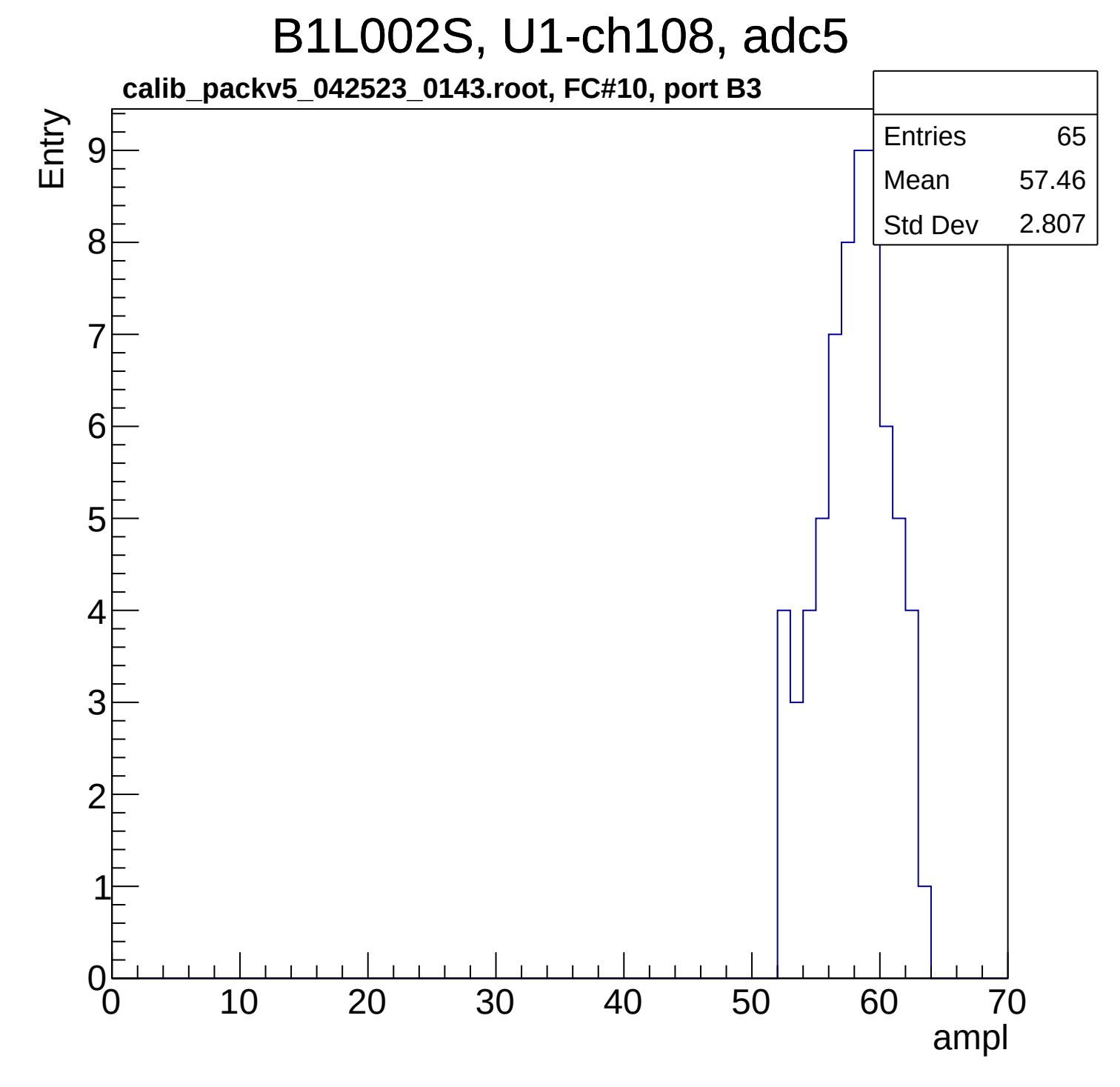
B1L002S, U1-ch108, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 57.46
Std Dev 2.807
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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60
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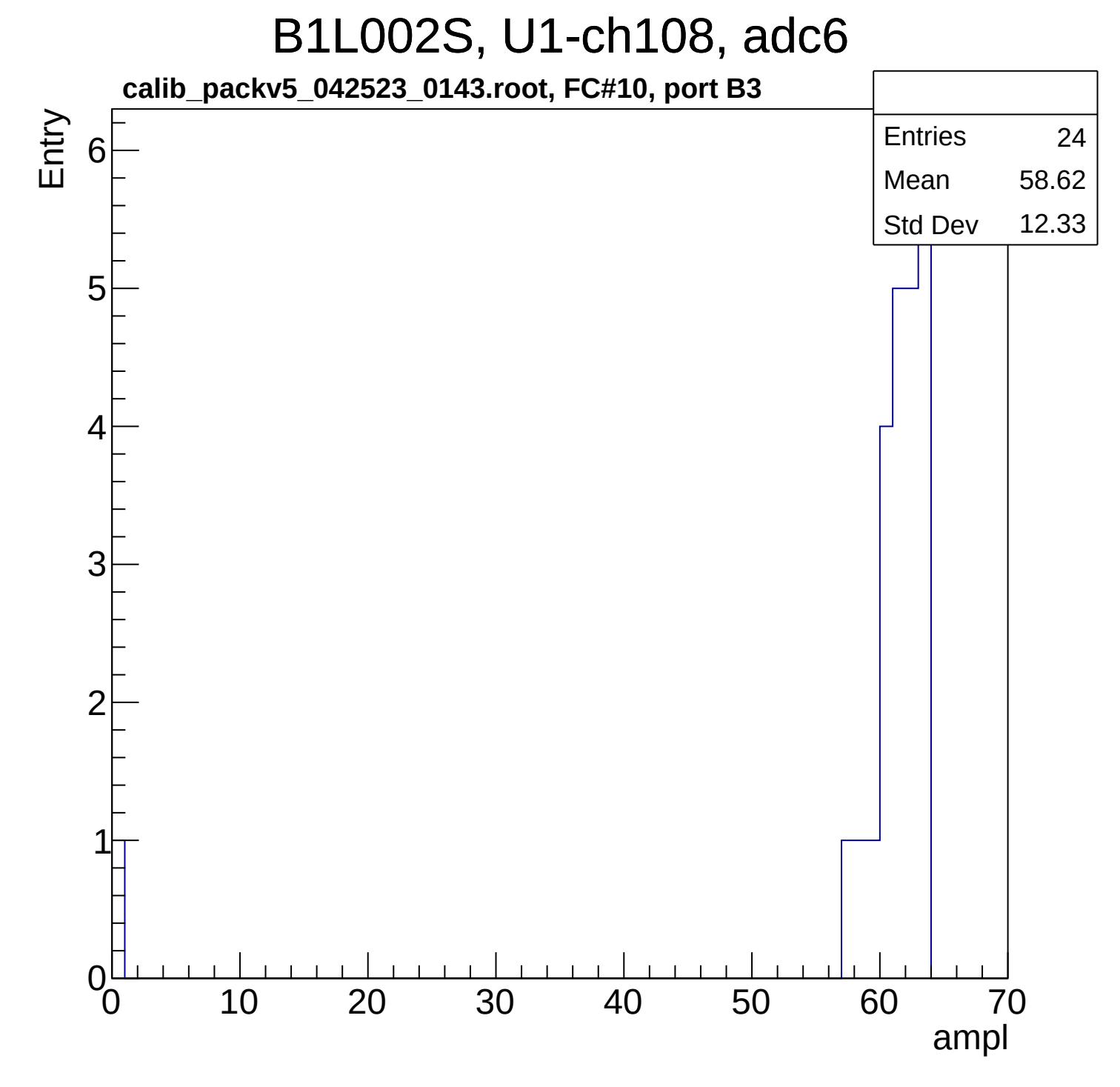
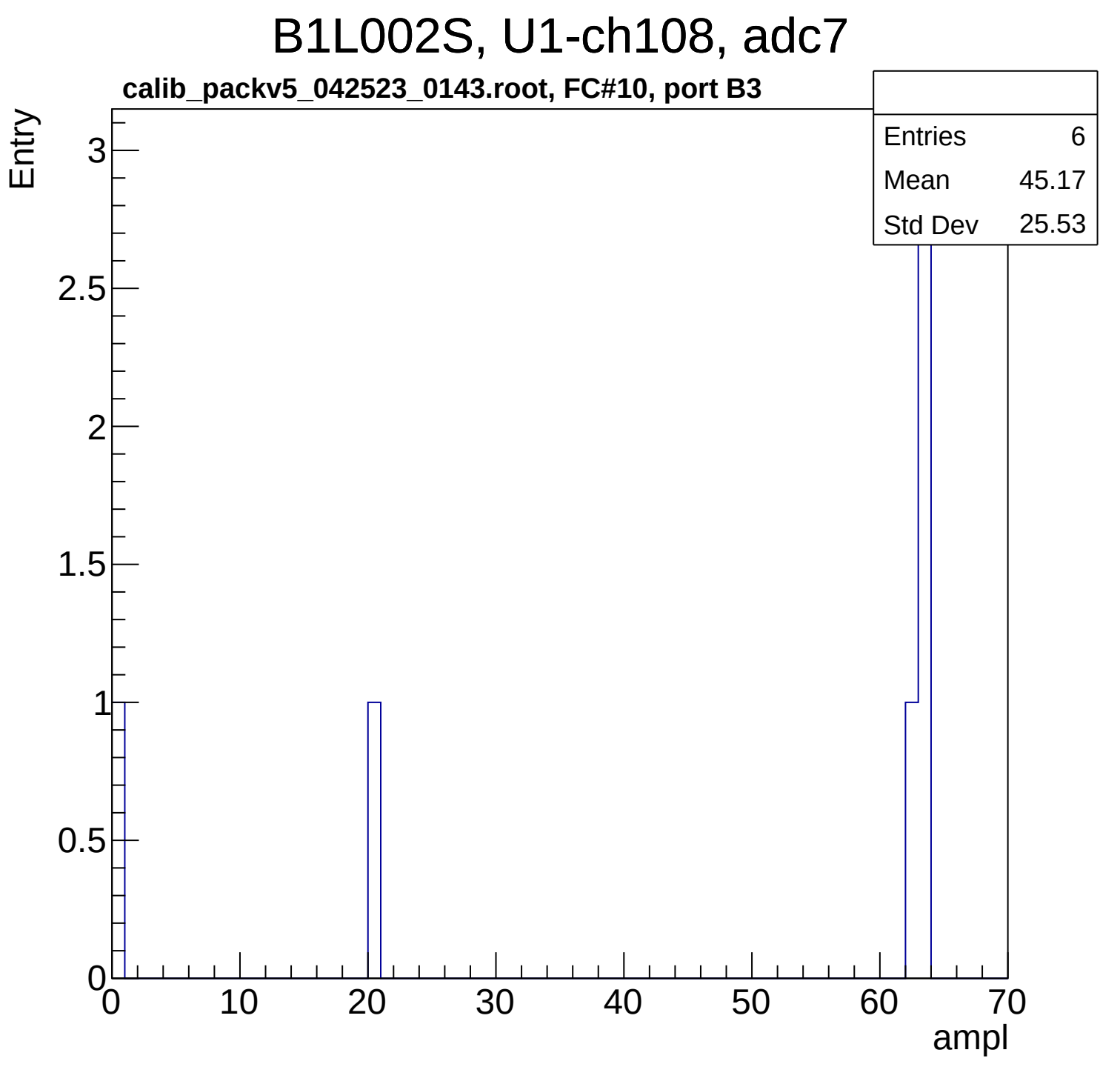
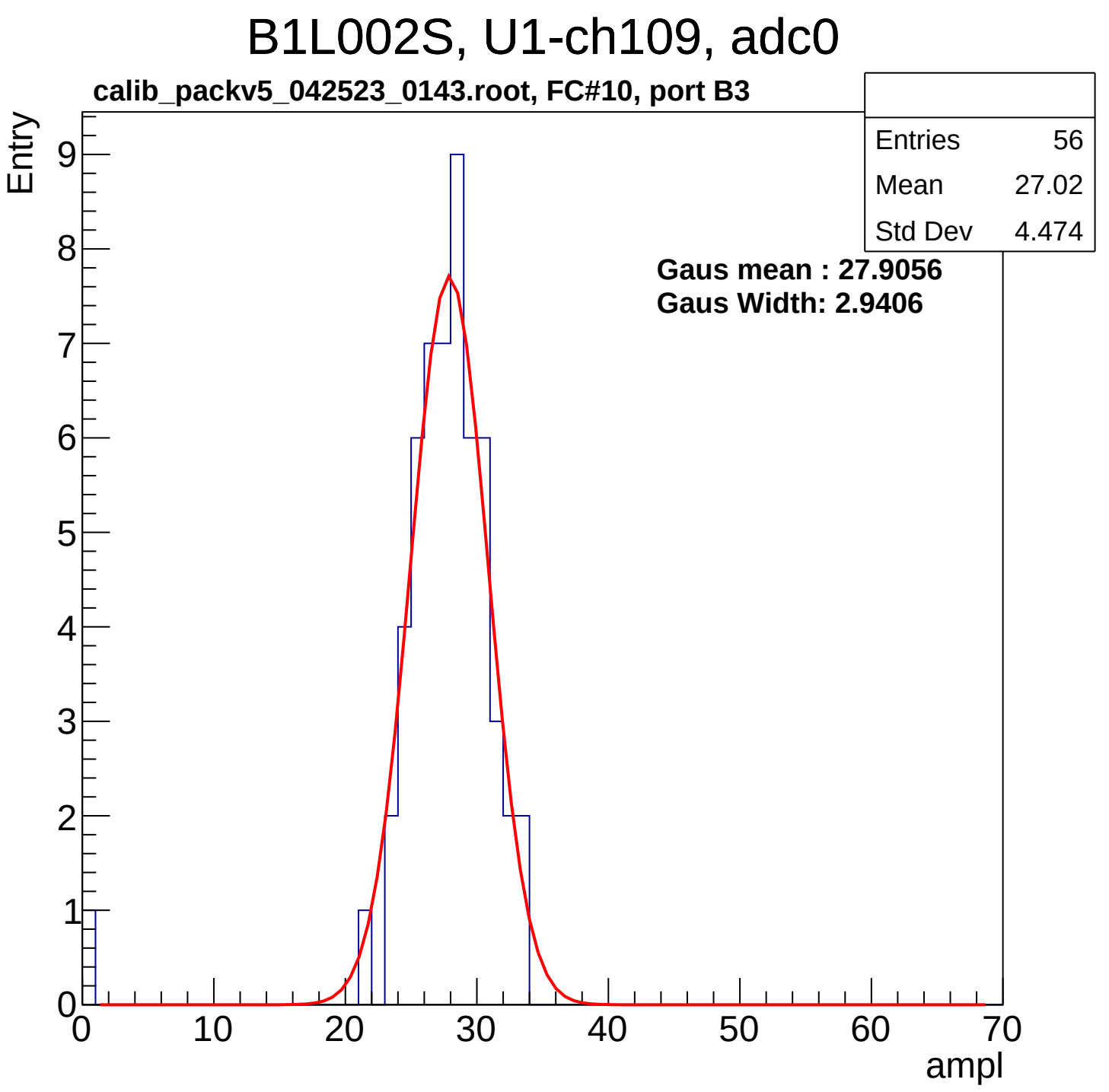

B1L002S, U1-ch108, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.62
Std Dev 12.33
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch108, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 45.17
Std Dev 25.53
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U1-ch109, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.02
Std Dev 4.474
Gaus mean : 27.9056
Gaus Width: 2.9406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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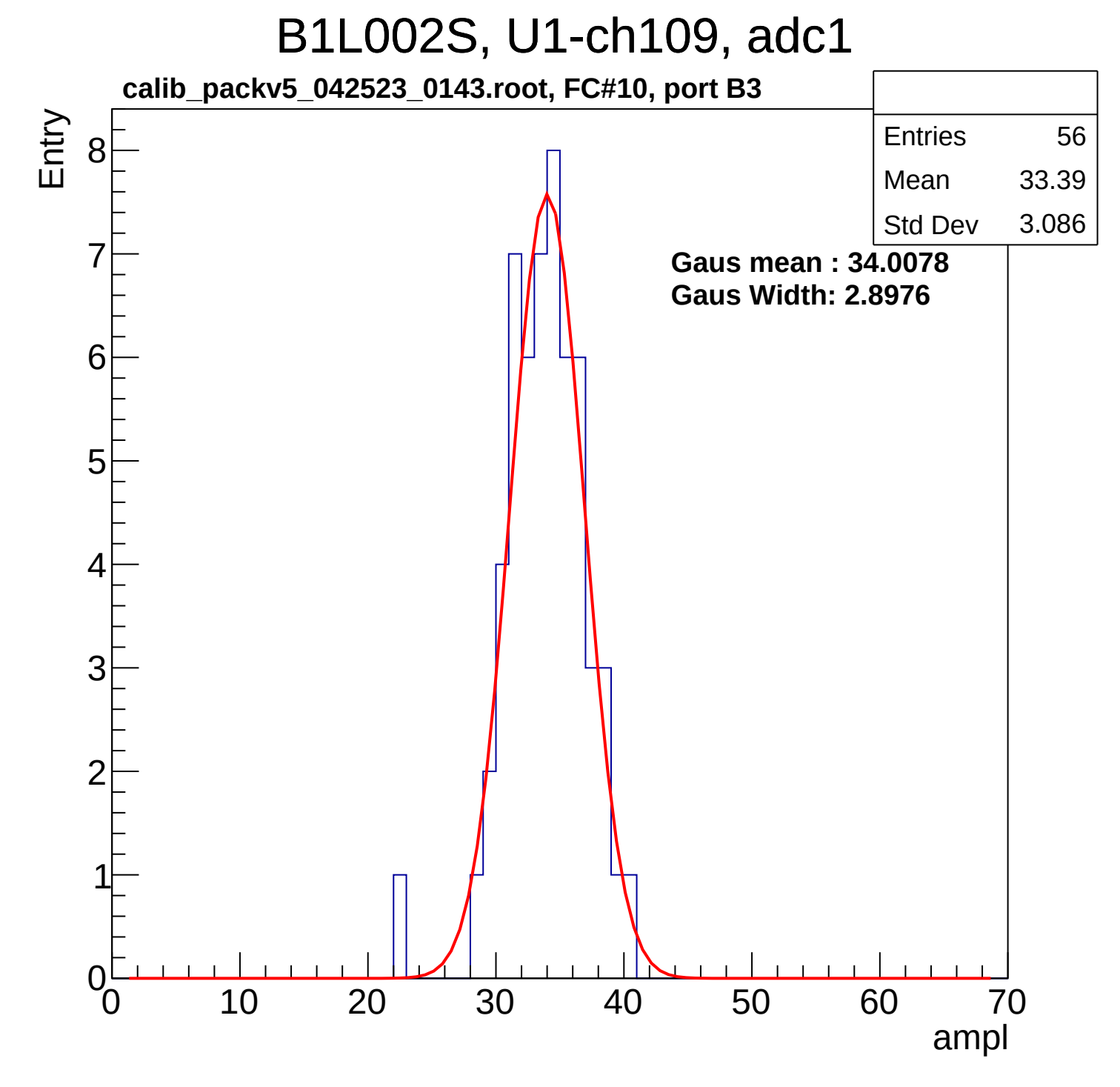
B1L002S, U1-ch109, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 33.39
Std Dev 3.086
Gaus mean : 34.0078
Gaus Width: 2.8976
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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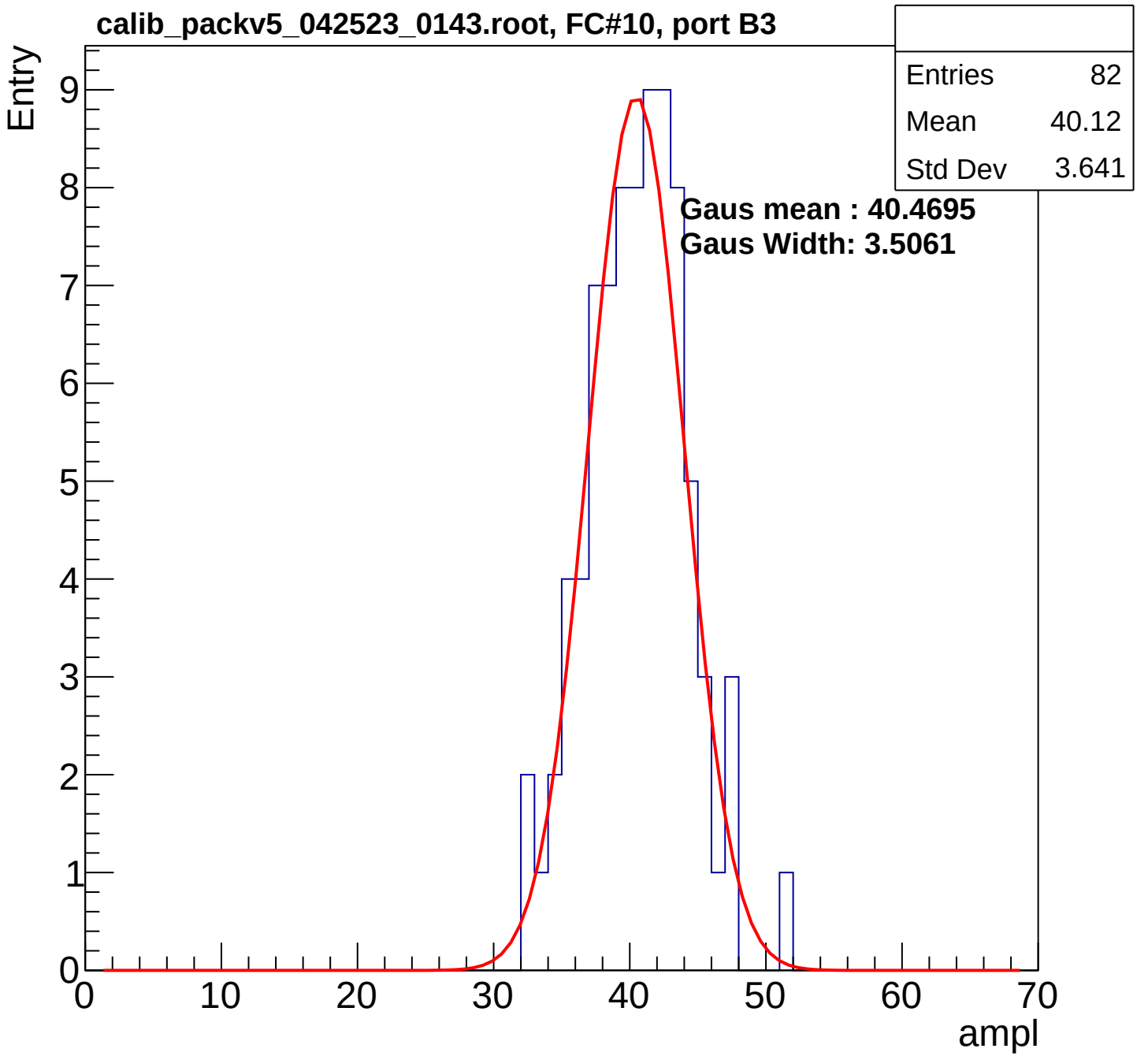
B1L002S, U1-ch109, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 40.12
Std Dev 3.641
Gaus mean : 40.4695
Gaus Width: 3.5061
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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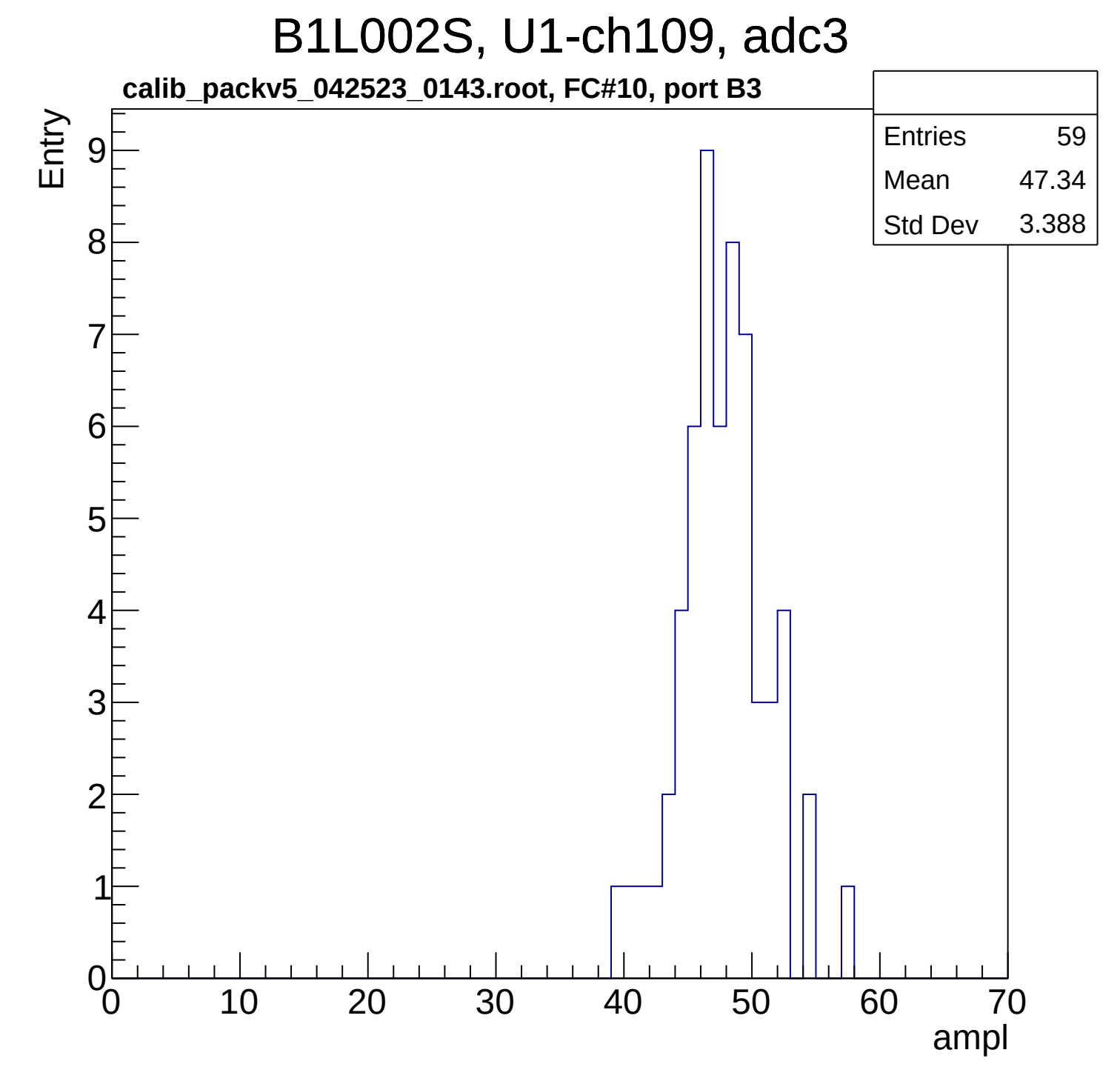
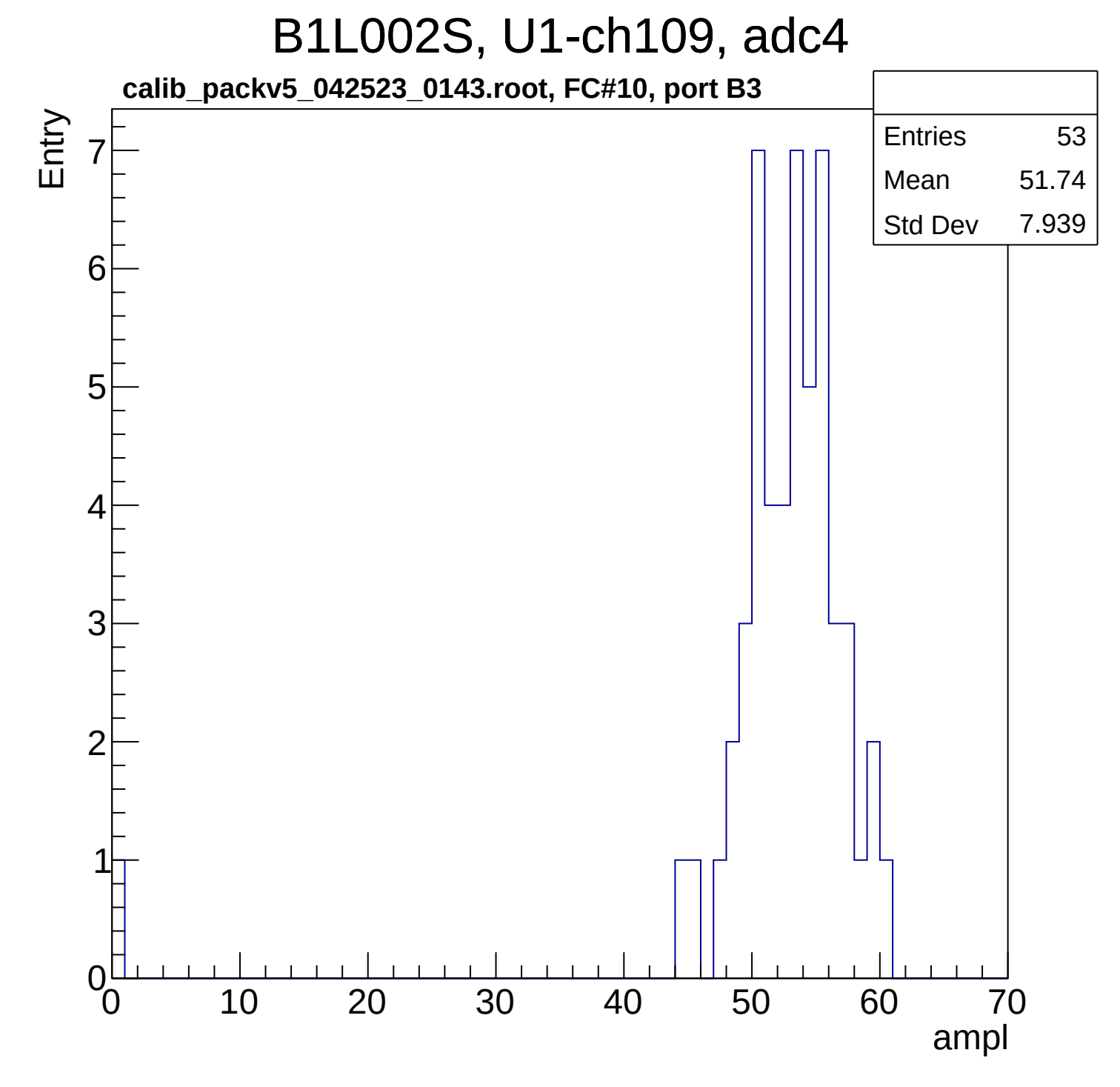

B1L002S, U1-ch109, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.34
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch109, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 51.74
Std Dev 7.939
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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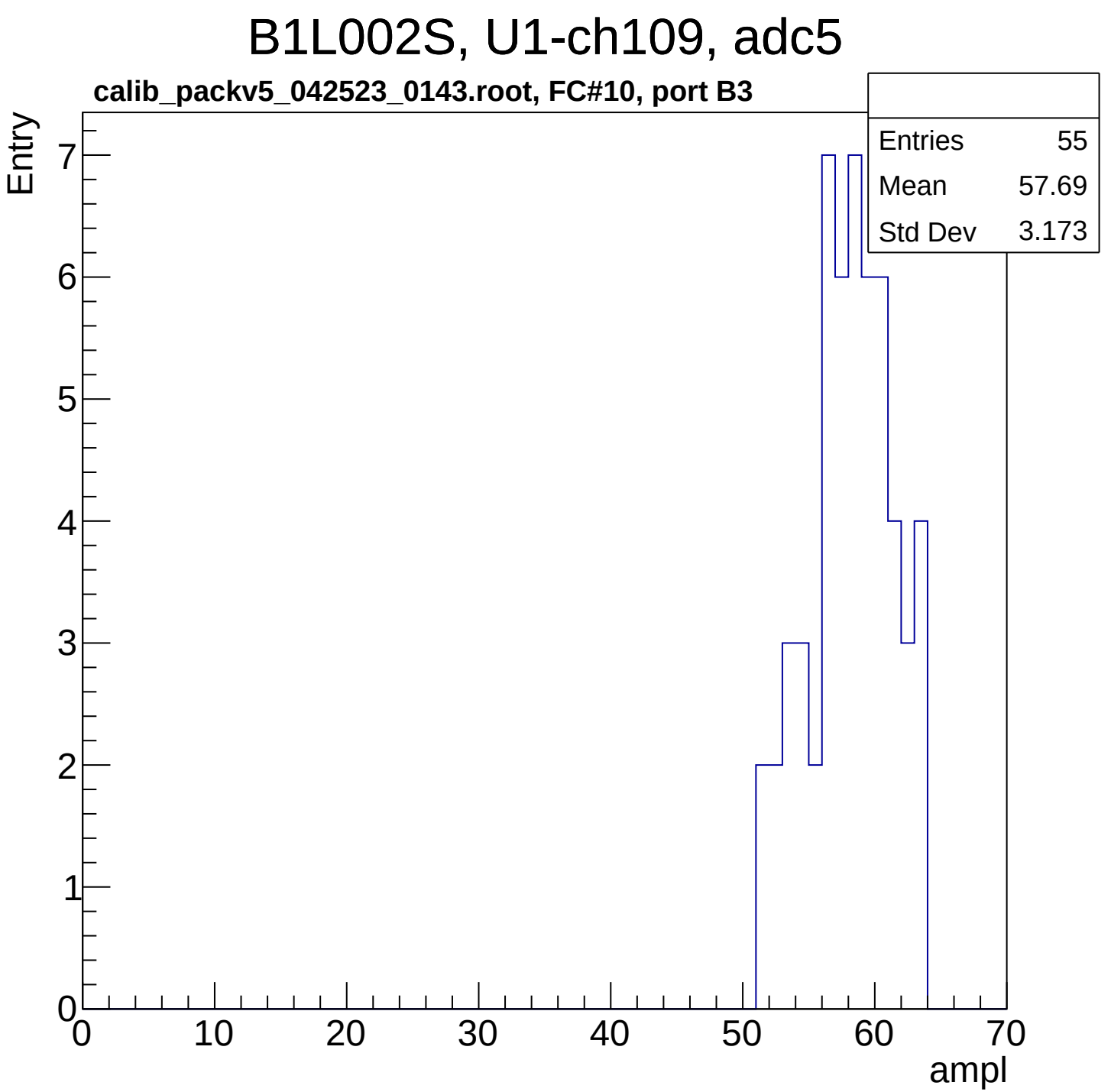
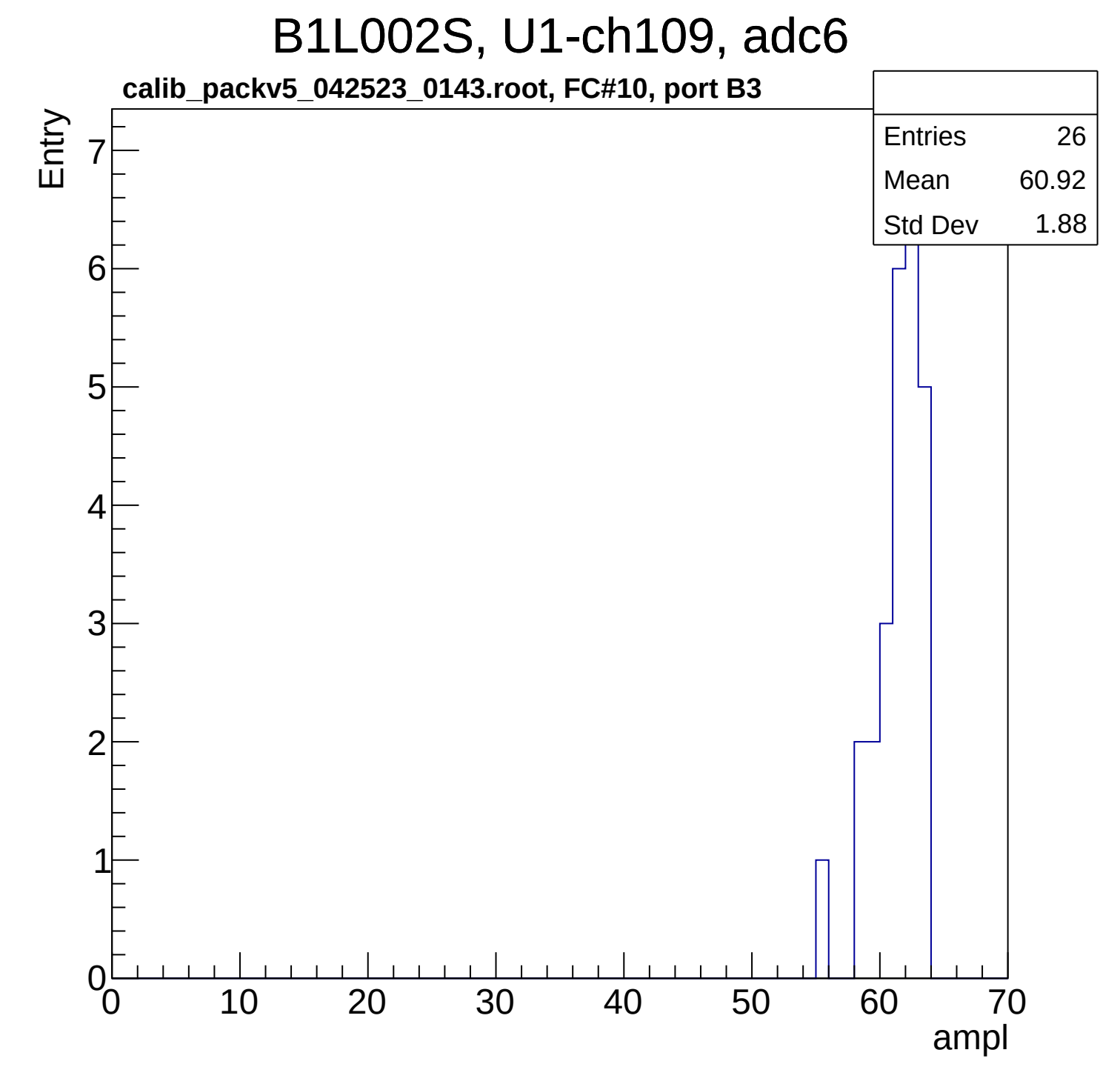
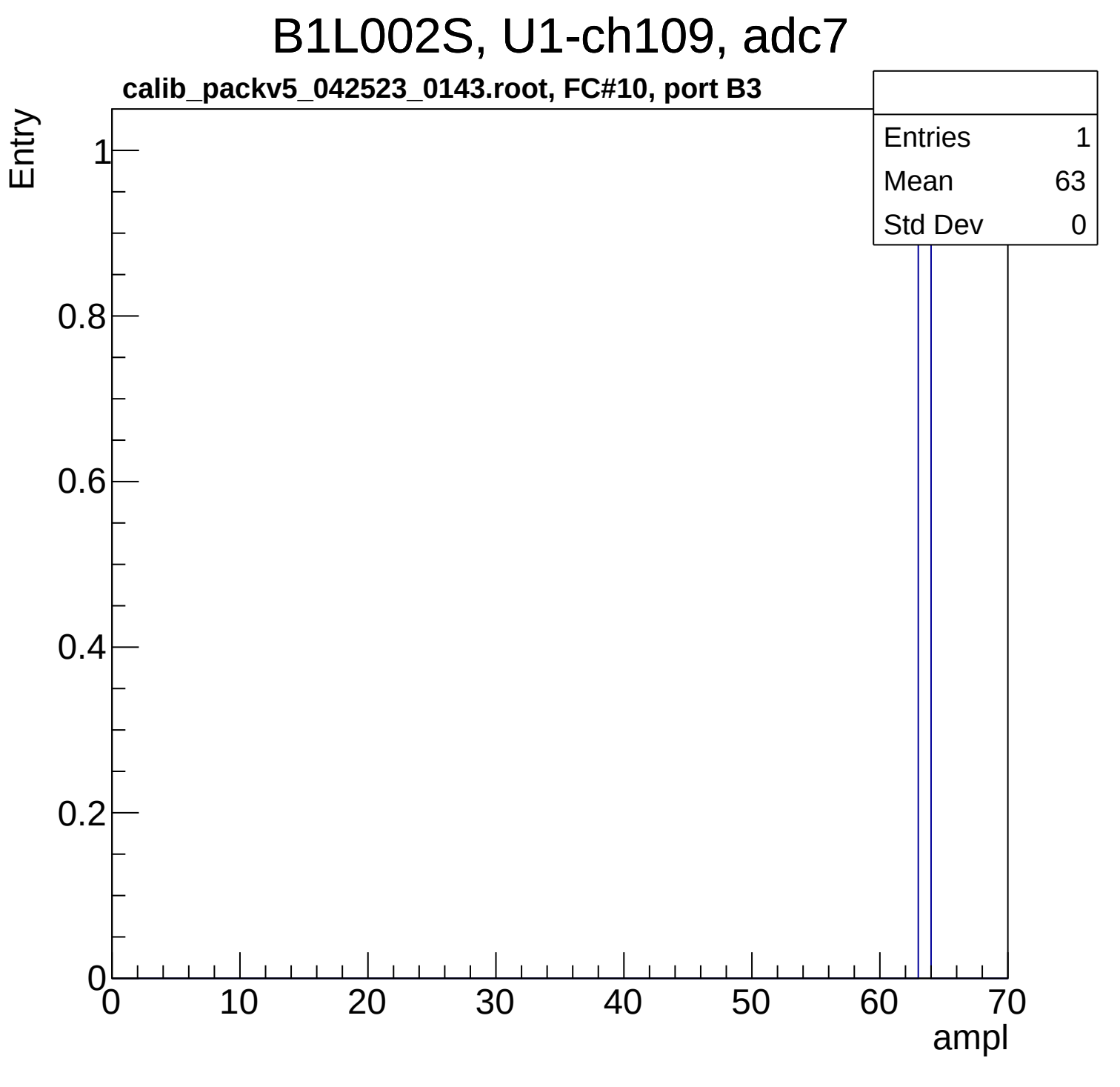
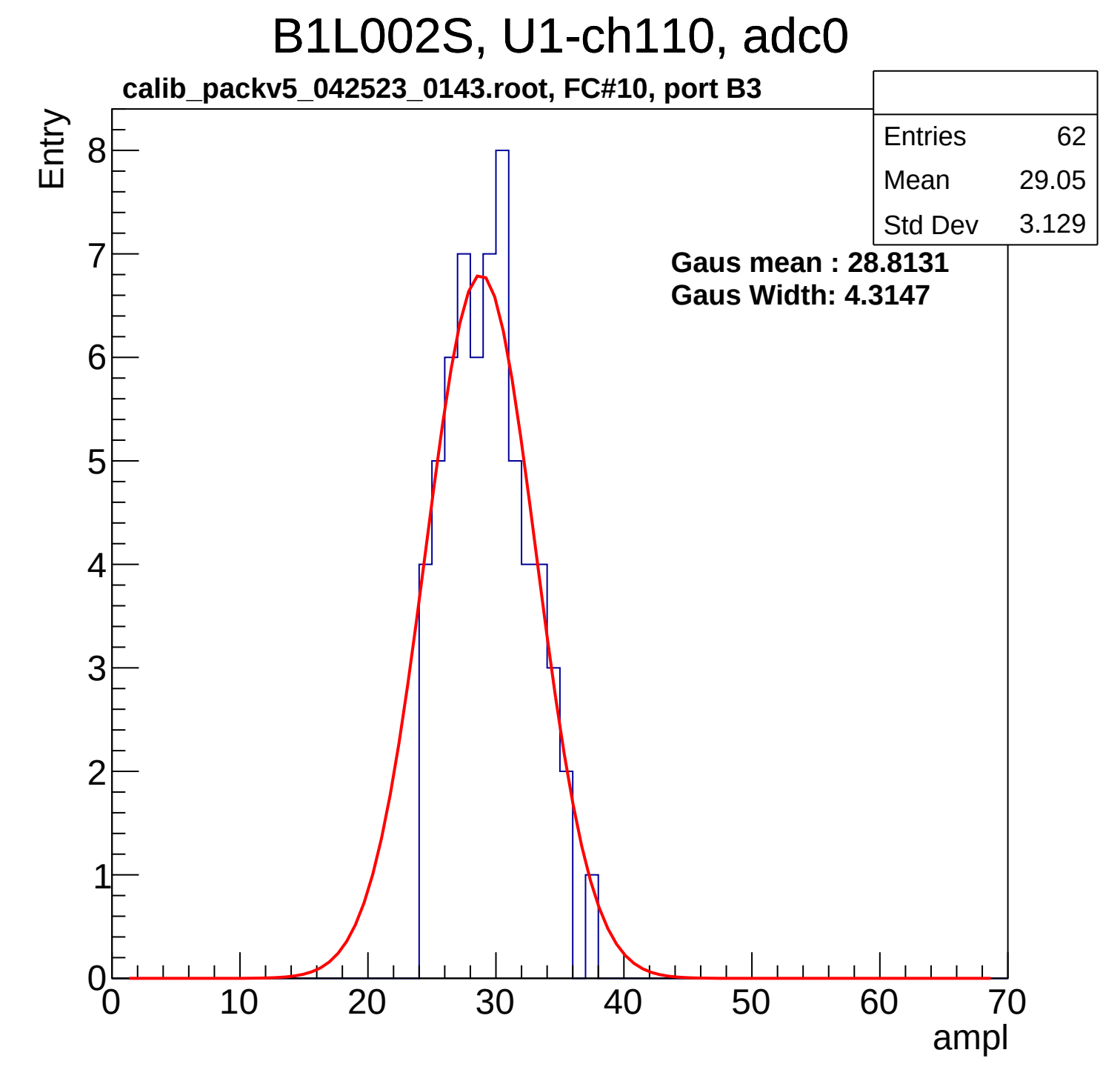
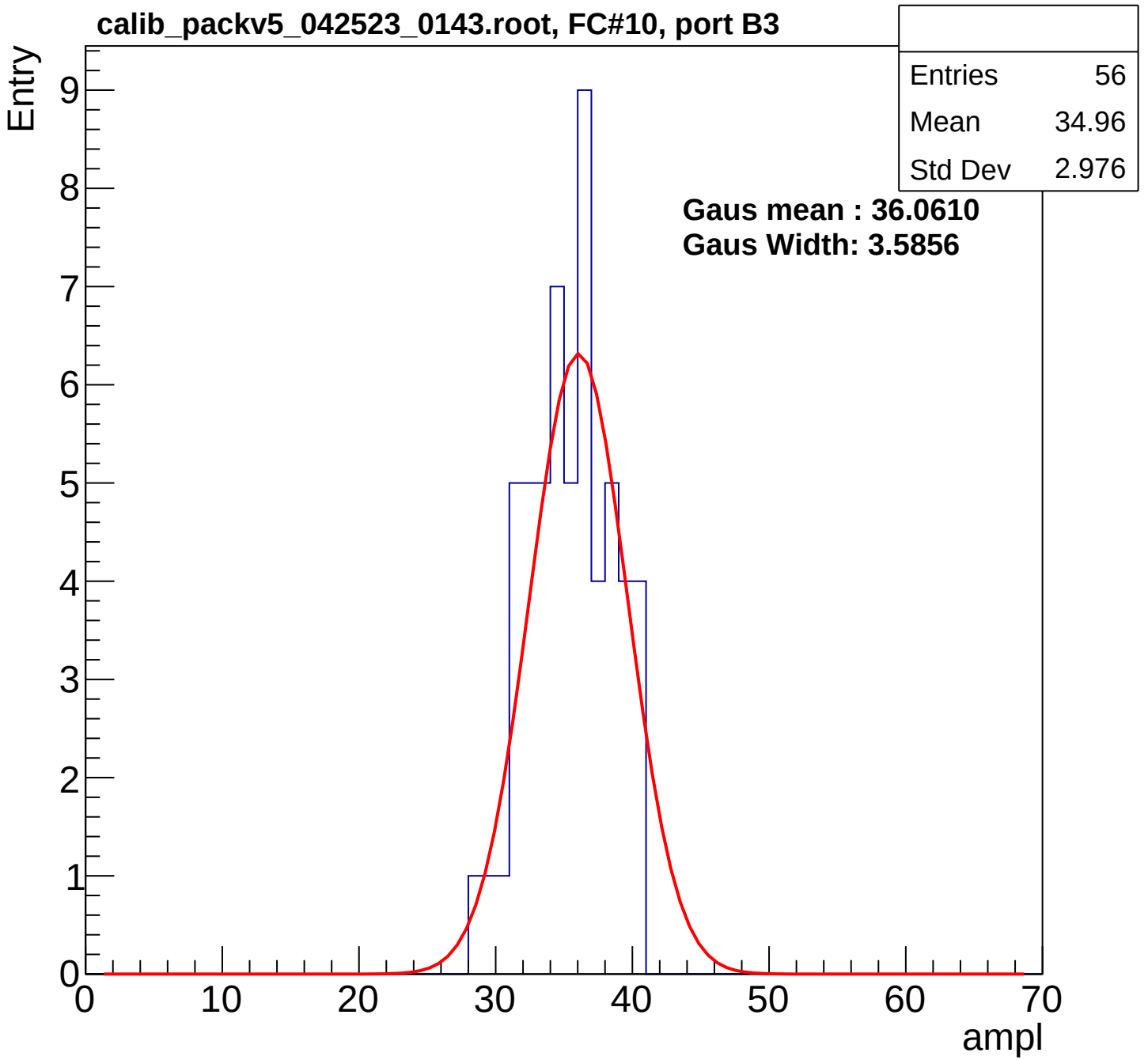
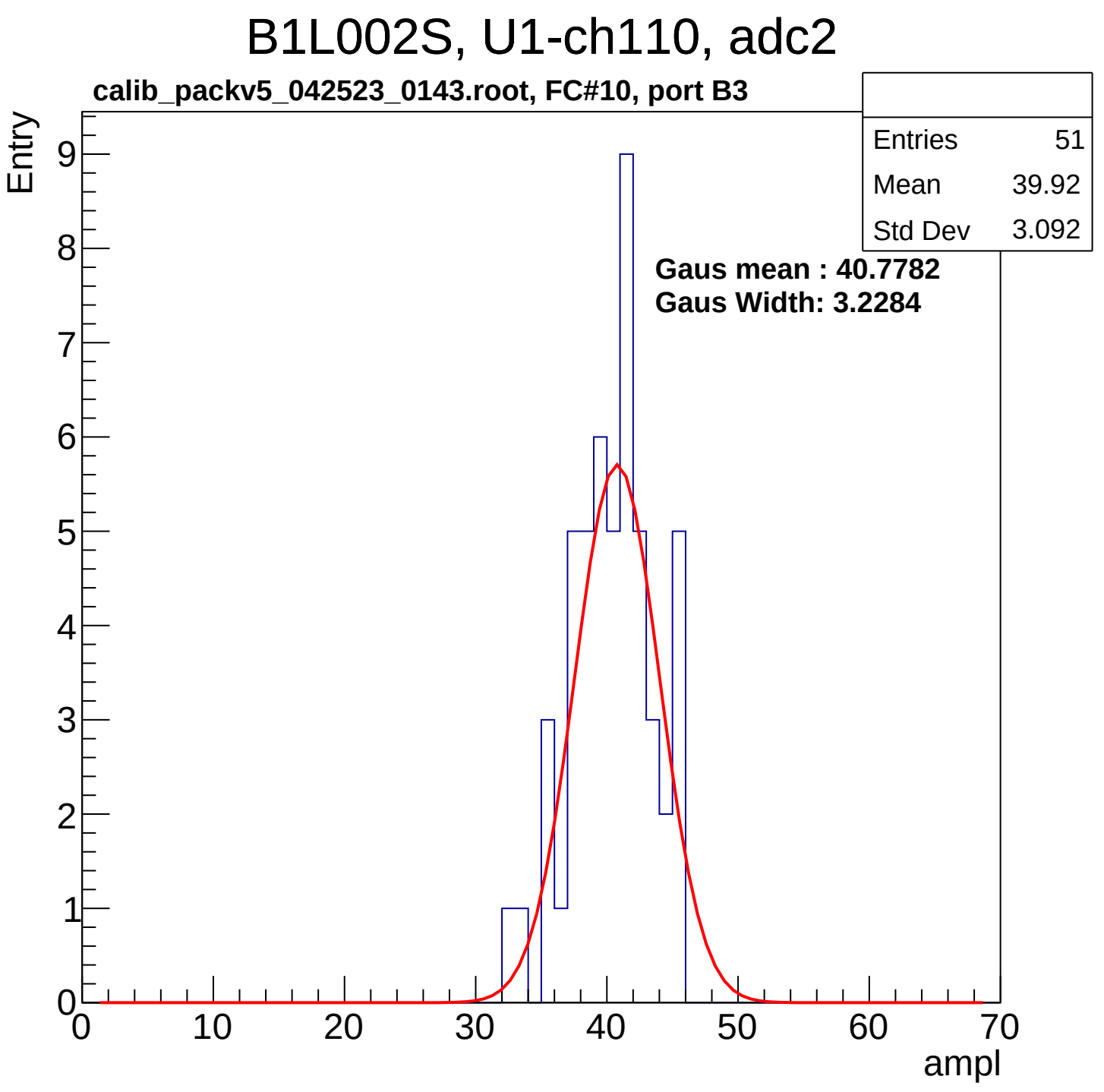

B1L002S, U1-ch109, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.69
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch109, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 60.92
Std Dev 1.88
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch109, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch110, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 29.05
Std Dev 3.129
Gaus mean : 28.8131
Gaus Width: 4.3147
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch110, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 34.96
Std Dev 2.976
Gaus mean : 36.0610
Gaus Width: 3.5856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch110, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 39.92
Std Dev 3.092
Gaus mean : 40.7782
Gaus Width: 3.2284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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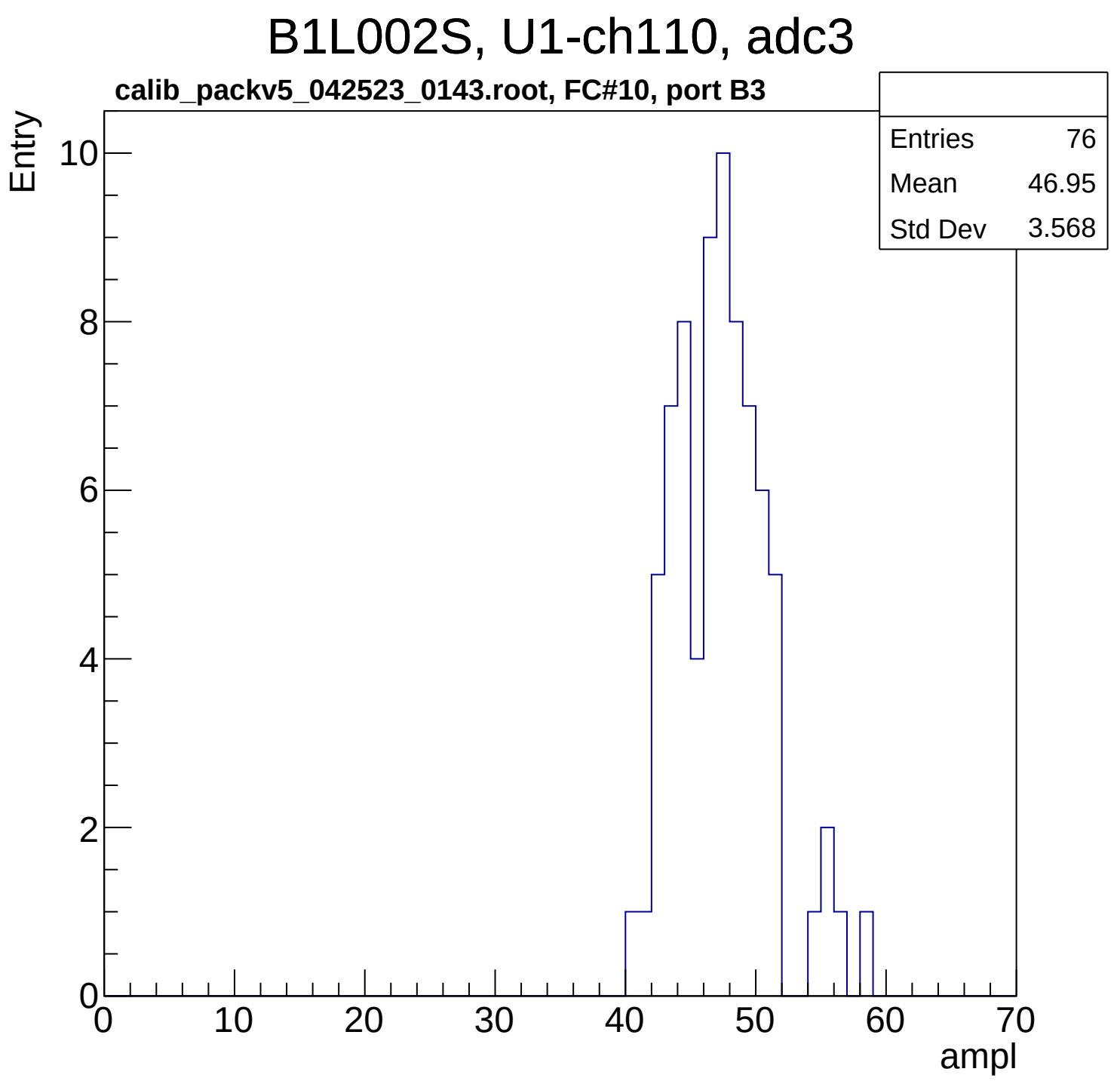
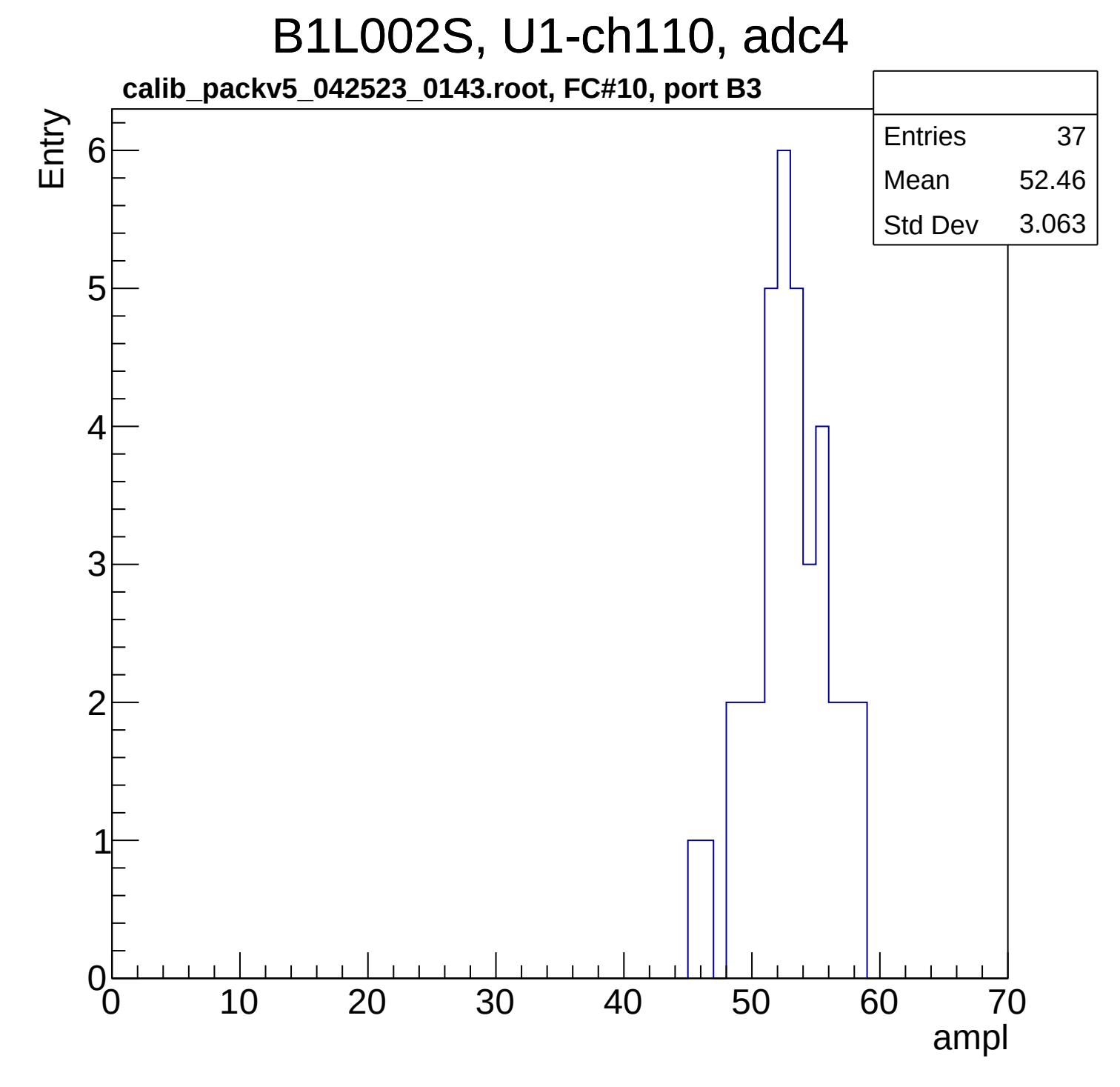
60
70


B1L002S, U1-ch110, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 46.95
Std Dev 3.568
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch110, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 52.46
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
0
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20
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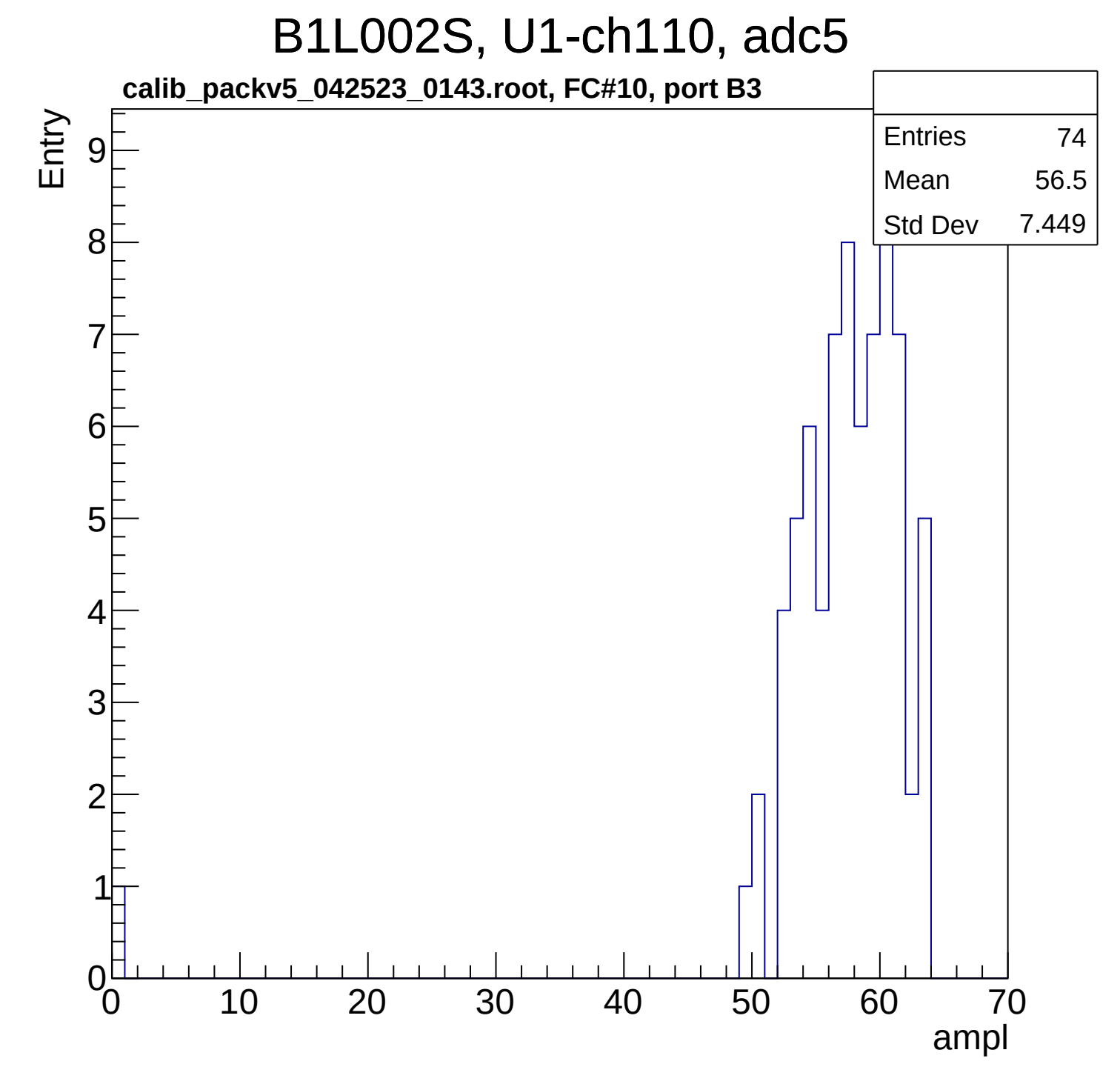
40
50
60
70


B1L002S, U1-ch110, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 56.5
Std Dev 7.449
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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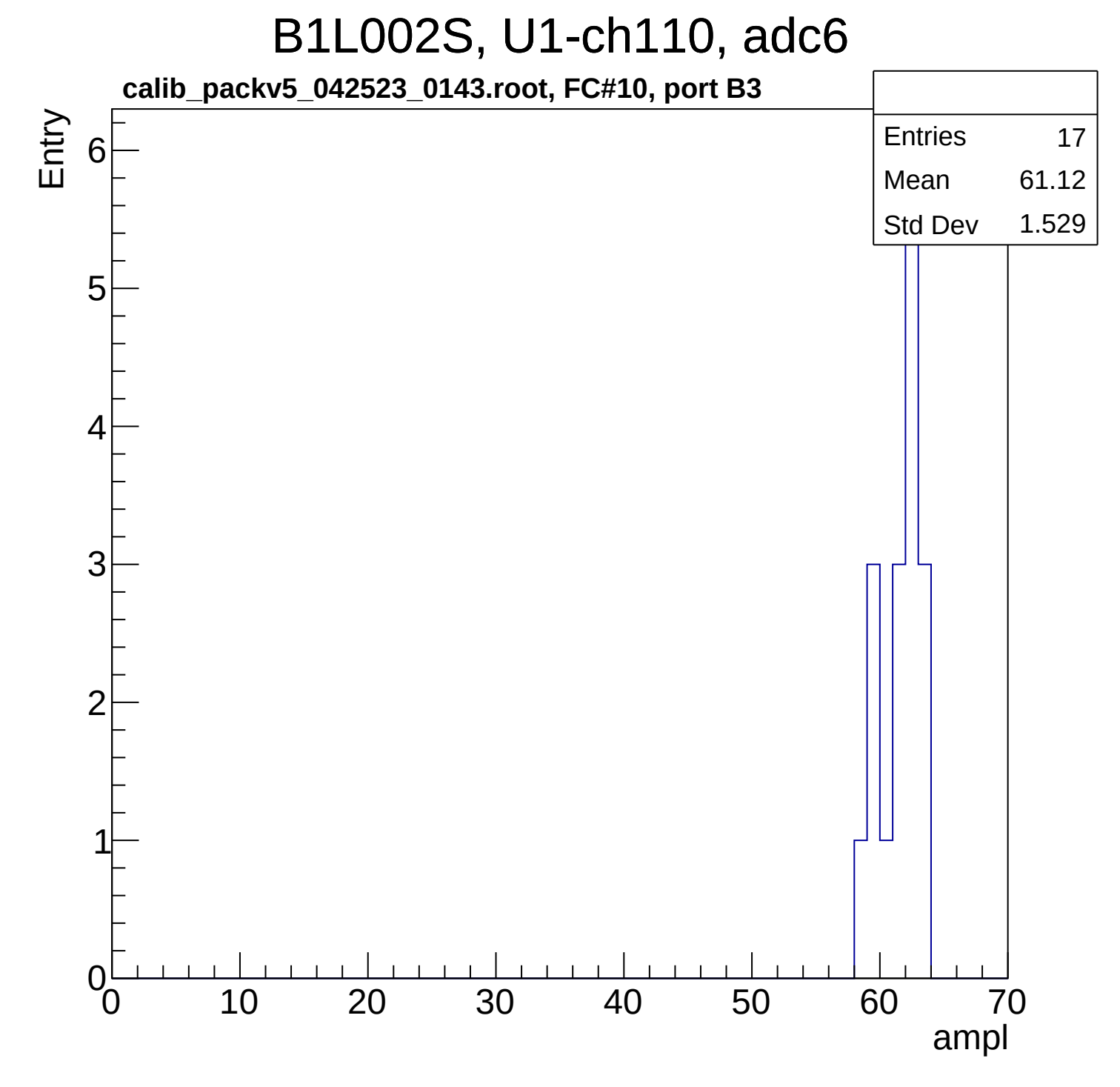
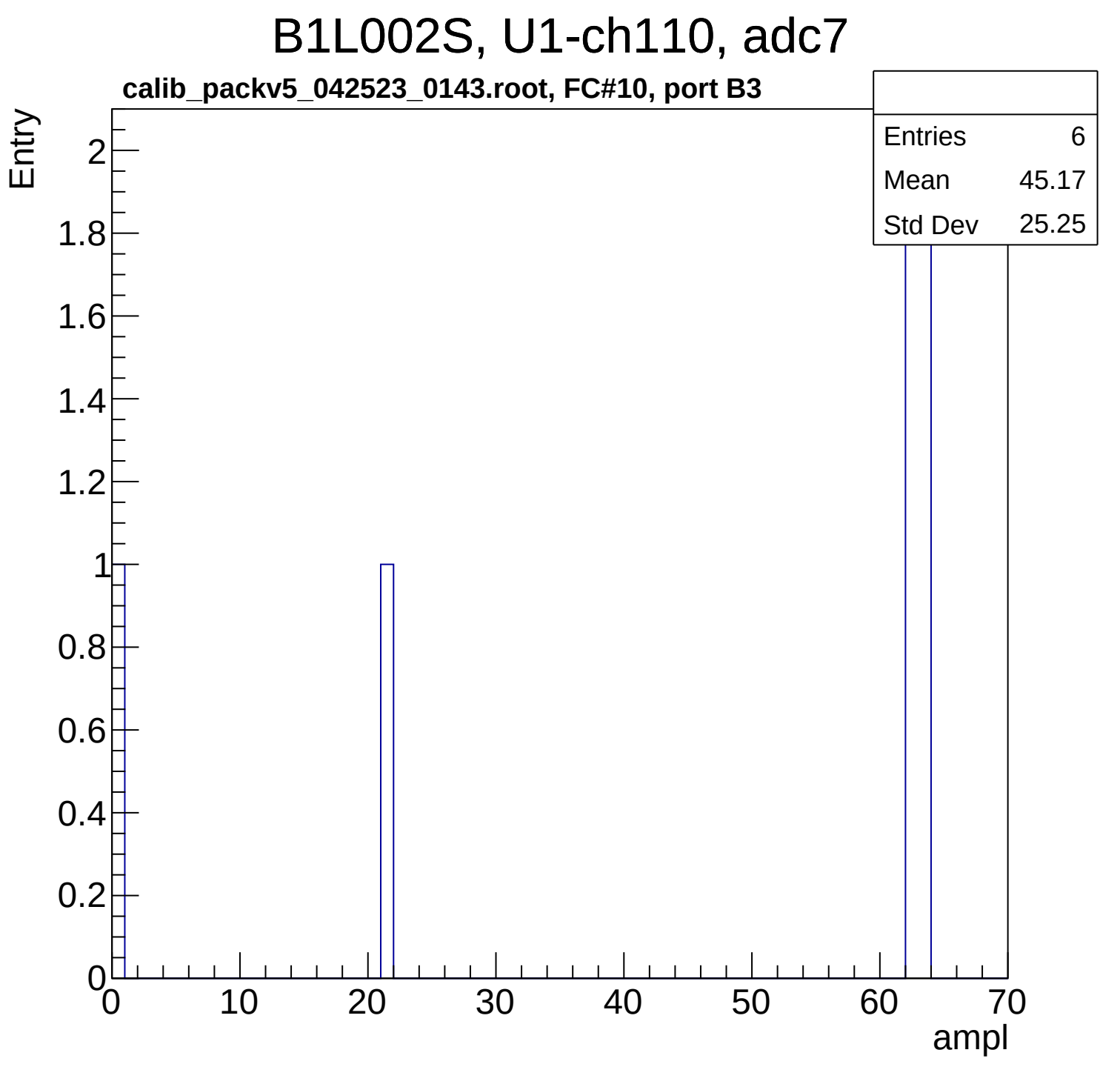
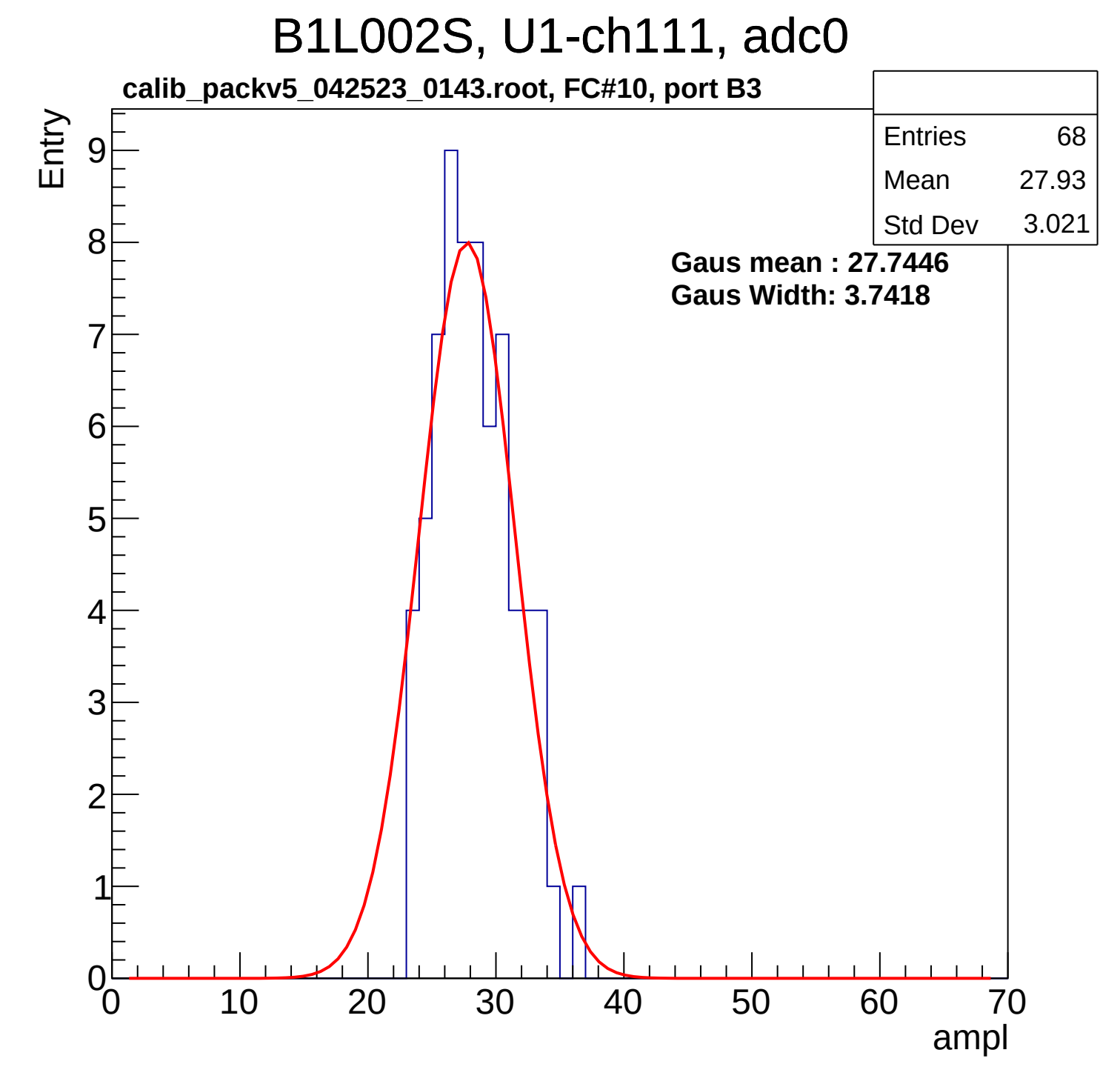
60
70


B1L002S, U1-ch110, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.12
Std Dev 1.529
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch110, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 45.17
Std Dev 25.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch111, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.93
Std Dev 3.021
Gaus mean : 27.7446
Gaus Width: 3.7418
Entry
ampl
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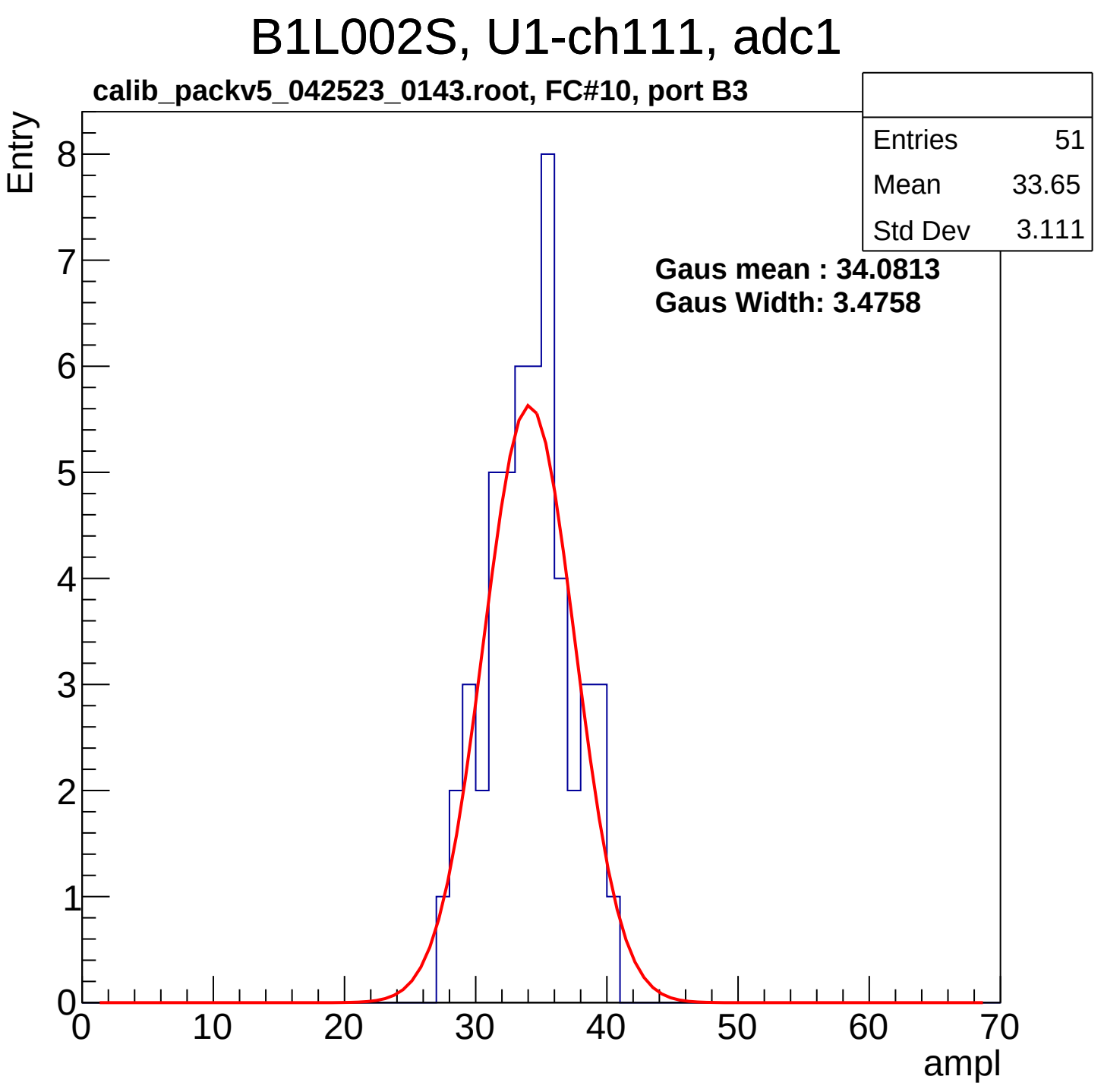
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch111, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 33.65
Std Dev 3.111
Gaus mean : 34.0813
Gaus Width: 3.4758
Entry
ampl
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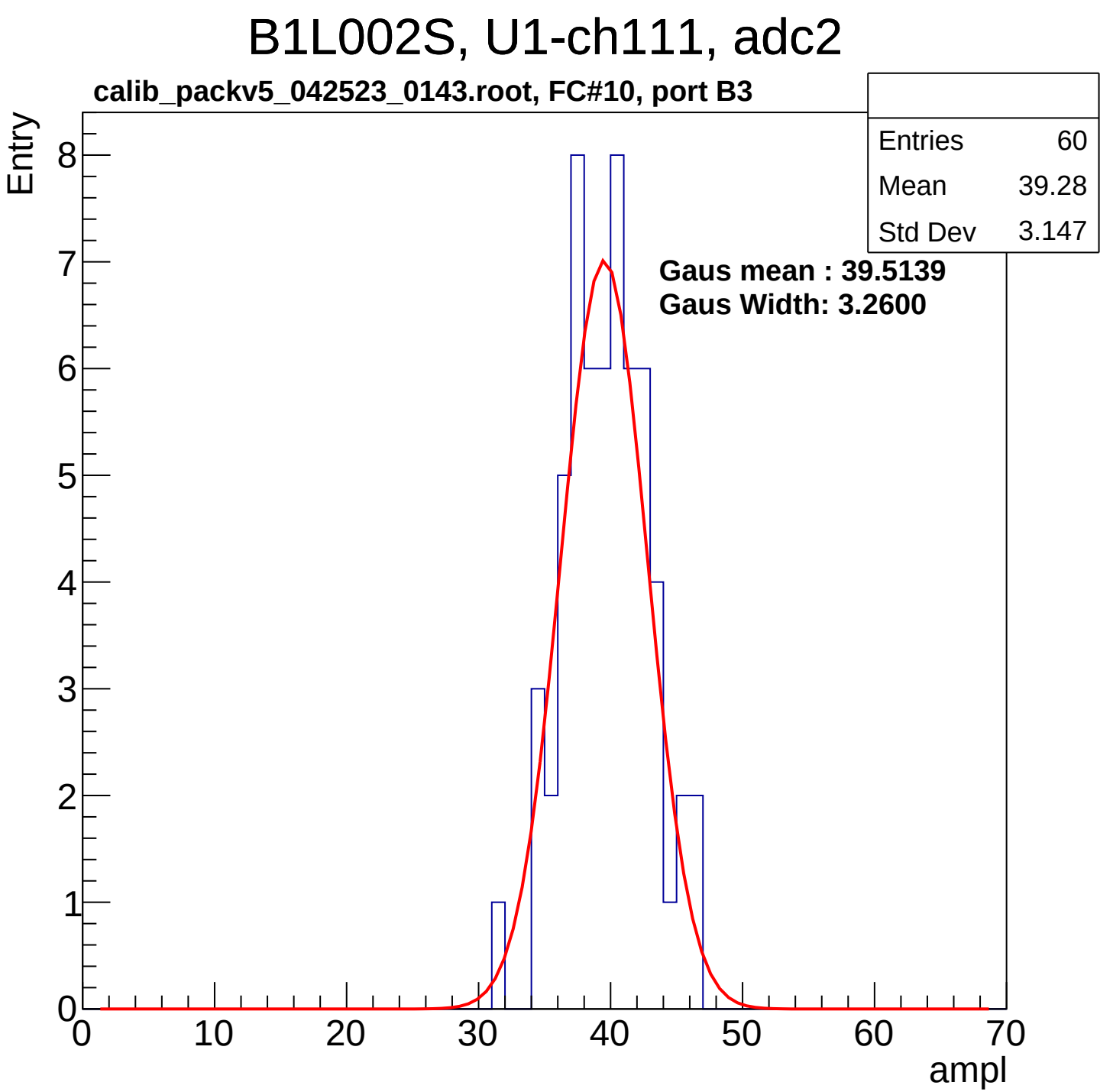
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch111, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 39.28
Std Dev 3.147
Gaus mean : 39.5139
Gaus Width: 3.2600
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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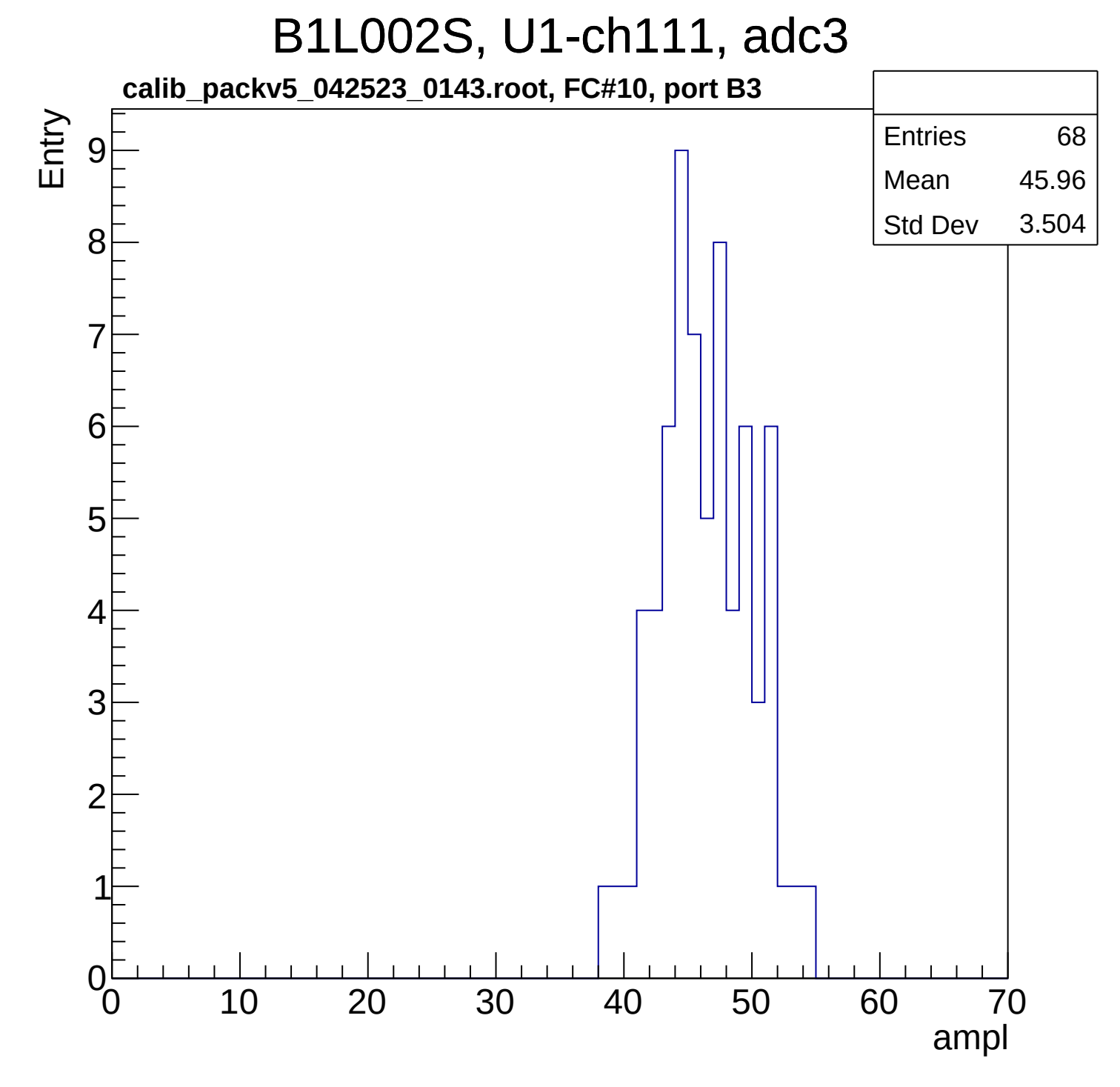
B1L002S, U1-ch111, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 45.96
Std Dev 3.504
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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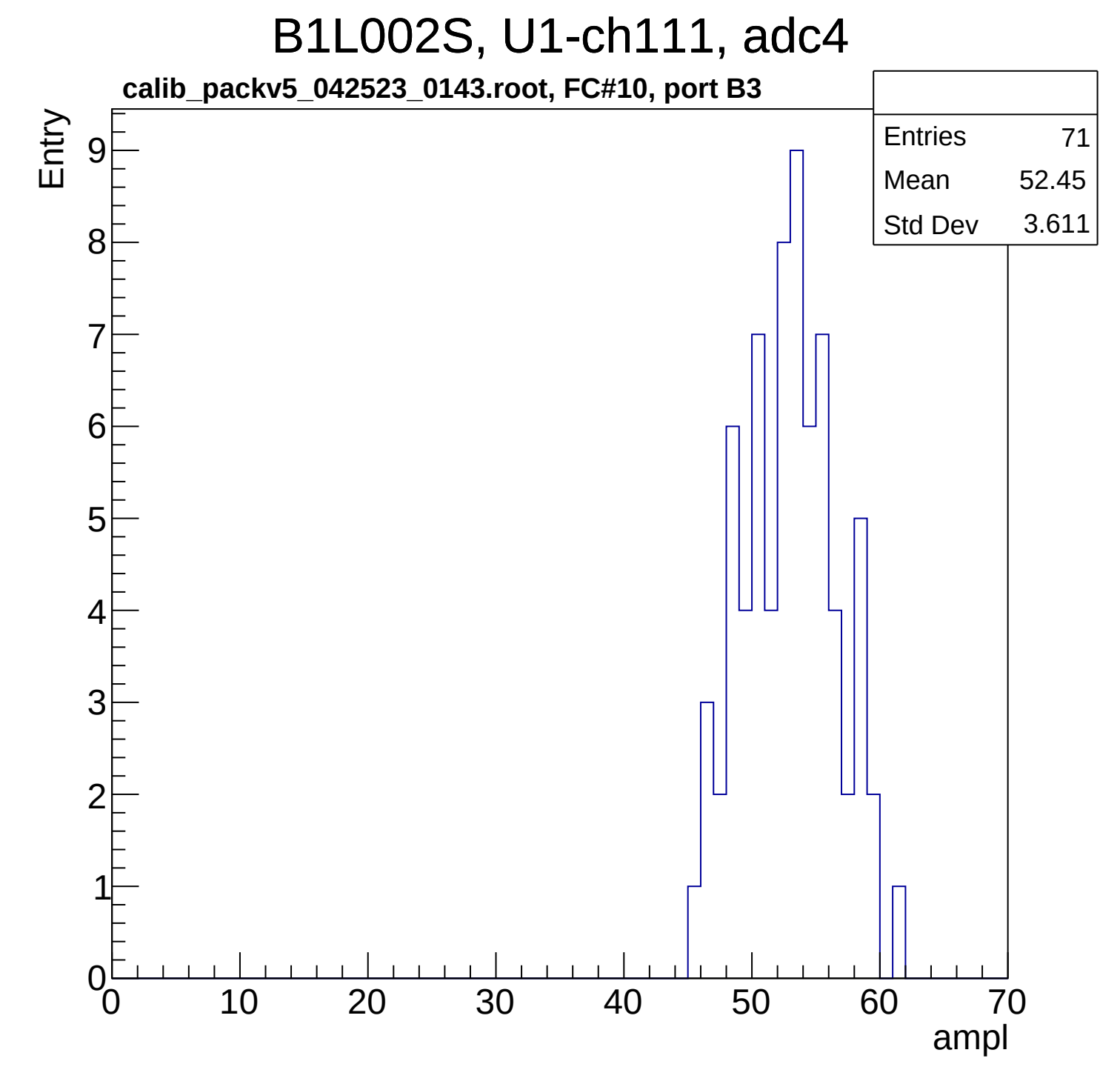
B1L002S, U1-ch111, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 52.45
Std Dev 3.611
Entry
ampl
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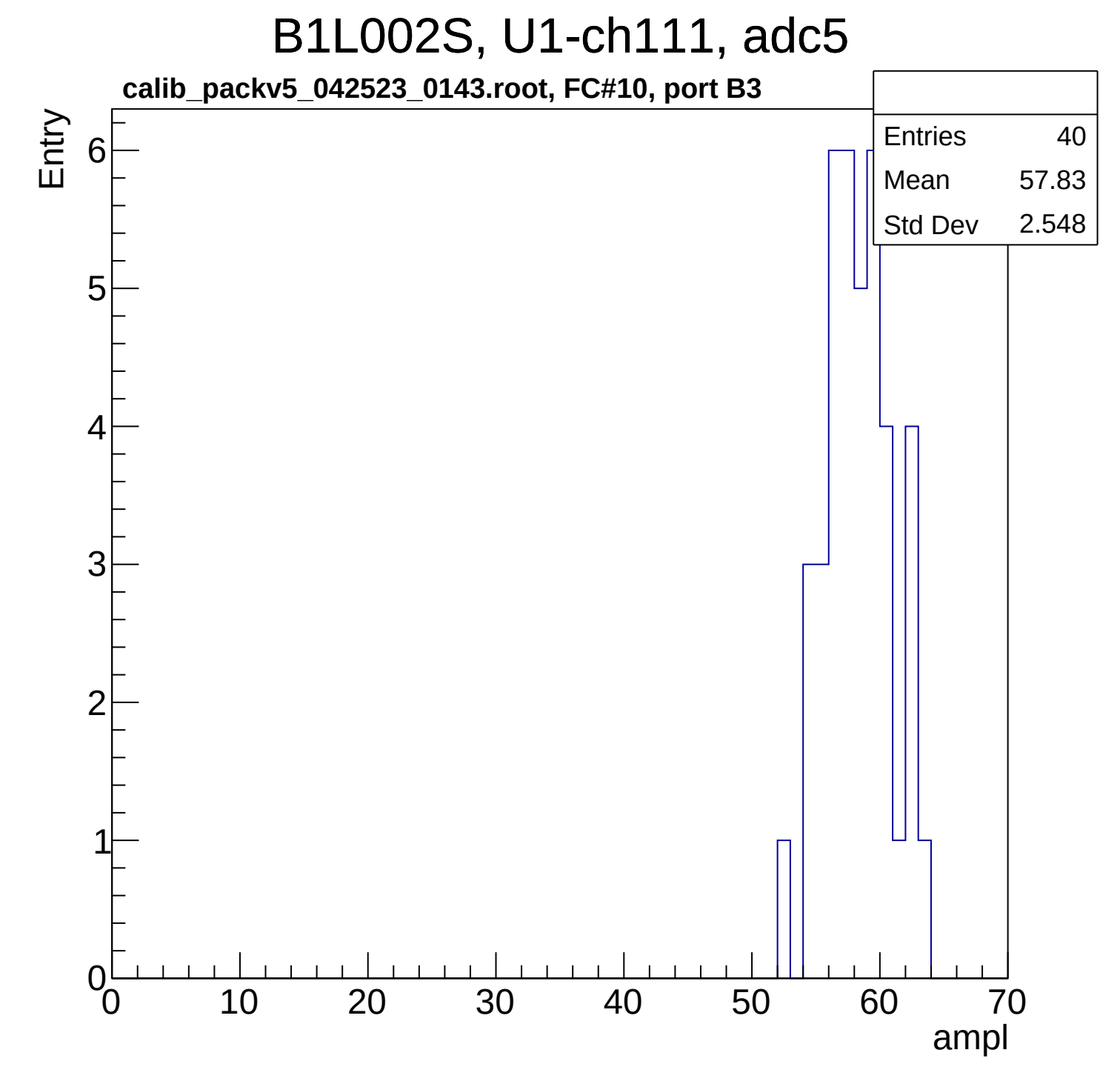
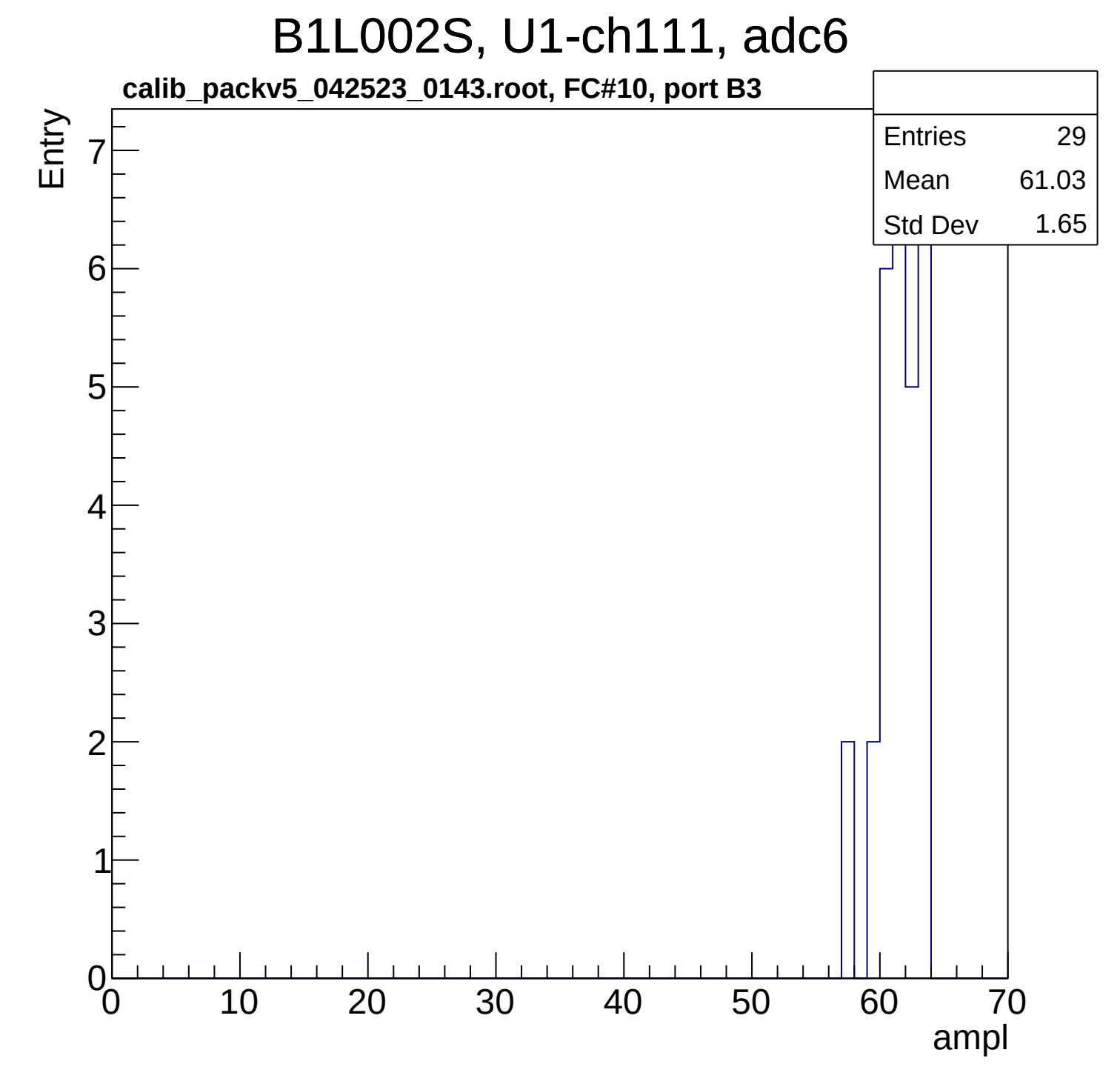
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch111, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 57.83
Std Dev 2.548
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch111, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 61.03
Std Dev 1.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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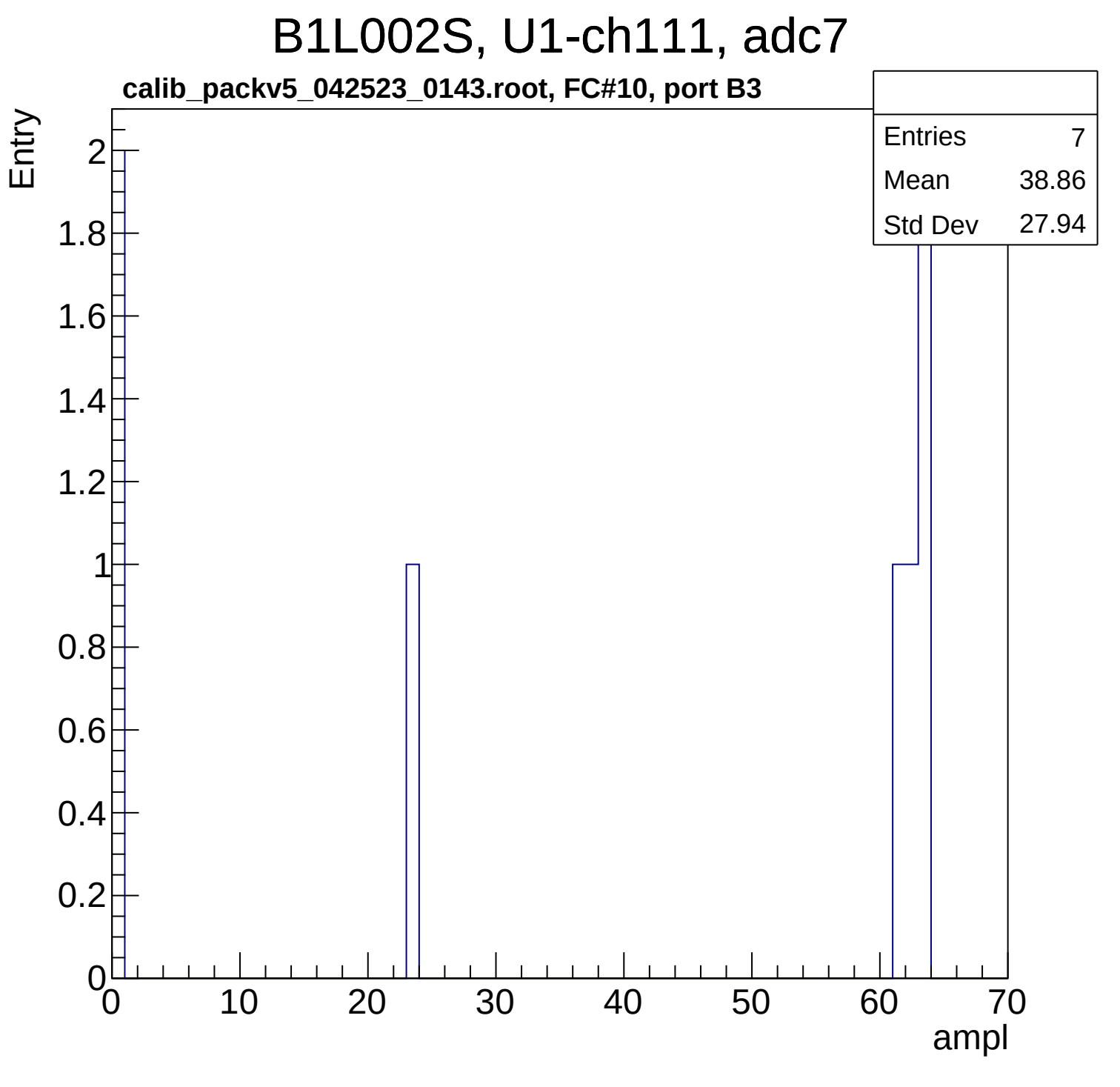
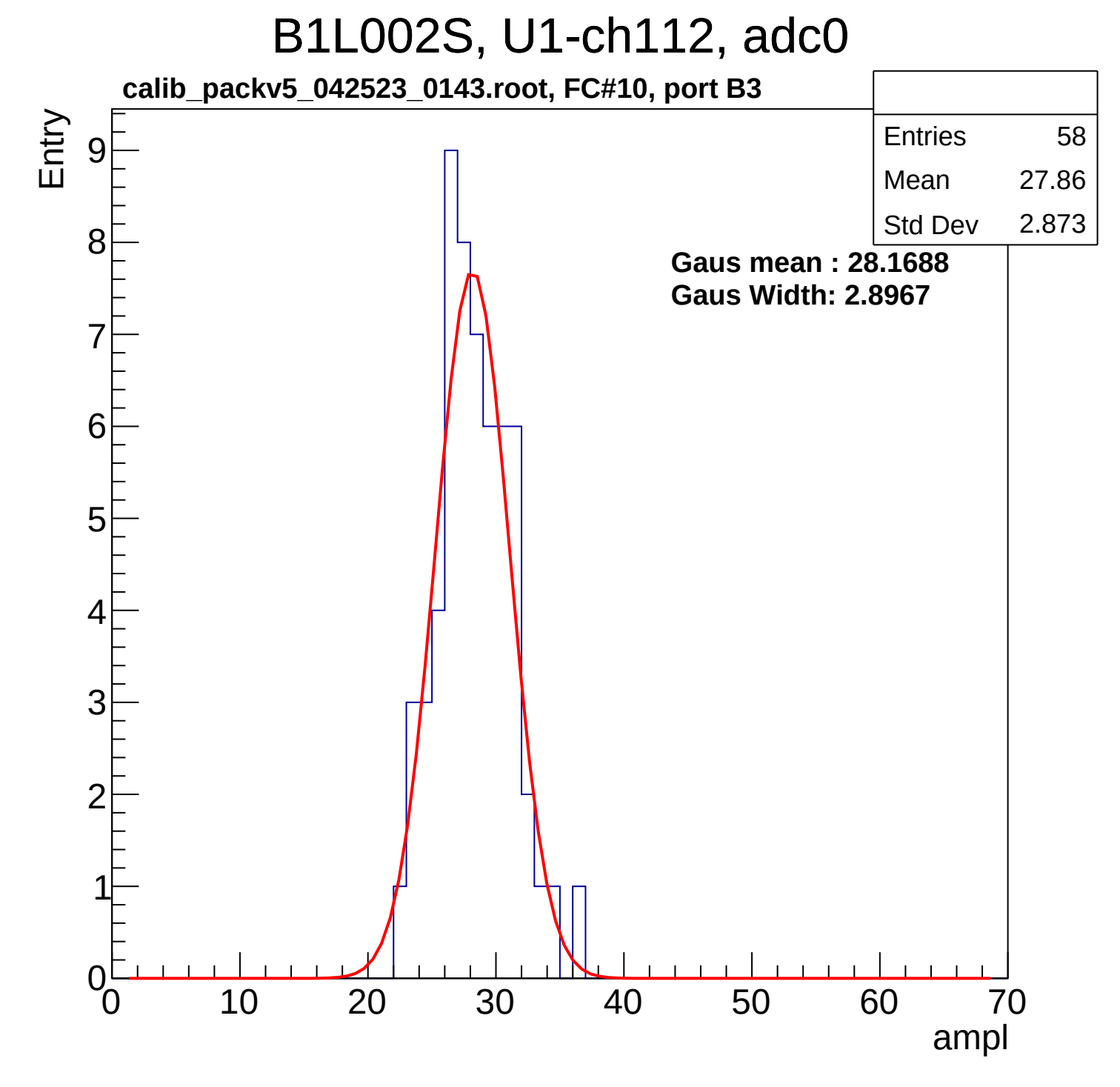
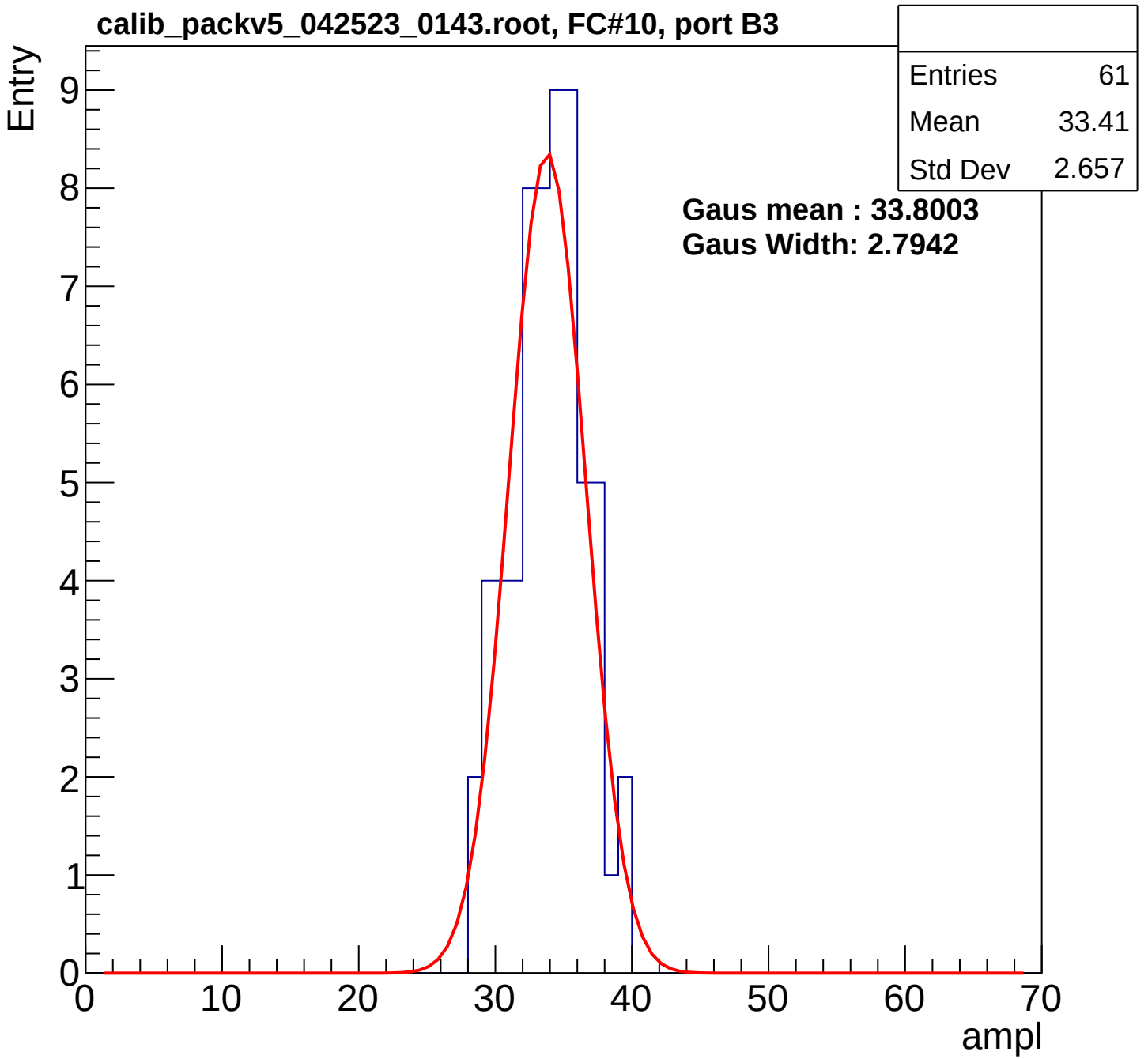

B1L002S, U1-ch111, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 38.86
Std Dev 27.94
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch112, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 27.86
Std Dev 2.873
Gaus mean : 28.1688
Gaus Width: 2.8967
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch112, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 33.41
Std Dev 2.657
Gaus mean : 33.8003
Gaus Width: 2.7942
Entry
ampl
0
10
20
30
40
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1
2
3
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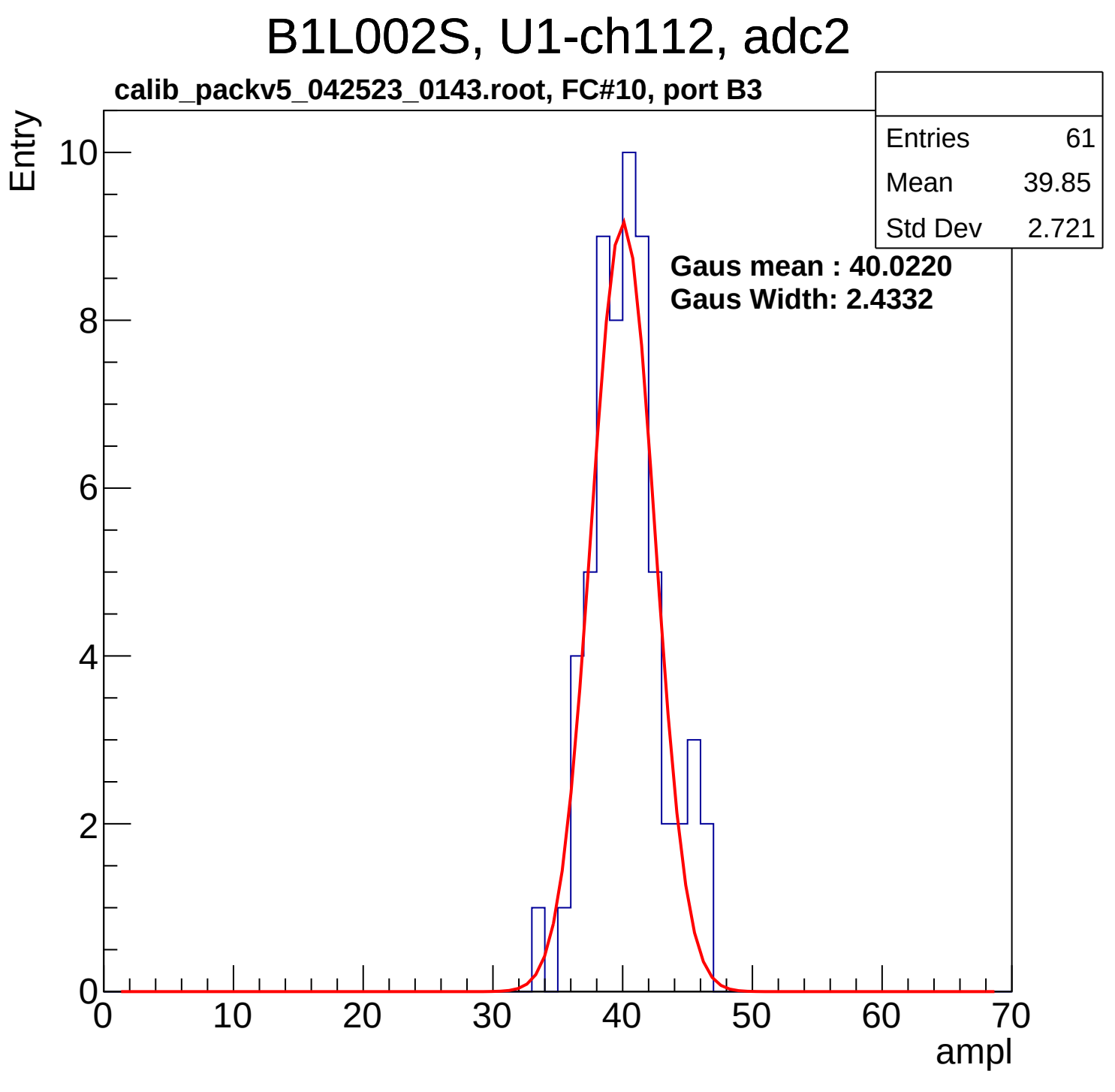
6
7
8
9


B1L002S, U1-ch112, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 39.85
Std Dev 2.721
Gaus mean : 40.0220
Gaus Width: 2.4332
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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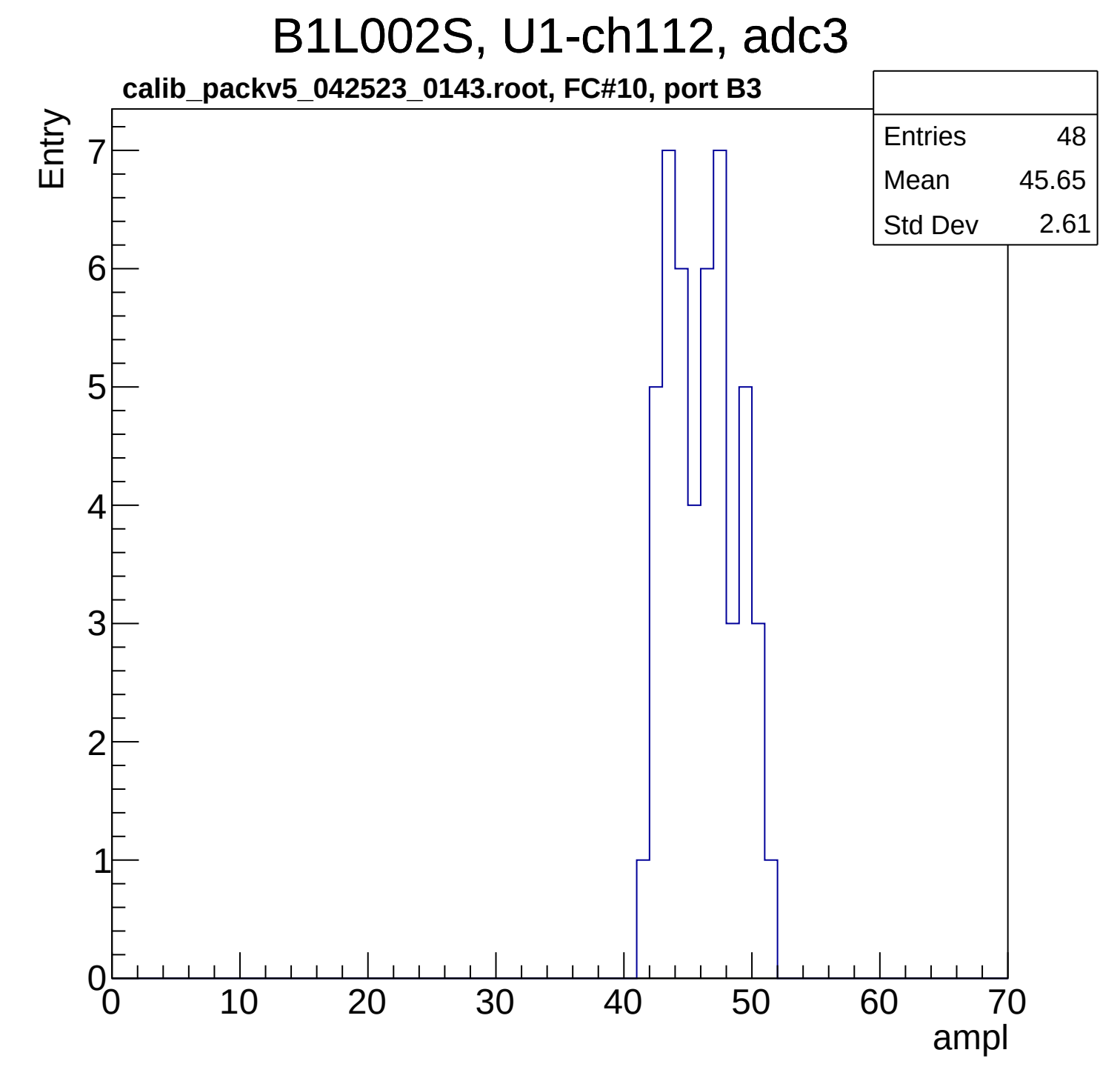
B1L002S, U1-ch112, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 45.65
Std Dev 2.61
Entry
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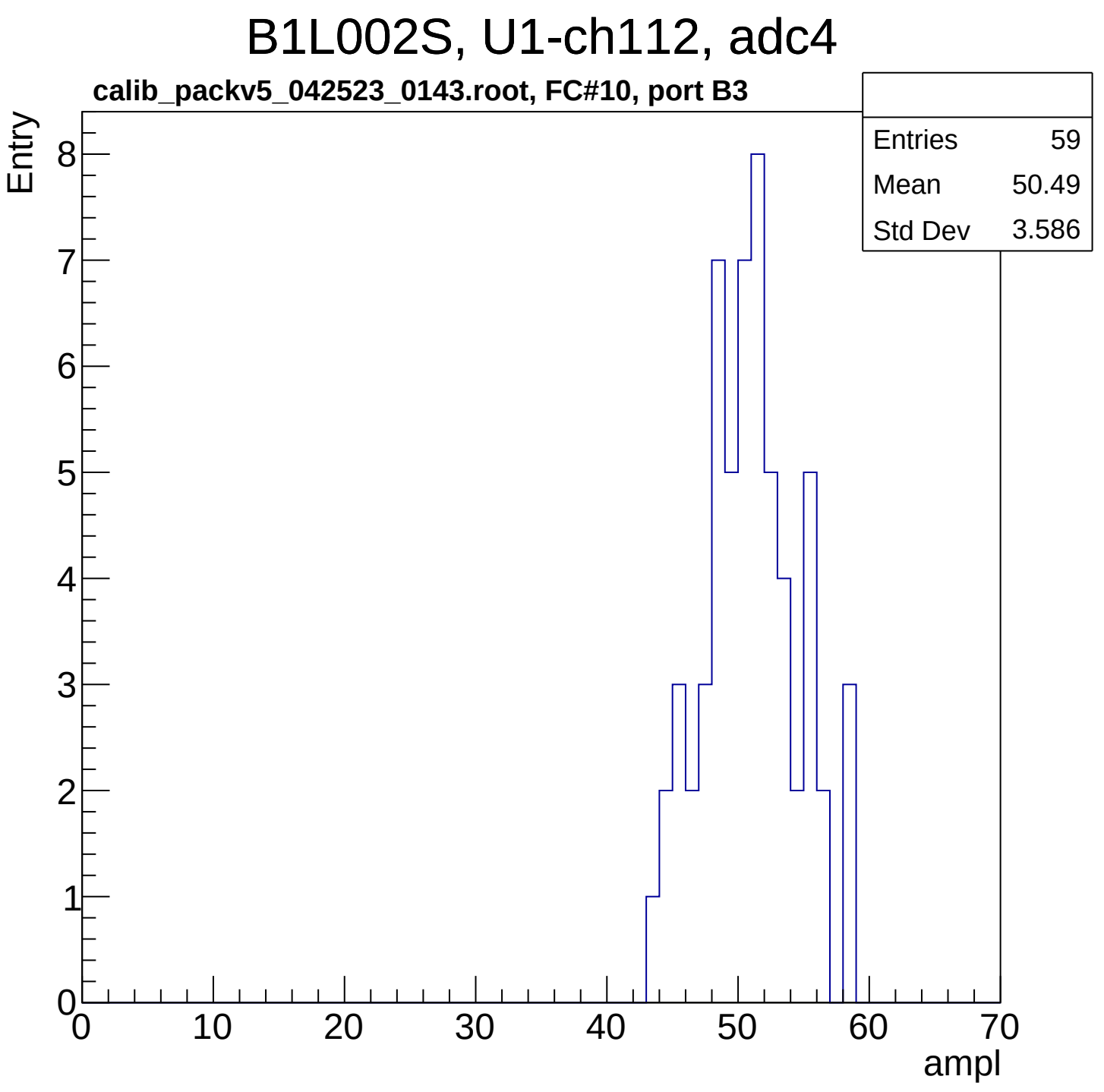
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch112, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 50.49
Std Dev 3.586
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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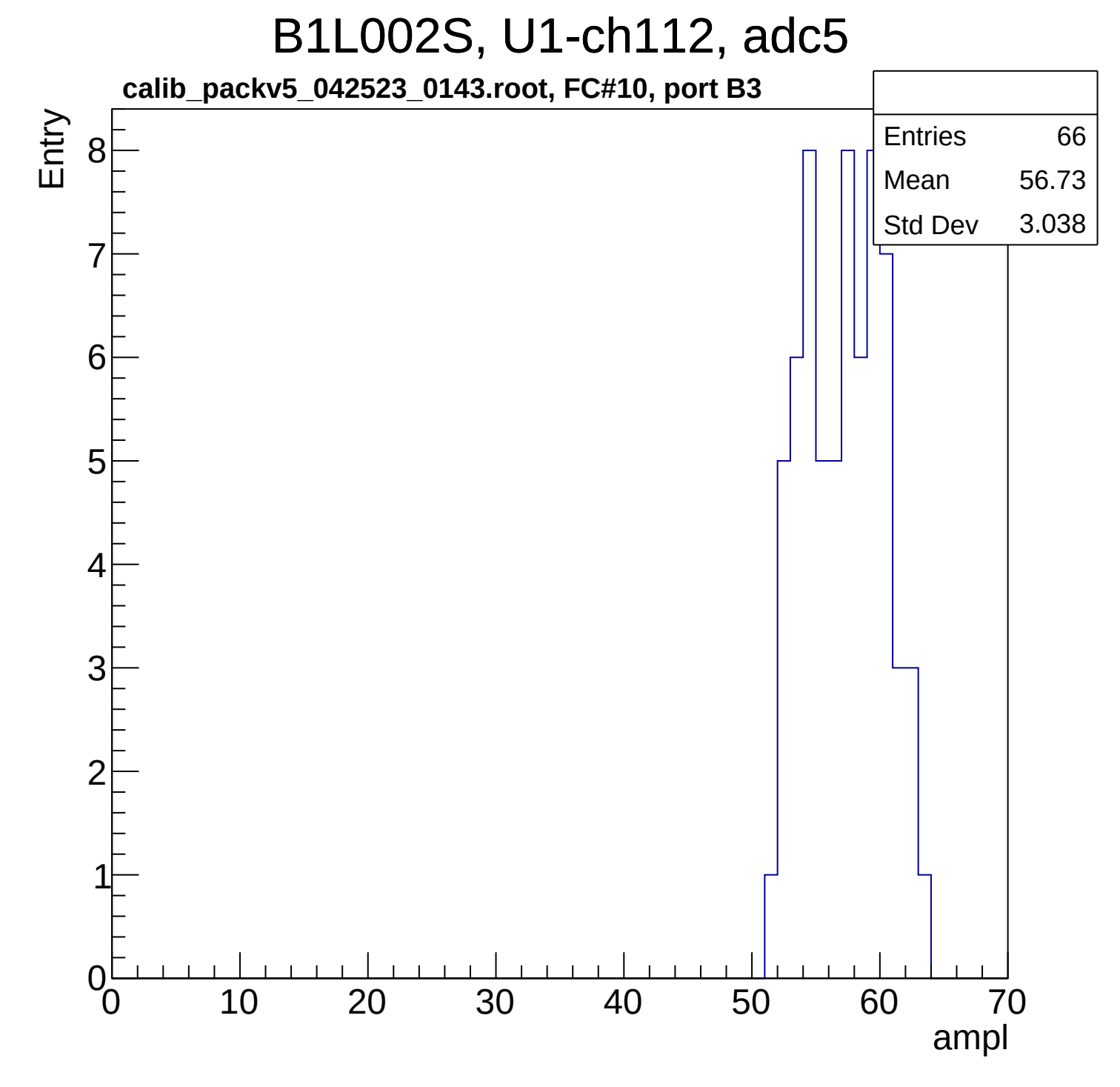
B1L002S, U1-ch112, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 56.73
Std Dev 3.038
Entry
ampl
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2
3
4
5
6
7
8
0
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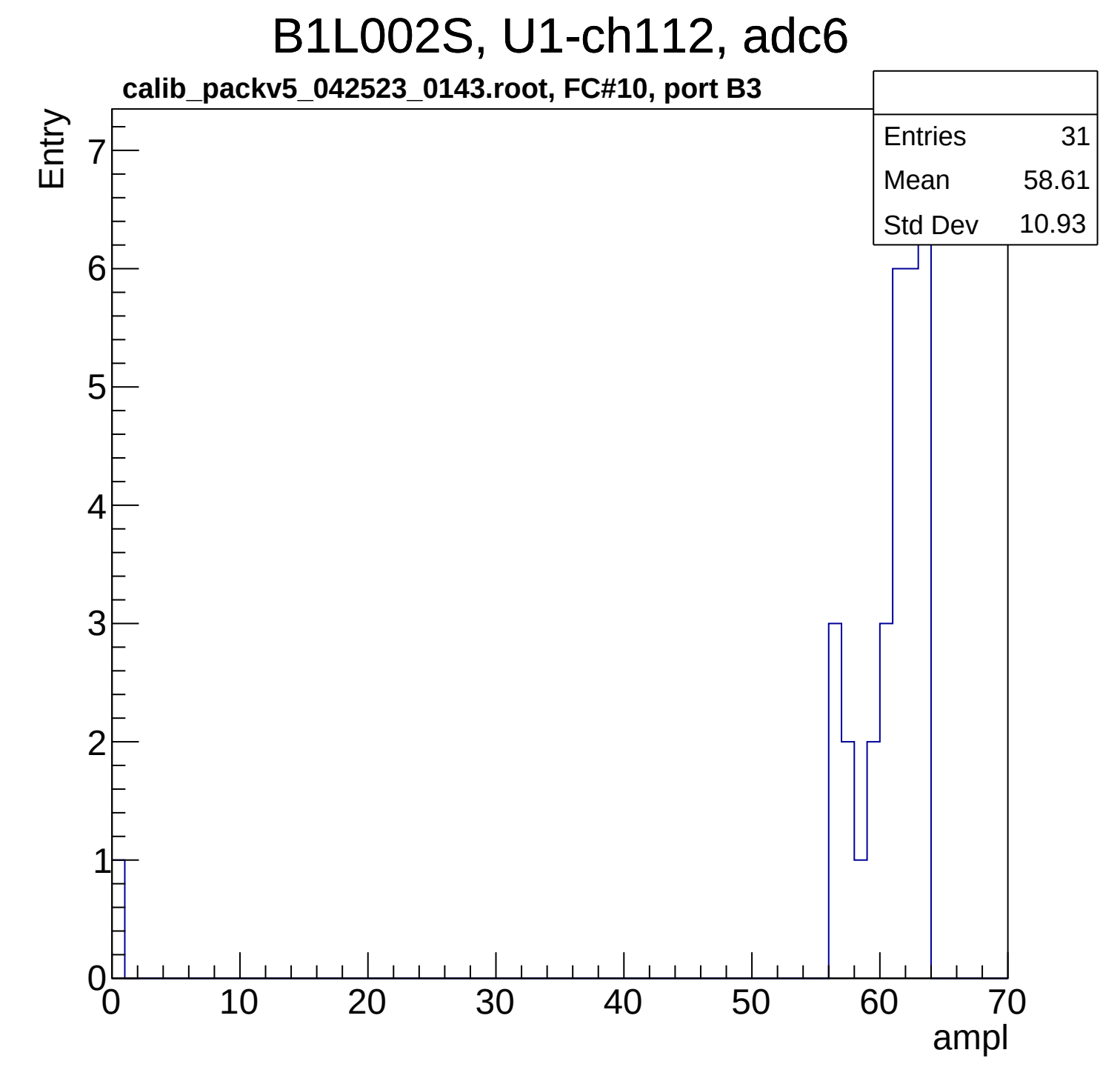
B1L002S, U1-ch112, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 58.61
Std Dev 10.93
Entry
0
1
2
3
4
5
6
7
0
10
20
30
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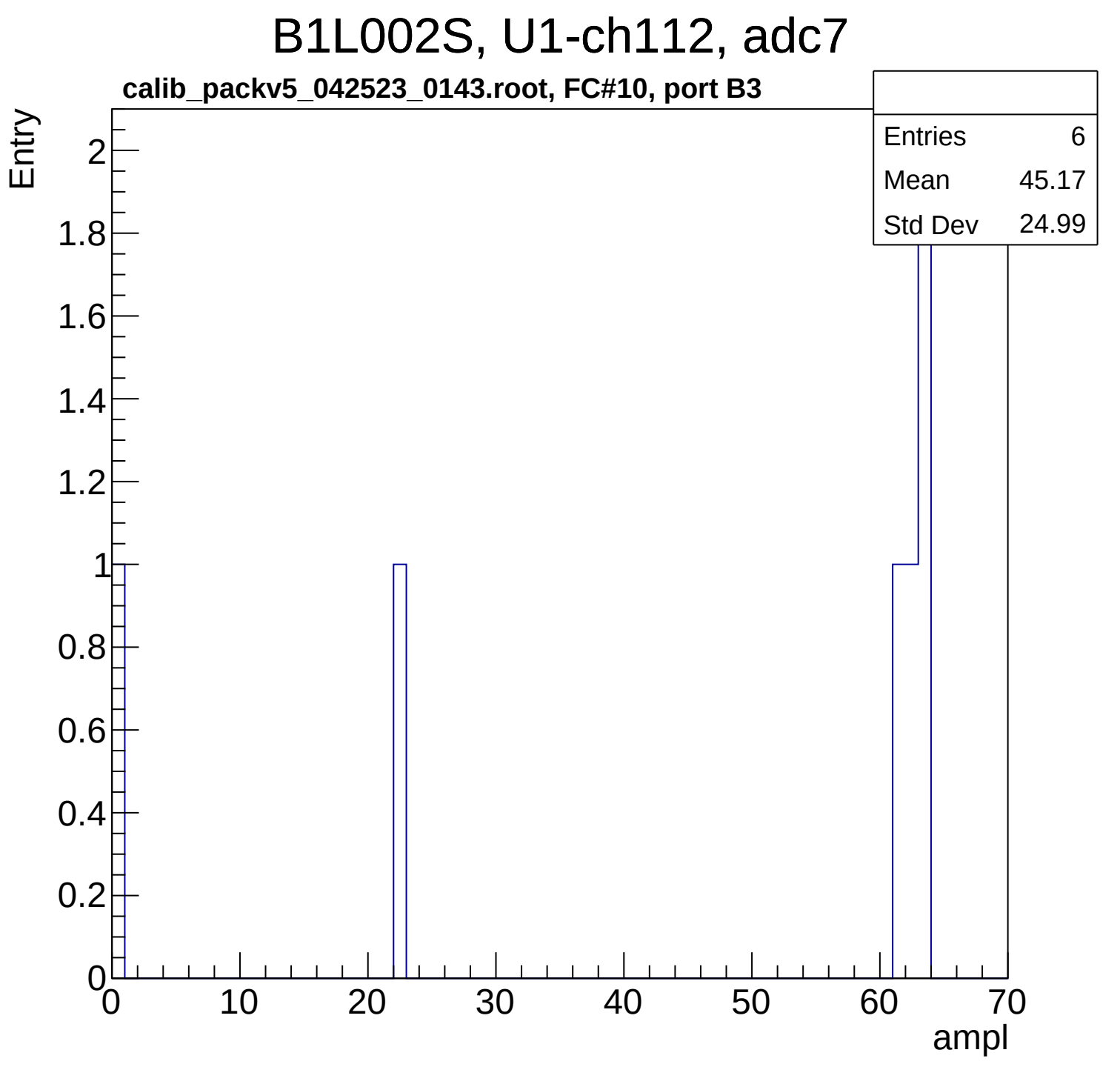
50
60
70
ampl


B1L002S, U1-ch112, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 45.17
Std Dev 24.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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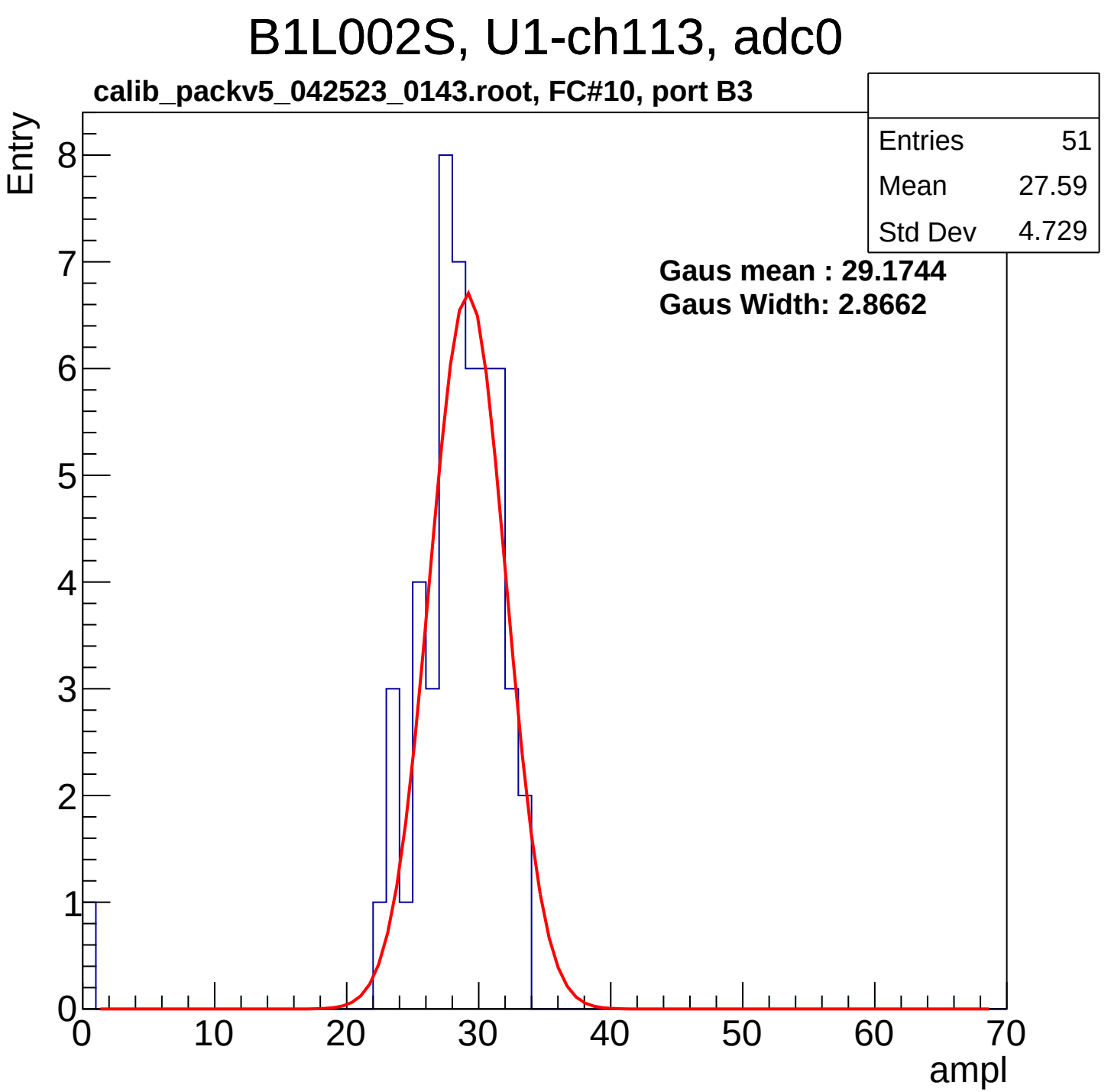
B1L002S, U1-ch113, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 27.59
Std Dev 4.729
Gaus mean : 29.1744
Gaus Width: 2.8662
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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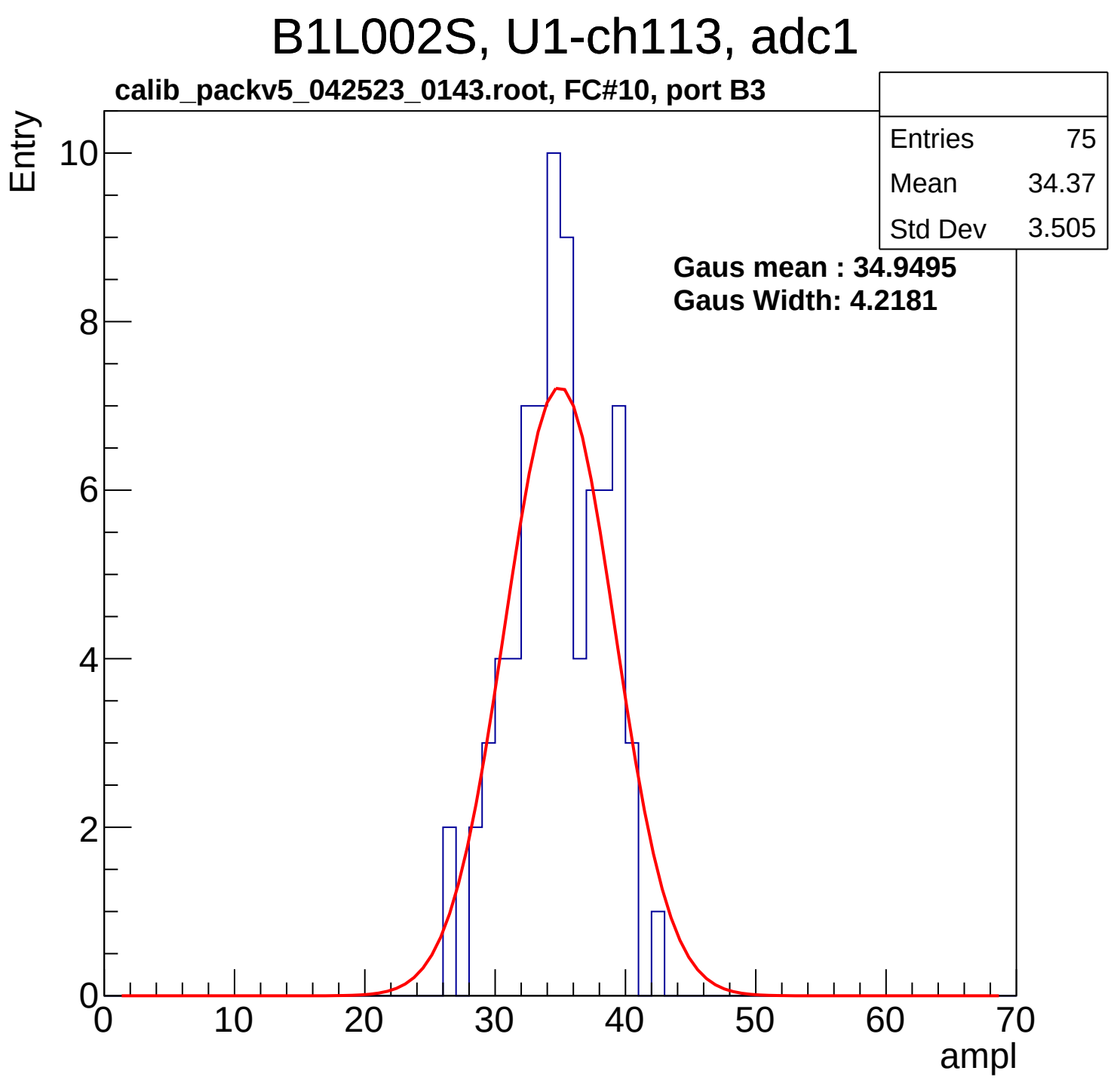
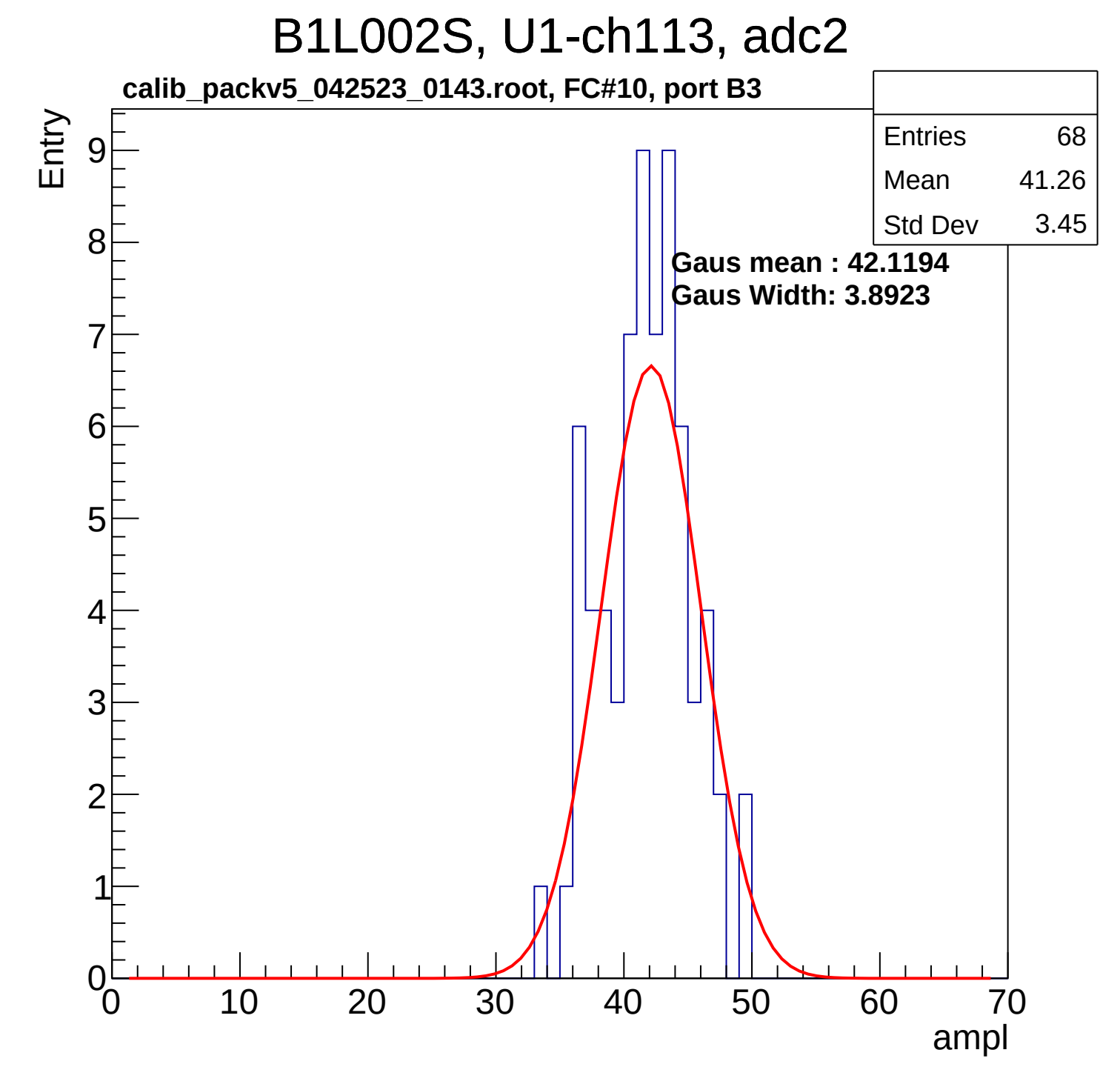
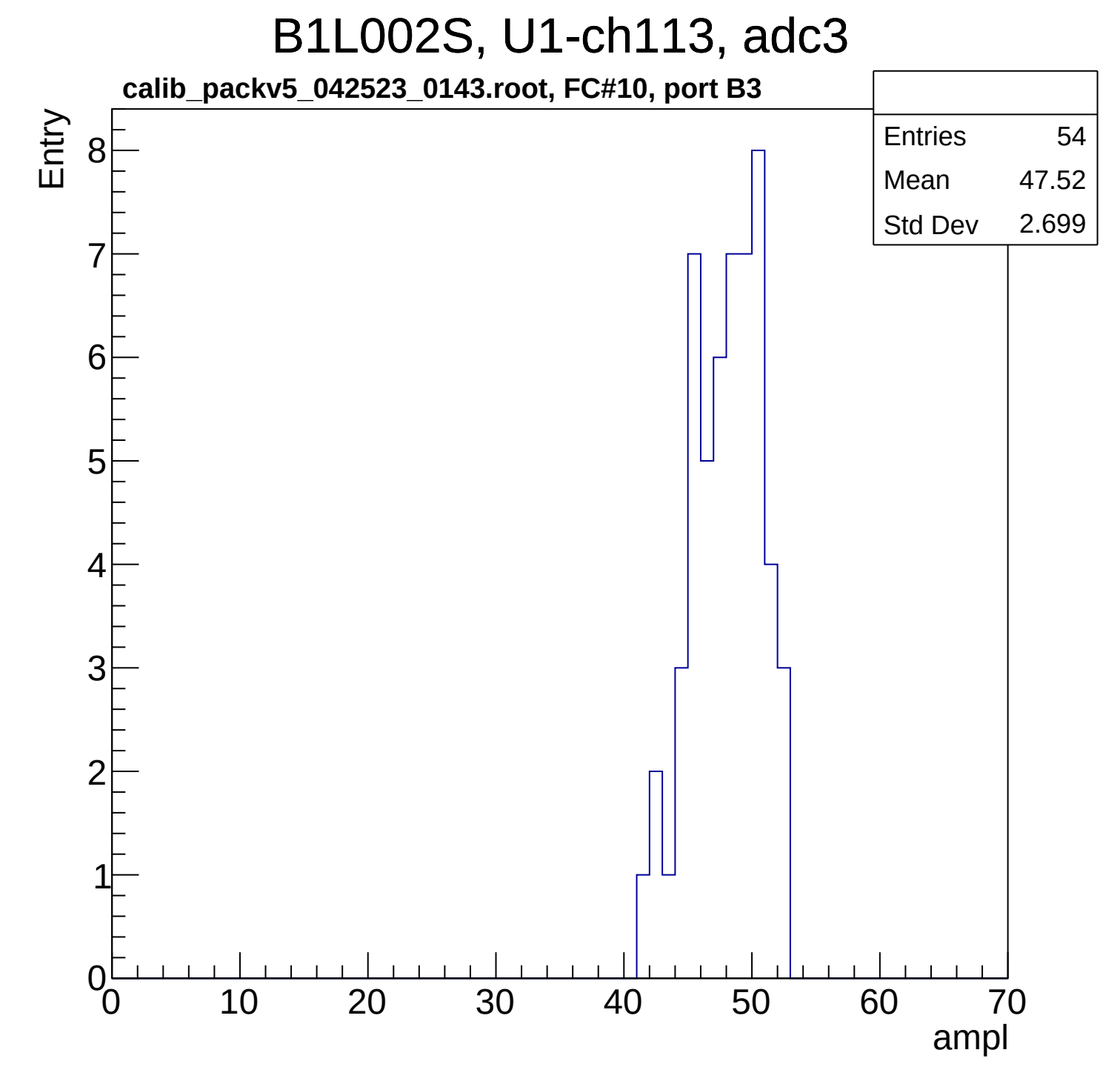

B1L002S, U1-ch113, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.37
Std Dev 3.505
Gaus mean : 34.9495
Gaus Width: 4.2181
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch113, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 41.26
Std Dev 3.45
Gaus mean : 42.1194
Gaus Width: 3.8923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch113, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 47.52
Std Dev 2.699
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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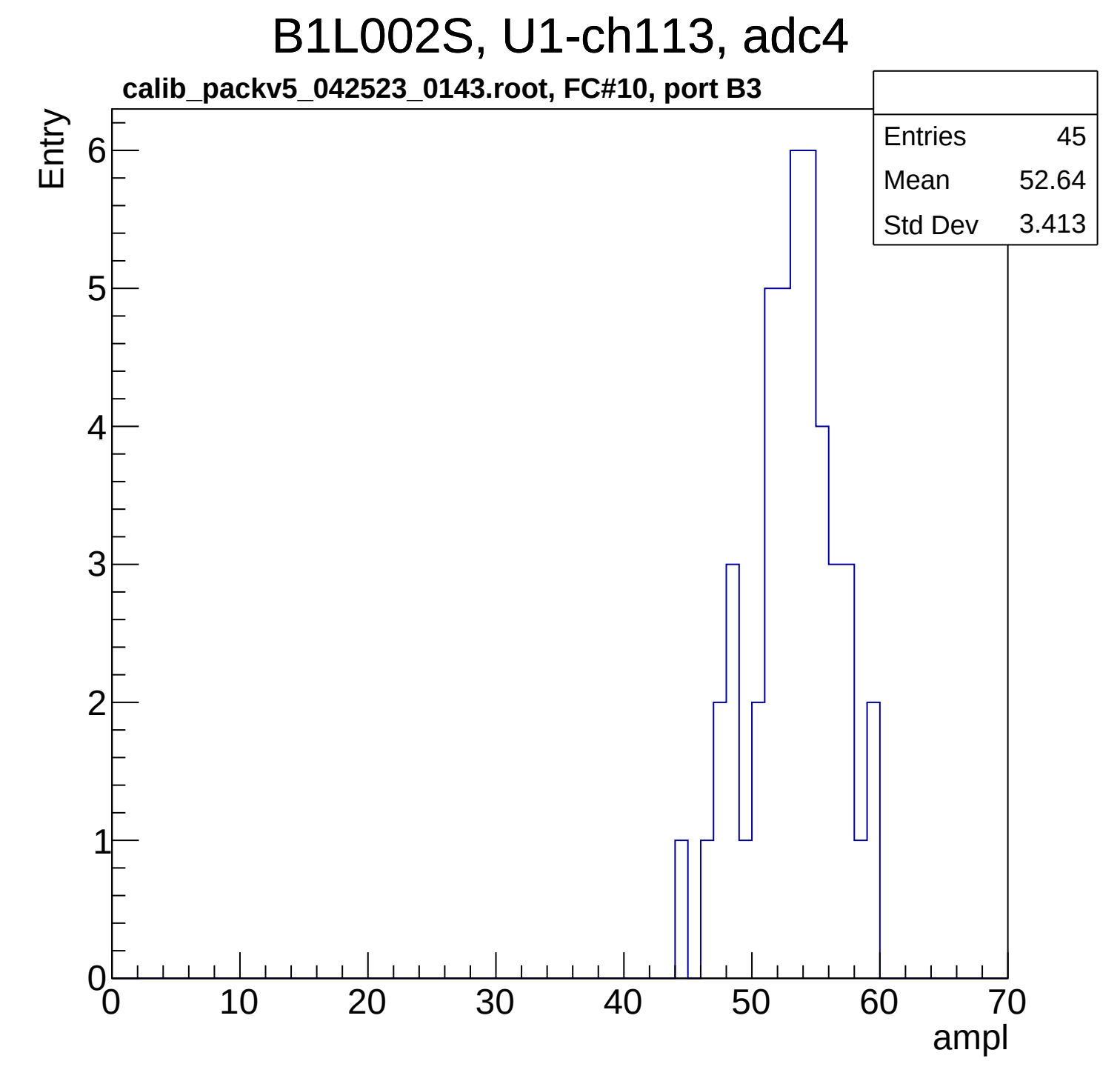
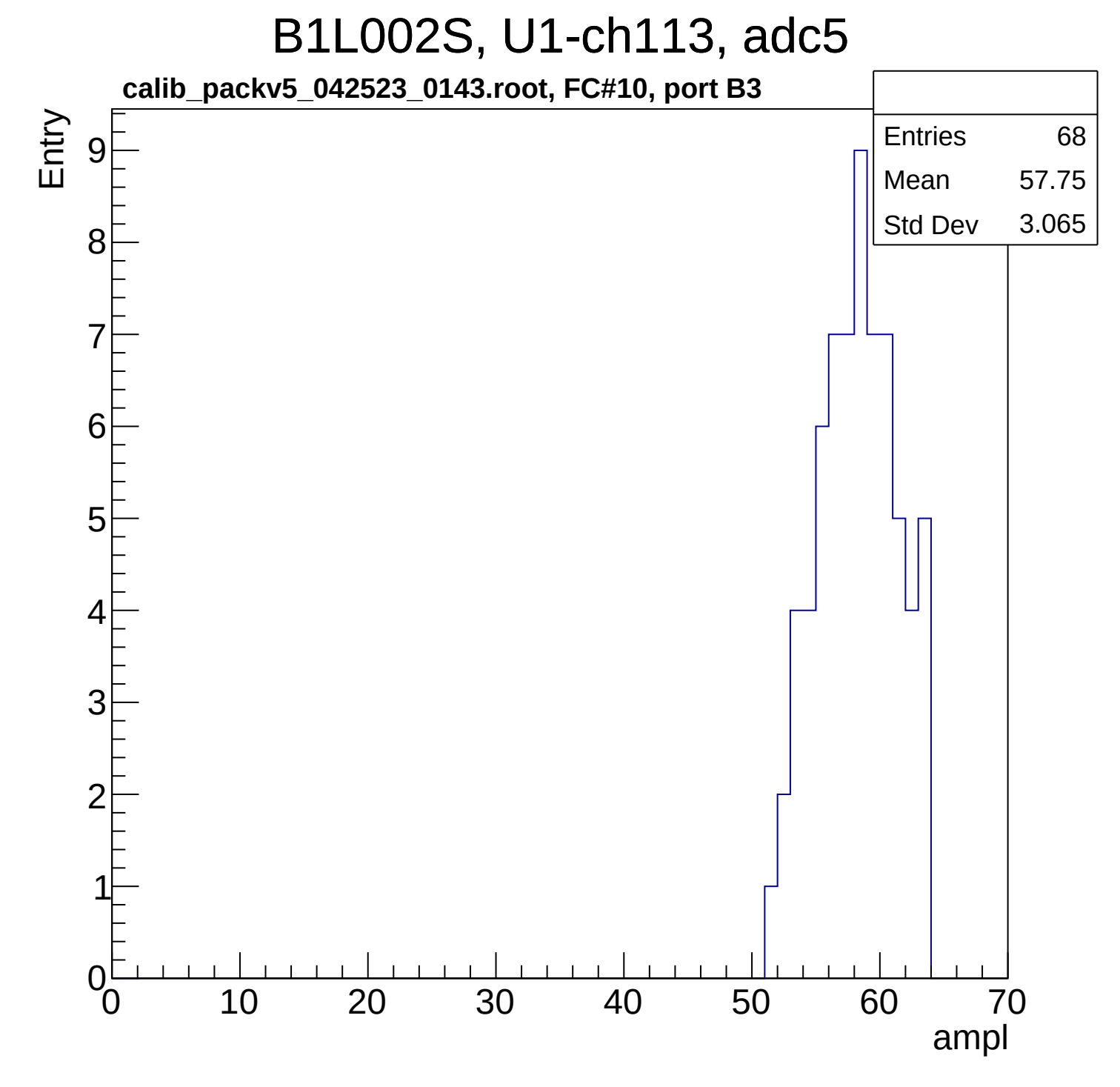

B1L002S, U1-ch113, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 52.64
Std Dev 3.413
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U1-ch113, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.75
Std Dev 3.065
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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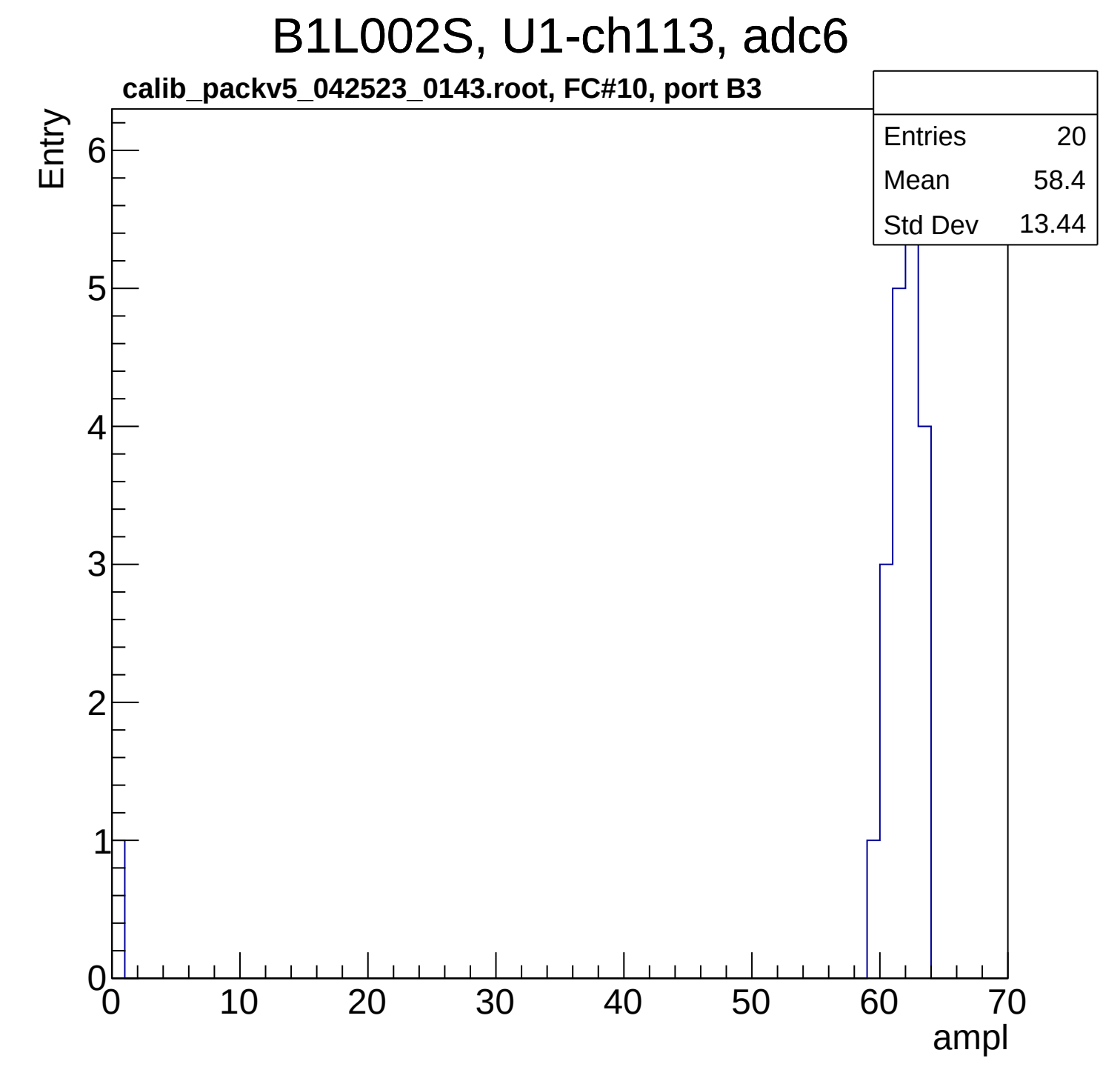
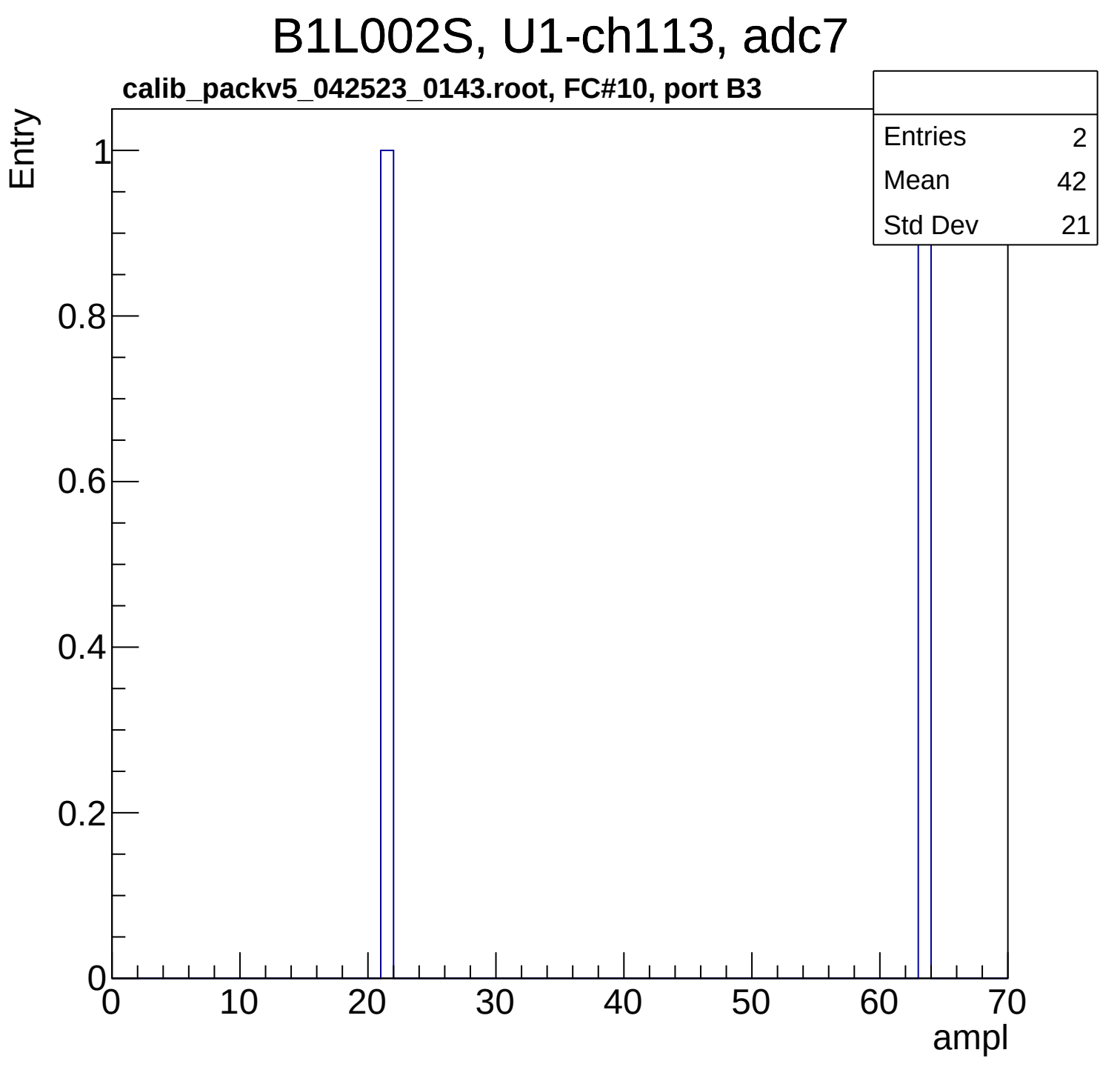
60
70


B1L002S, U1-ch113, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.4
Std Dev 13.44
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch113, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 21
Entry
ampl
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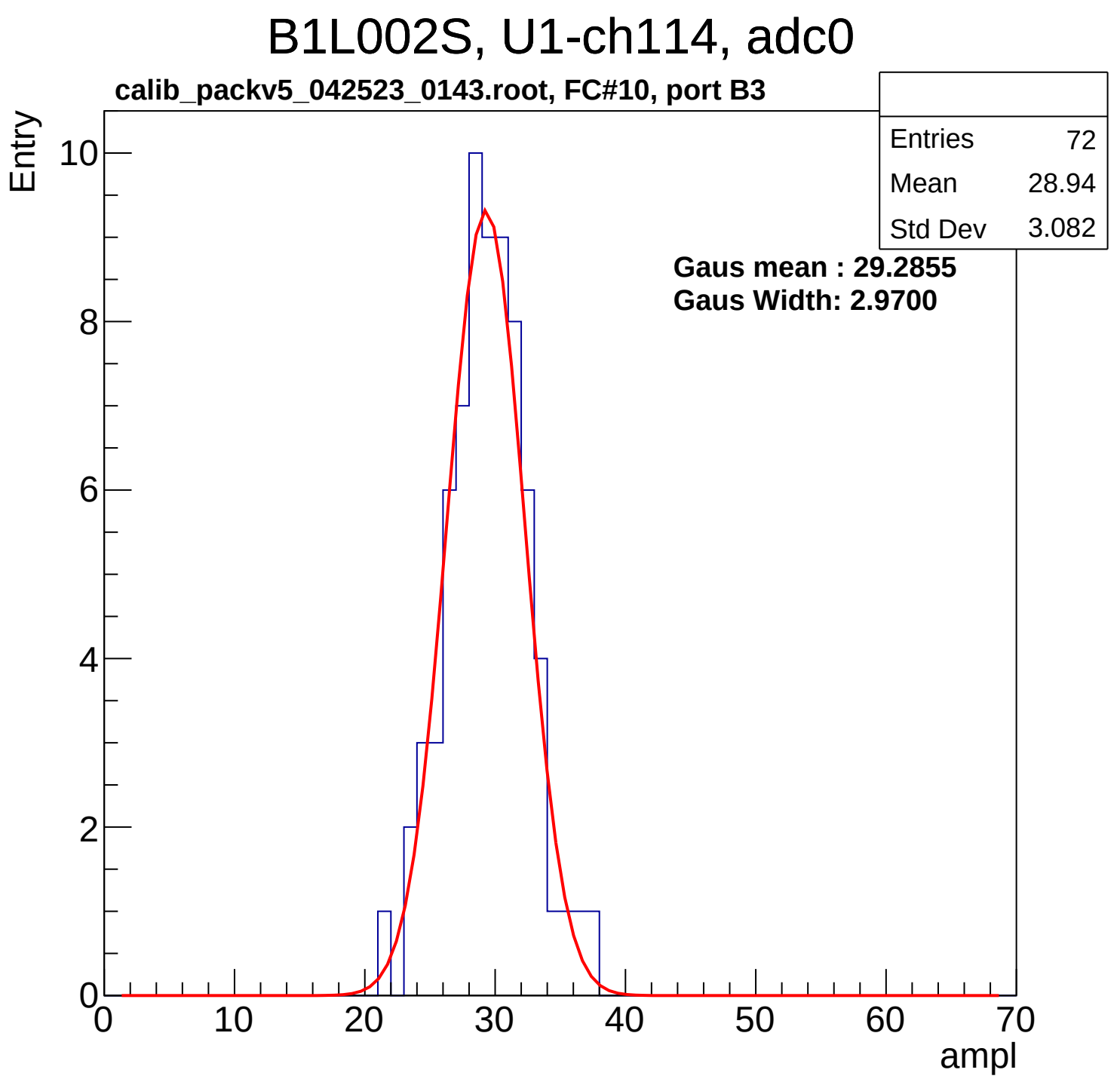
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U1-ch114, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.94
Std Dev 3.082
Gaus mean : 29.2855
Gaus Width: 2.9700
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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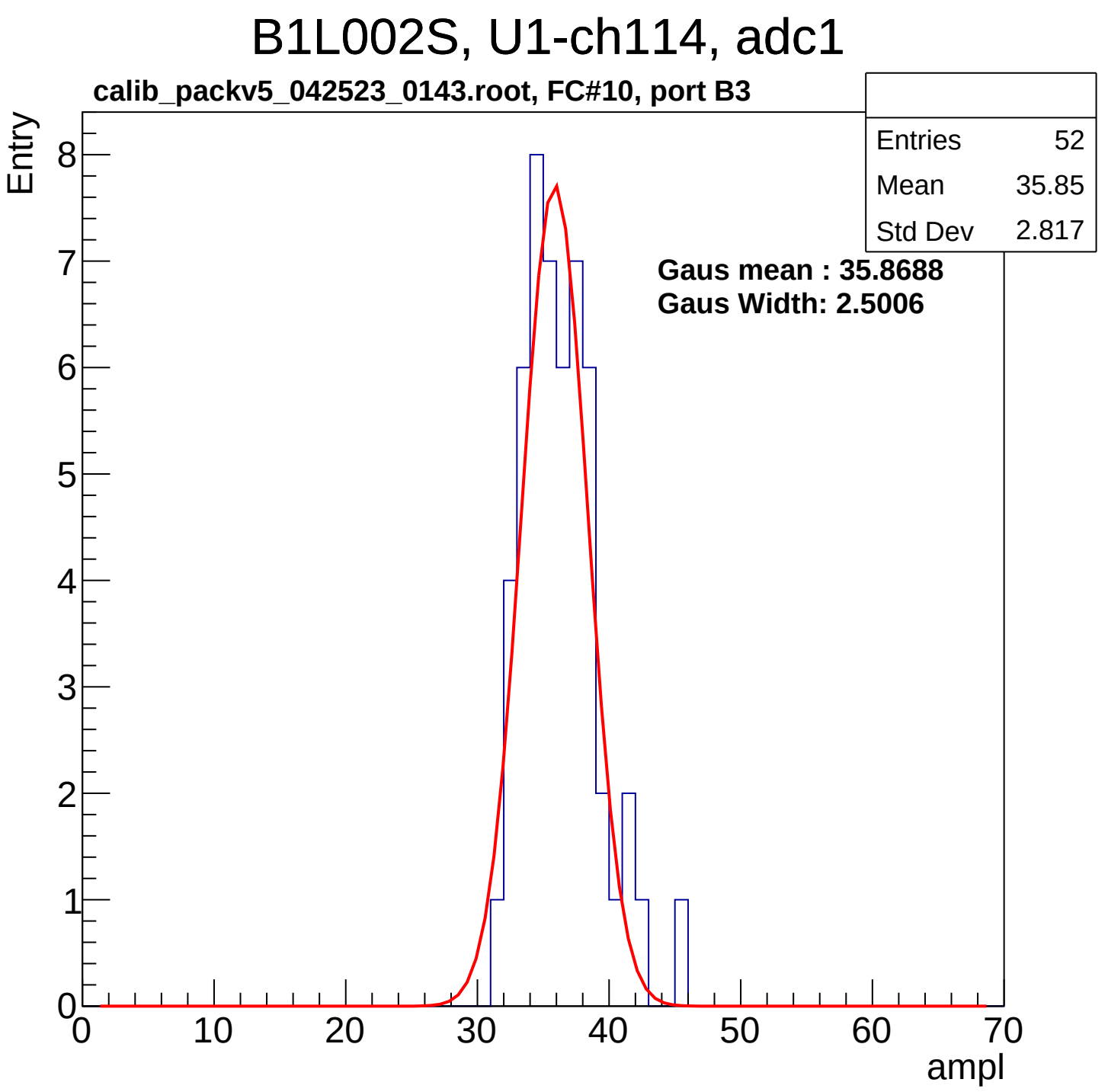
B1L002S, U1-ch114, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 35.85
Std Dev 2.817
Gaus mean : 35.8688
Gaus Width: 2.5006
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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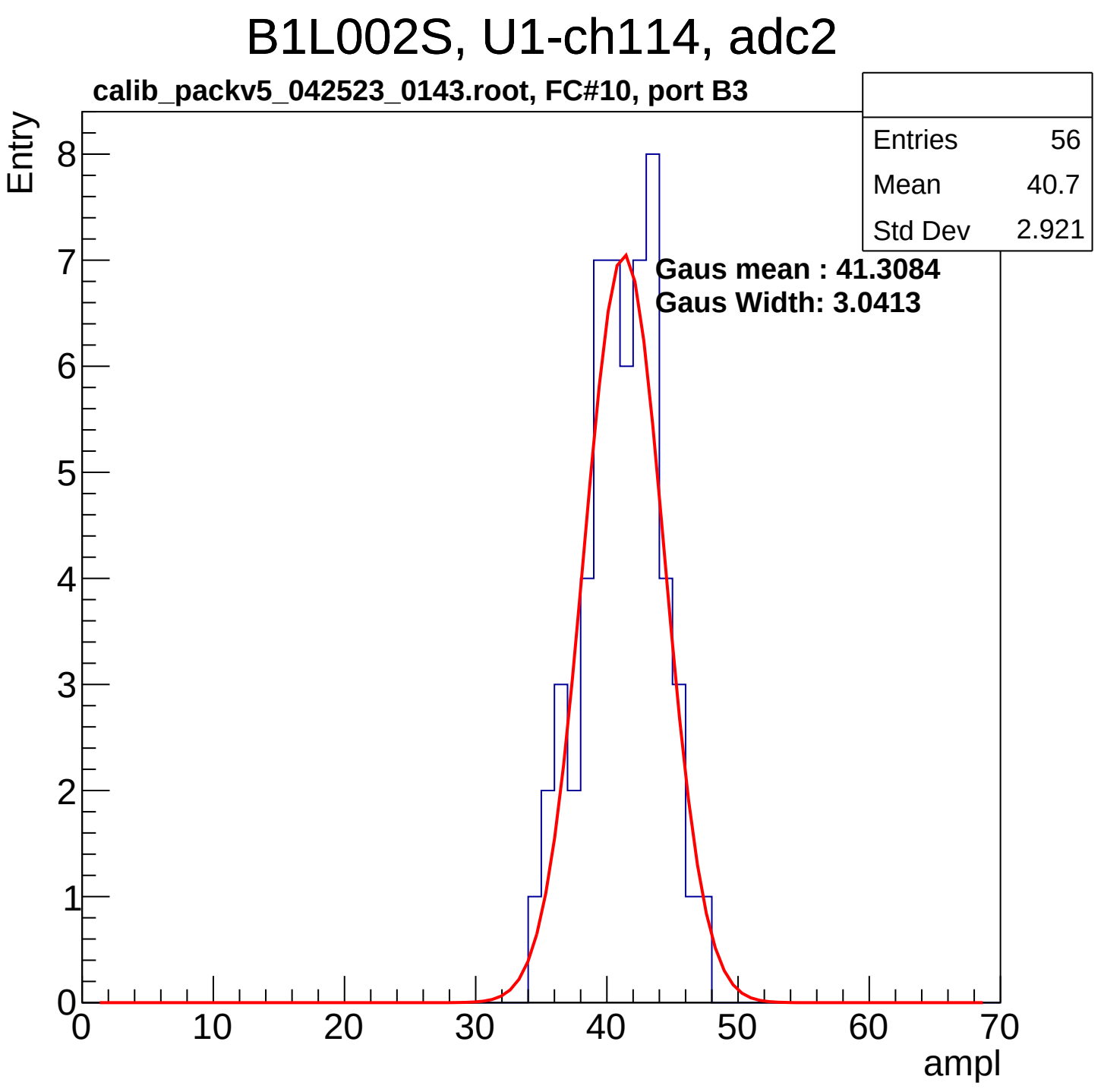
B1L002S, U1-ch114, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.7
Std Dev 2.921
Gaus mean : 41.3084
Gaus Width: 3.0413
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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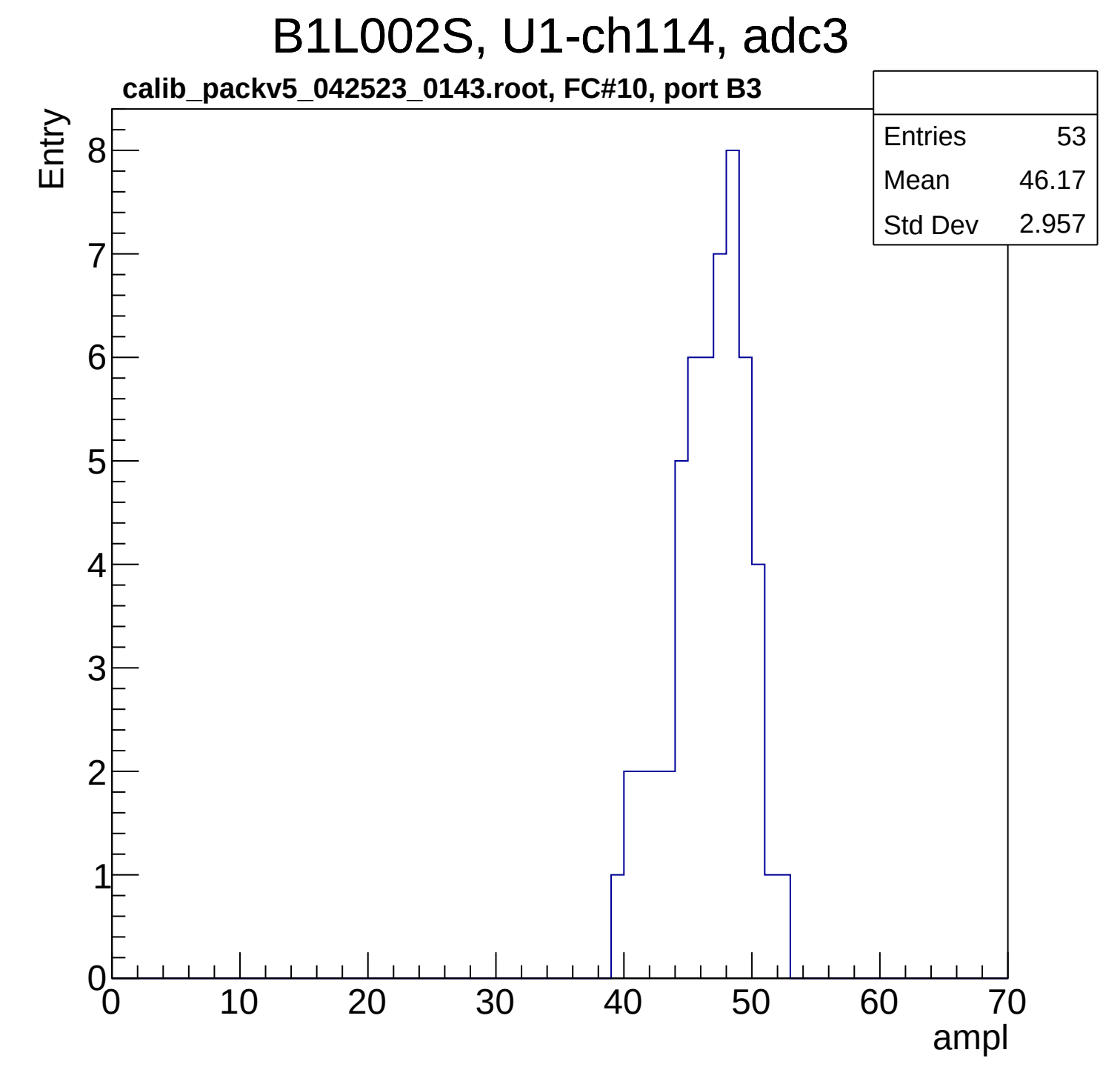
B1L002S, U1-ch114, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 46.17
Std Dev 2.957
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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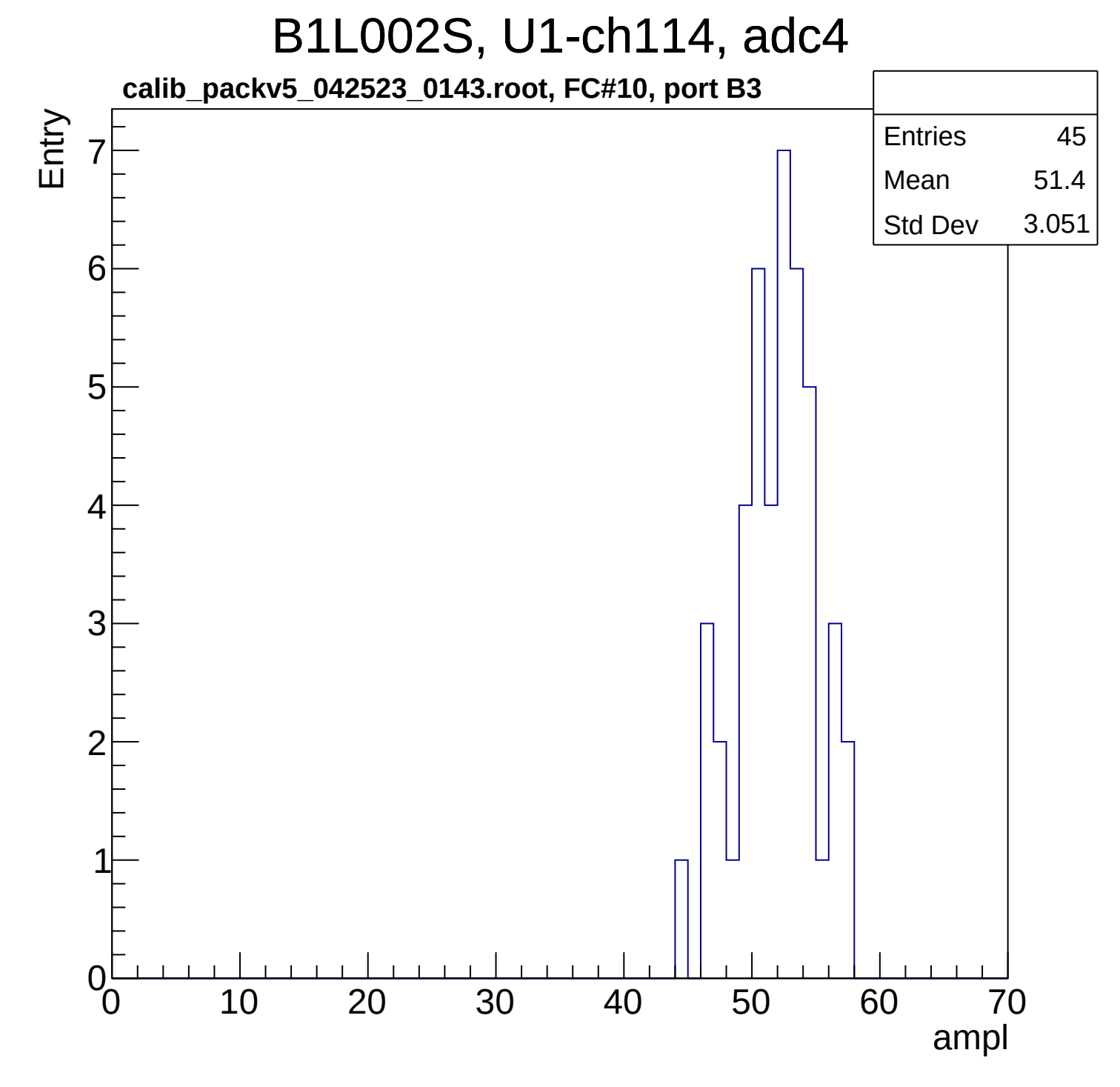
B1L002S, U1-ch114, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 51.4
Std Dev 3.051
Entry
ampl
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0
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60
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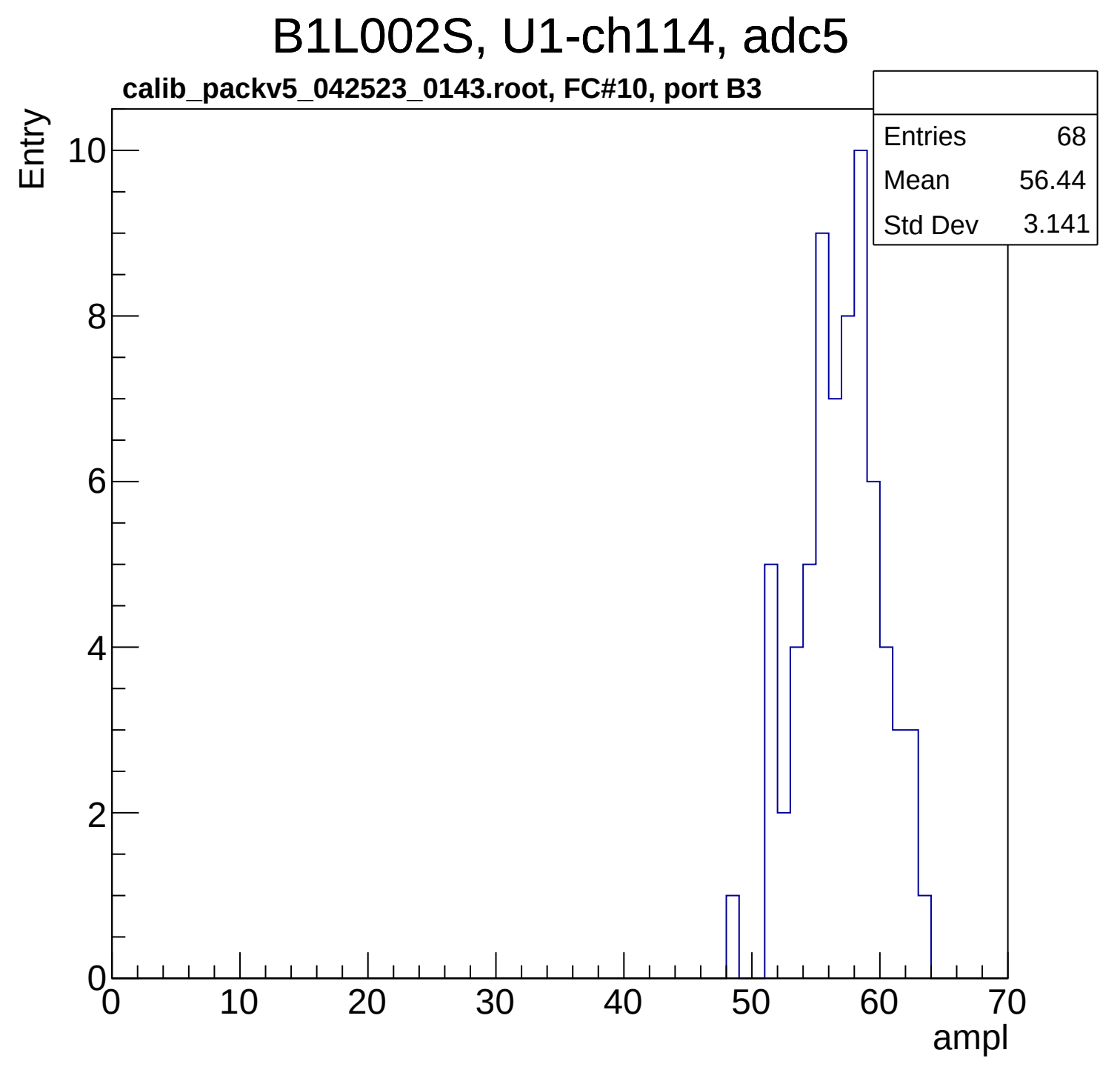
B1L002S, U1-ch114, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 56.44
Std Dev 3.141
Entry
ampl
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0
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60
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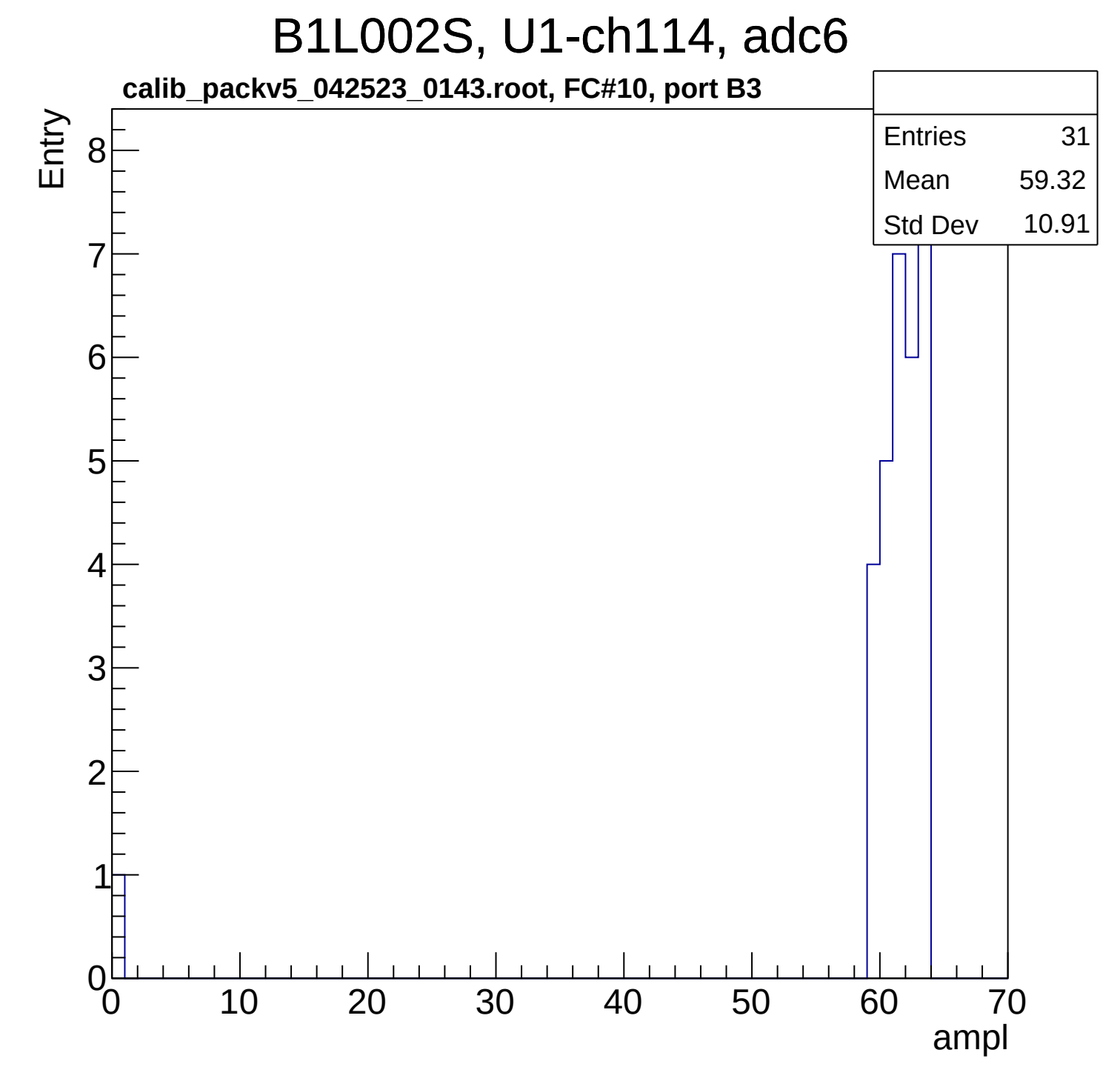
B1L002S, U1-ch114, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 59.32
Std Dev 10.91
Entry
ampl
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1
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3
4
5
6
7
8
0
10
20
30
40
50
60
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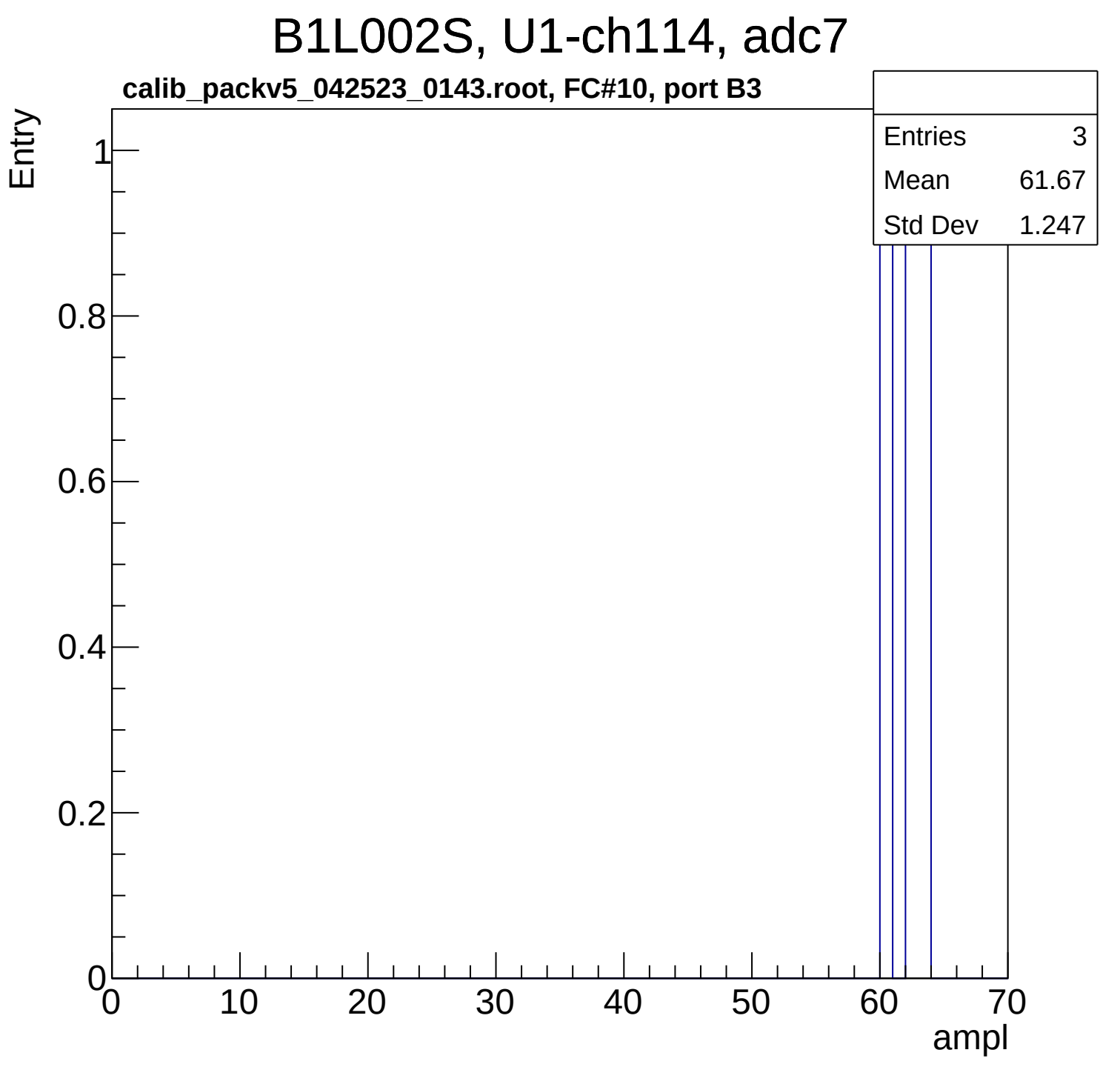
B1L002S, U1-ch114, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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0.4
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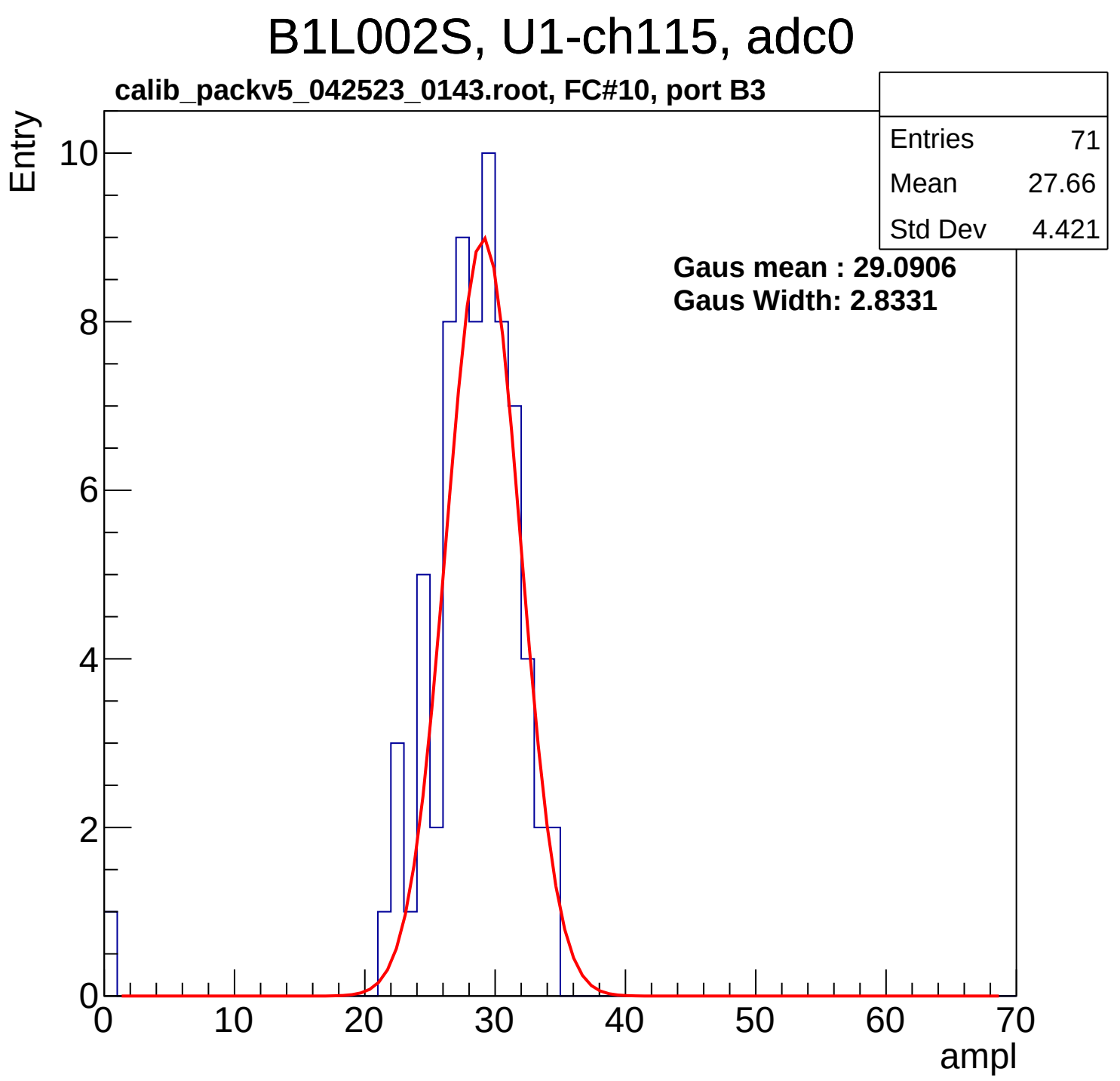
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch115, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.66
Std Dev 4.421
Gaus mean : 29.0906
Gaus Width: 2.8331
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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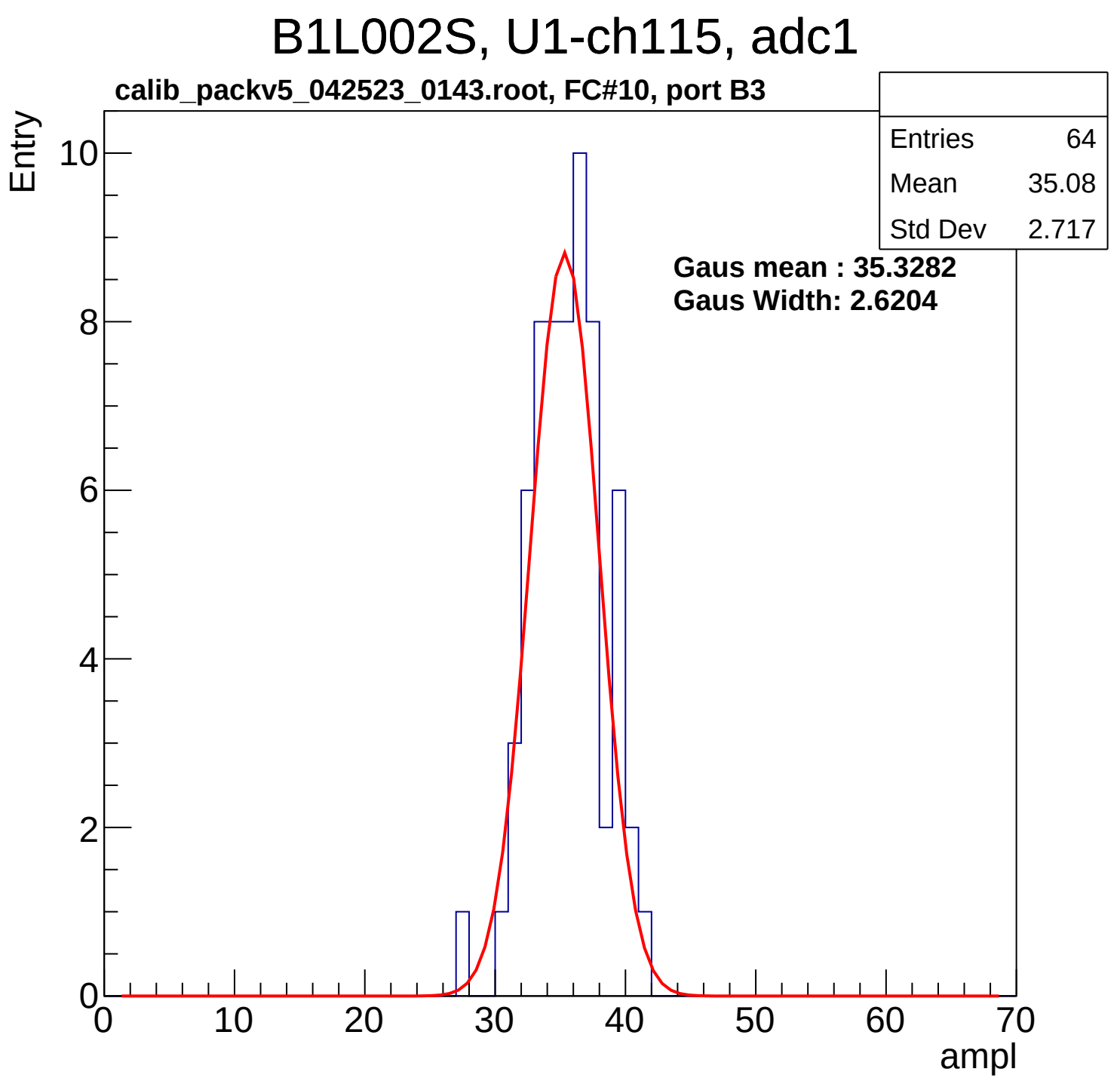
B1L002S, U1-ch115, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.08
Std Dev 2.717
Gaus mean : 35.3282
Gaus Width: 2.6204
Entry
ampl
0
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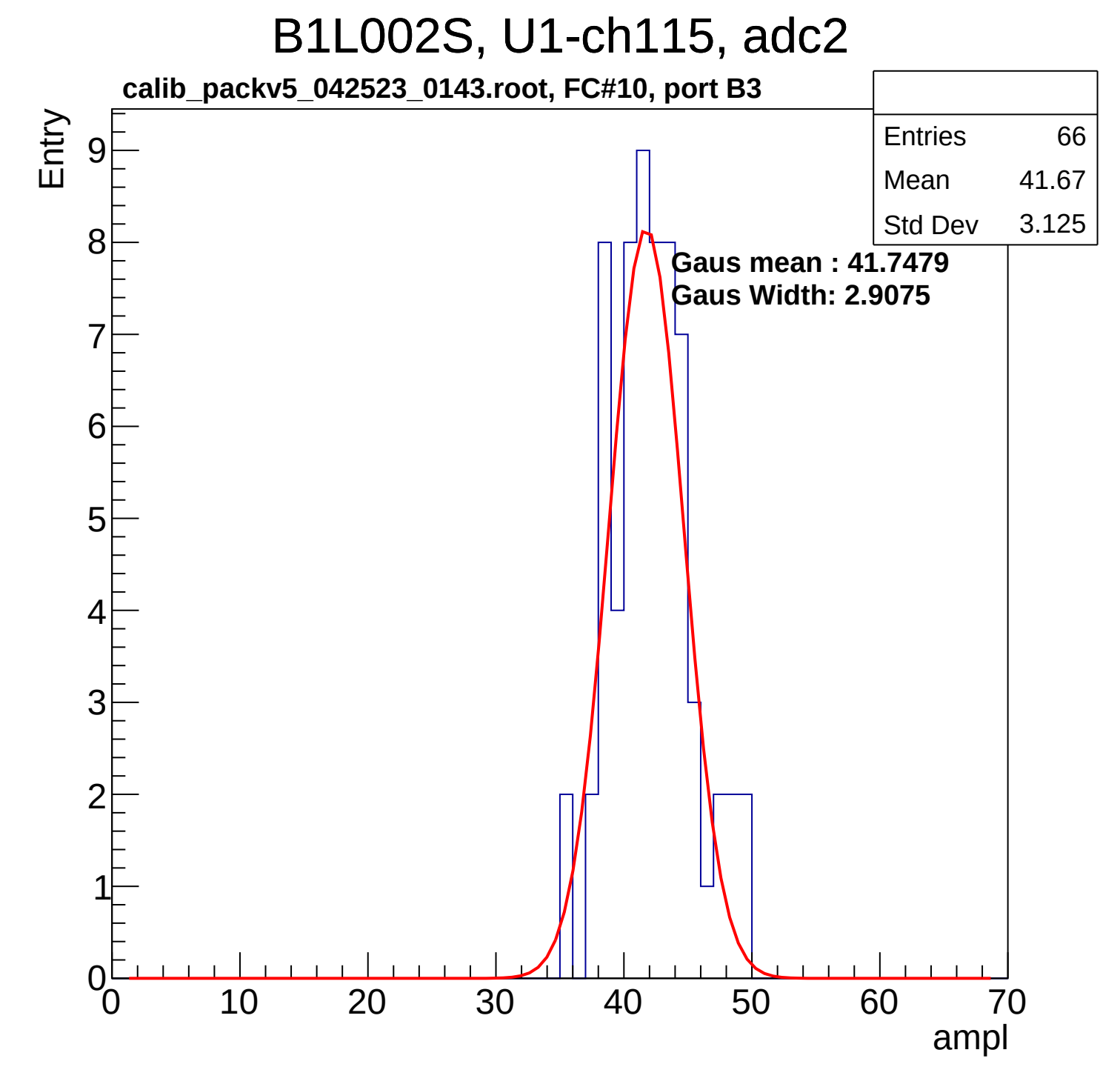
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch115, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.67
Std Dev 3.125
Gaus mean : 41.7479
Gaus Width: 2.9075
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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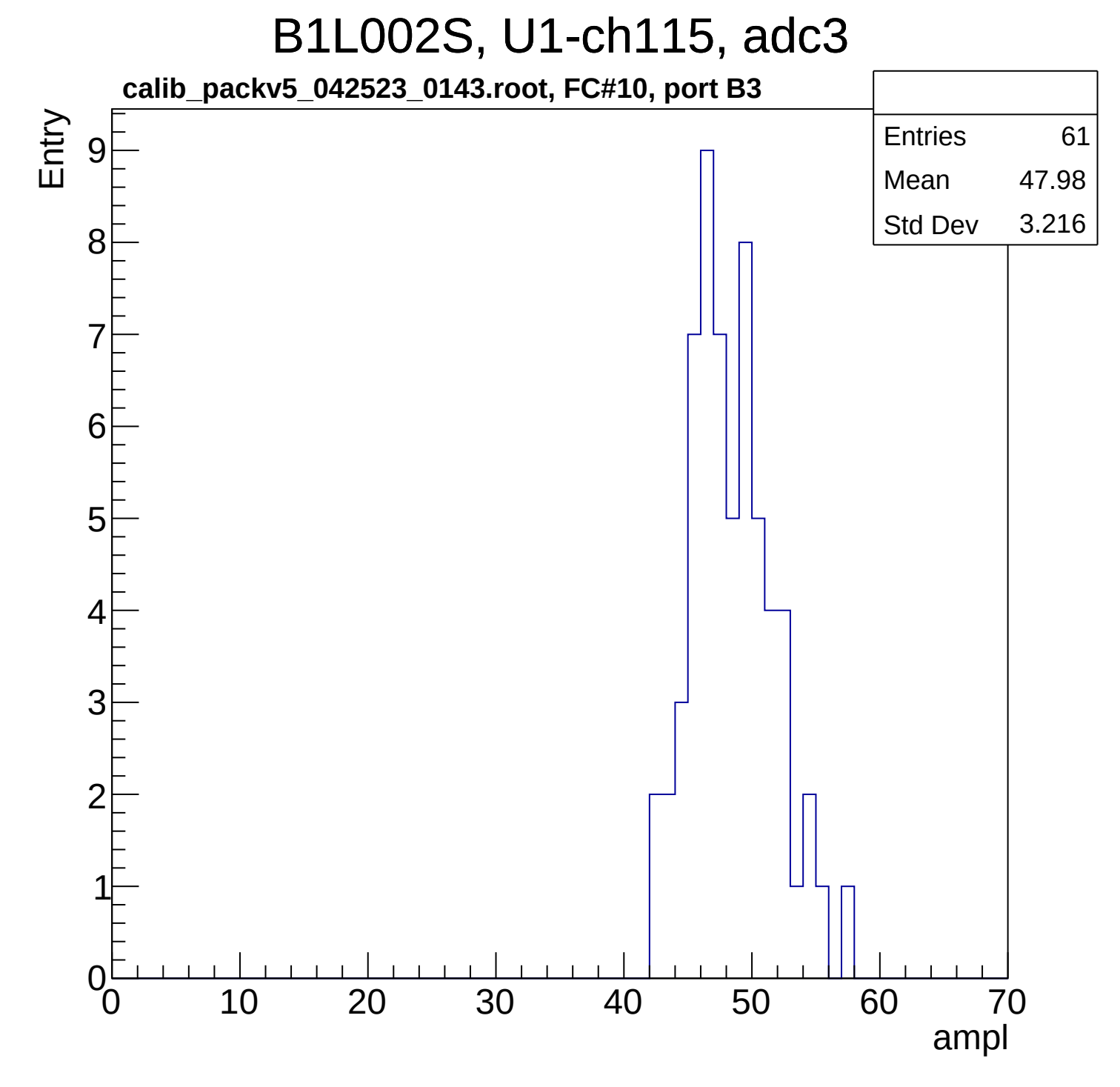
B1L002S, U1-ch115, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.98
Std Dev 3.216
Entry
ampl
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3
4
5
6
7
8
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0
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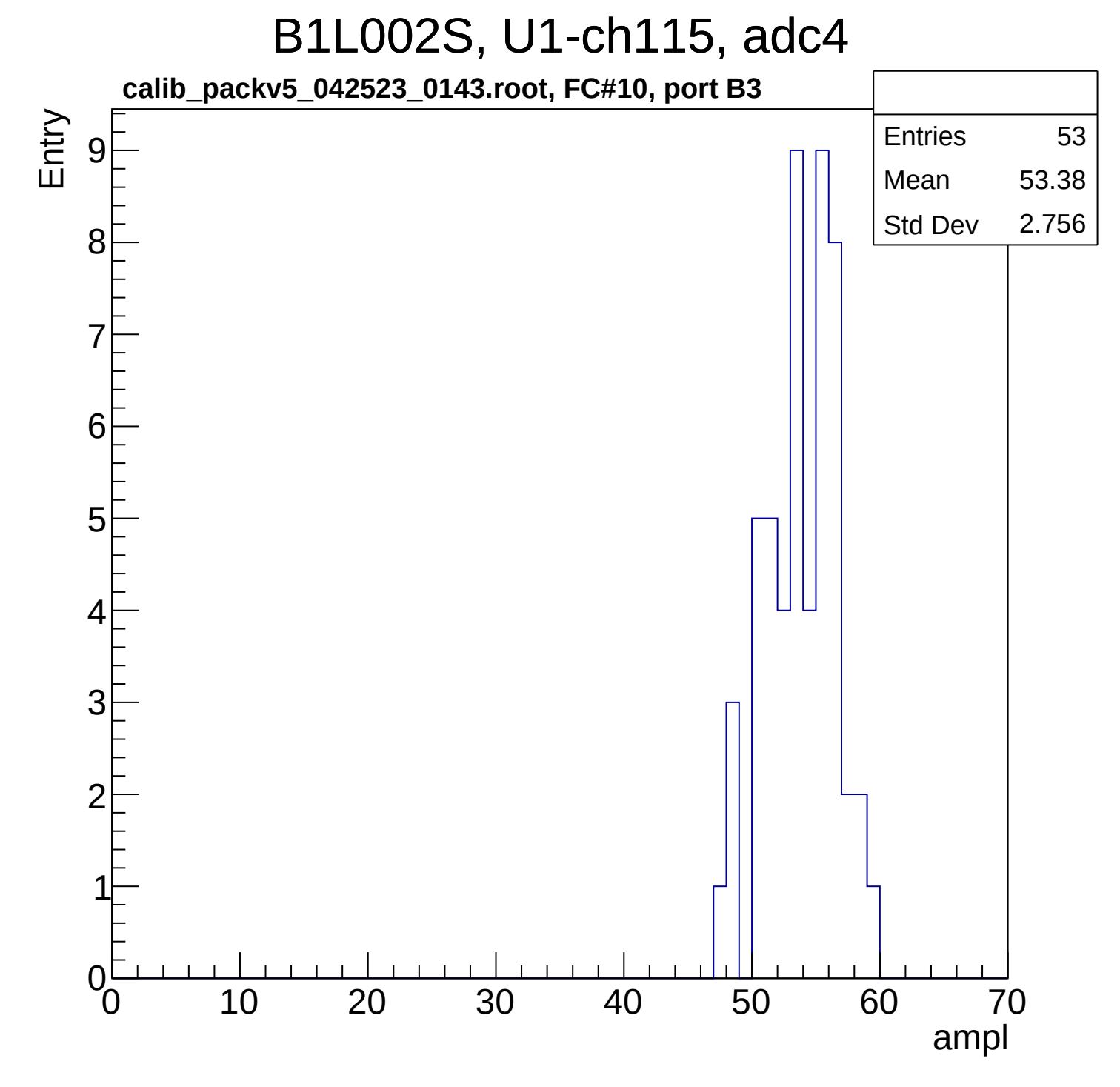
B1L002S, U1-ch115, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.38
Std Dev 2.756
Entry
ampl
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3
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5
6
7
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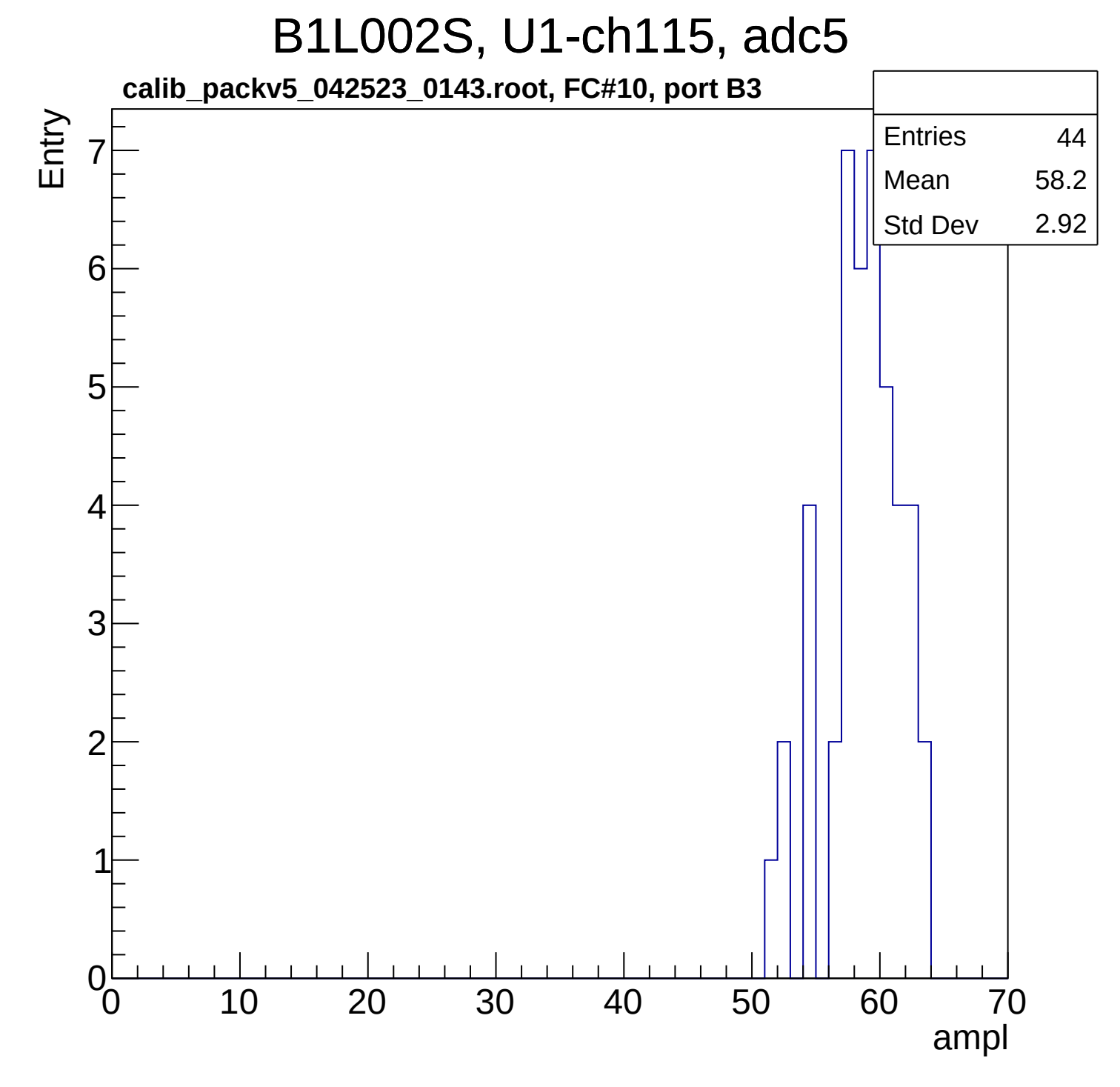
B1L002S, U1-ch115, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 58.2
Std Dev 2.92
Entry
ampl
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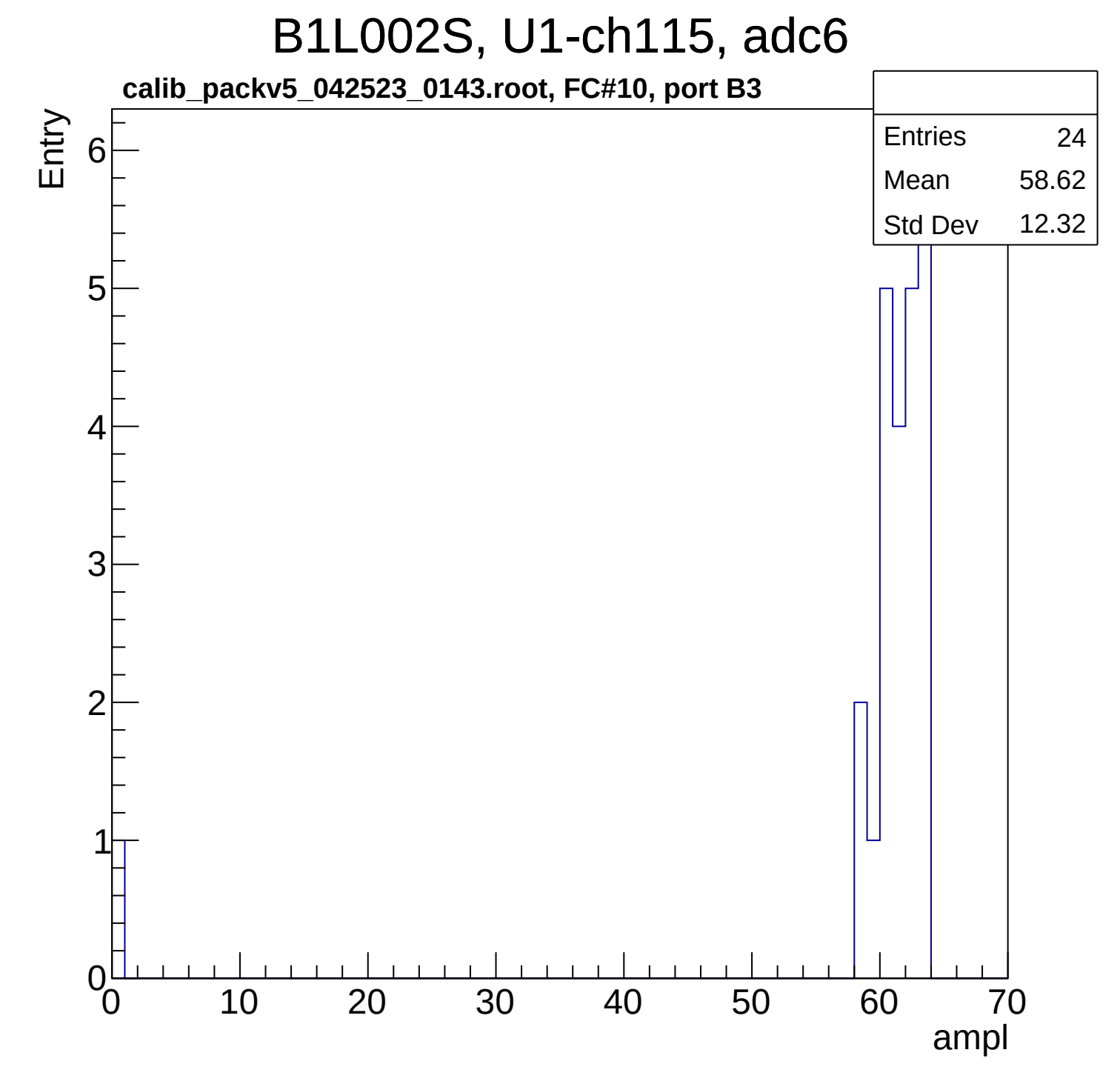
B1L002S, U1-ch115, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.62
Std Dev 12.32
Entry
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20
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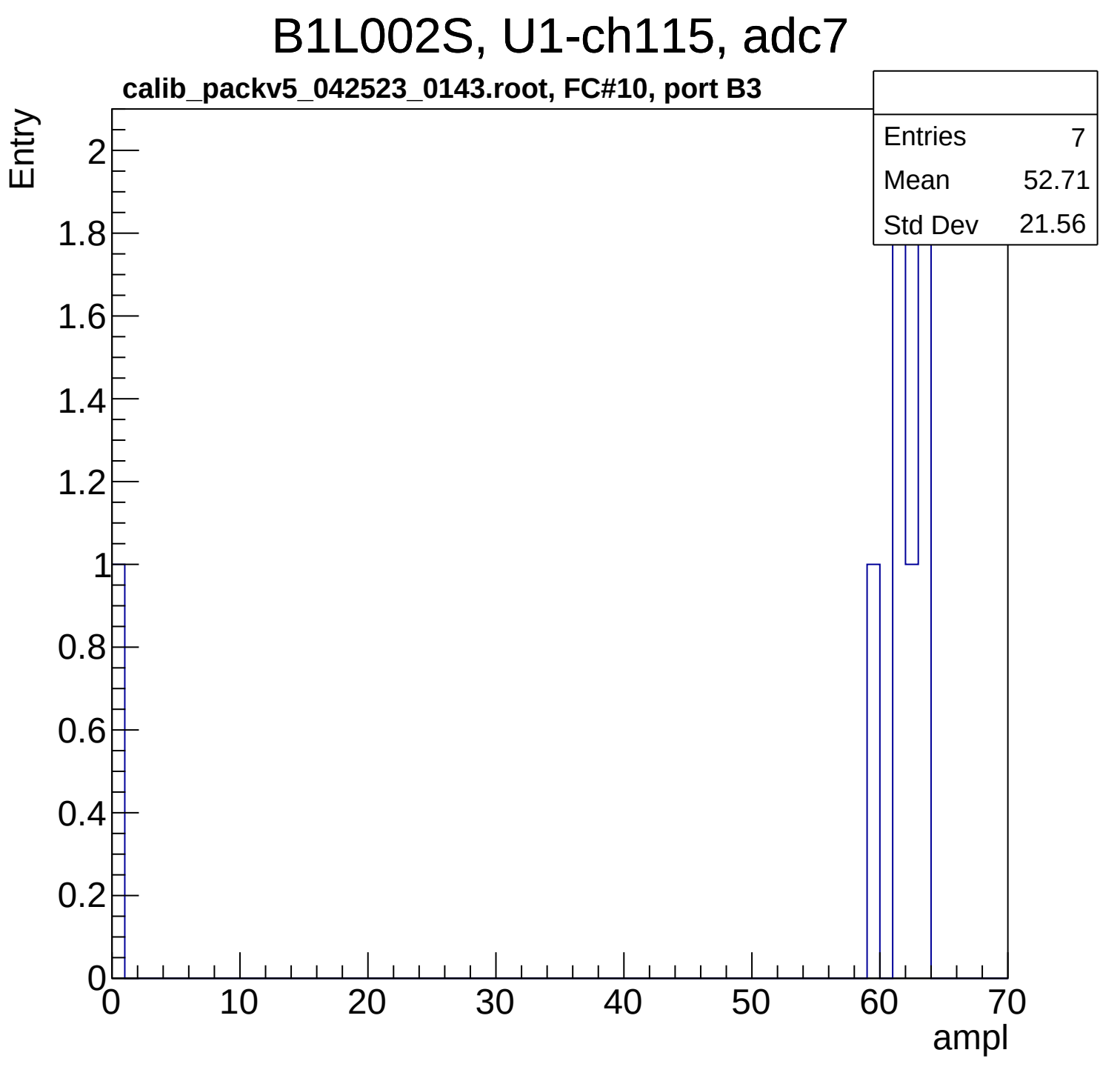
60
70
ampl


B1L002S, U1-ch115, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 52.71
Std Dev 21.56
Entry
ampl
0
0.2
0.4
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1.4
1.6
1.8
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0
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60
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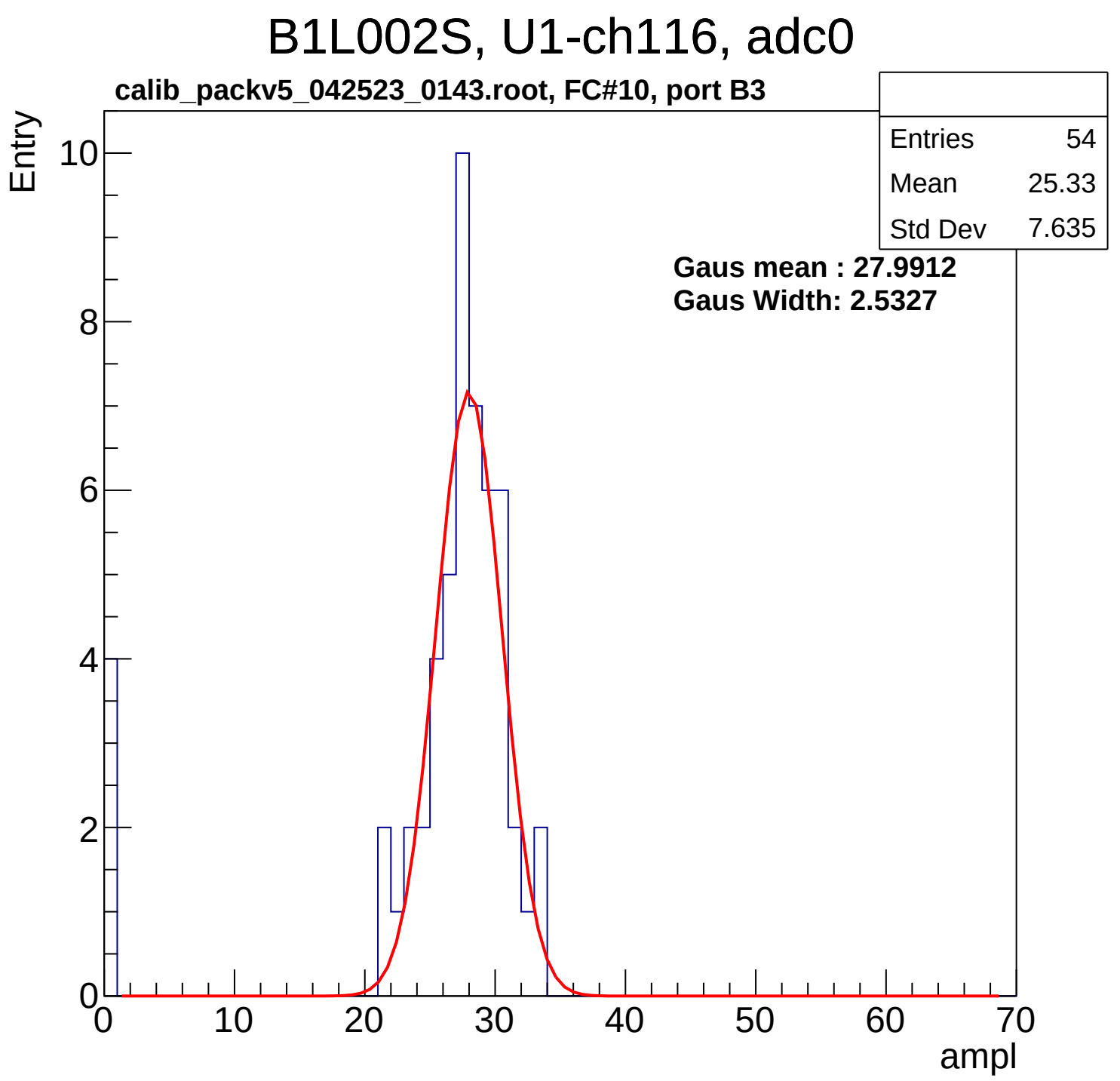
B1L002S, U1-ch116, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 25.33
Std Dev 7.635
Gaus mean : 27.9912
Gaus Width: 2.5327
Entry
ampl
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2
4
6
8
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0
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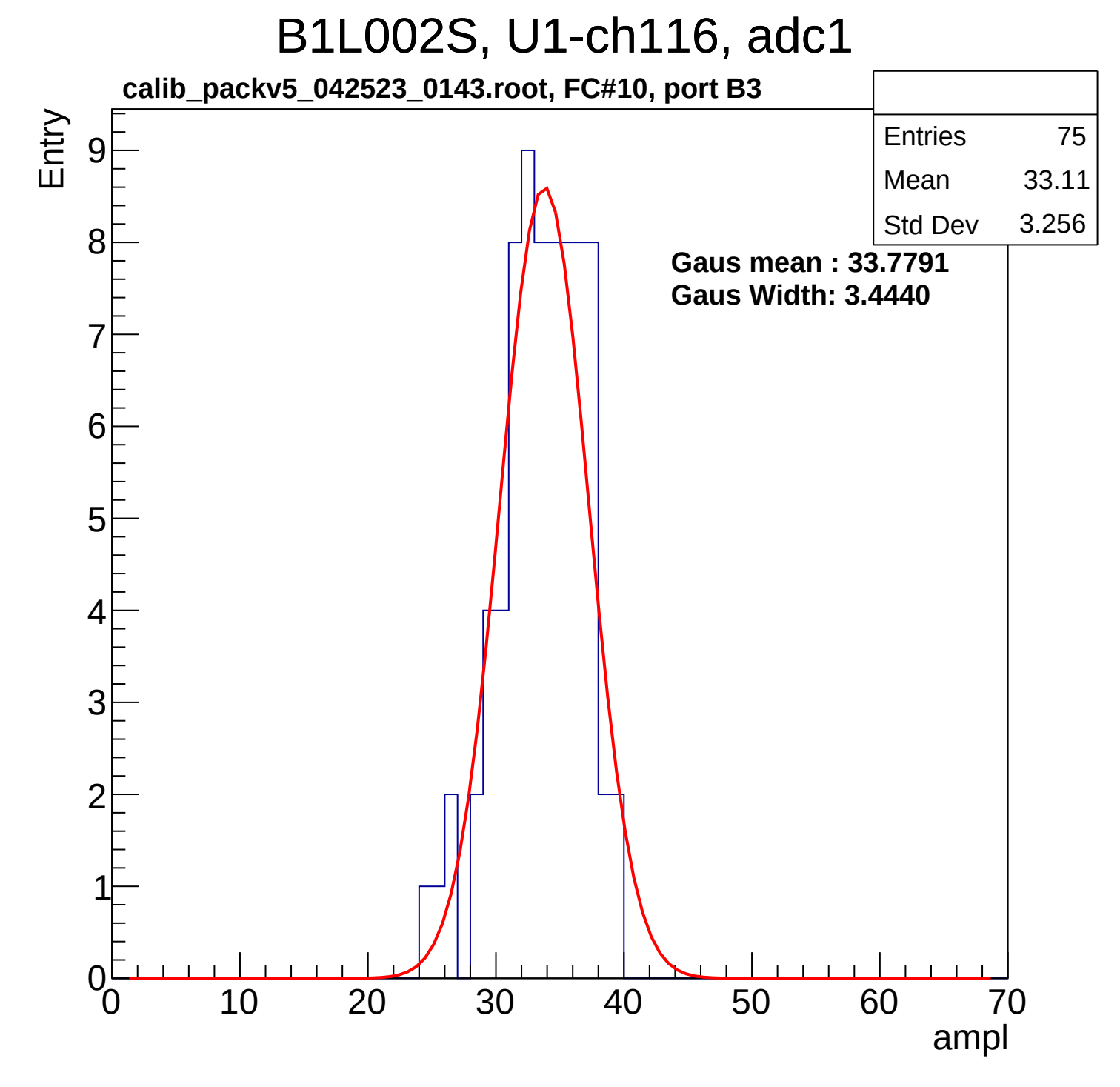
B1L002S, U1-ch116, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 33.11
Std Dev 3.256
Gaus mean : 33.7791
Gaus Width: 3.4440
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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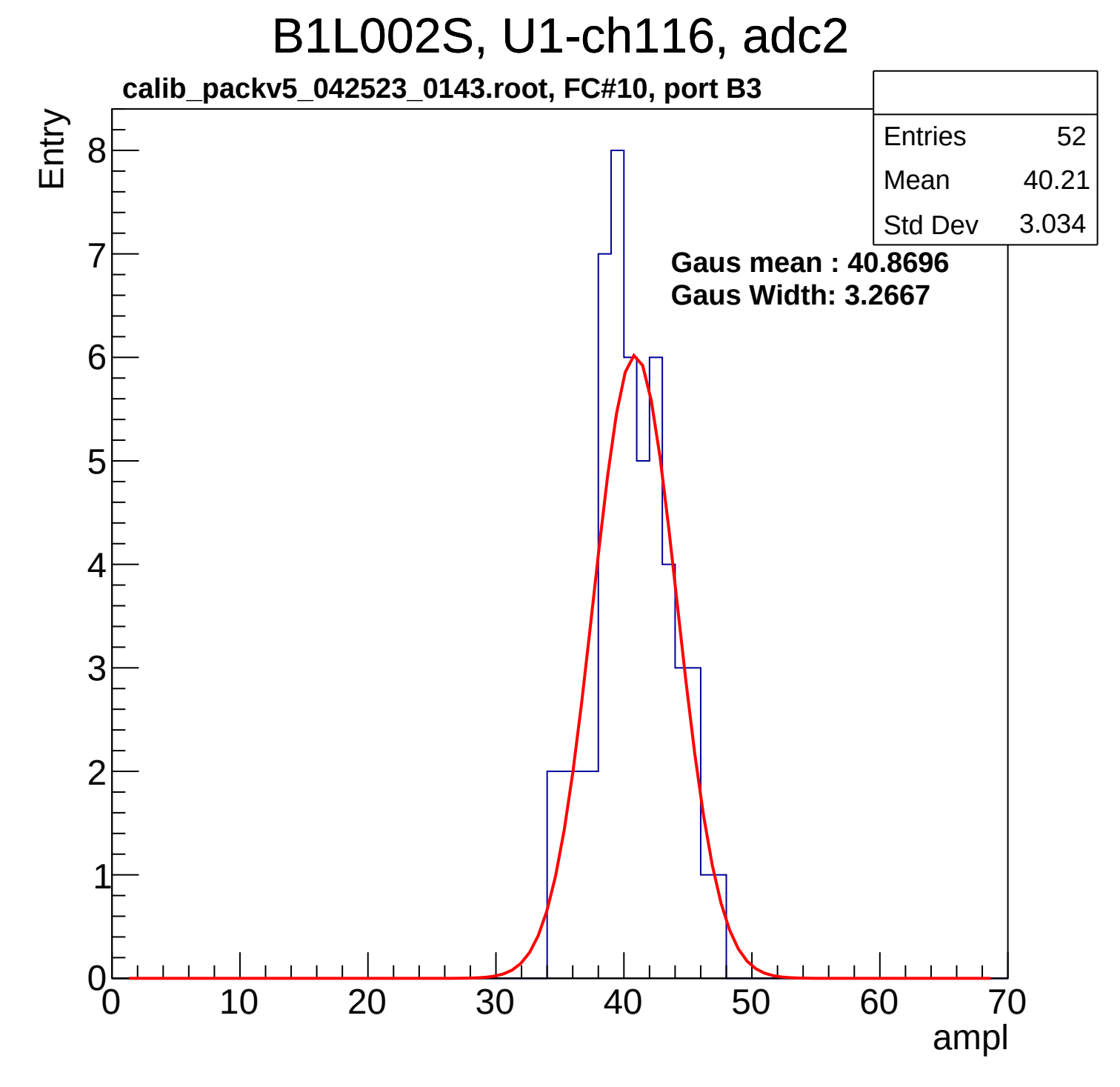
B1L002S, U1-ch116, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.21
Std Dev 3.034
Gaus mean : 40.8696
Gaus Width: 3.2667
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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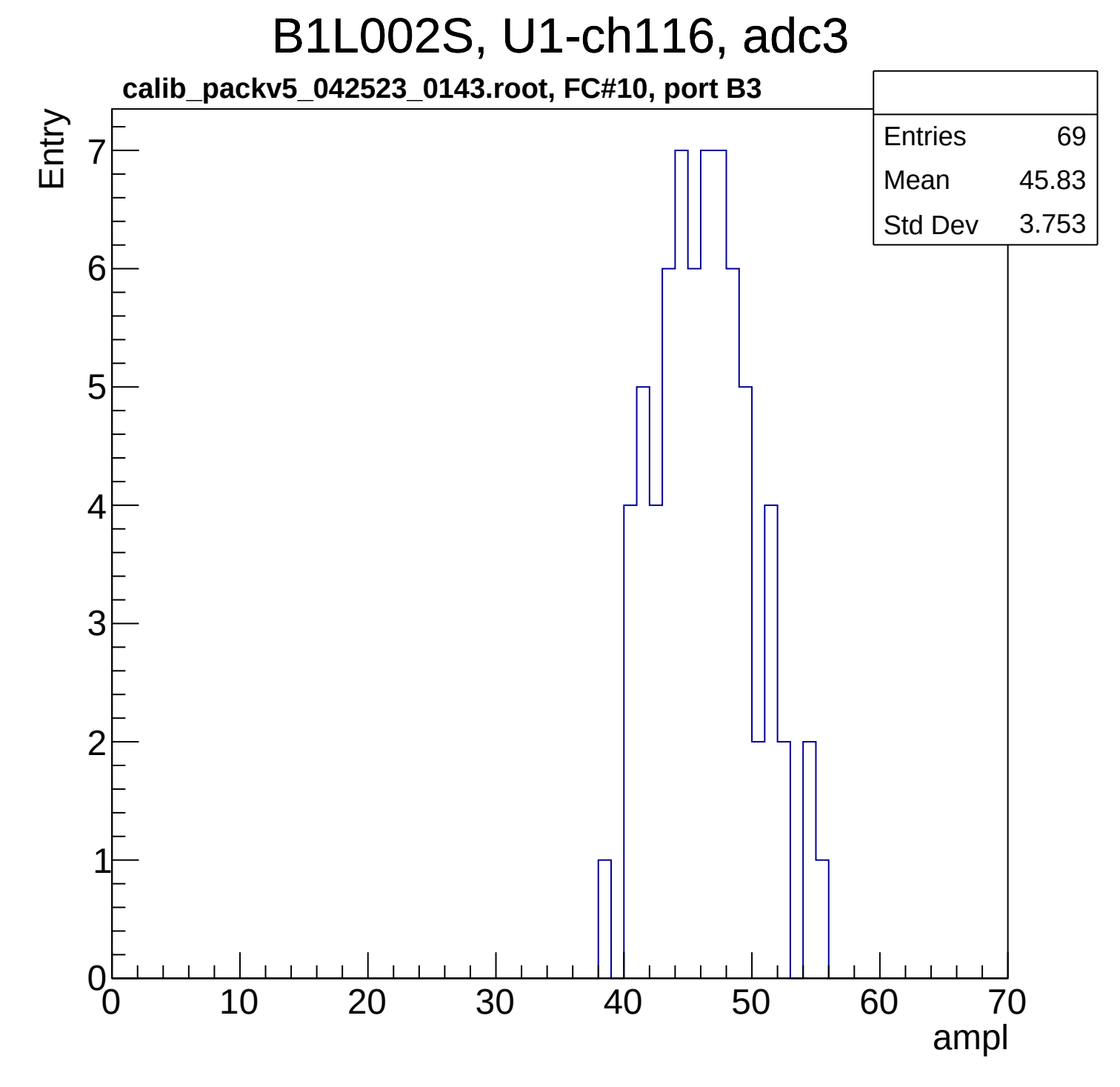
B1L002S, U1-ch116, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 45.83
Std Dev 3.753
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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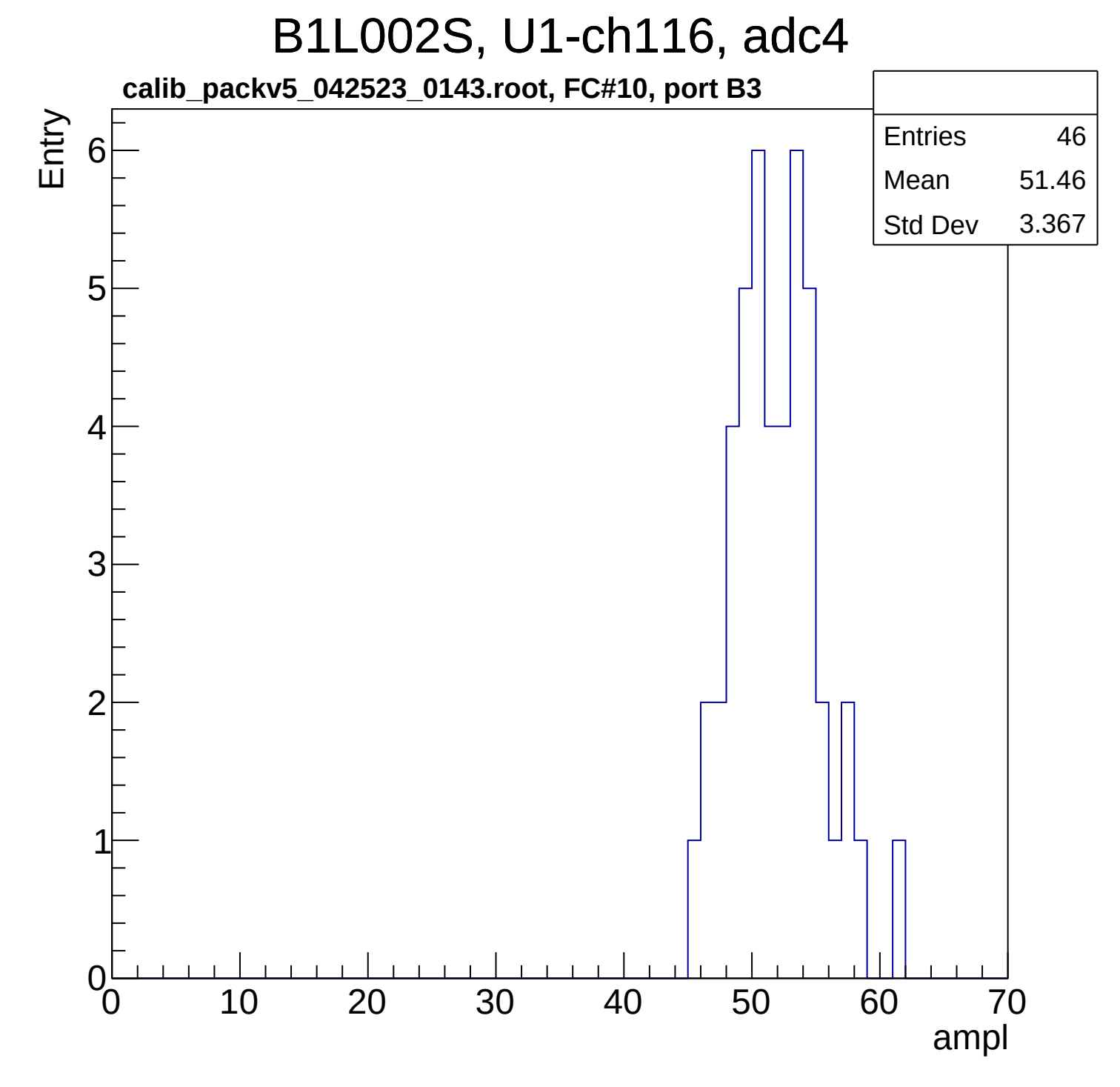
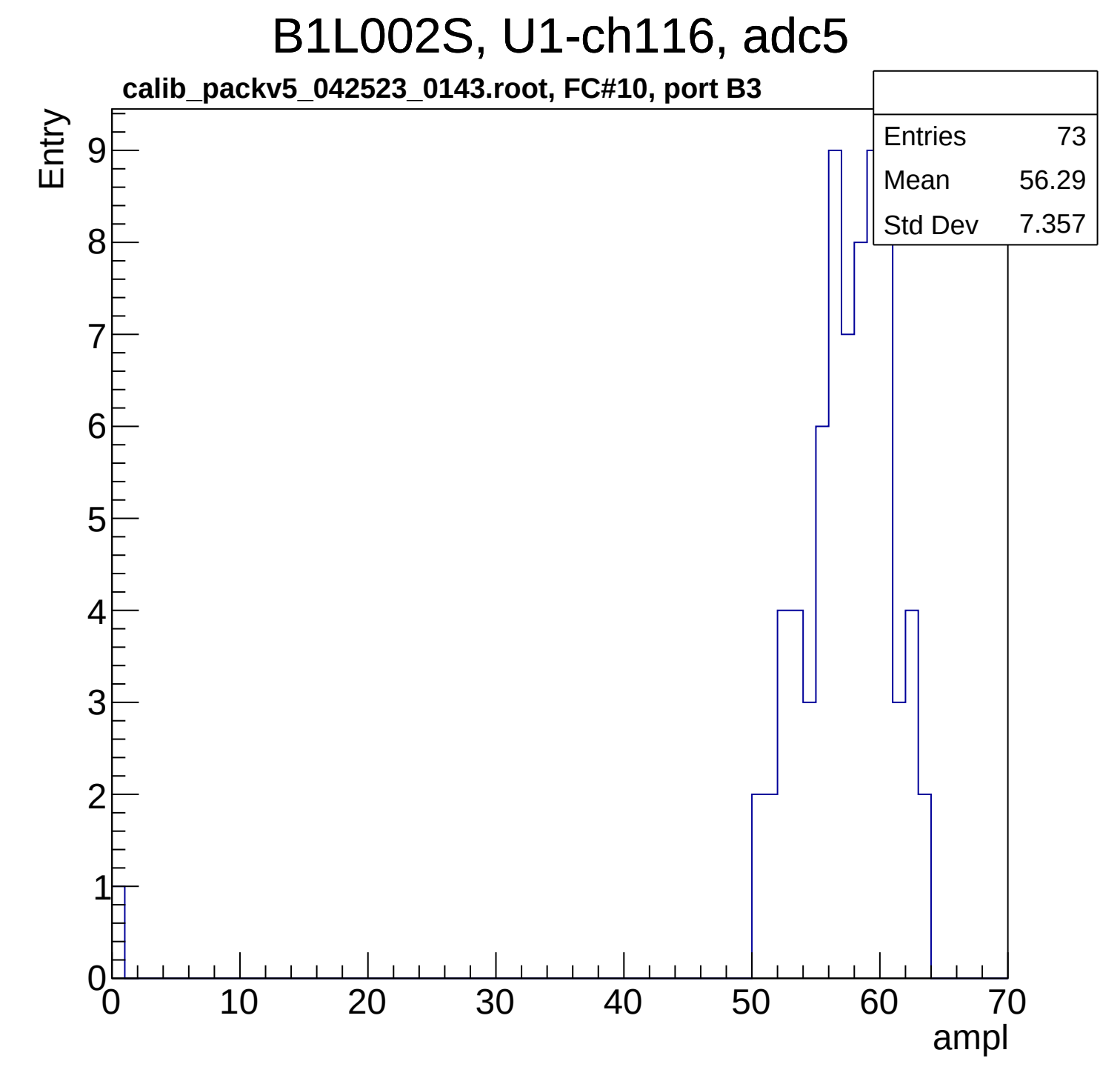

B1L002S, U1-ch116, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 51.46
Std Dev 3.367
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch116, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 56.29
Std Dev 7.357
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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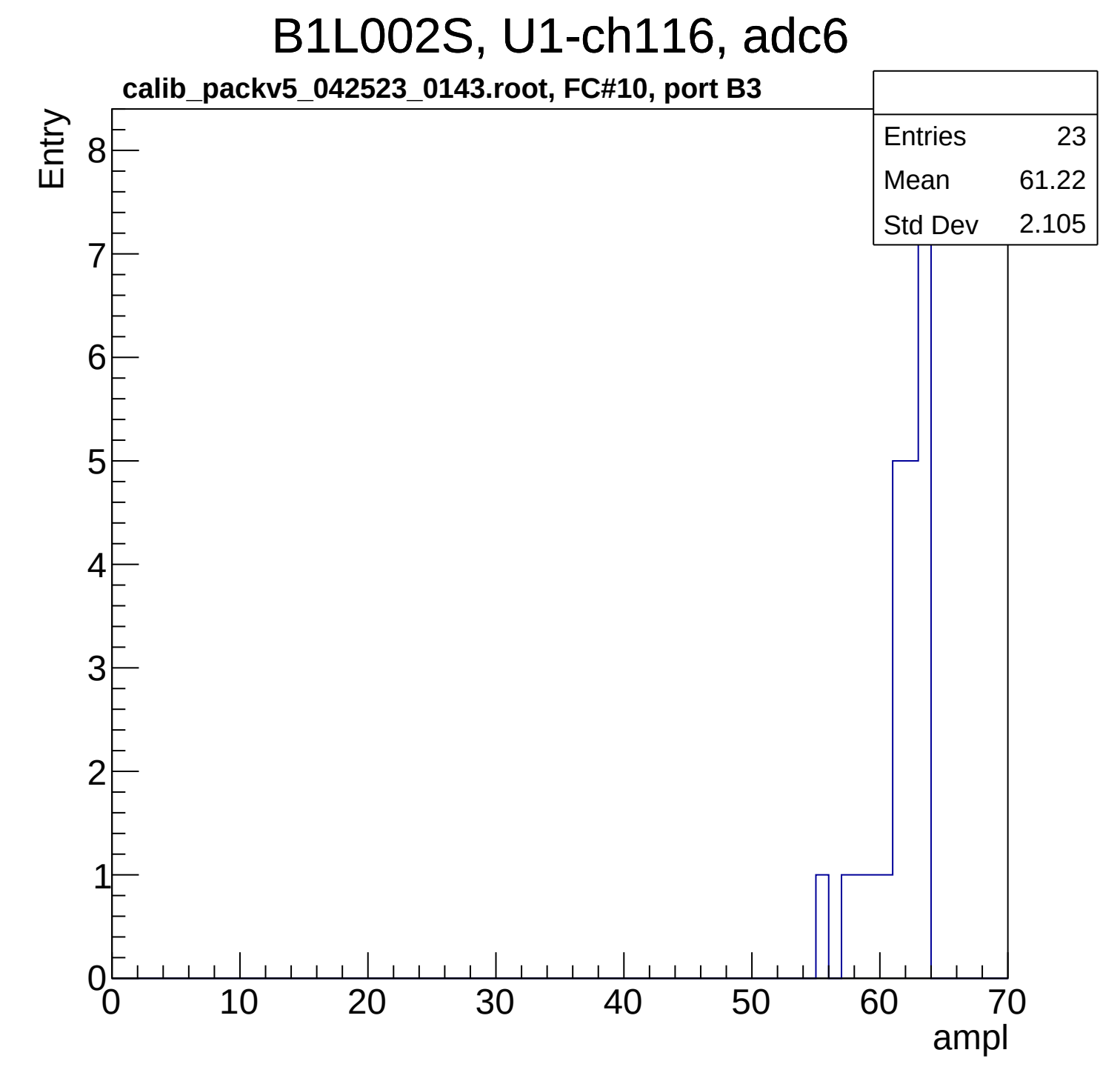
B1L002S, U1-ch116, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.22
Std Dev 2.105
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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60
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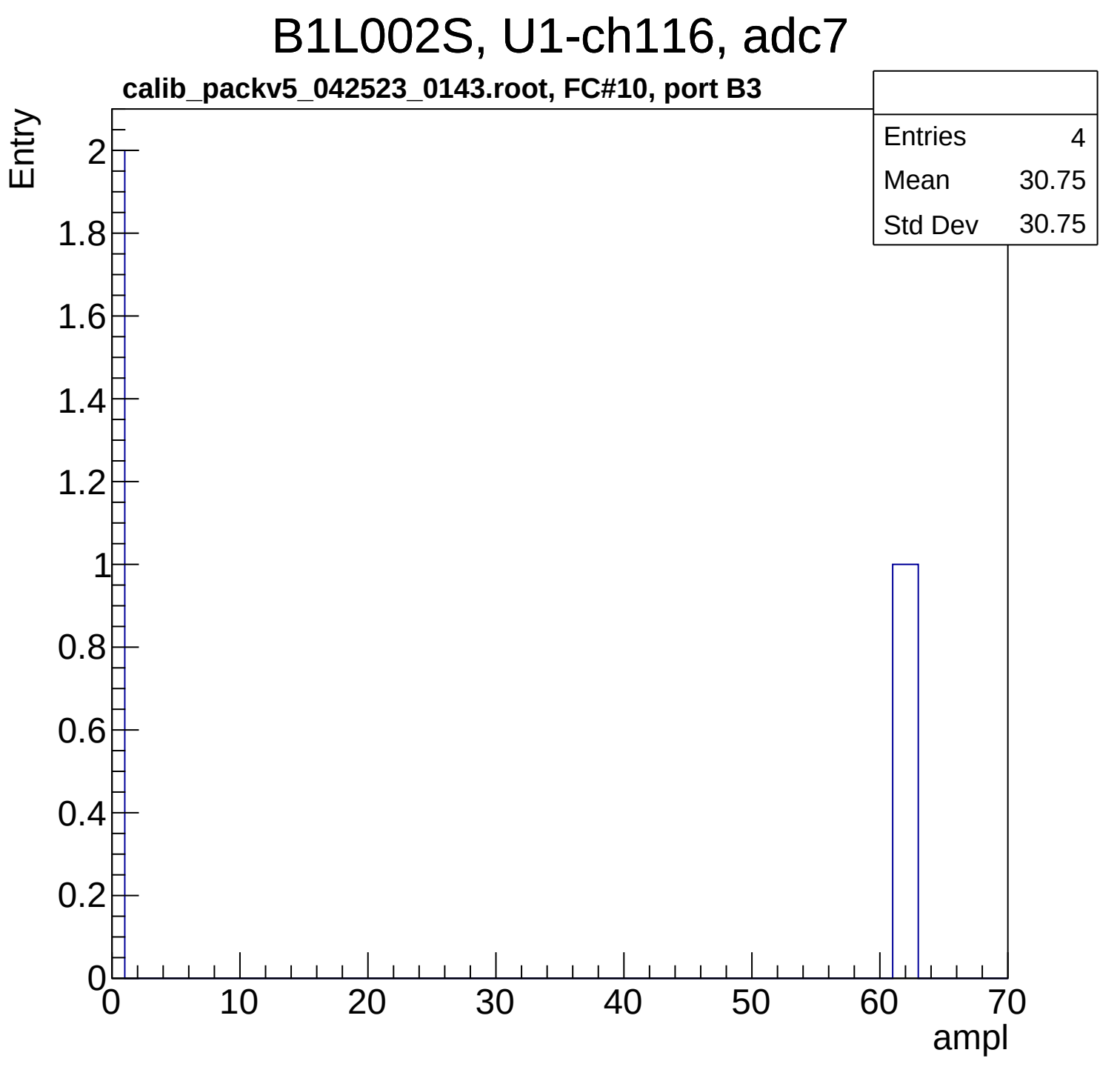
B1L002S, U1-ch116, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 30.75
Std Dev 30.75
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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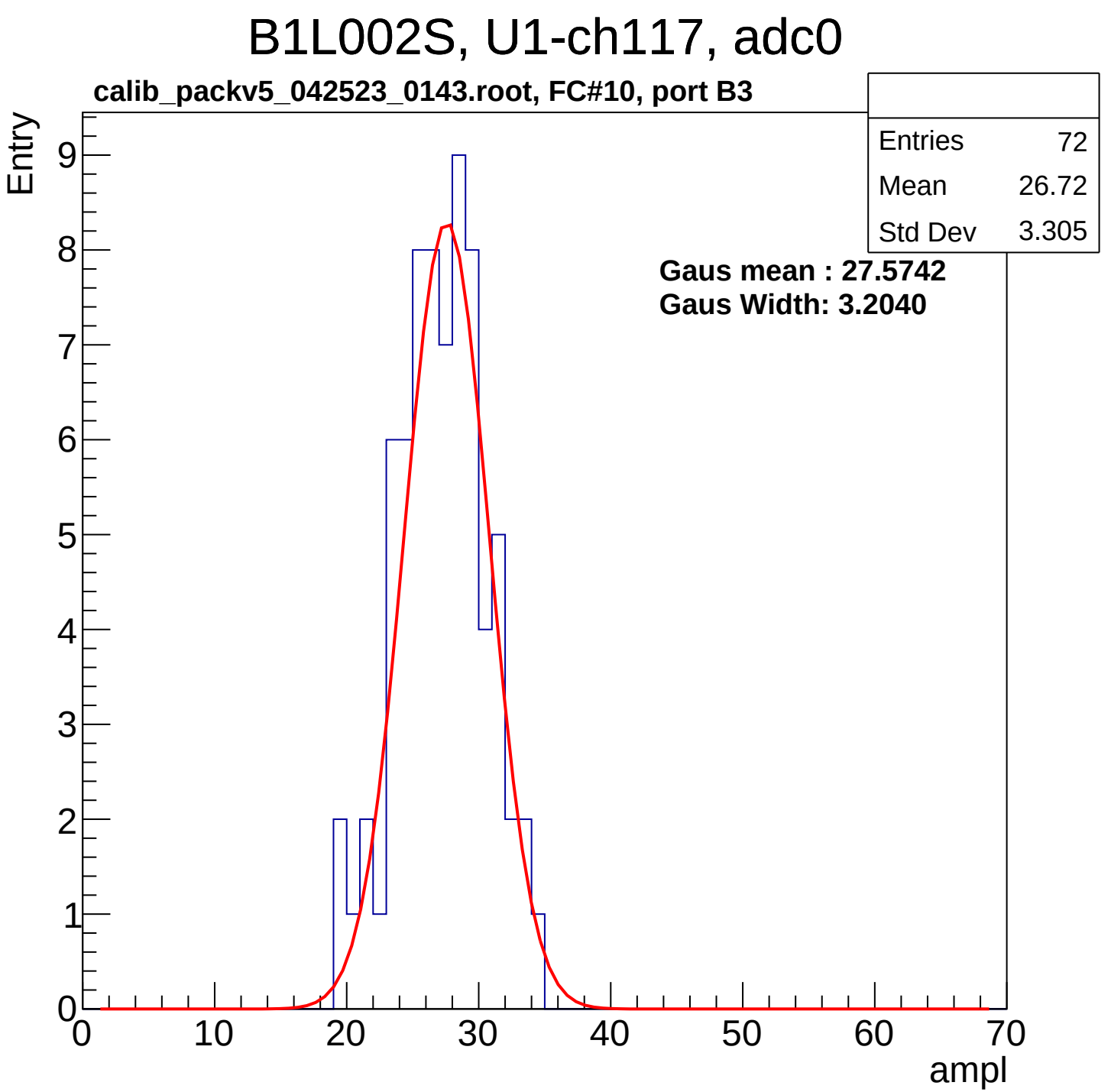
B1L002S, U1-ch117, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 26.72
Std Dev 3.305
Gaus mean : 27.5742
Gaus Width: 3.2040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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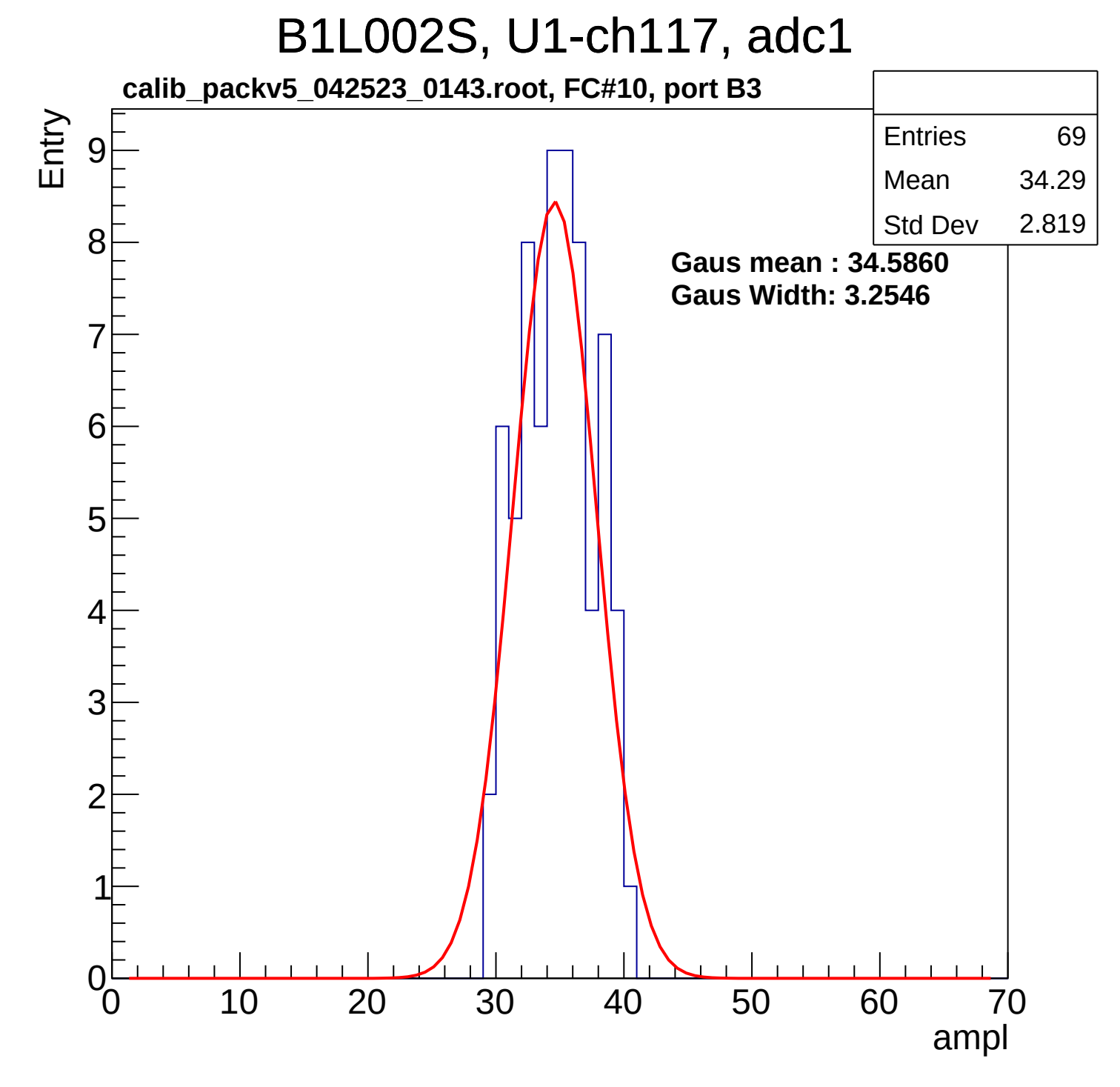
B1L002S, U1-ch117, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.29
Std Dev 2.819
Gaus mean : 34.5860
Gaus Width: 3.2546
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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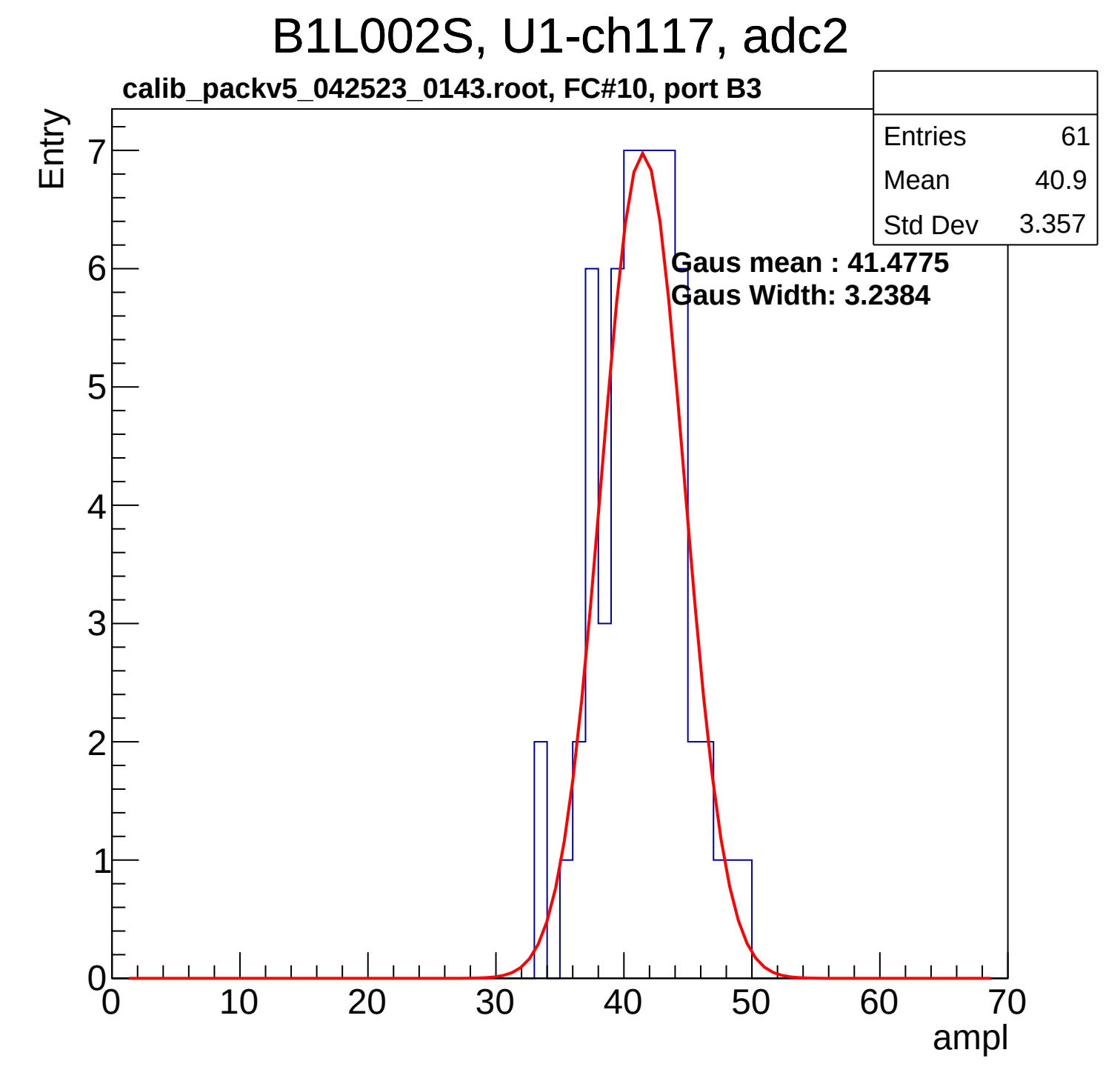
B1L002S, U1-ch117, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 40.9
Std Dev 3.357
Gaus mean : 41.4775
Gaus Width: 3.2384
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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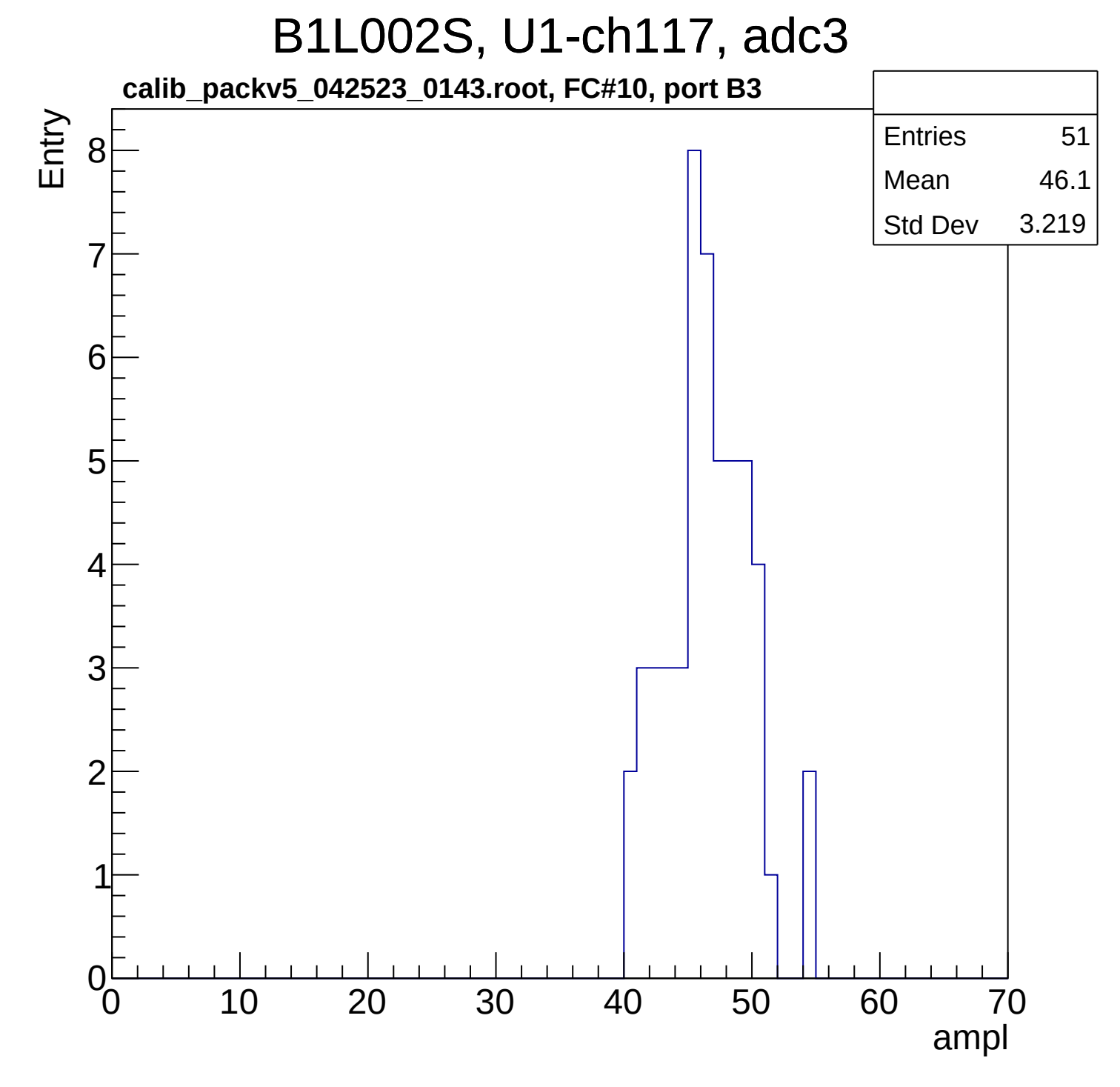
B1L002S, U1-ch117, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.1
Std Dev 3.219
Entry
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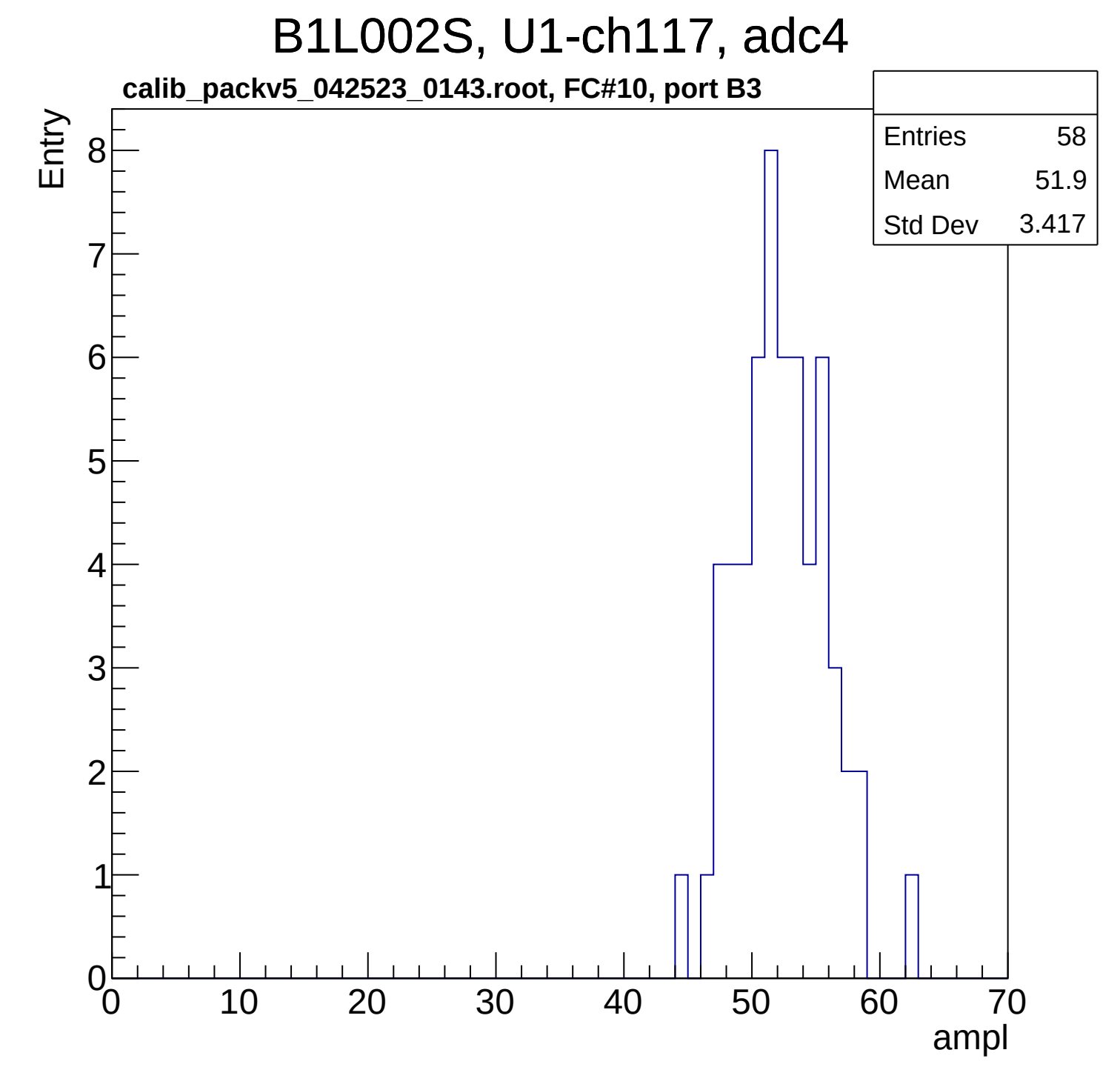
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch117, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 51.9
Std Dev 3.417
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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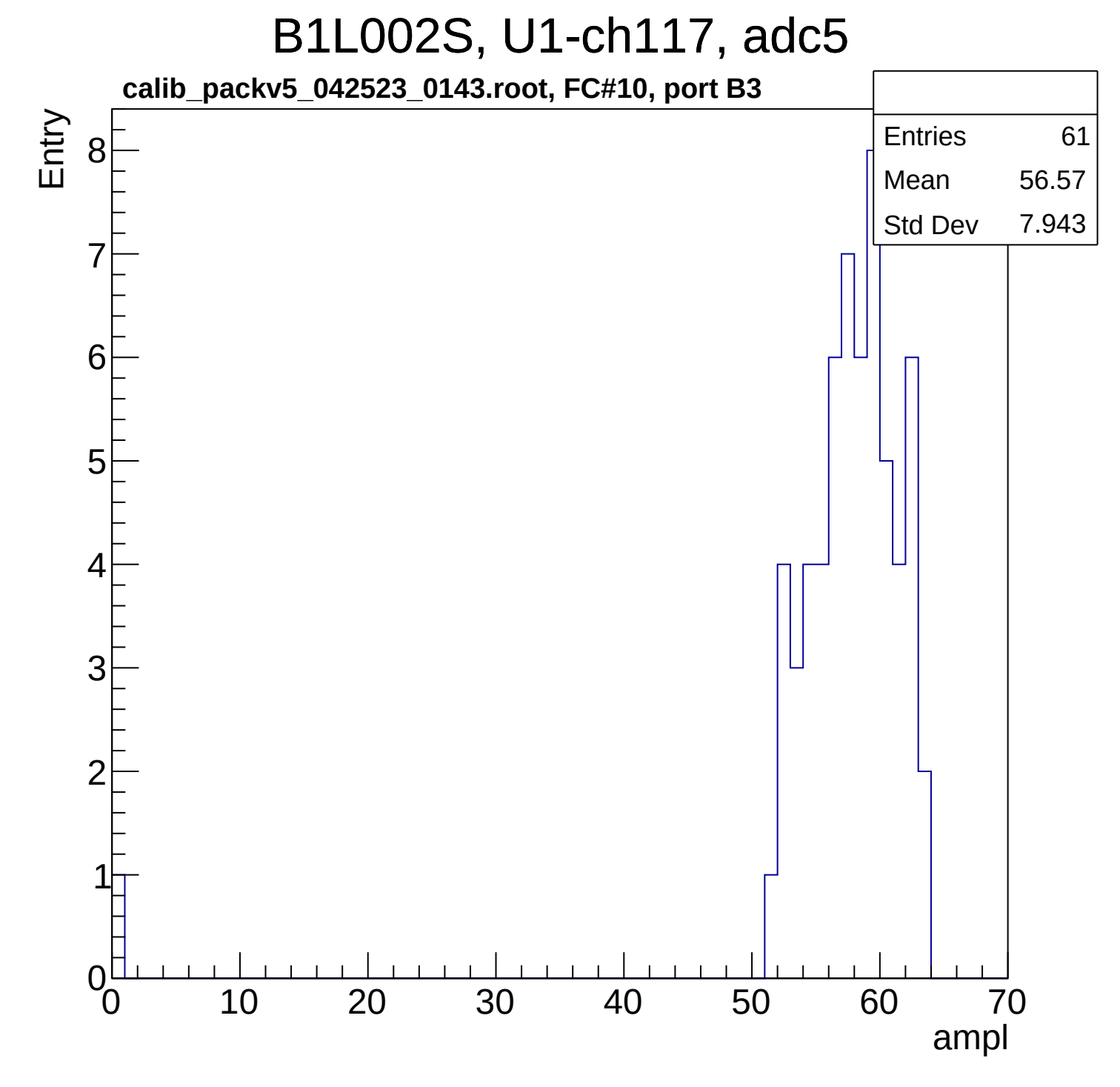
B1L002S, U1-ch117, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 56.57
Std Dev 7.943
Entry
ampl
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4
5
6
7
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0
10
20
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40
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60
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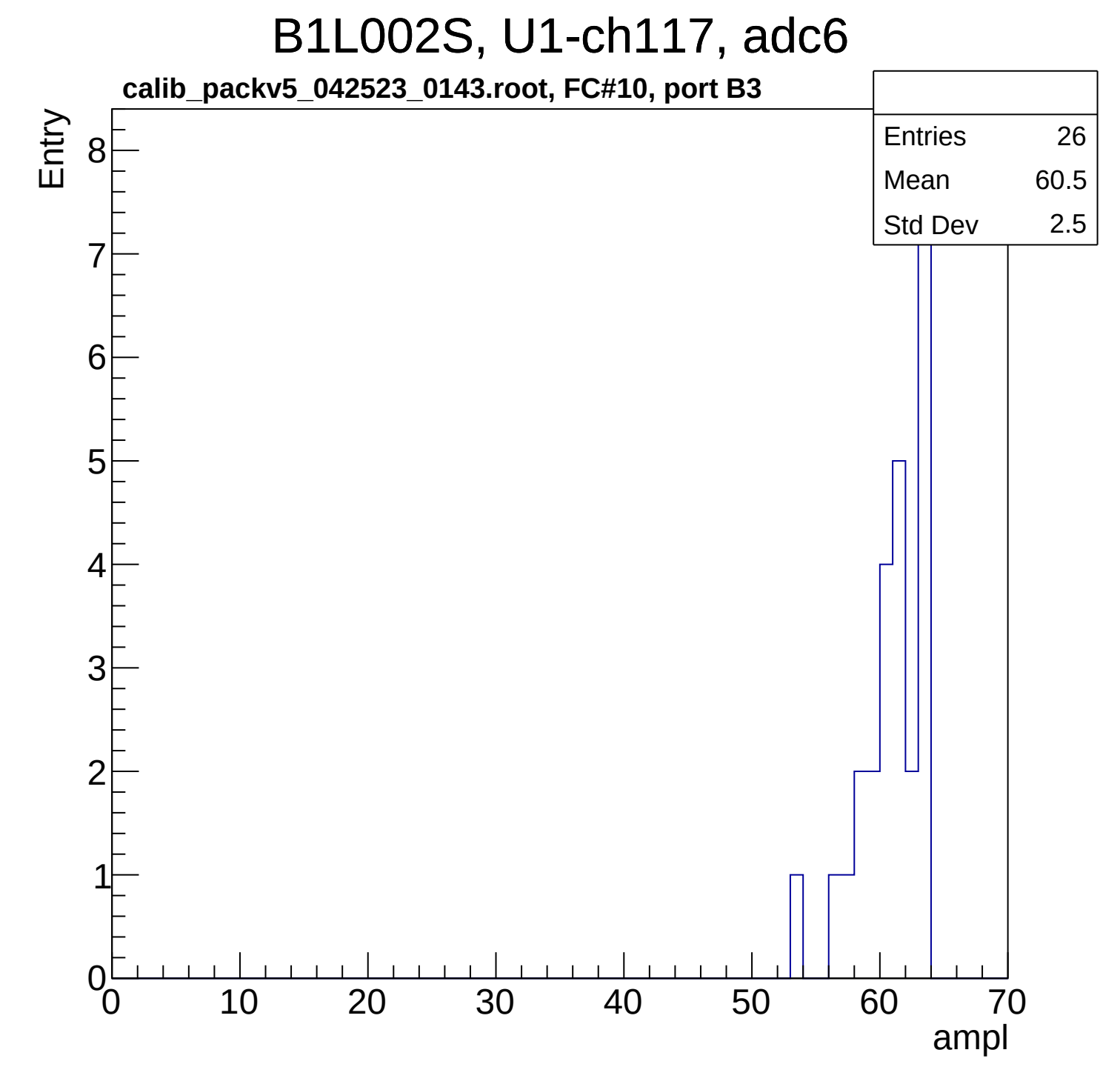
B1L002S, U1-ch117, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 60.5
Std Dev 2.5
Entry
ampl
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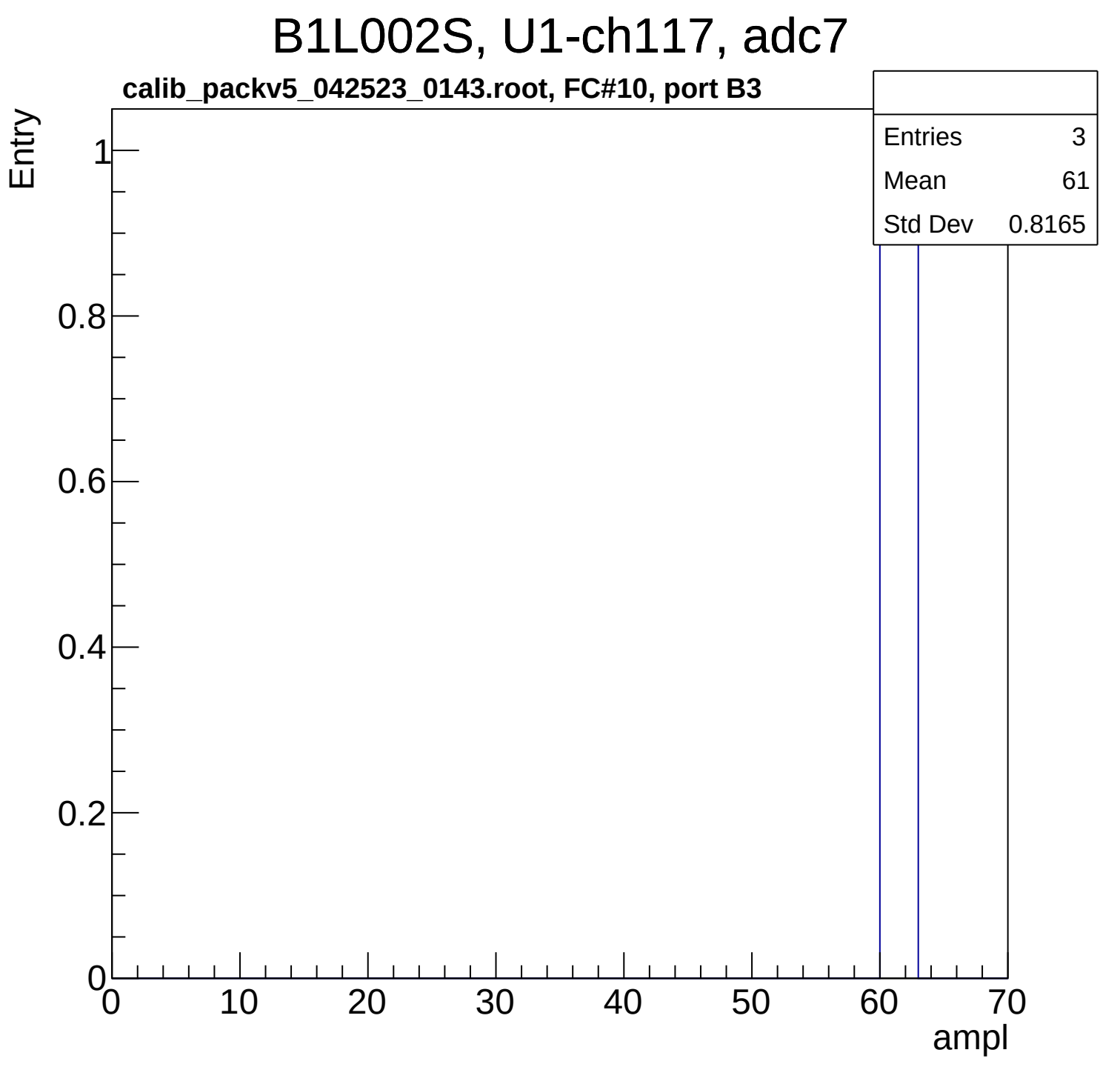
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch117, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 61
Std Dev 0.8165
Entry
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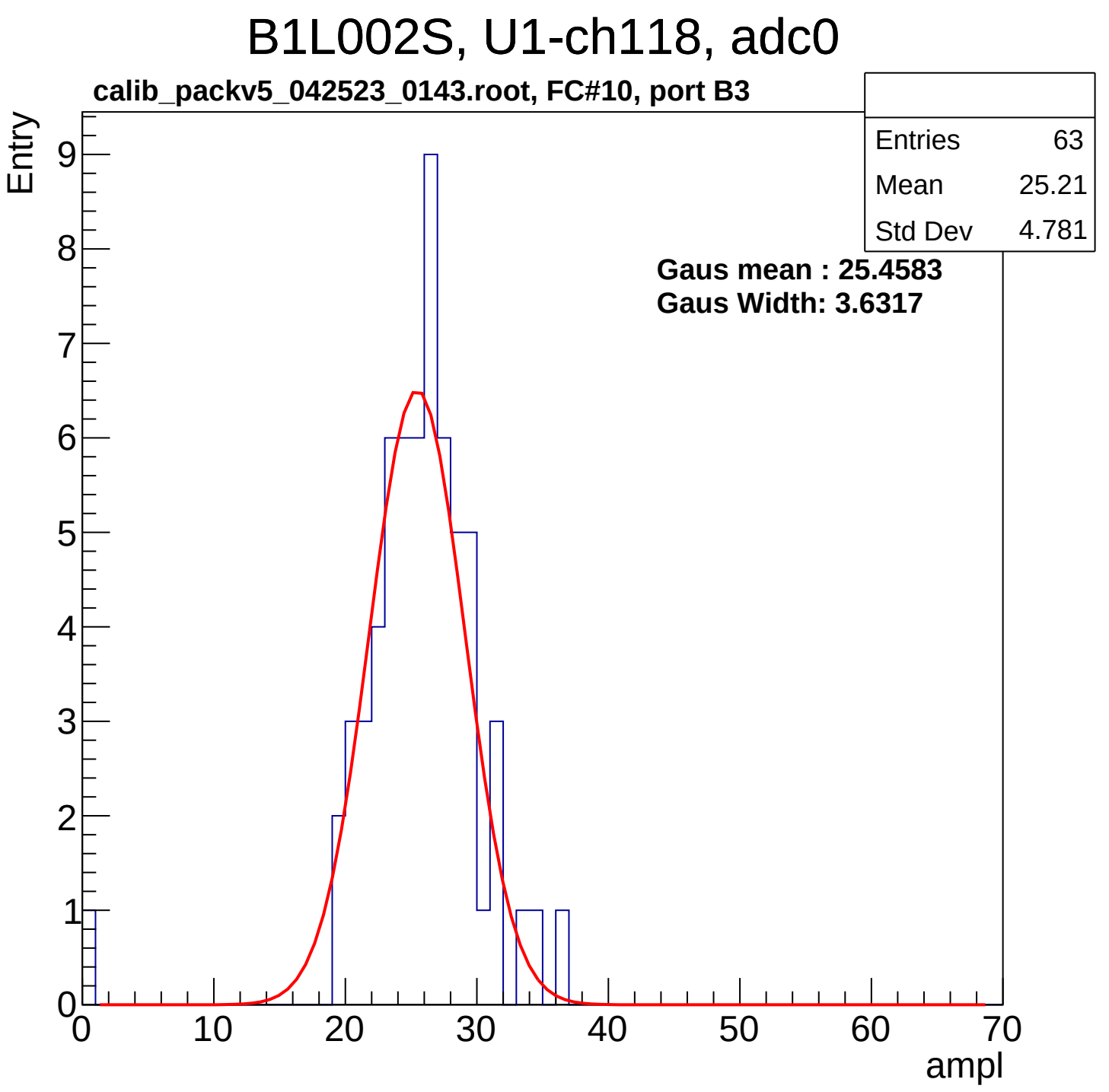
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch118, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 25.21
Std Dev 4.781
Gaus mean : 25.4583
Gaus Width: 3.6317
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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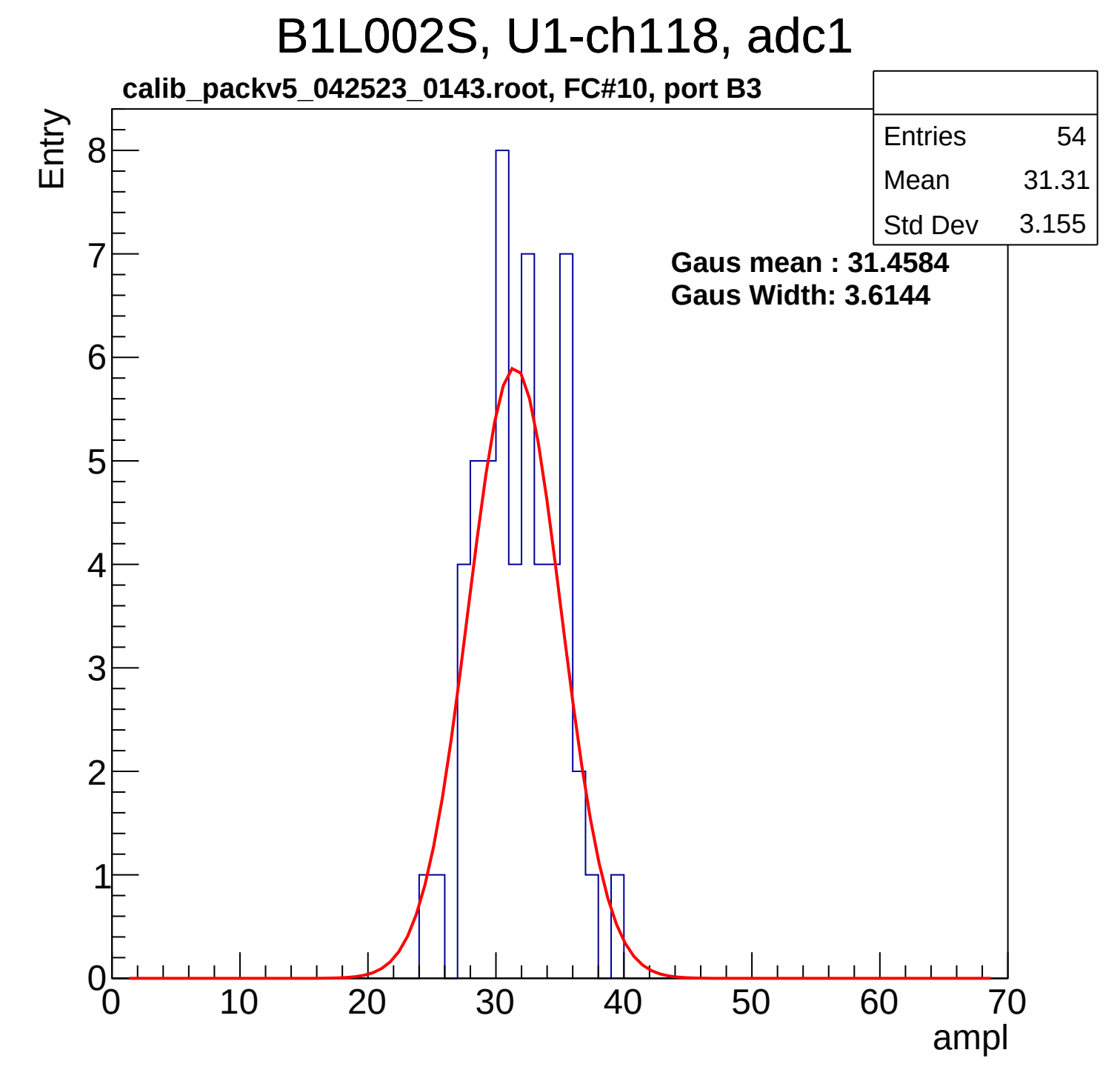
B1L002S, U1-ch118, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 31.31
Std Dev 3.155
Gaus mean : 31.4584
Gaus Width: 3.6144
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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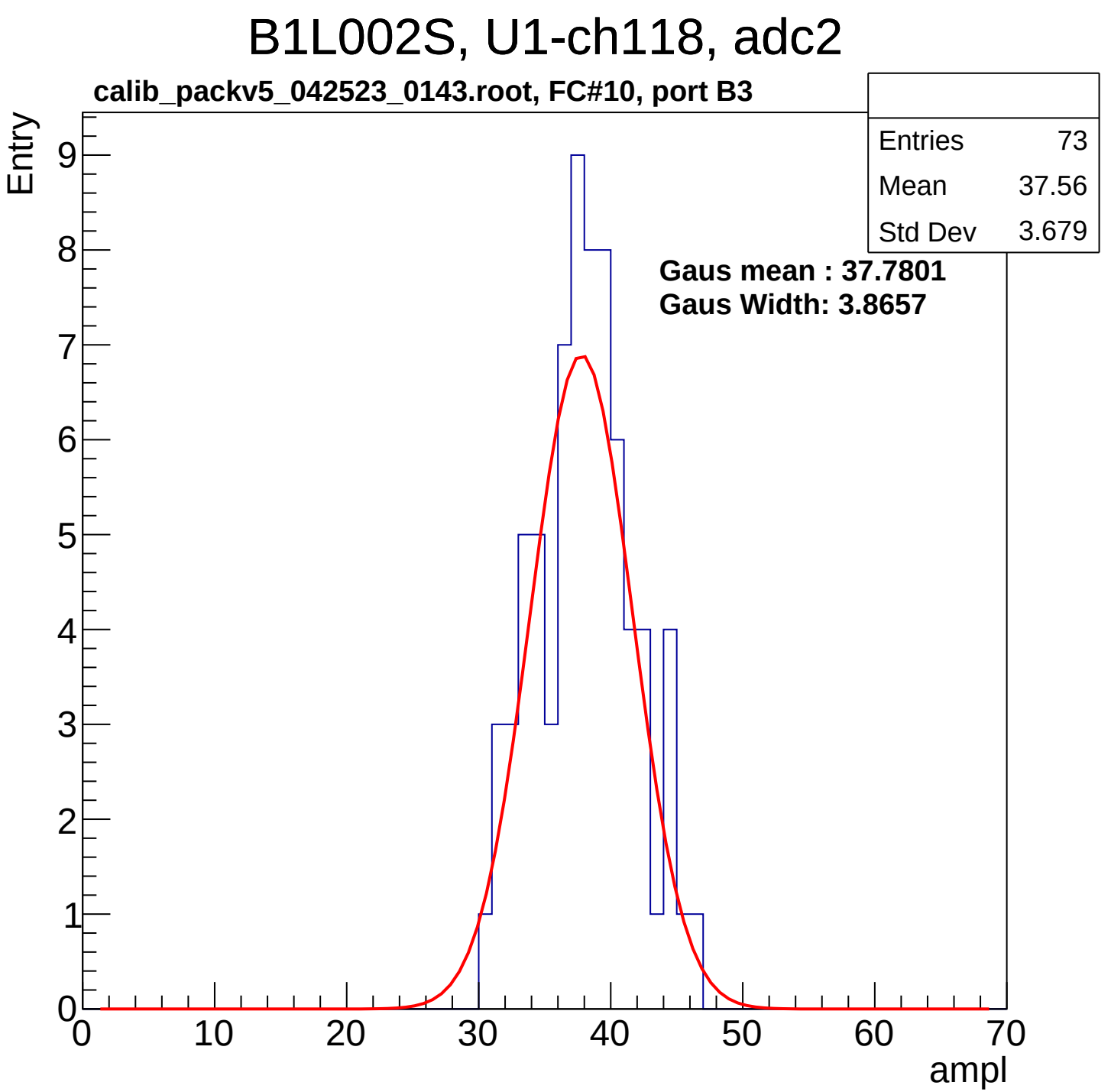
B1L002S, U1-ch118, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 37.56
Std Dev 3.679
Gaus mean : 37.7801
Gaus Width: 3.8657
Entry
ampl
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5
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0
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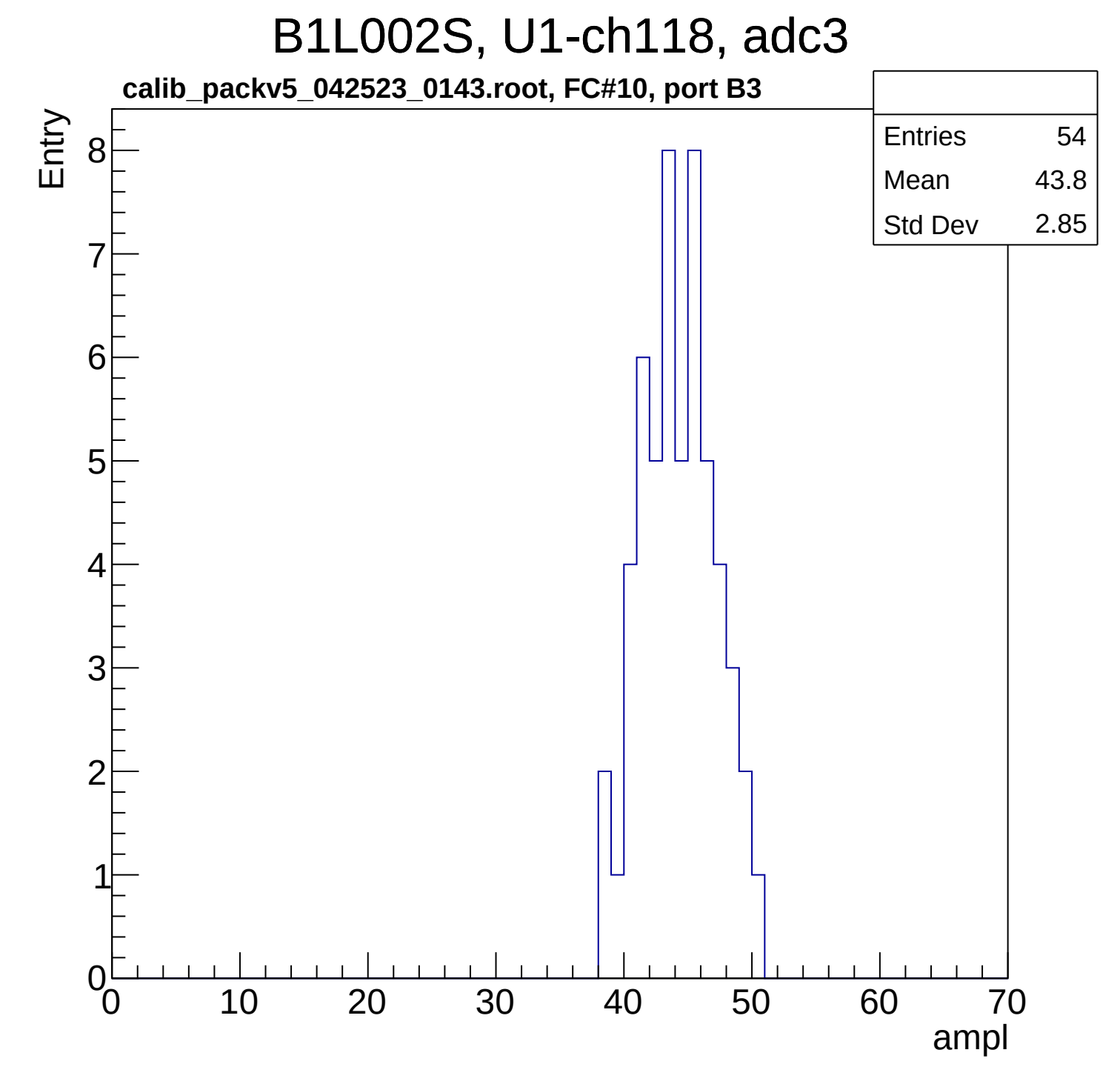
B1L002S, U1-ch118, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 43.8
Std Dev 2.85
Entry
ampl
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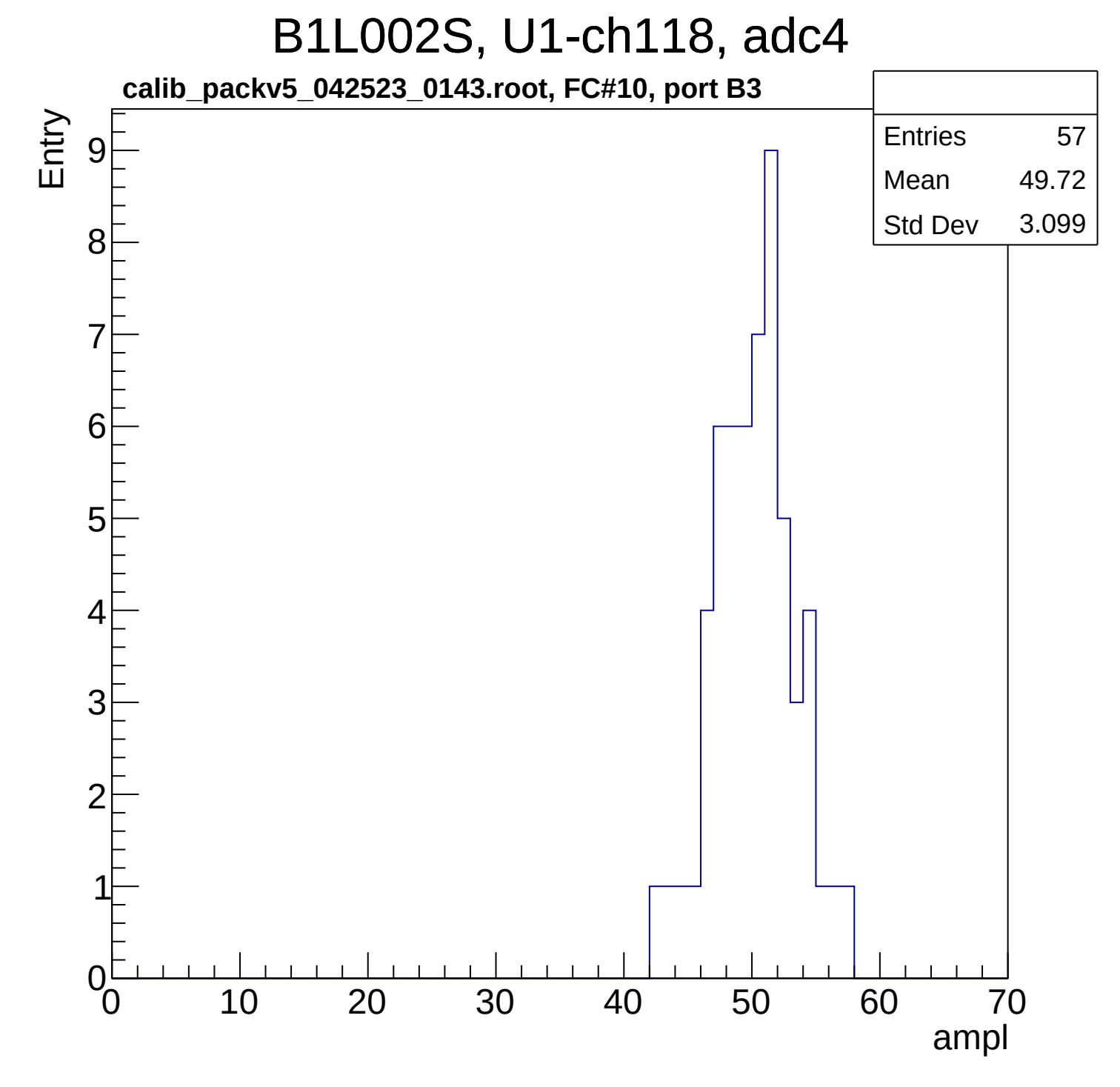
B1L002S, U1-ch118, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 49.72
Std Dev 3.099
Entry
ampl
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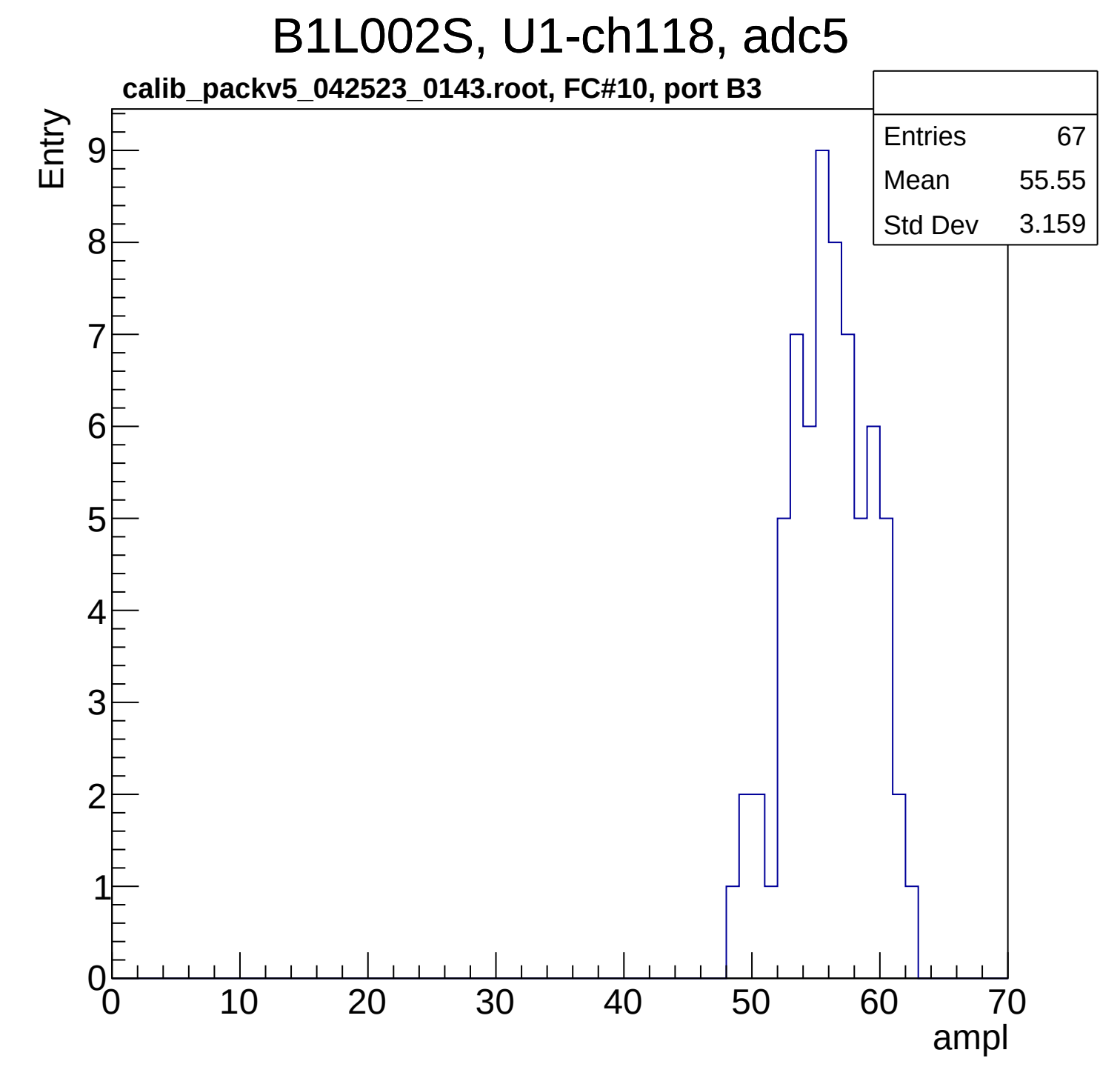
B1L002S, U1-ch118, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 55.55
Std Dev 3.159
Entry
ampl
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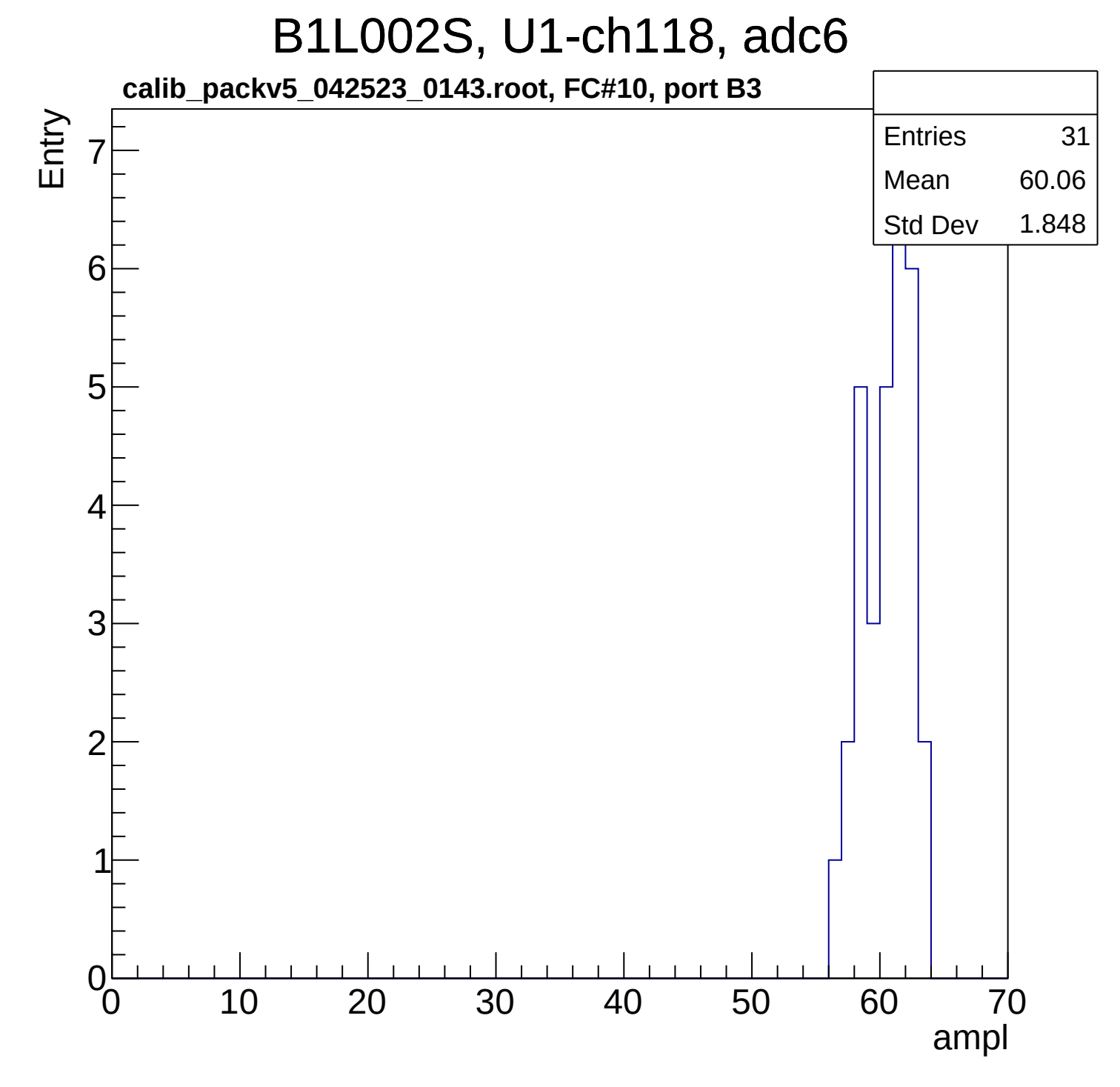
B1L002S, U1-ch118, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 60.06
Std Dev 1.848
Entry
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5
6
7
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10
20
30
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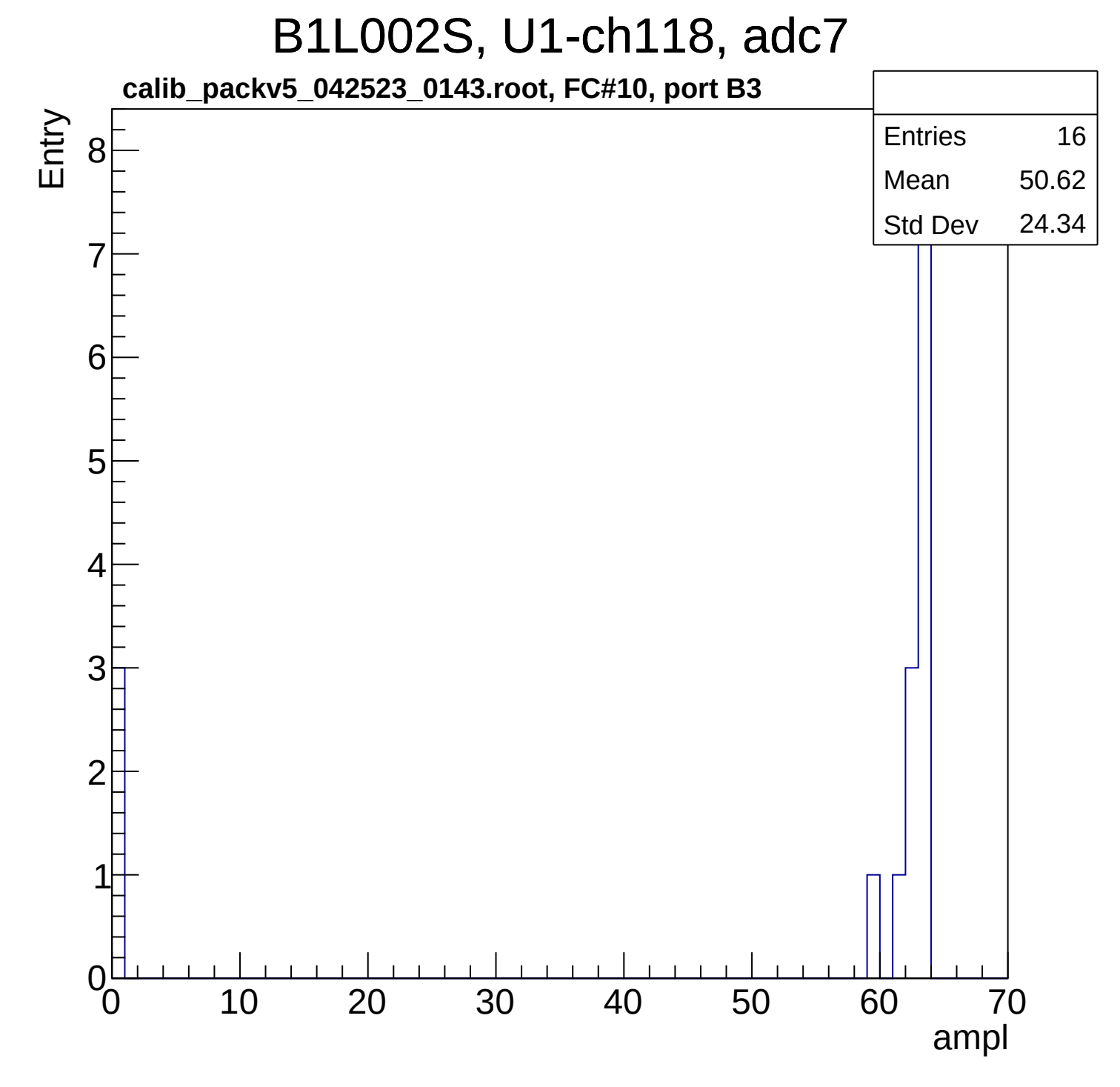
50
60
70
ampl


B1L002S, U1-ch118, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 50.62
Std Dev 24.34
Entry
ampl
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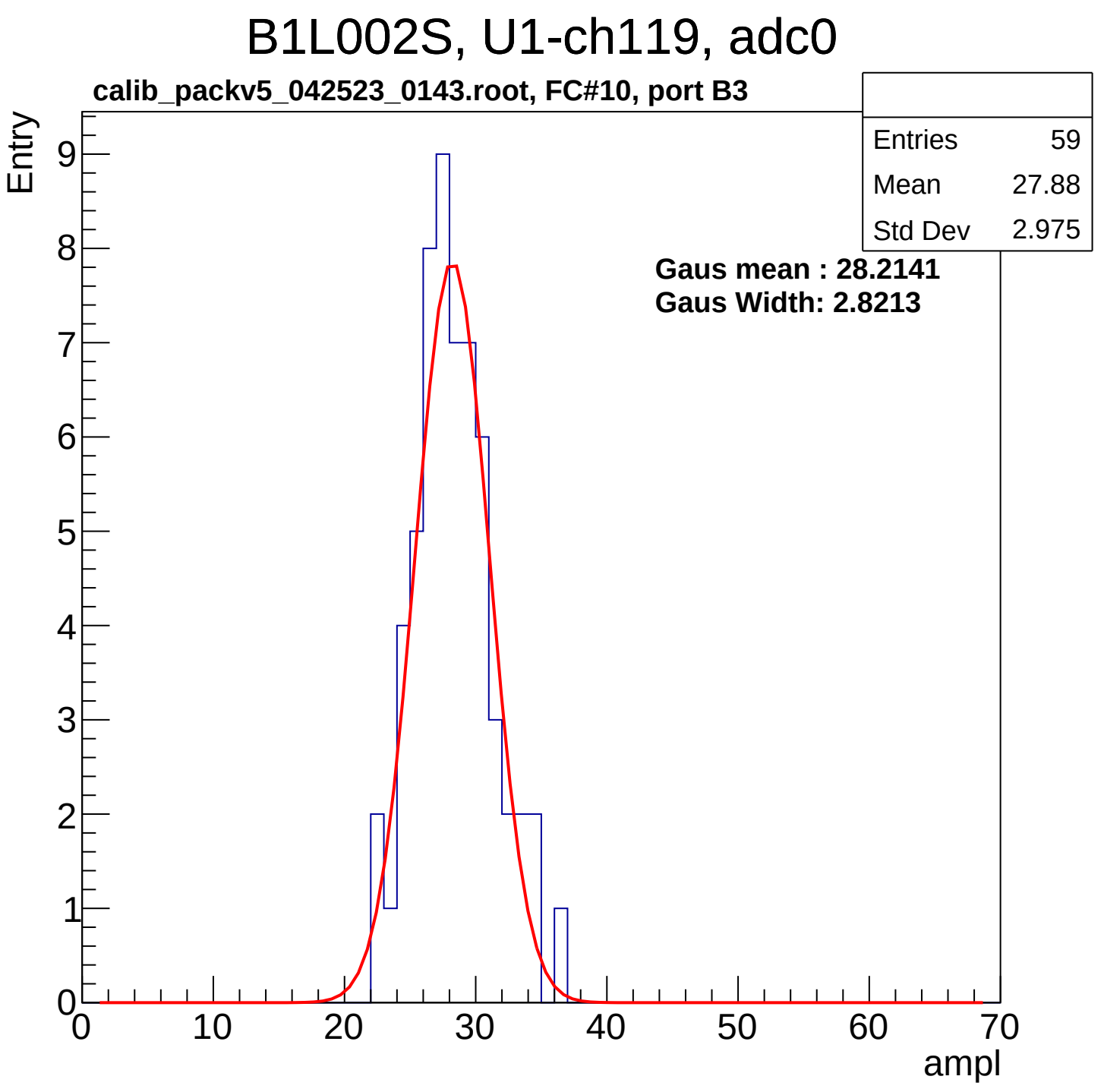
B1L002S, U1-ch119, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.88
Std Dev 2.975
Gaus mean : 28.2141
Gaus Width: 2.8213
Entry
ampl
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2
3
4
5
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7
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0
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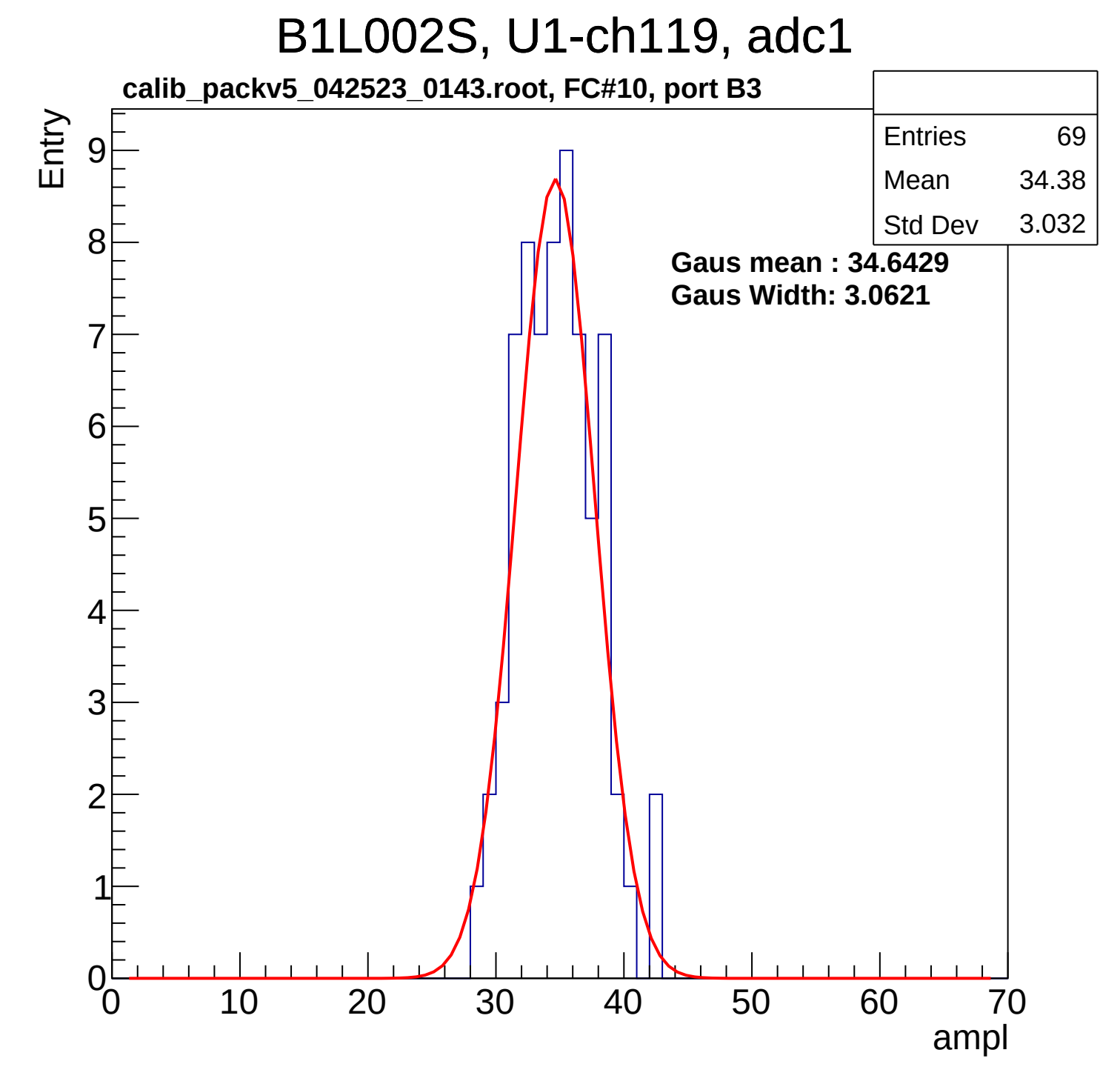
B1L002S, U1-ch119, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.38
Std Dev 3.032
Gaus mean : 34.6429
Gaus Width: 3.0621
Entry
ampl
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3
4
5
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0
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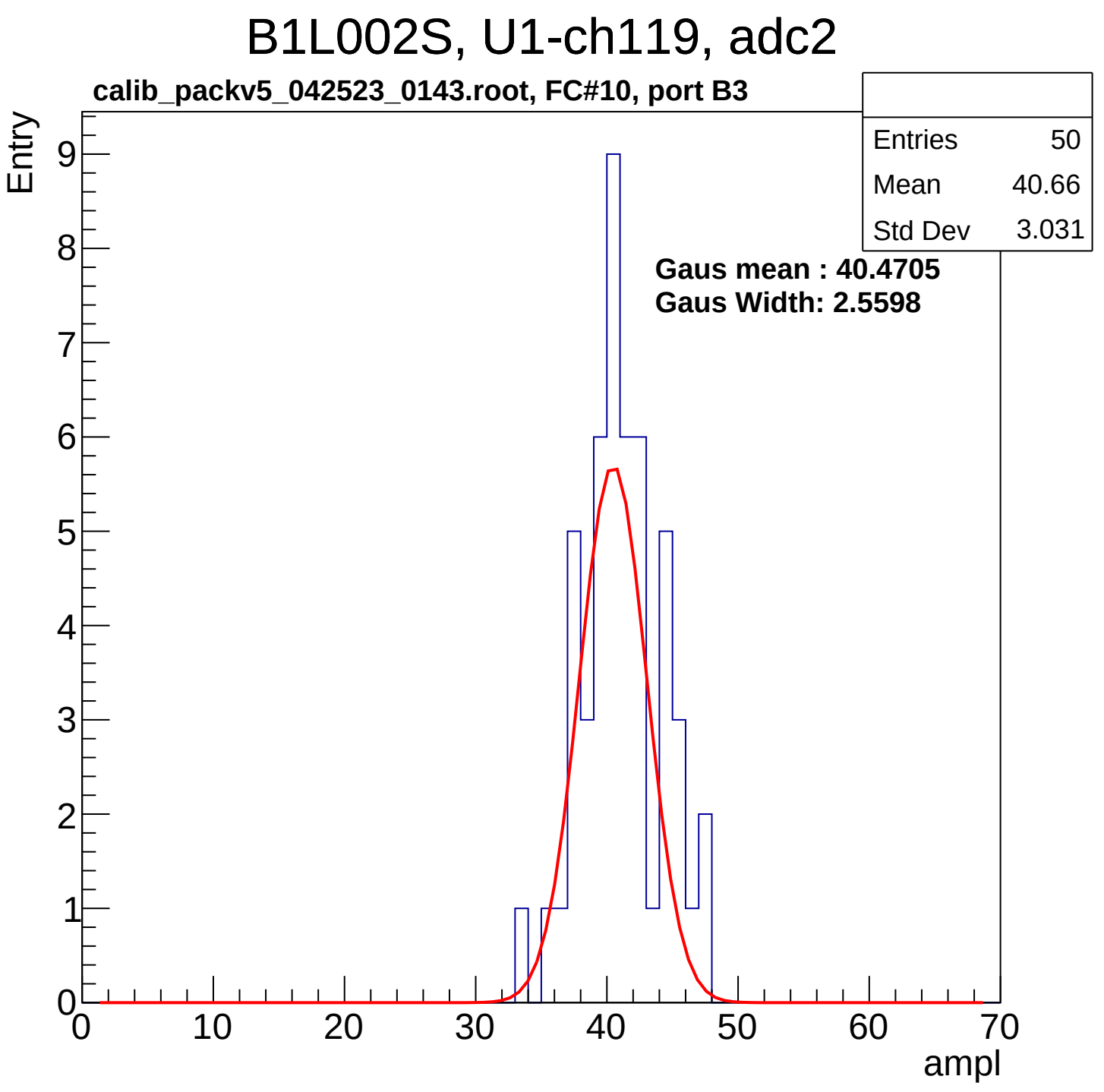
B1L002S, U1-ch119, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 40.66
Std Dev 3.031
Gaus mean : 40.4705
Gaus Width: 2.5598
Entry
ampl
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7
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0
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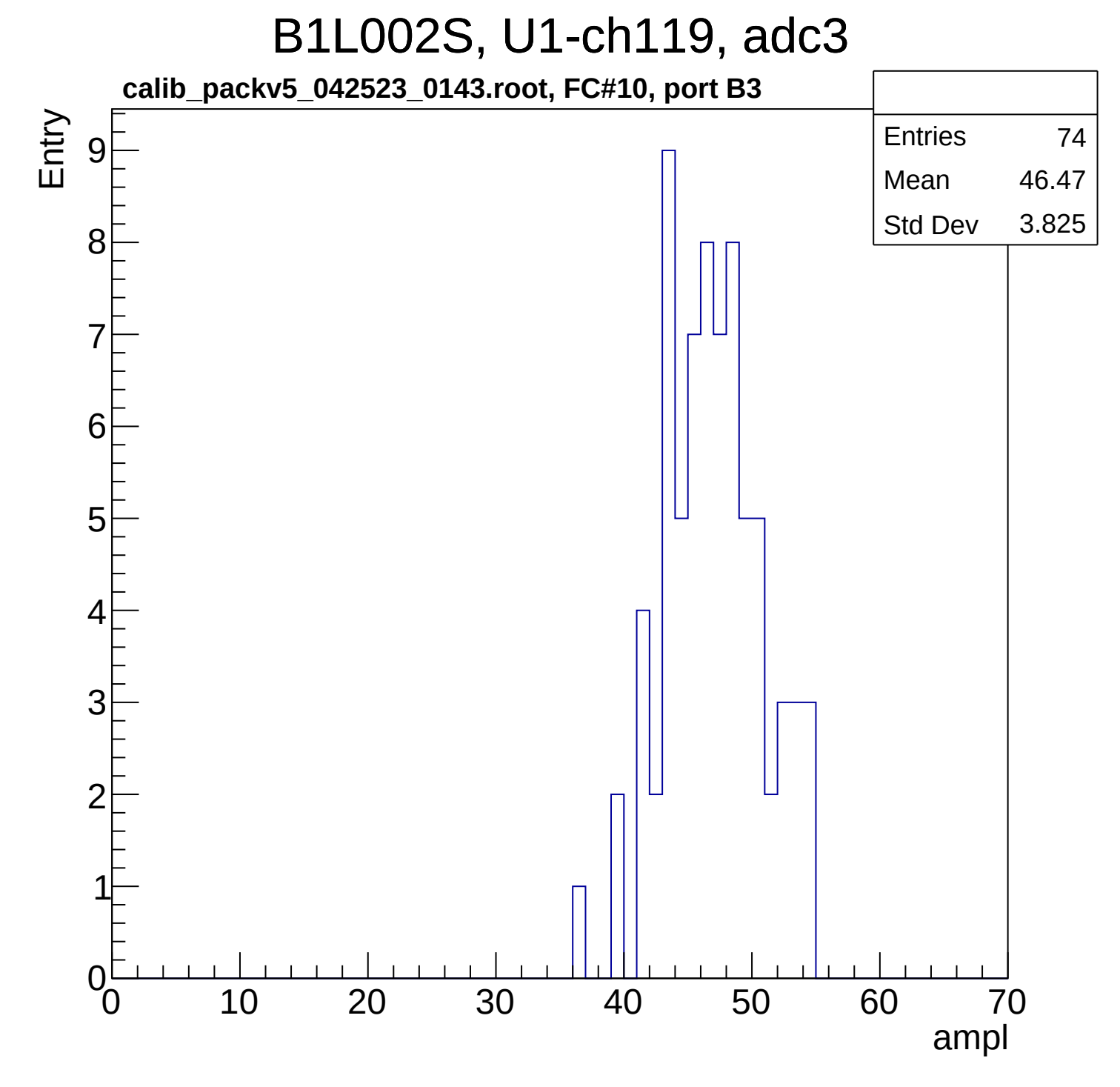
B1L002S, U1-ch119, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.47
Std Dev 3.825
Entry
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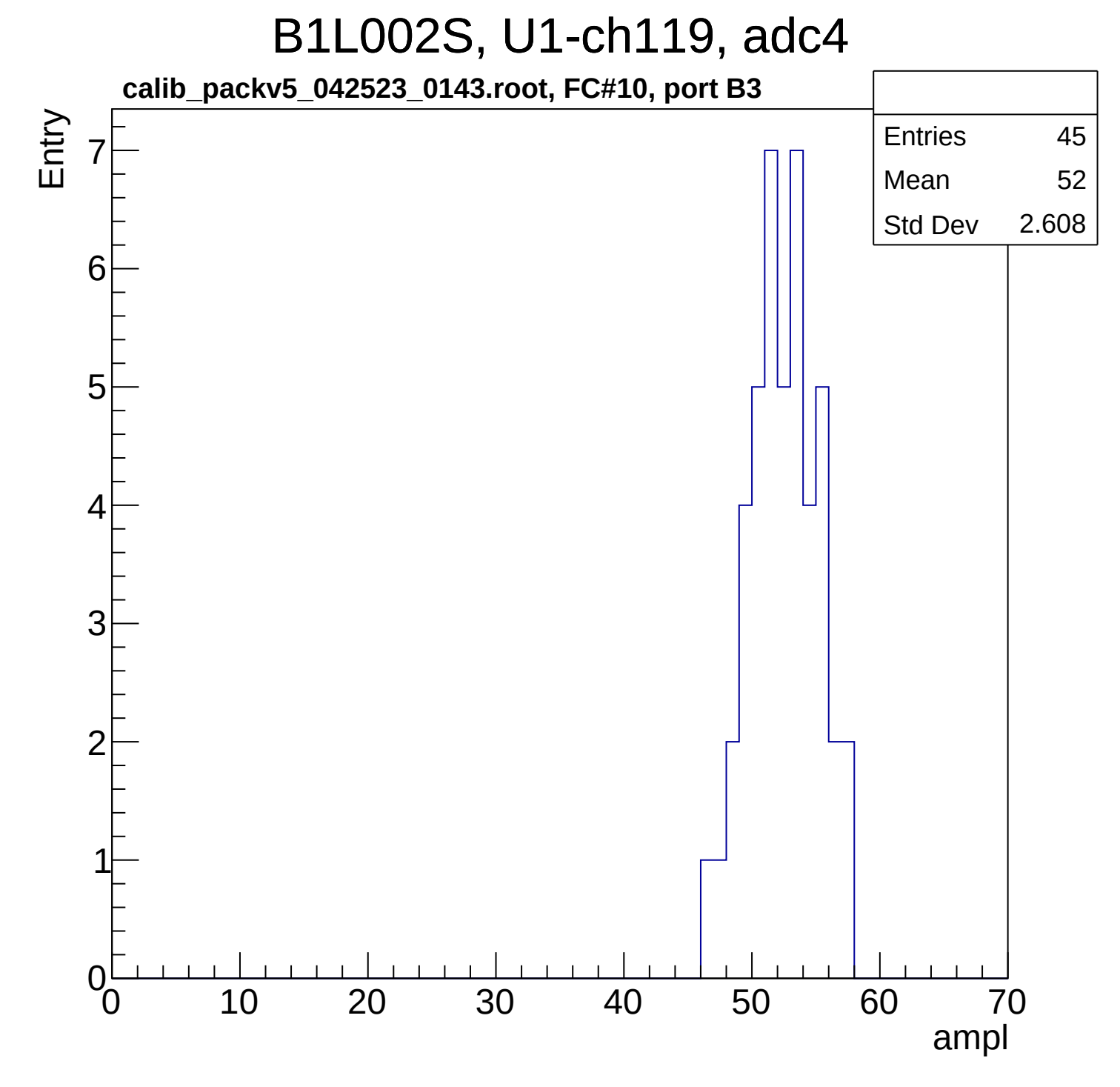
B1L002S, U1-ch119, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 52
Std Dev 2.608
Entry
ampl
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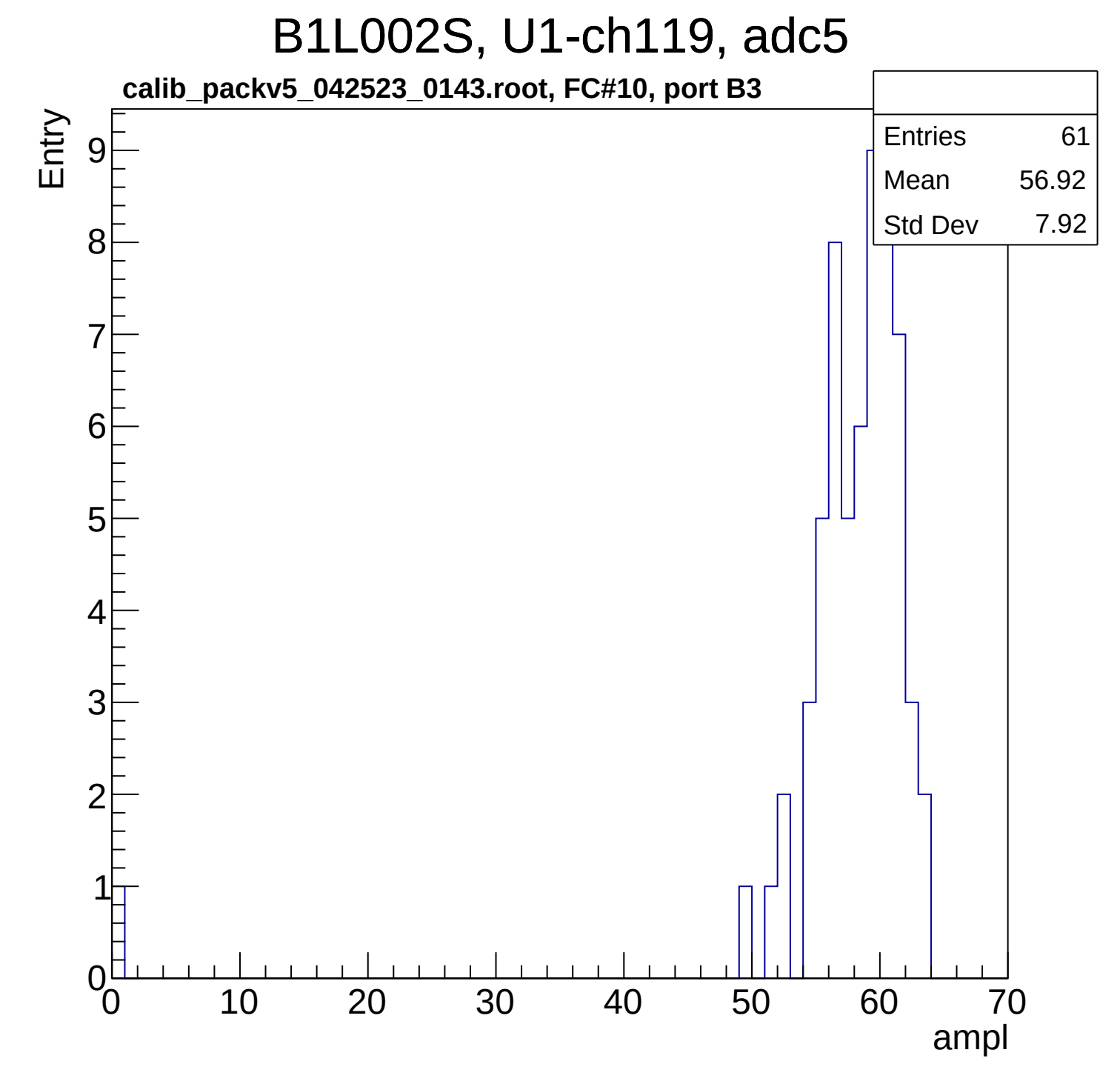
B1L002S, U1-ch119, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 56.92
Std Dev 7.92
Entry
ampl
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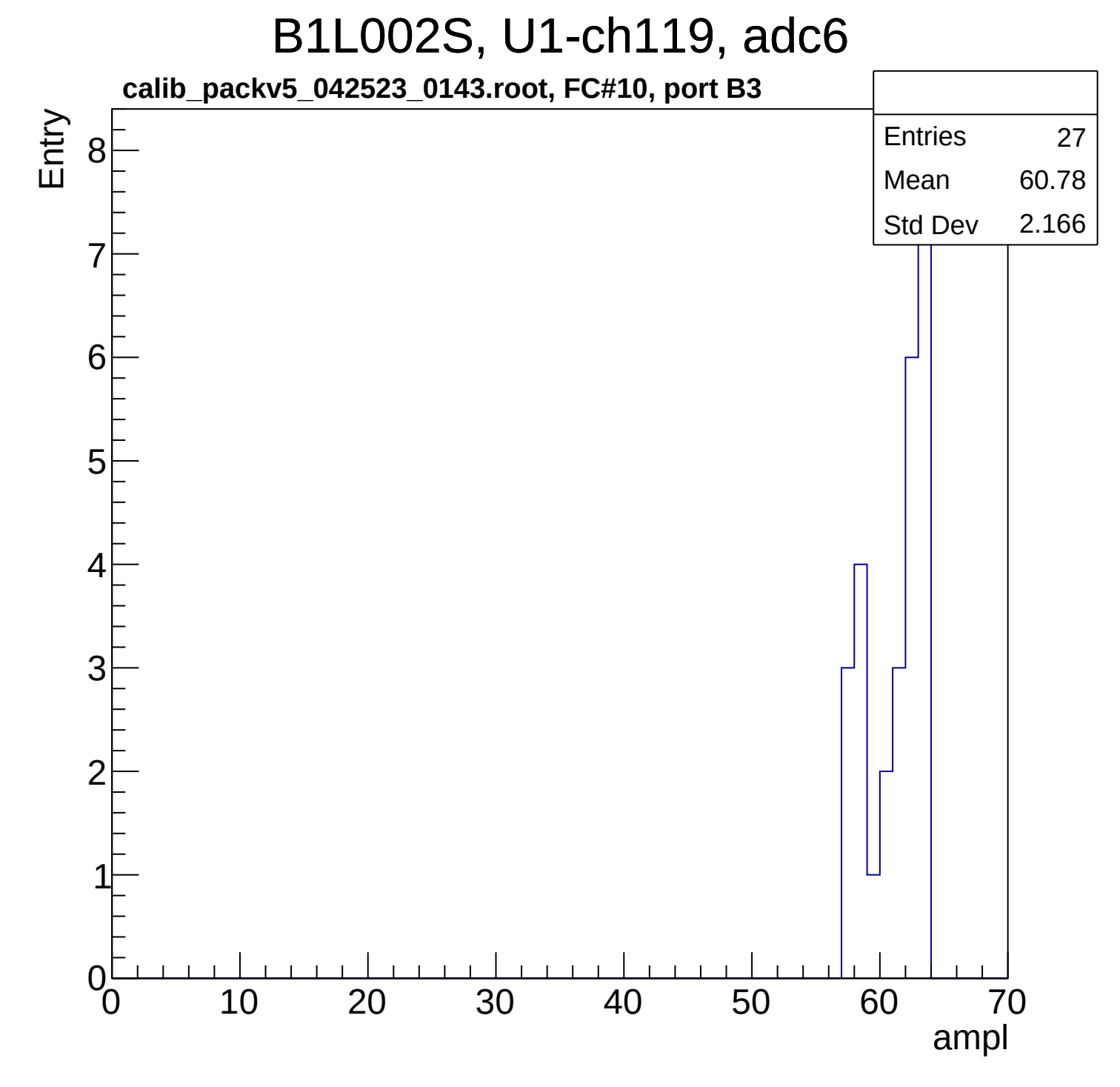
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch119, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 60.78
Std Dev 2.166
Entry
ampl
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1
2
3
4
5
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7
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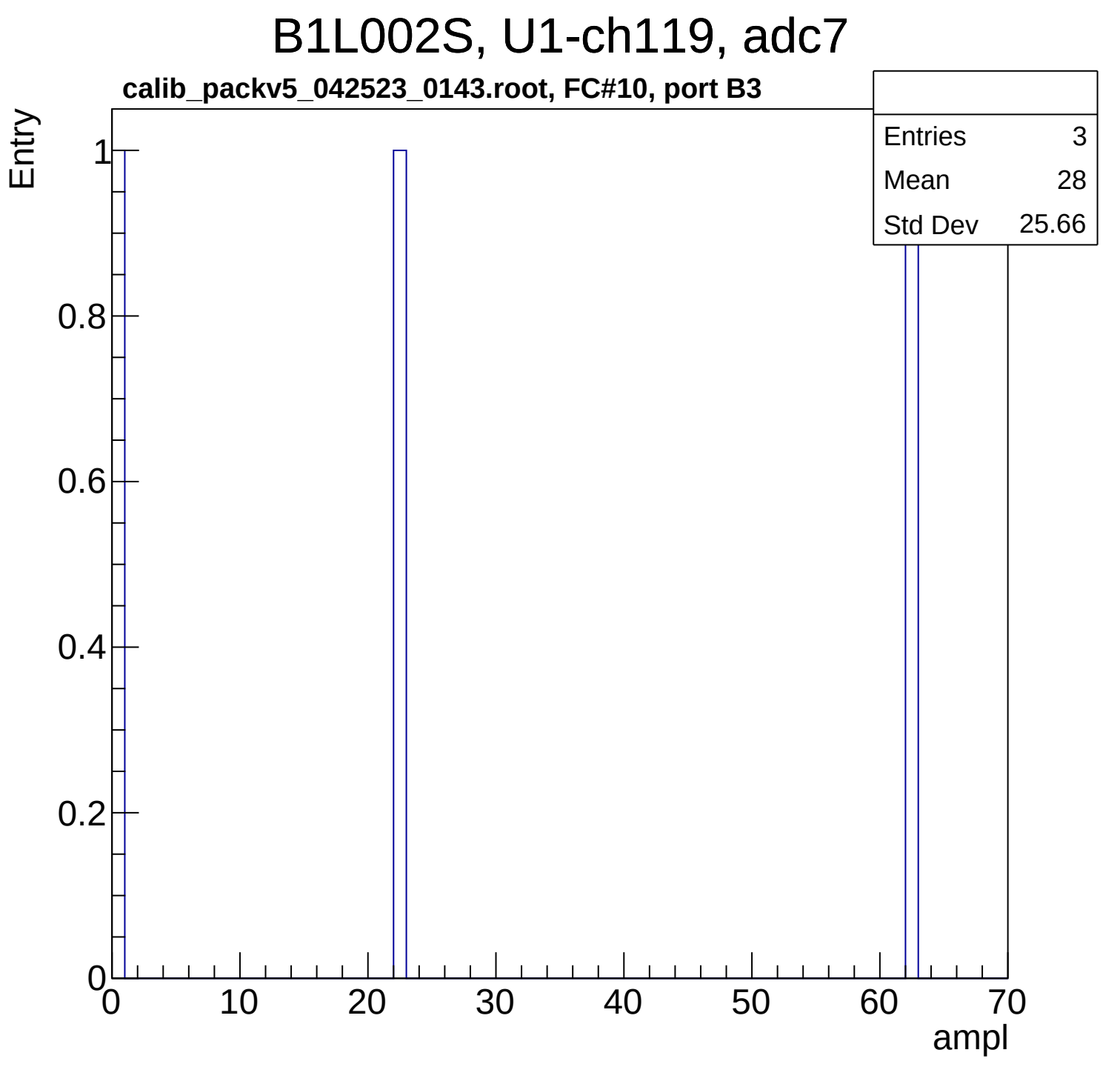
B1L002S, U1-ch119, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28
Std Dev 25.66
Entry
ampl
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0.2
0.4
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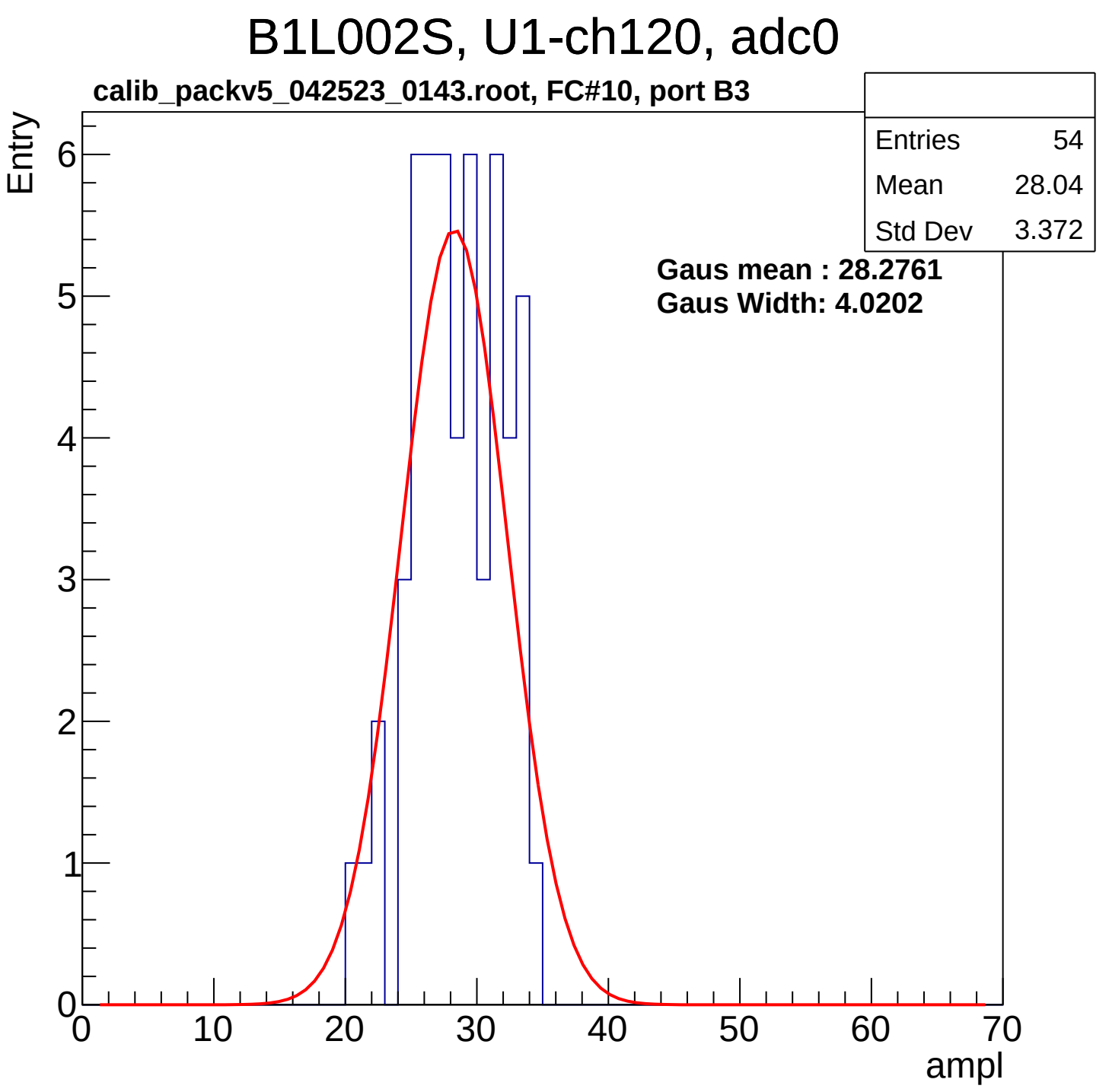
B1L002S, U1-ch120, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 28.04
Std Dev 3.372
Gaus mean : 28.2761
Gaus Width: 4.0202
Entry
ampl
0
1
2
3
4
5
6
0
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40
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60
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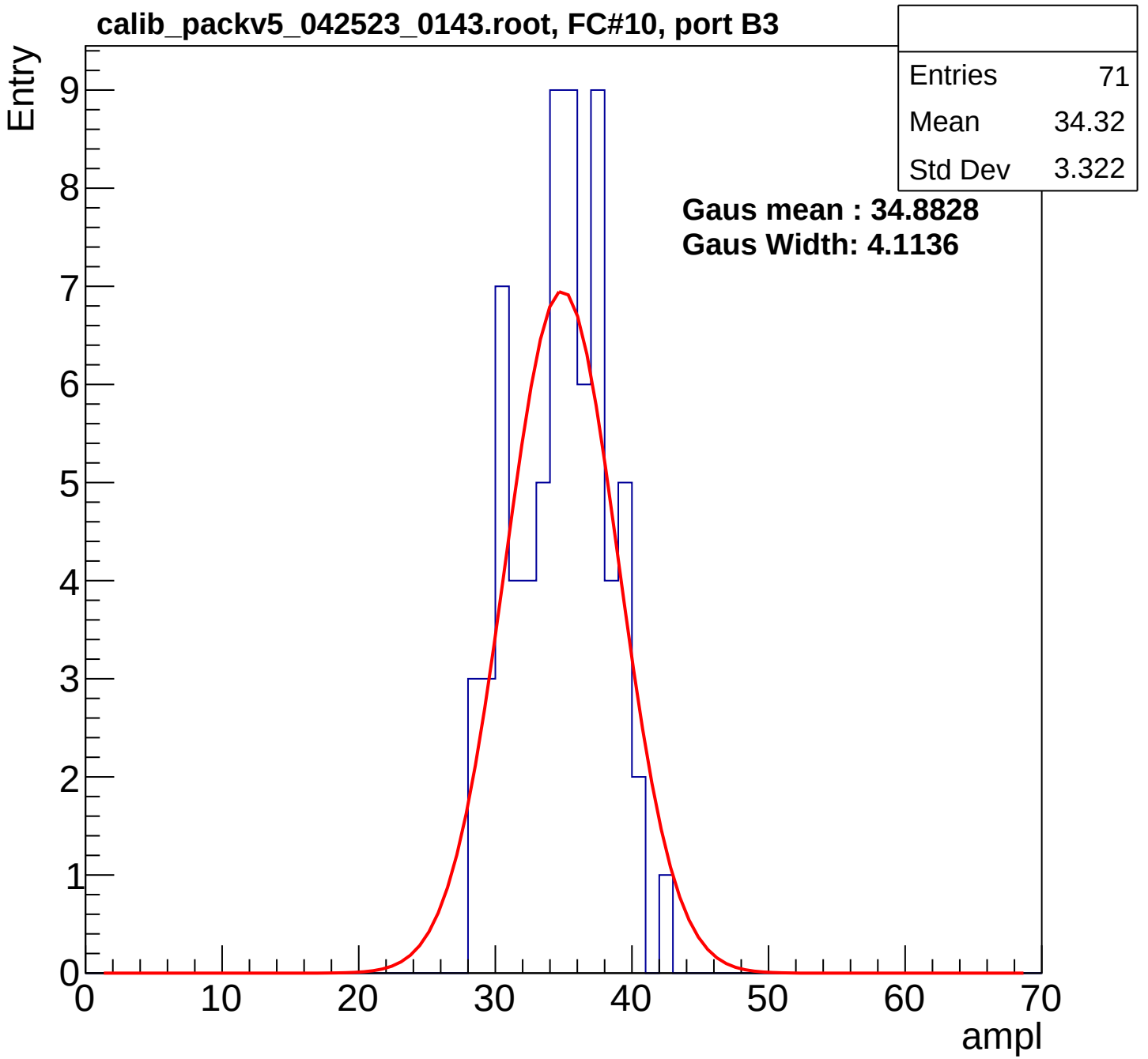
B1L002S, U1-ch120, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.32
Std Dev 3.322
Gaus mean : 34.8828
Gaus Width: 4.1136
Entry
ampl
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2
3
4
5
6
7
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0
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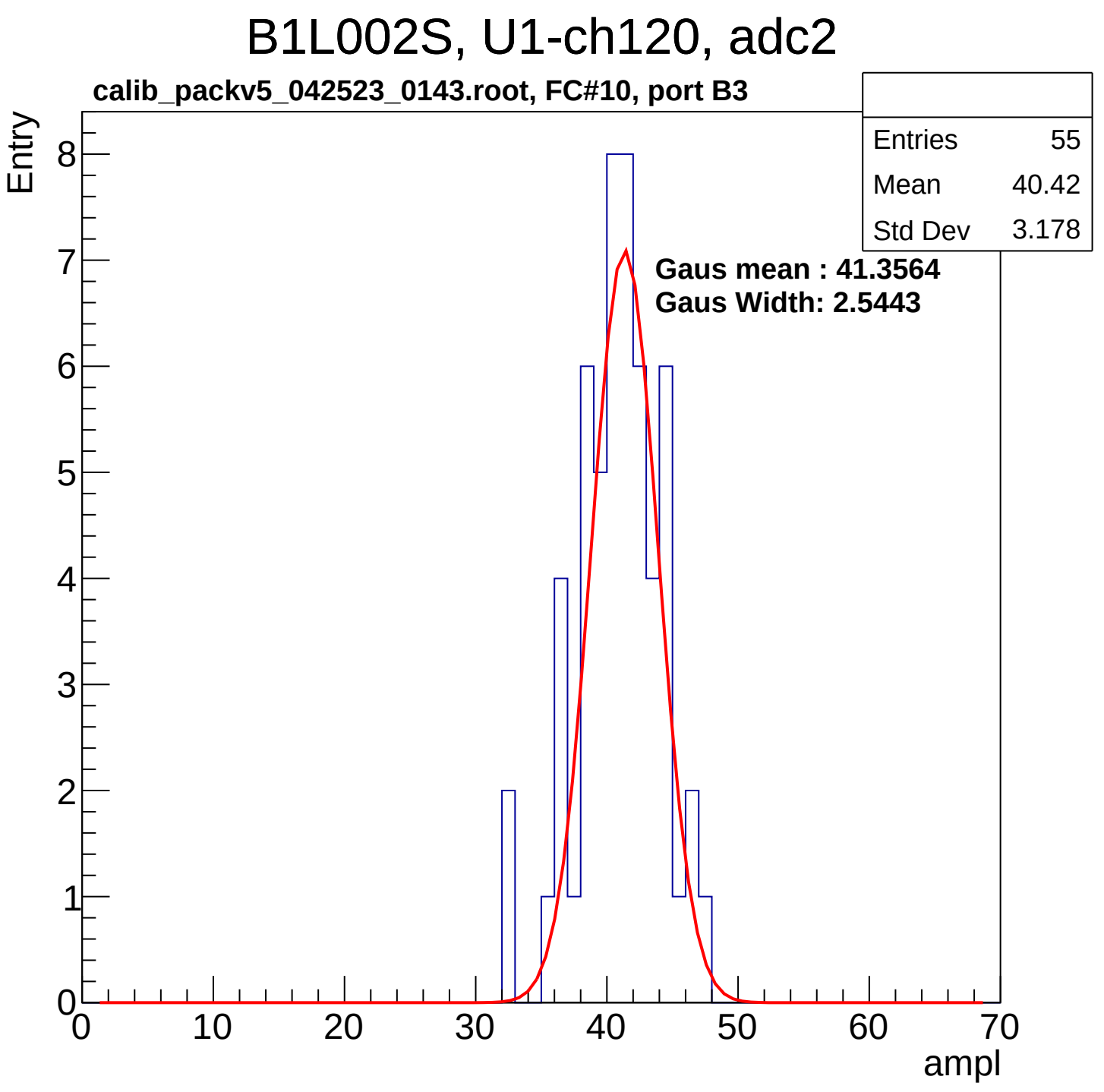
B1L002S, U1-ch120, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.42
Std Dev 3.178
Gaus mean : 41.3564
Gaus Width: 2.5443
Entry
ampl
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1
2
3
4
5
6
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8
0
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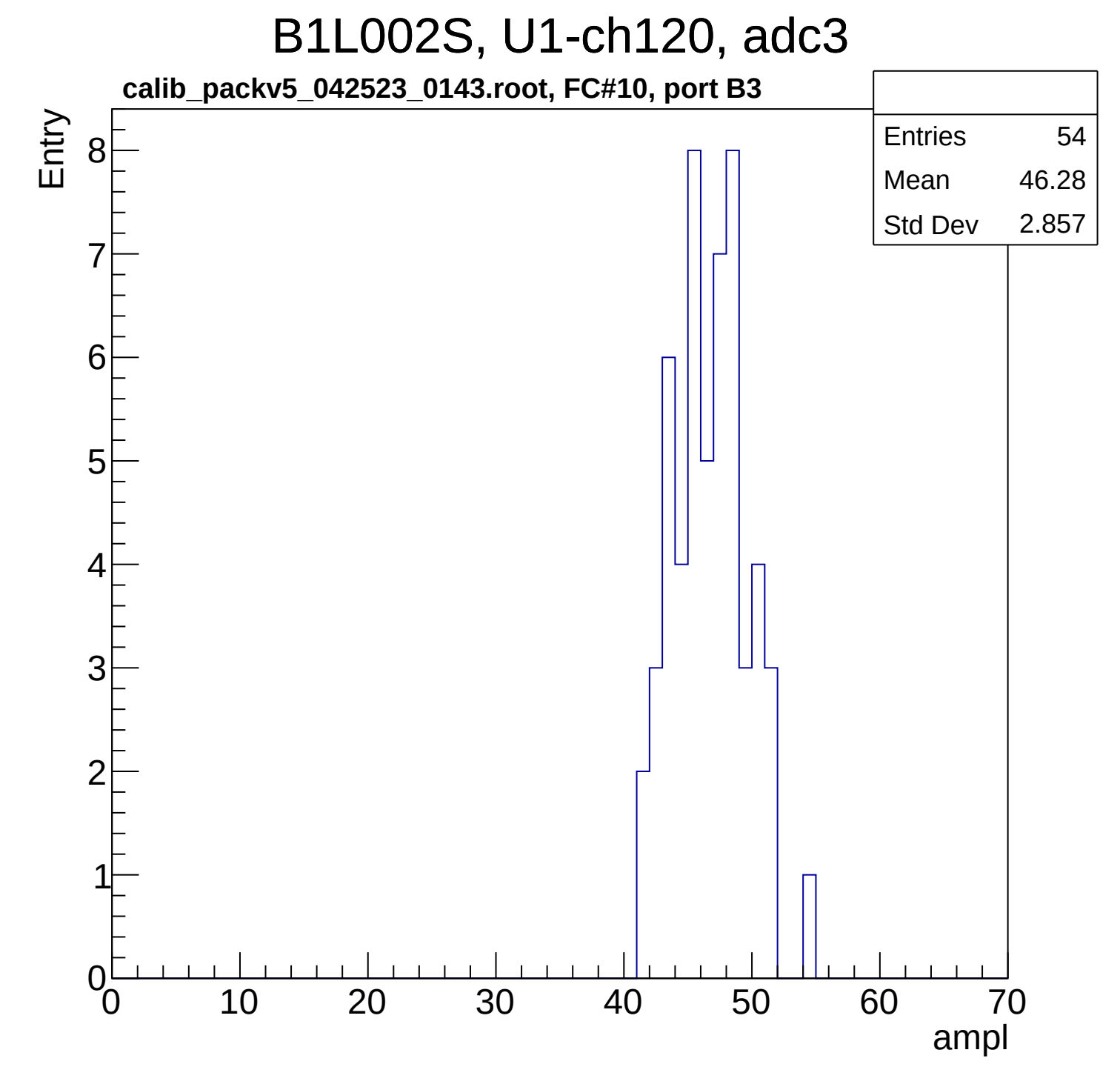
B1L002S, U1-ch120, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.28
Std Dev 2.857
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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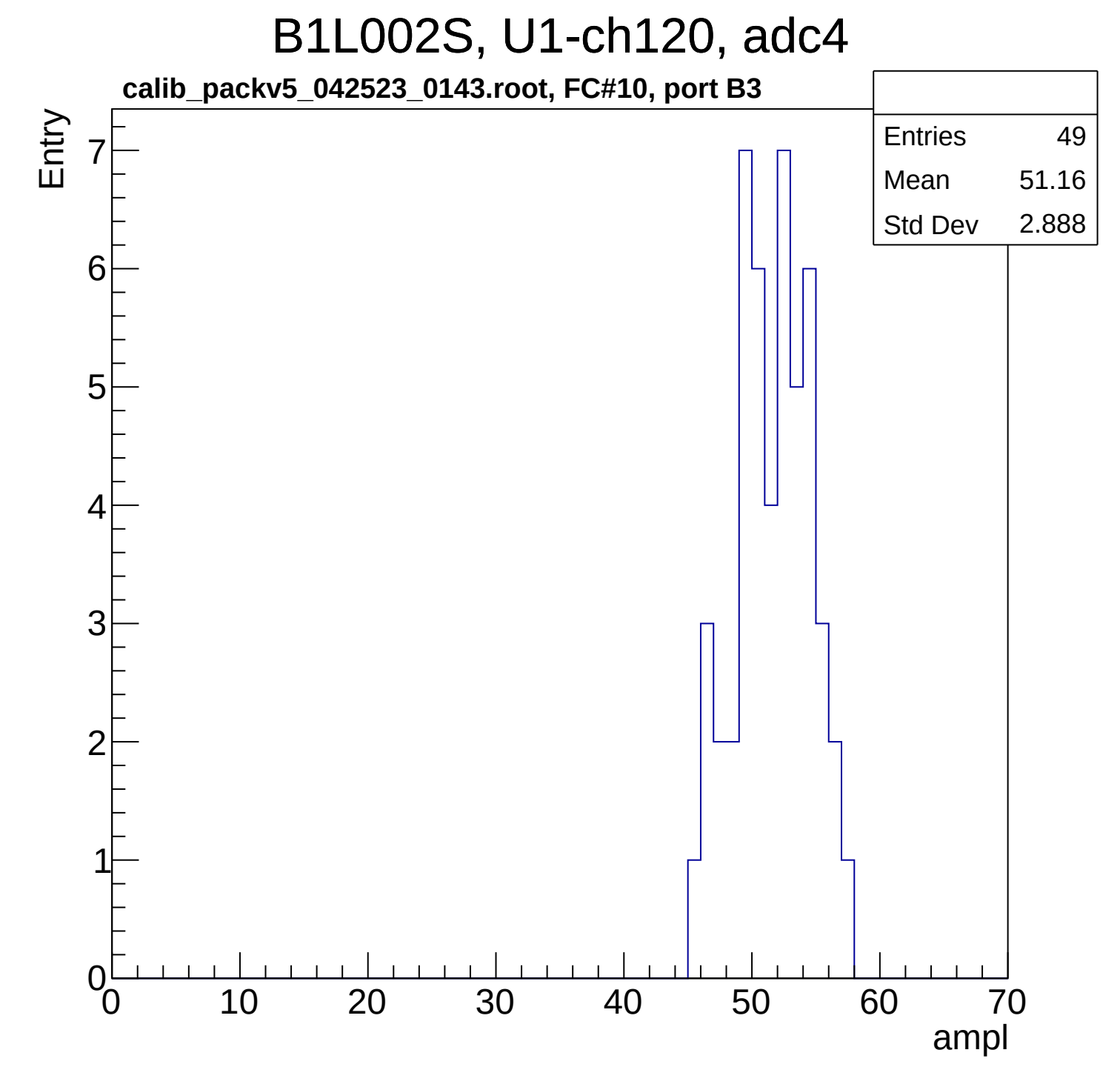
B1L002S, U1-ch120, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 51.16
Std Dev 2.888
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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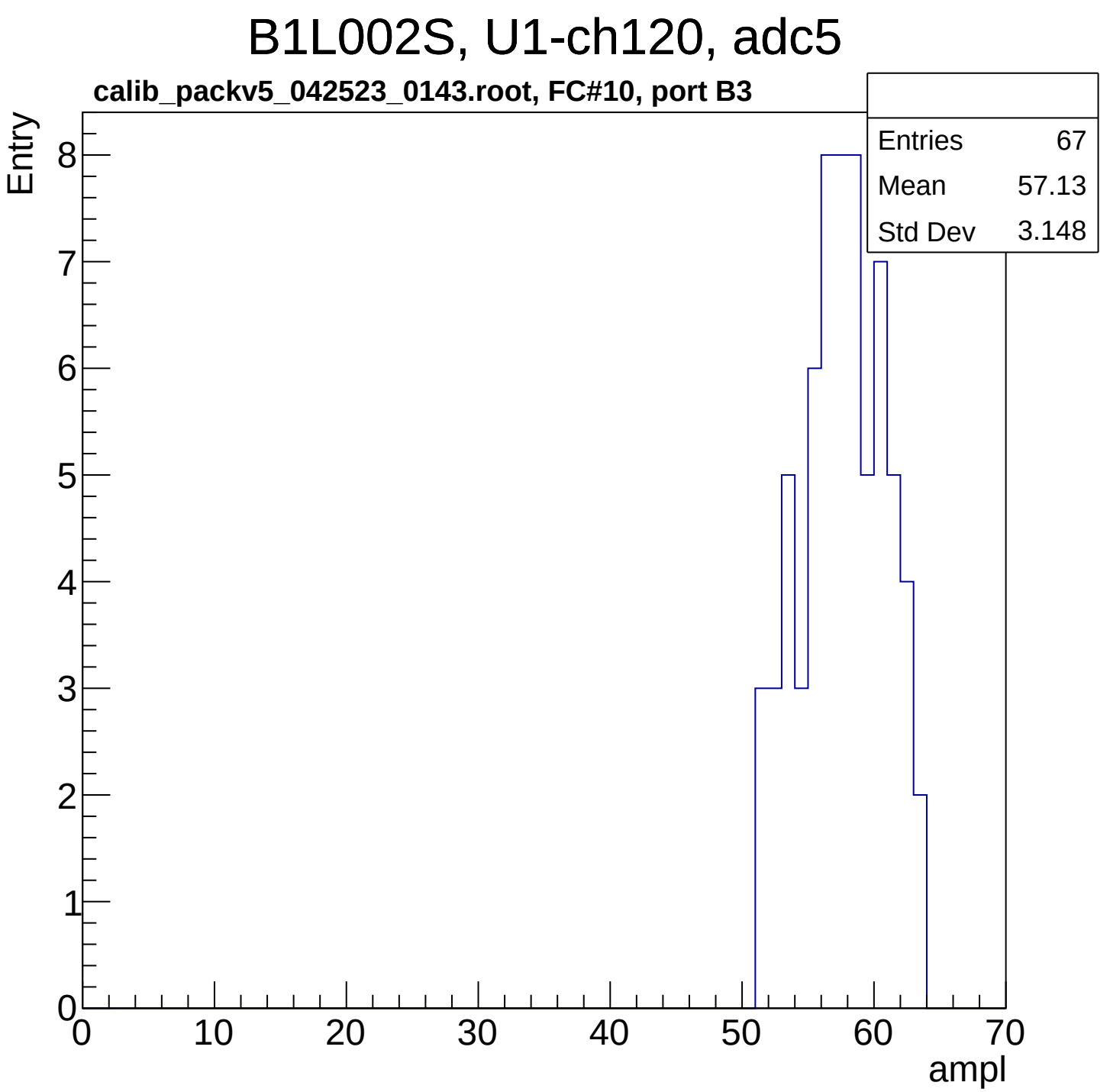
B1L002S, U1-ch120, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 57.13
Std Dev 3.148
Entry
ampl
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2
3
4
5
6
7
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0
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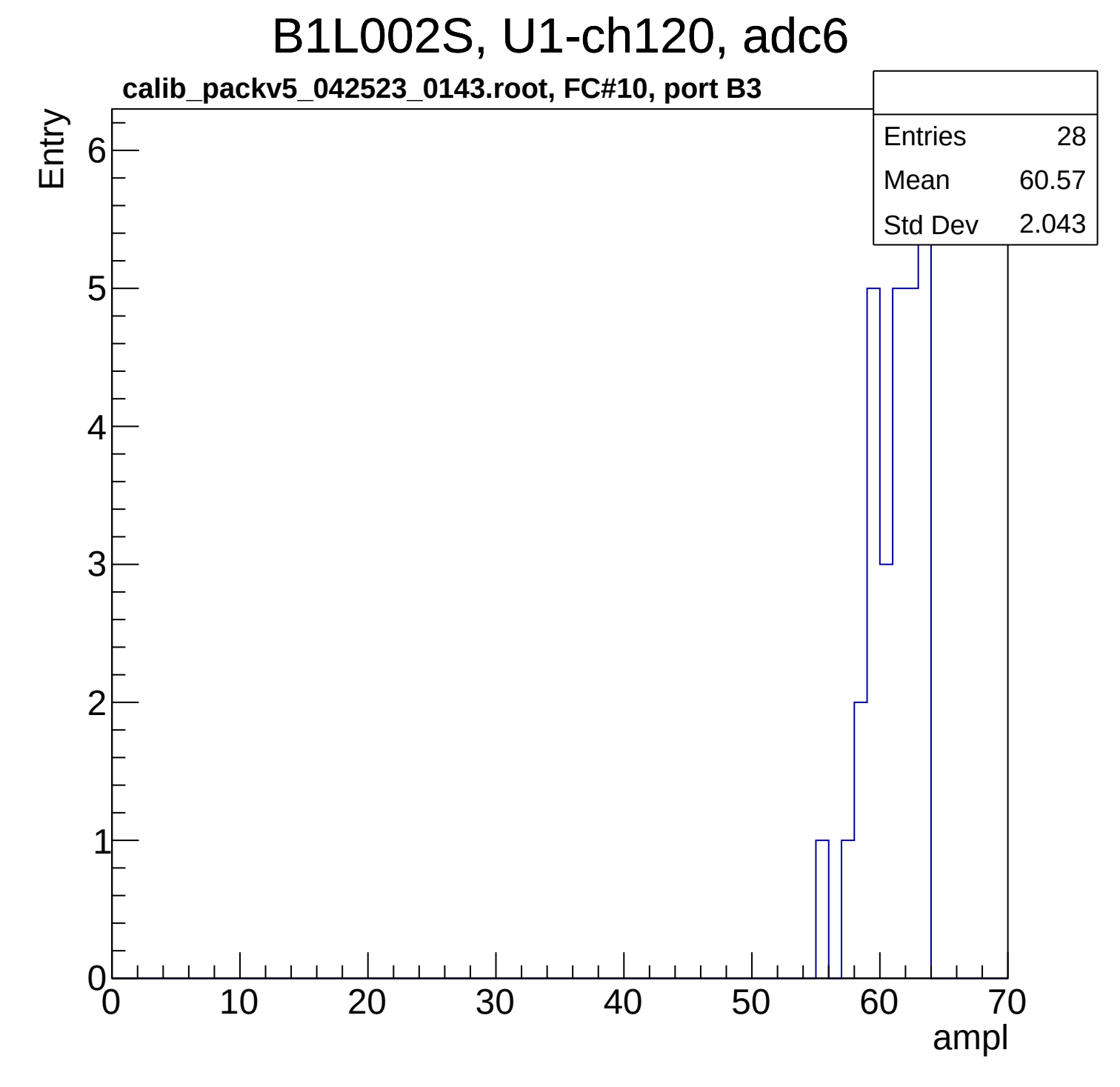
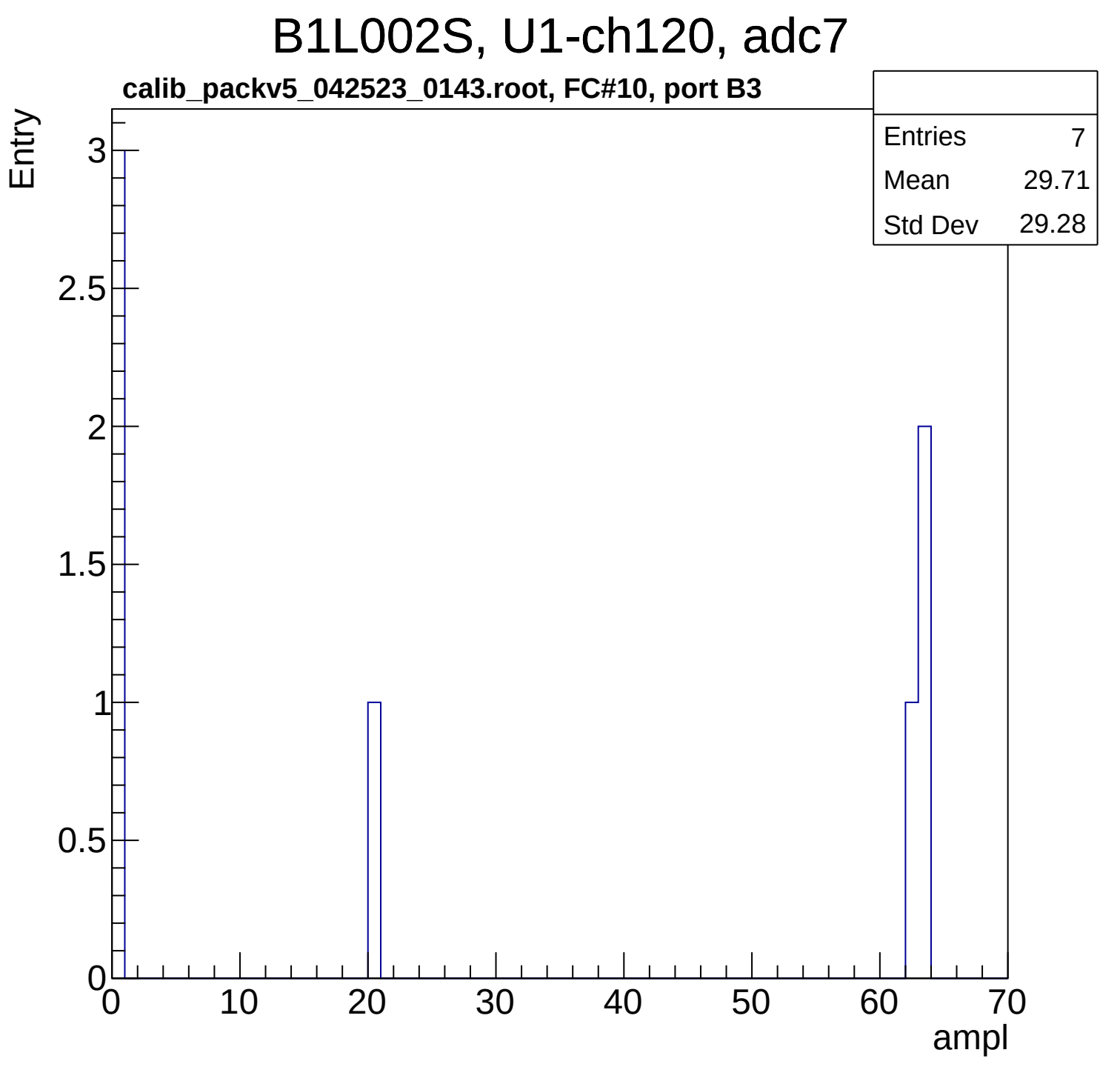
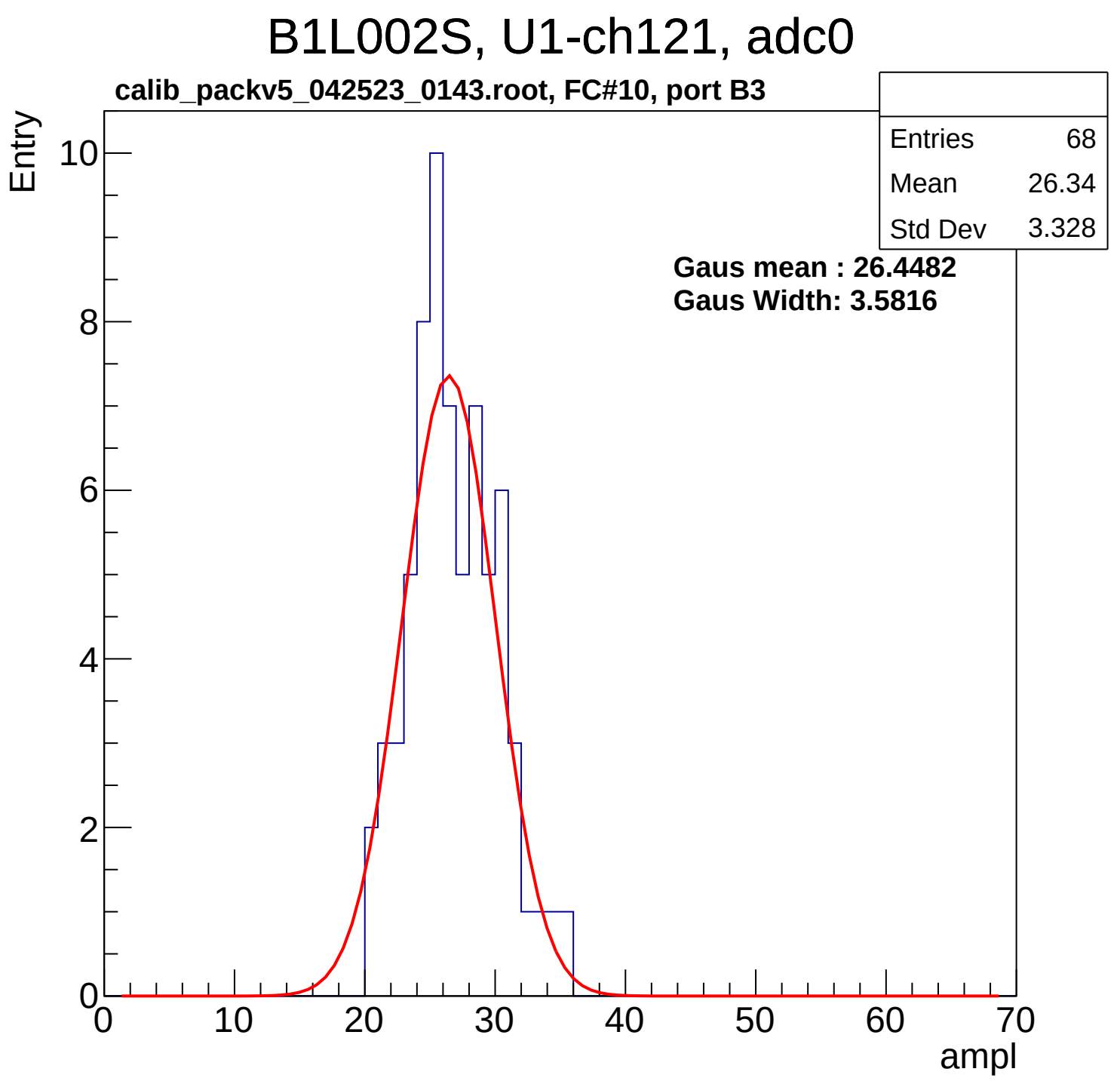
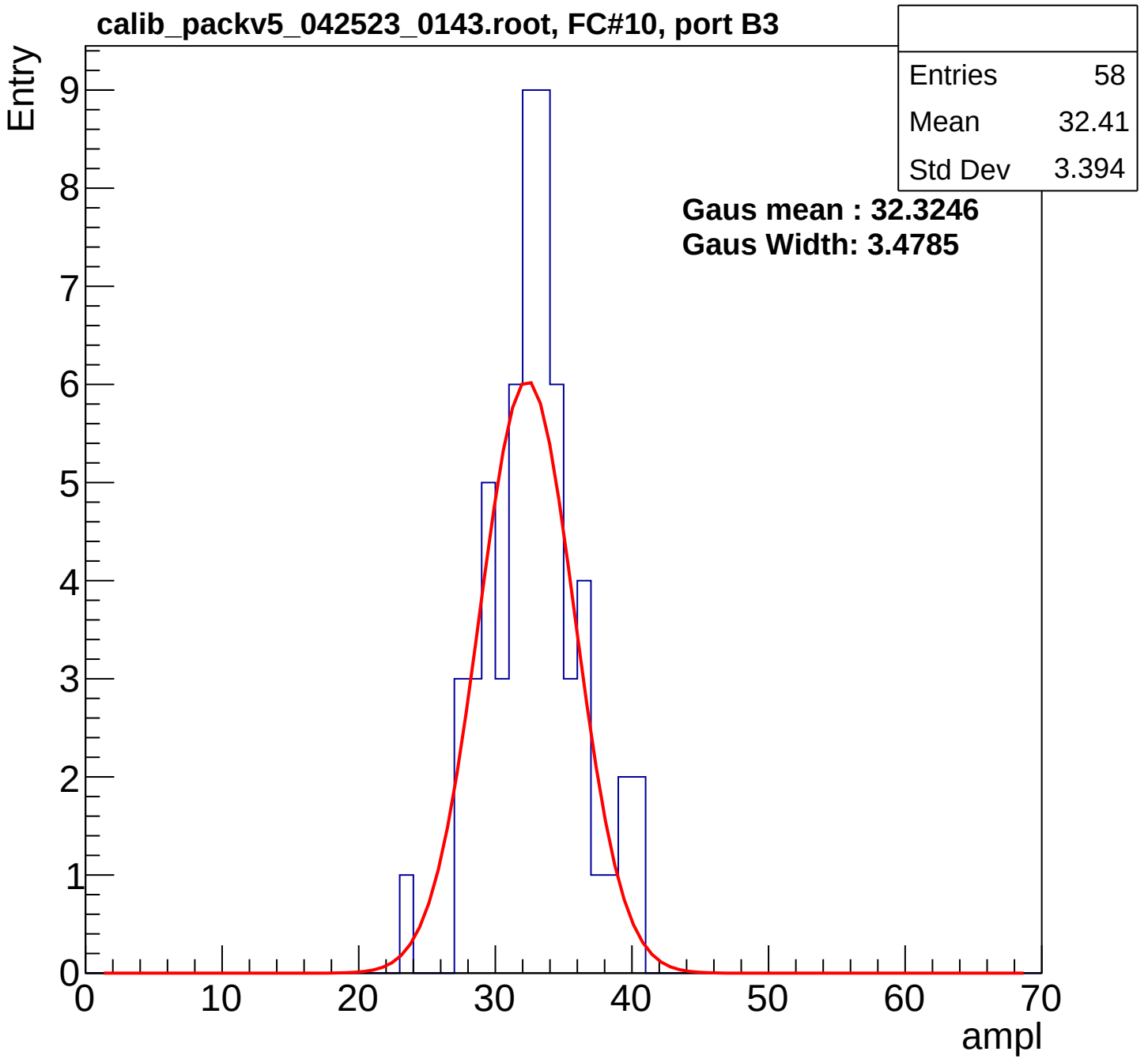

B1L002S, U1-ch120, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 60.57
Std Dev 2.043
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch120, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 29.71
Std Dev 29.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U1-ch121, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 26.34
Std Dev 3.328
Gaus mean : 26.4482
Gaus Width: 3.5816
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U1-ch121, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 32.41
Std Dev 3.394
Gaus mean : 32.3246
Gaus Width: 3.4785
Entry
ampl
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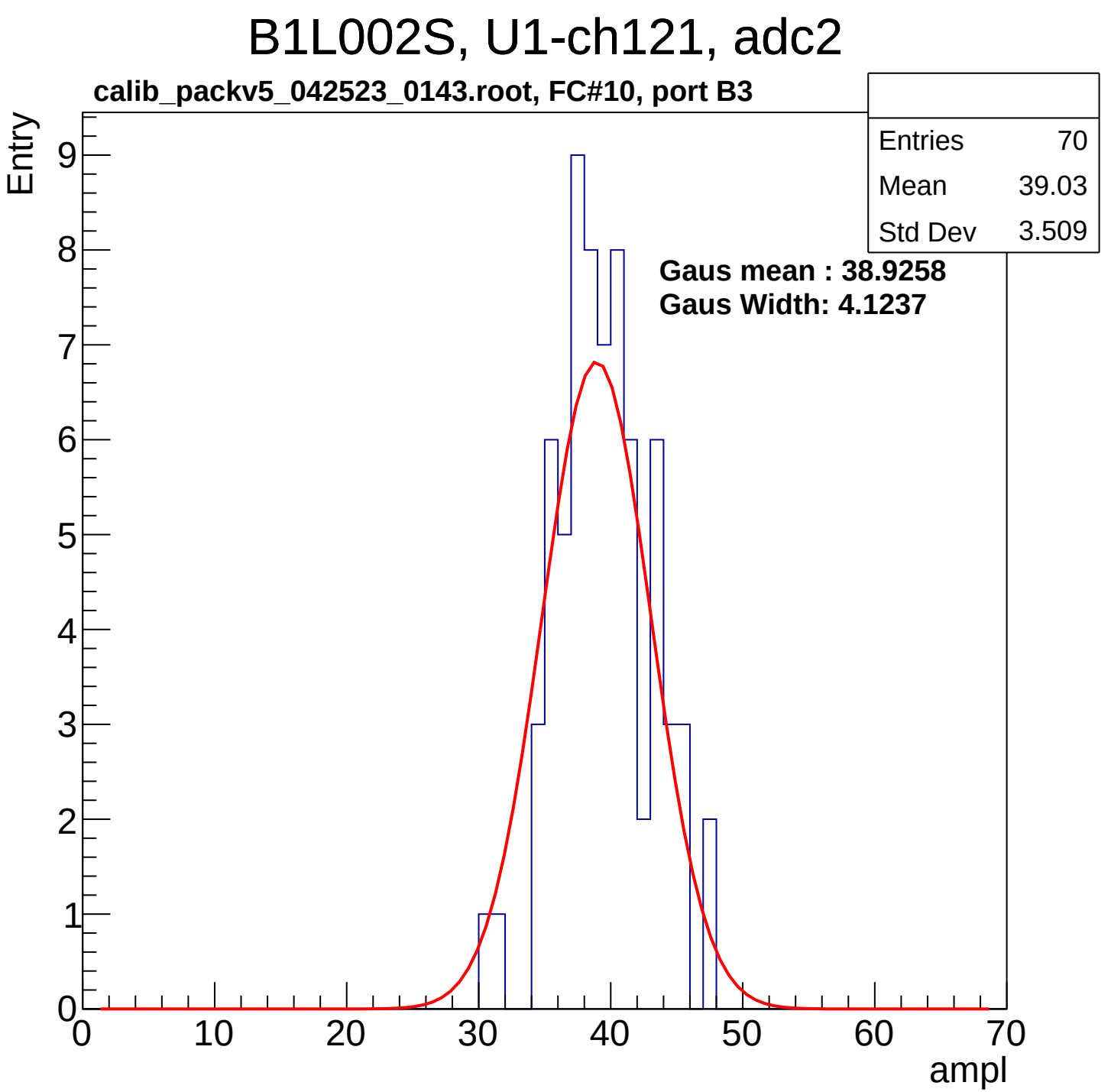
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch121, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 39.03
Std Dev 3.509
Gaus mean : 38.9258
Gaus Width: 4.1237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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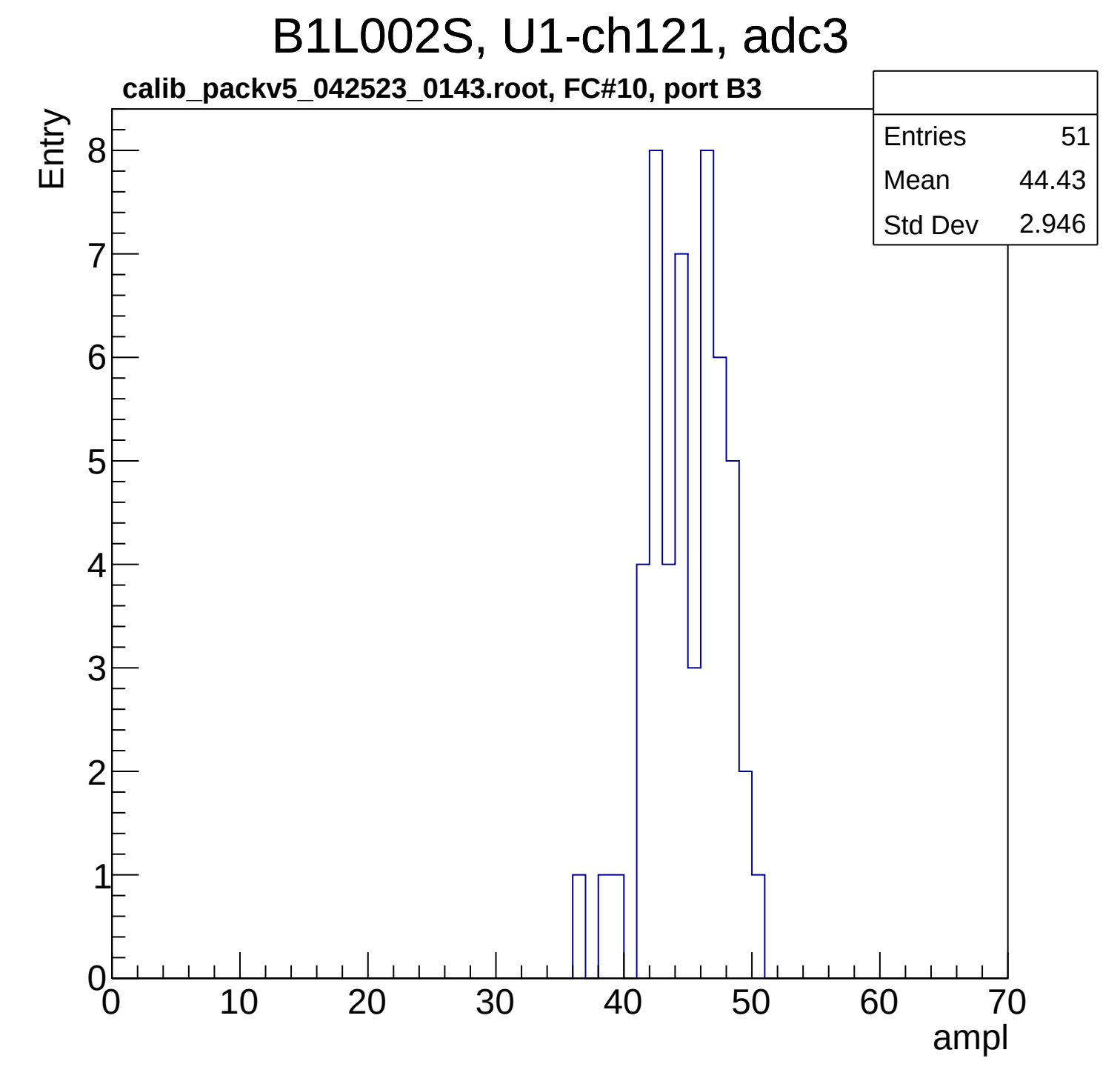
B1L002S, U1-ch121, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 44.43
Std Dev 2.946
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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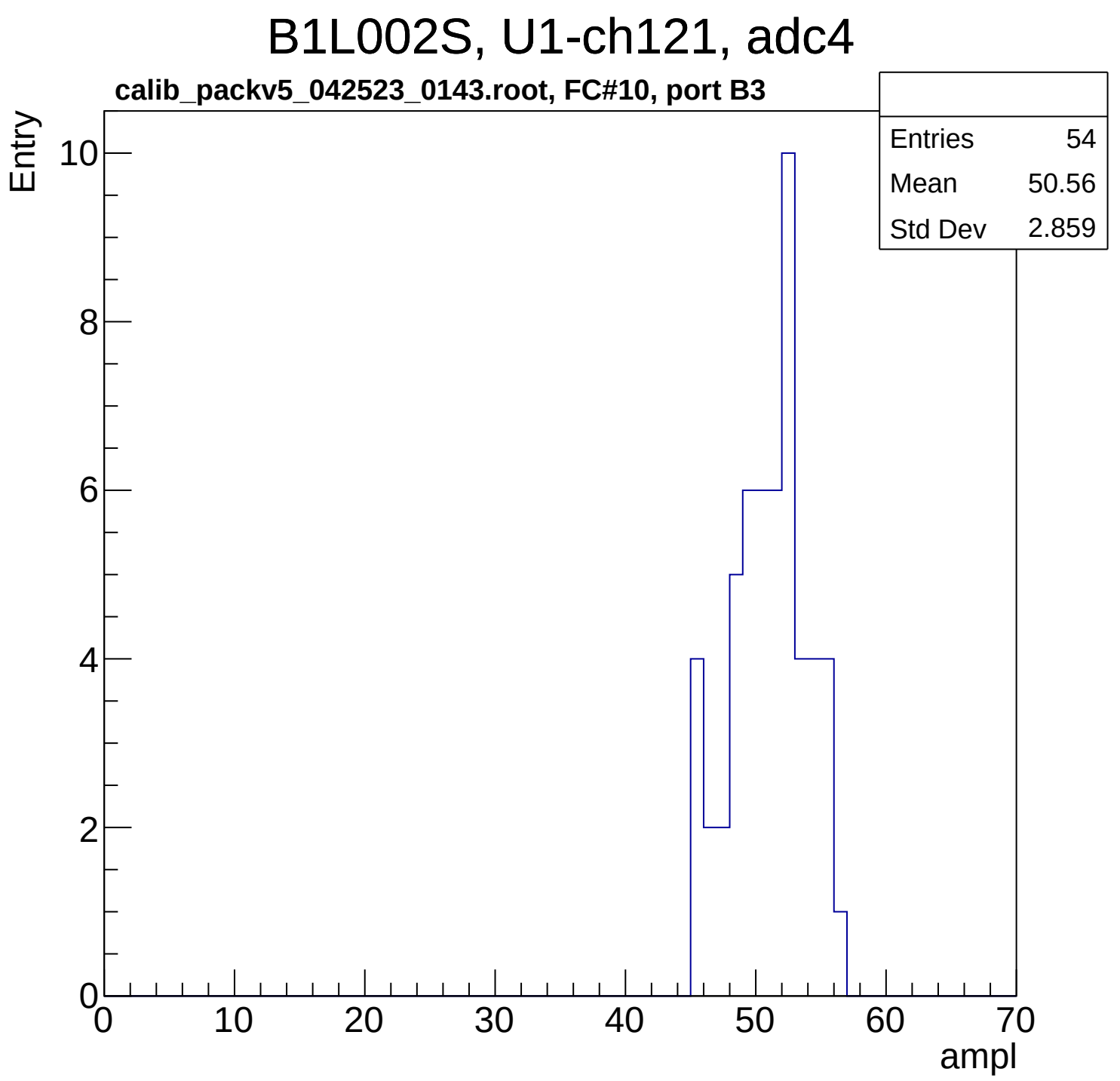
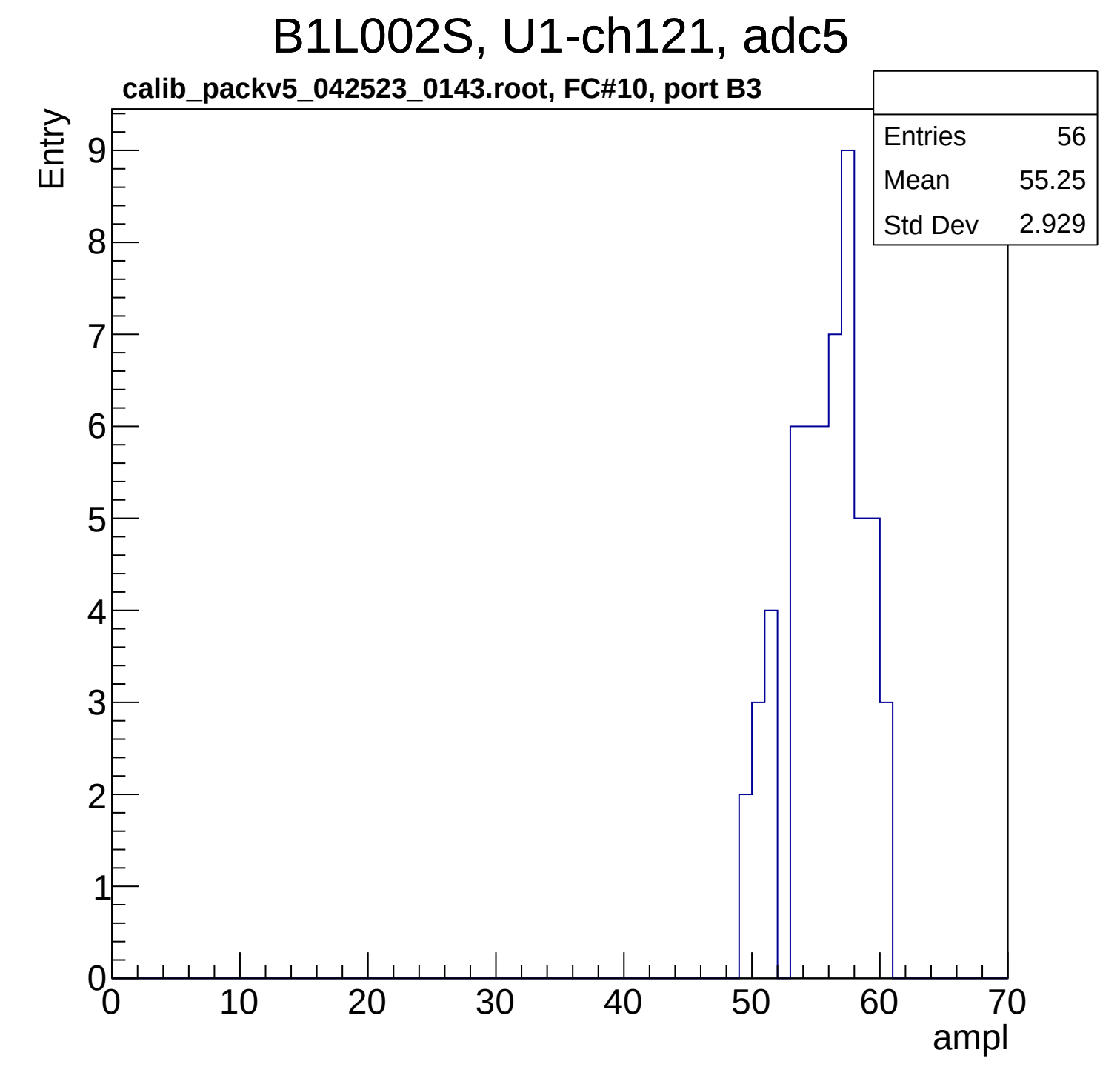

B1L002S, U1-ch121, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 50.56
Std Dev 2.859
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch121, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 55.25
Std Dev 2.929
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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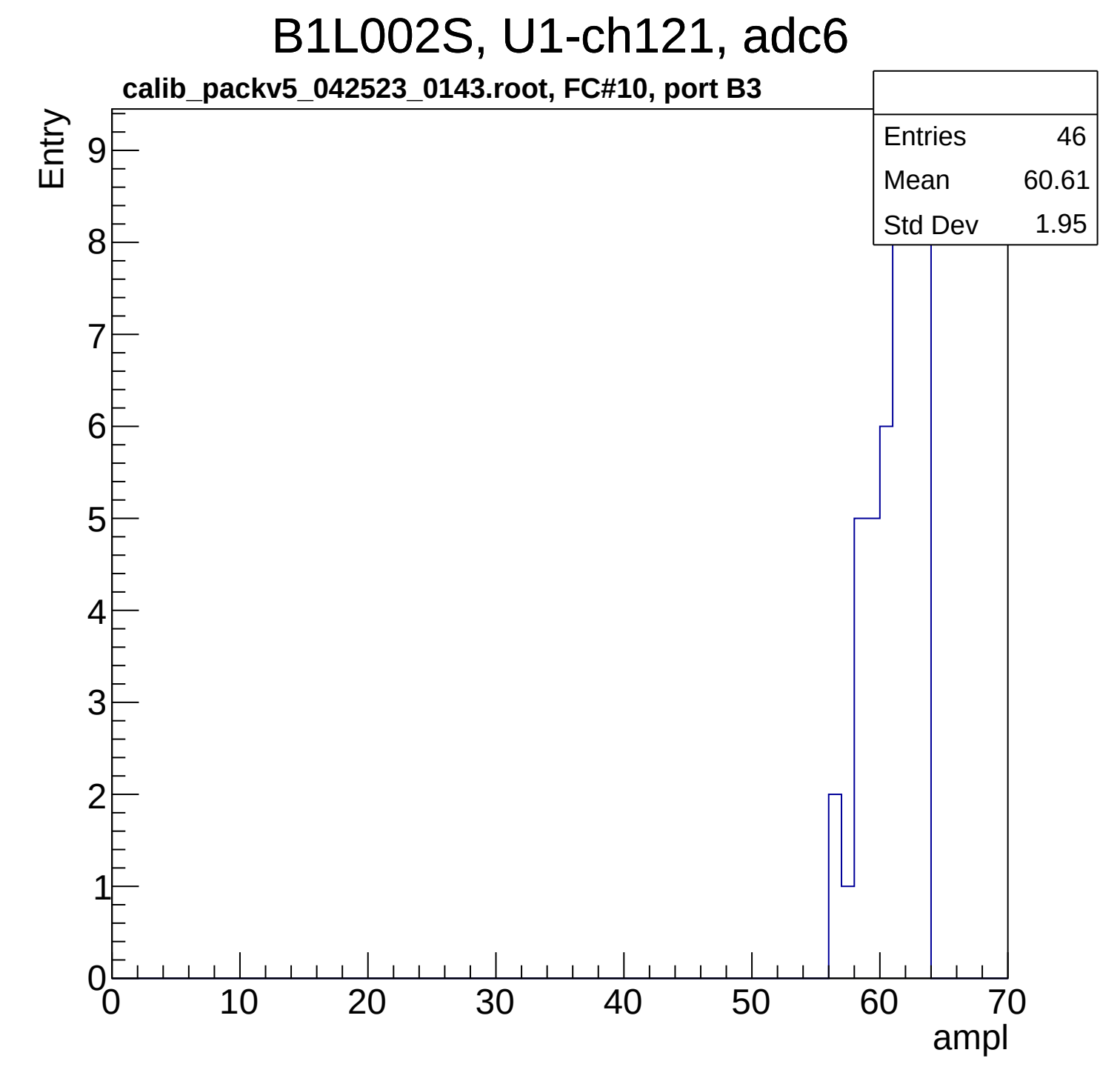
B1L002S, U1-ch121, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 60.61
Std Dev 1.95
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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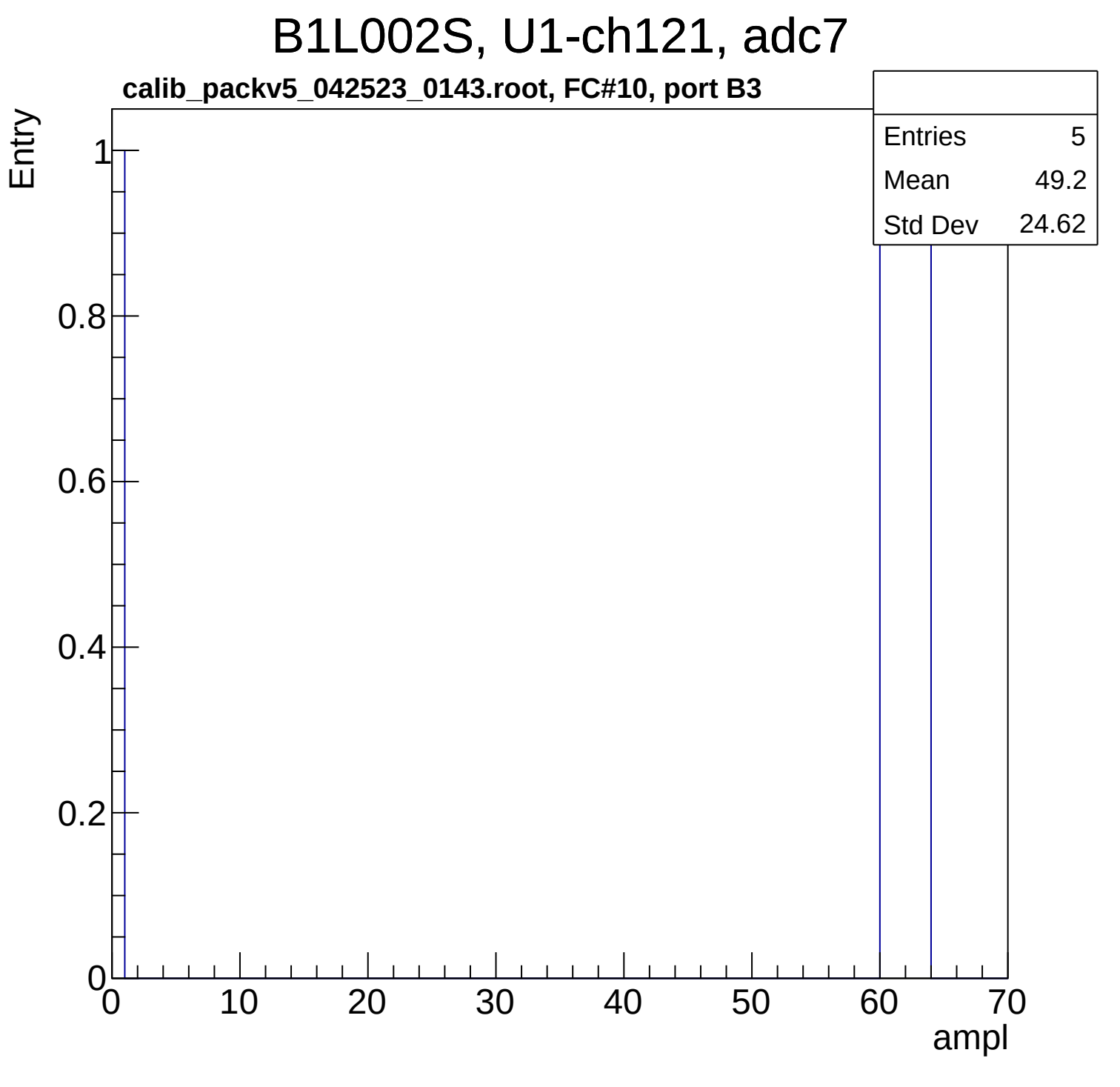
B1L002S, U1-ch121, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 49.2
Std Dev 24.62
Entry
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0.6
0.4
0.2
0
0
10
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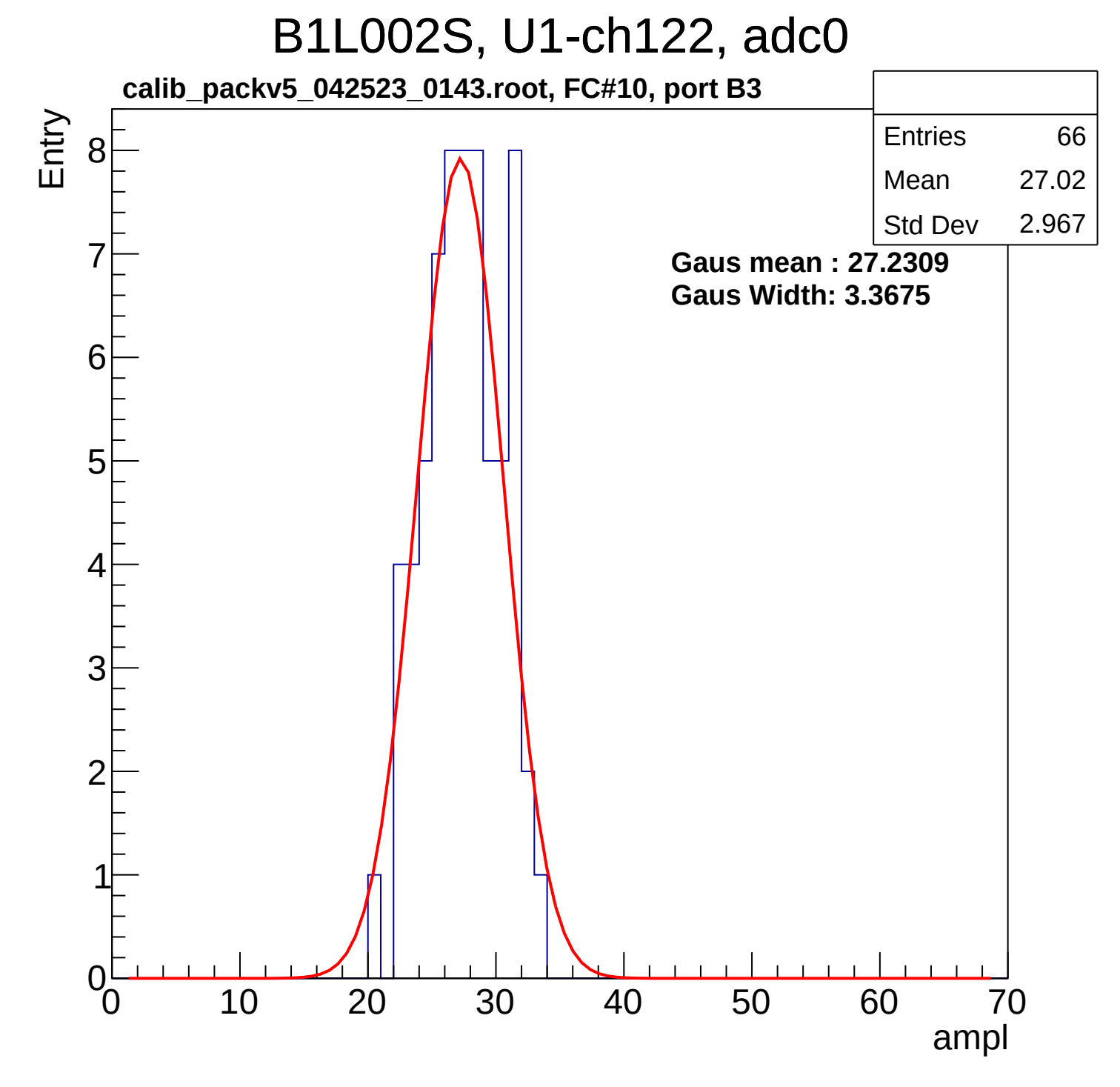
50
60
70
ampl


B1L002S, U1-ch122, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 27.02
Std Dev 2.967
Gaus mean : 27.2309
Gaus Width: 3.3675
Entry
ampl
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2
3
4
5
6
7
8
0
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20
30
40
50
60
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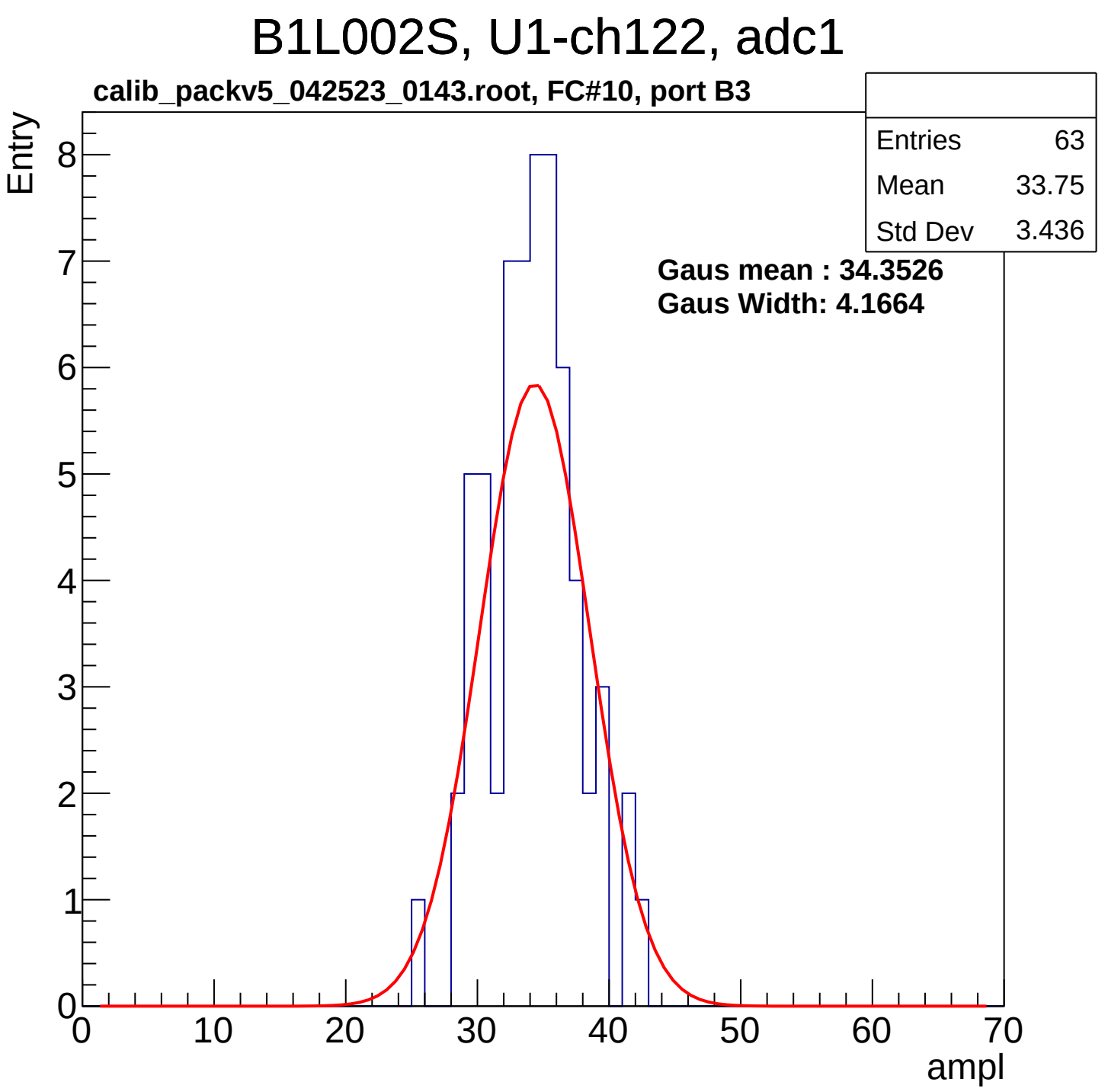
B1L002S, U1-ch122, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.75
Std Dev 3.436
Gaus mean : 34.3526
Gaus Width: 4.1664
Entry
ampl
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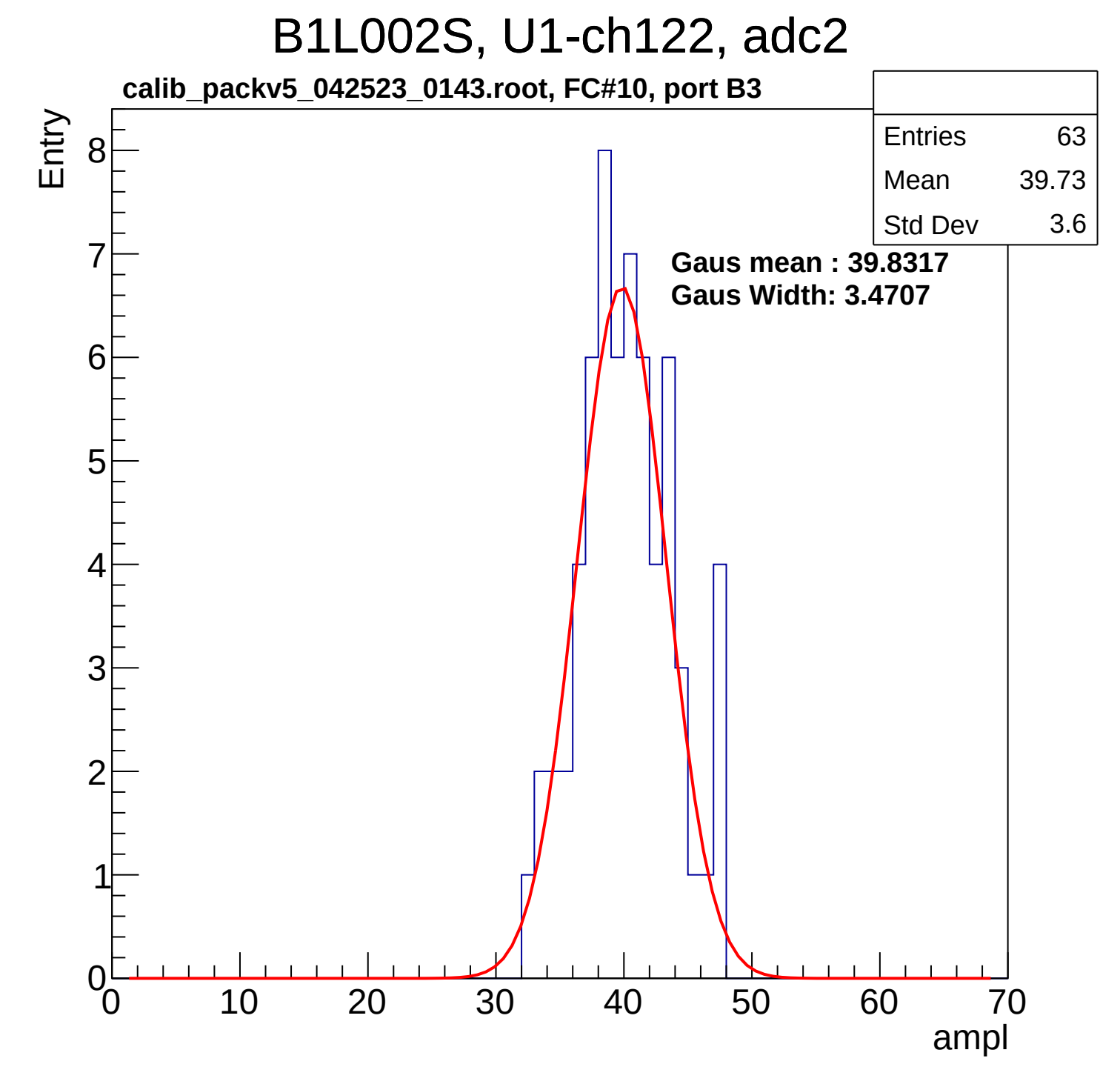
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch122, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.73
Std Dev 3.6
Gaus mean : 39.8317
Gaus Width: 3.4707
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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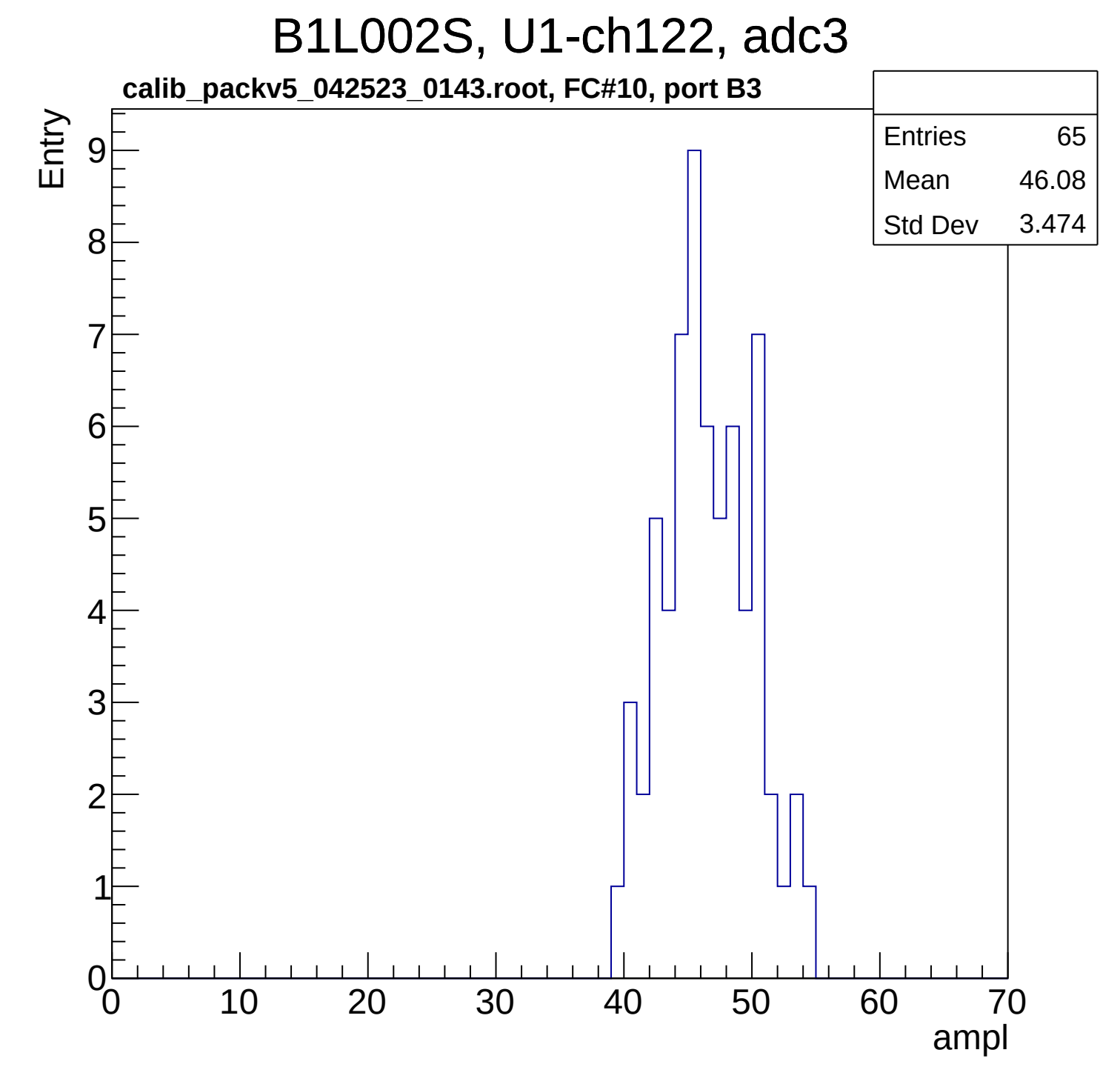
B1L002S, U1-ch122, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.08
Std Dev 3.474
Entry
ampl
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4
5
6
7
8
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0
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60
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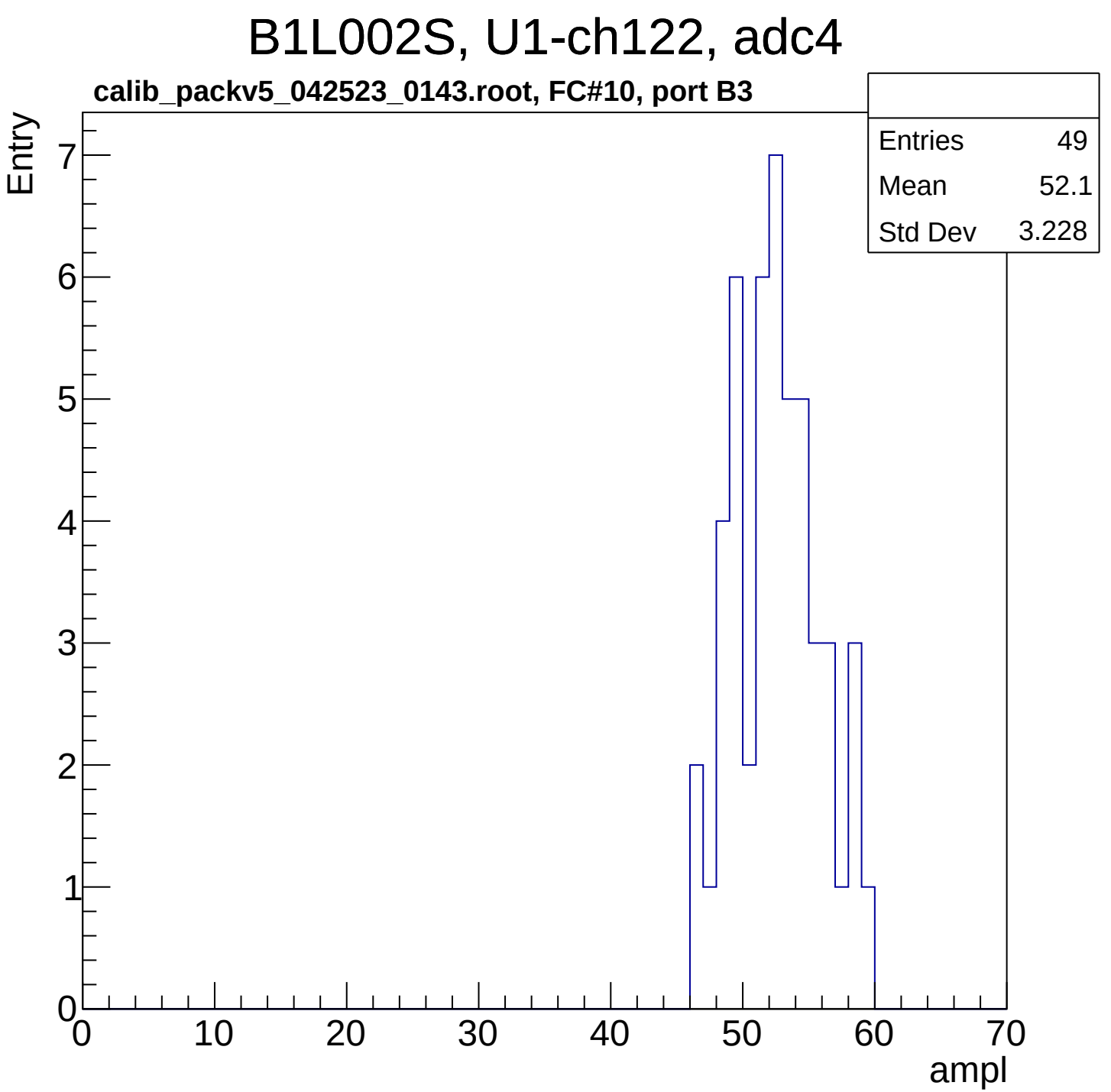
B1L002S, U1-ch122, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.1
Std Dev 3.228
Entry
ampl
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4
5
6
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0
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60
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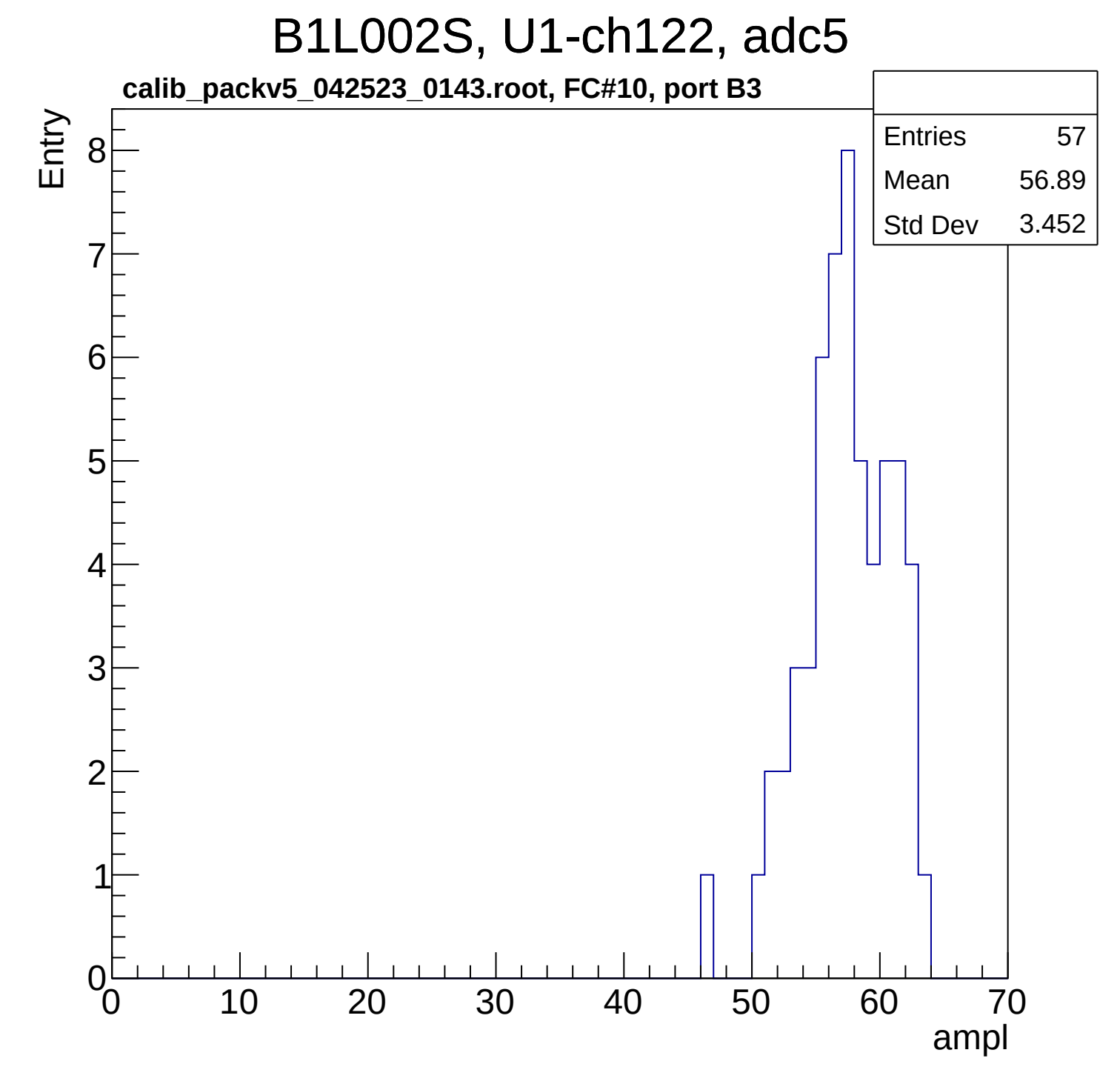
B1L002S, U1-ch122, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 56.89
Std Dev 3.452
Entry
ampl
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3
4
5
6
7
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0
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60
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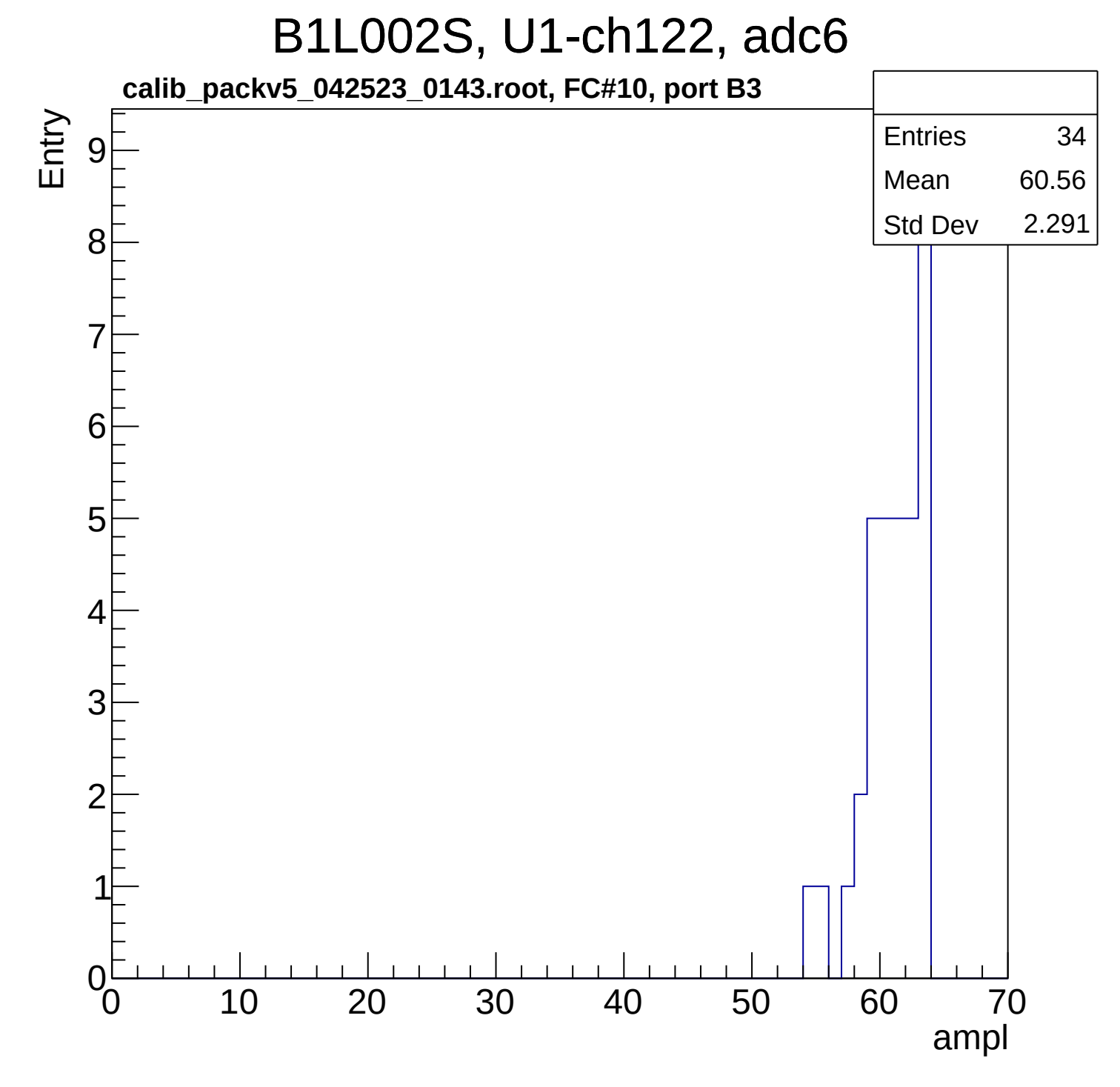
B1L002S, U1-ch122, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 60.56
Std Dev 2.291
Entry
ampl
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3
4
5
6
7
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0
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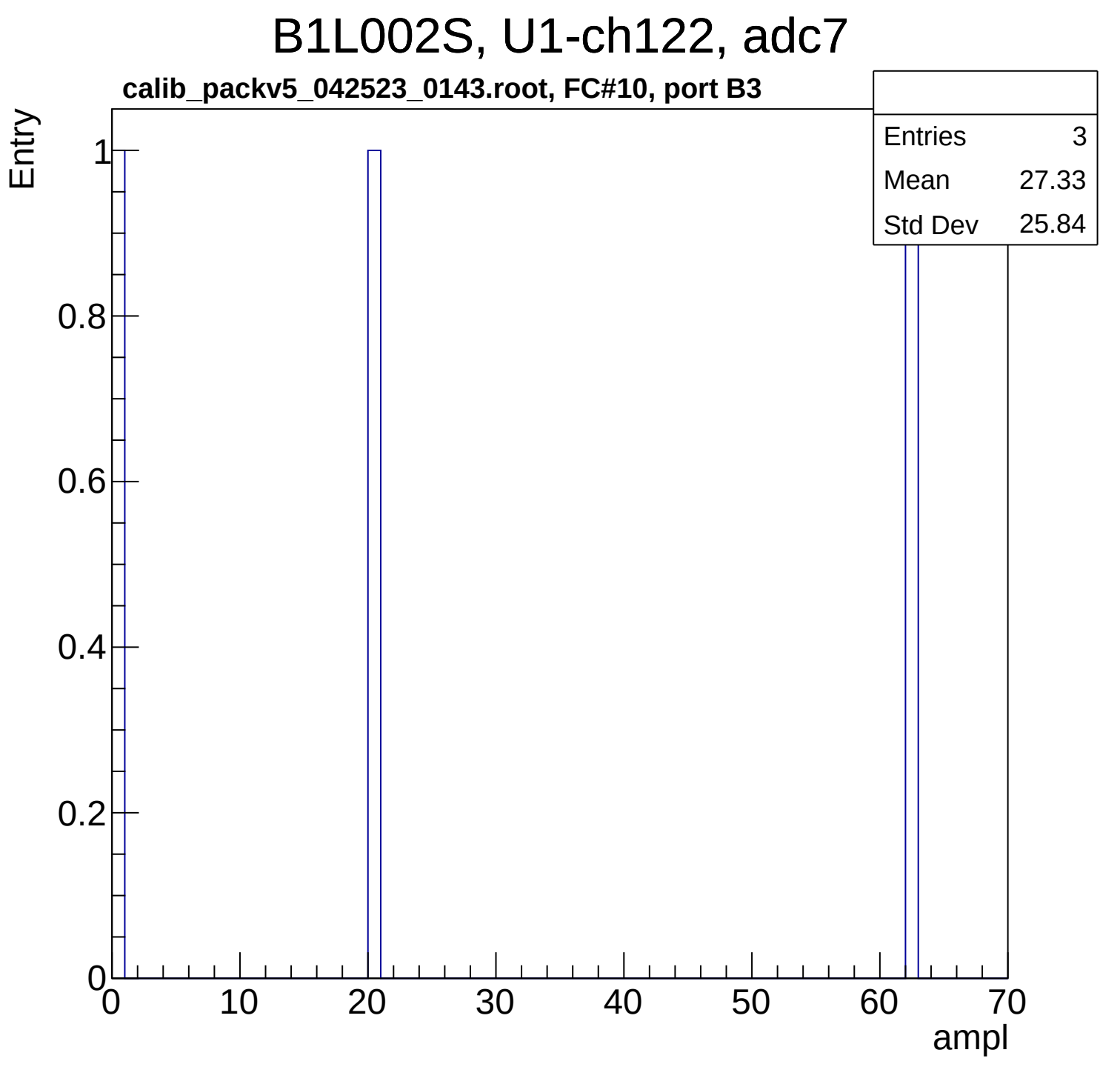
B1L002S, U1-ch122, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
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0.2
0.4
0.6
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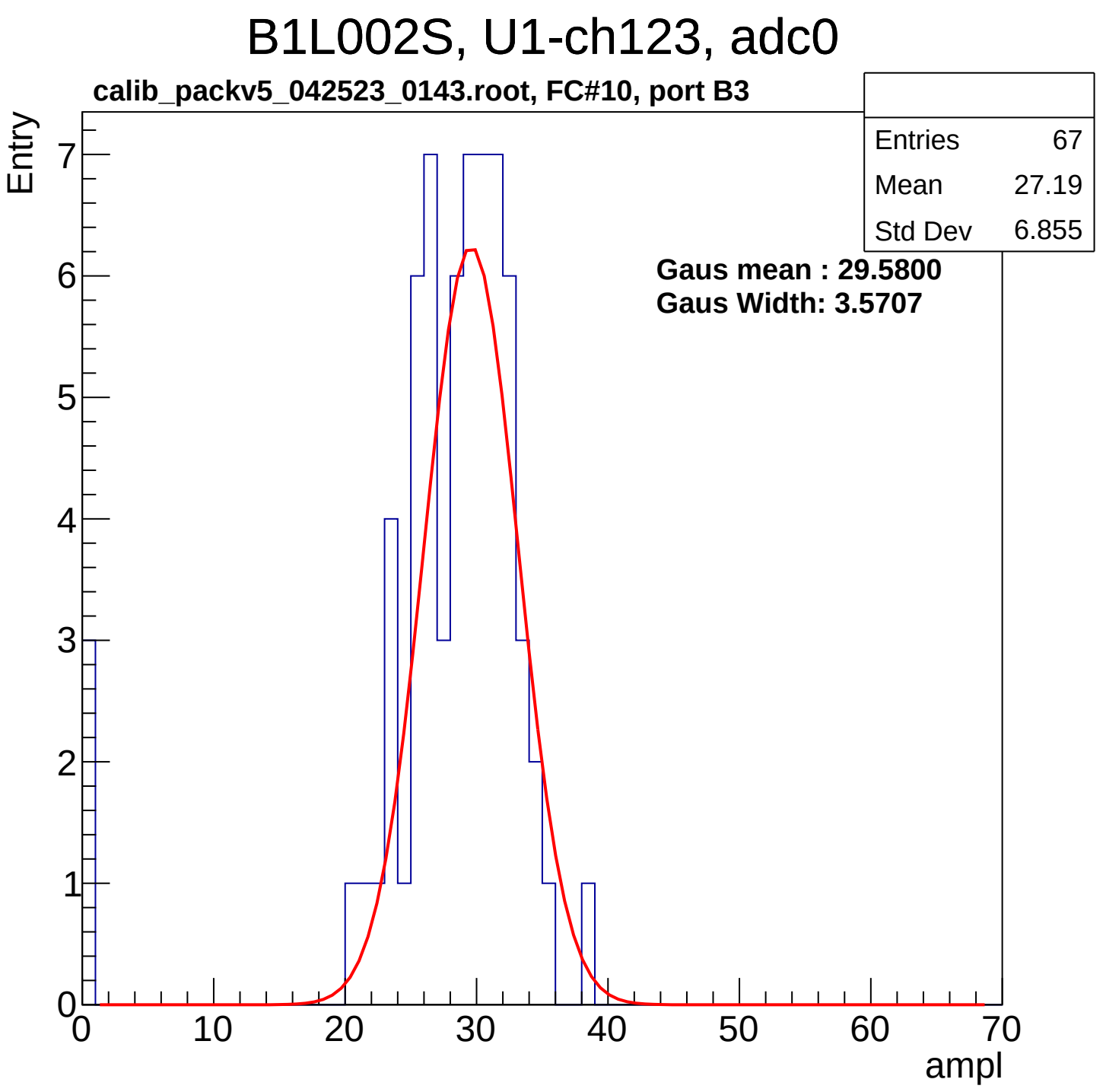
0
10
20
30
40
50
60
70


B1L002S, U1-ch123, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.19
Std Dev 6.855
Gaus mean : 29.5800
Gaus Width: 3.5707
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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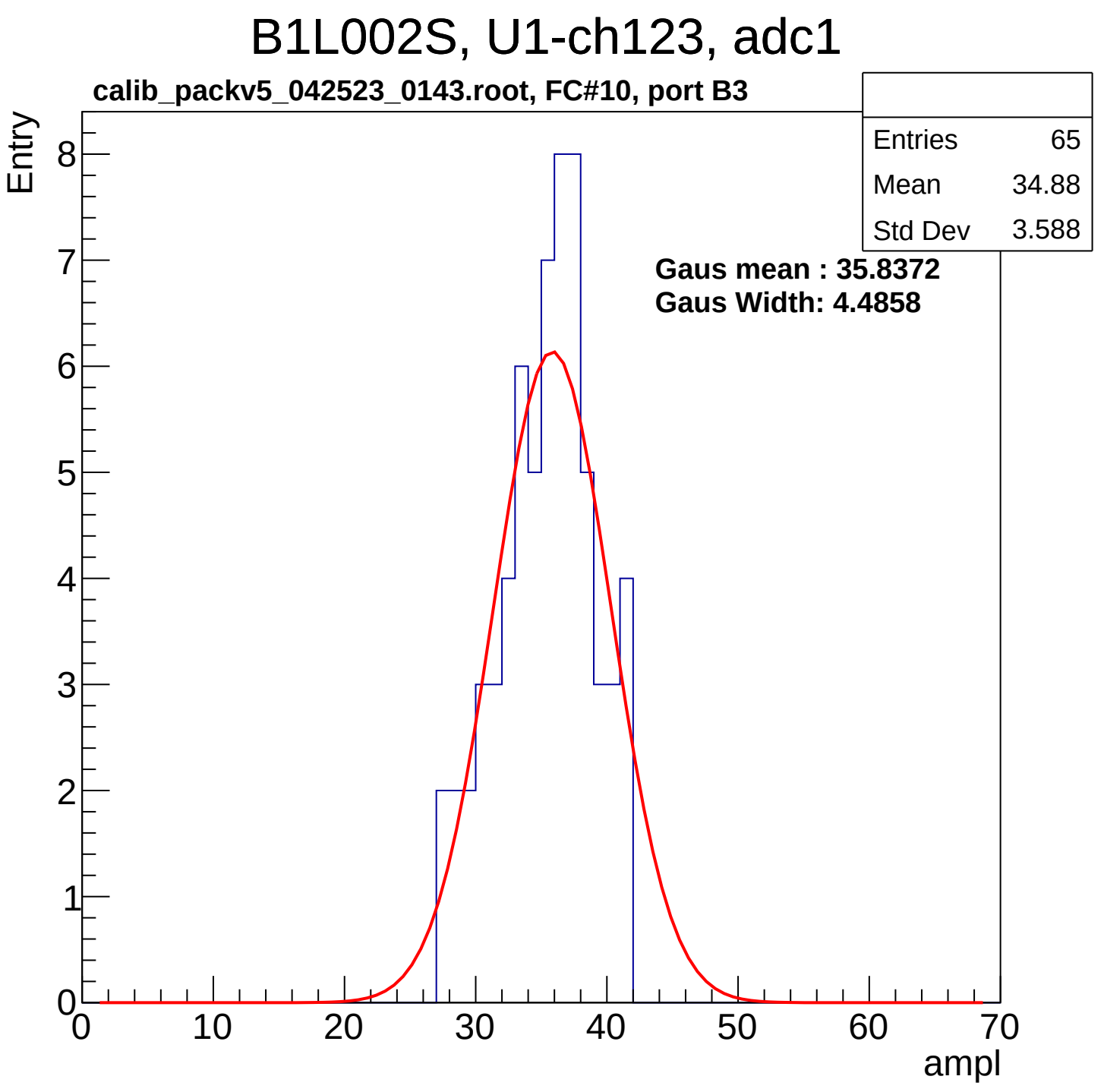
B1L002S, U1-ch123, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.88
Std Dev 3.588
Gaus mean : 35.8372
Gaus Width: 4.4858
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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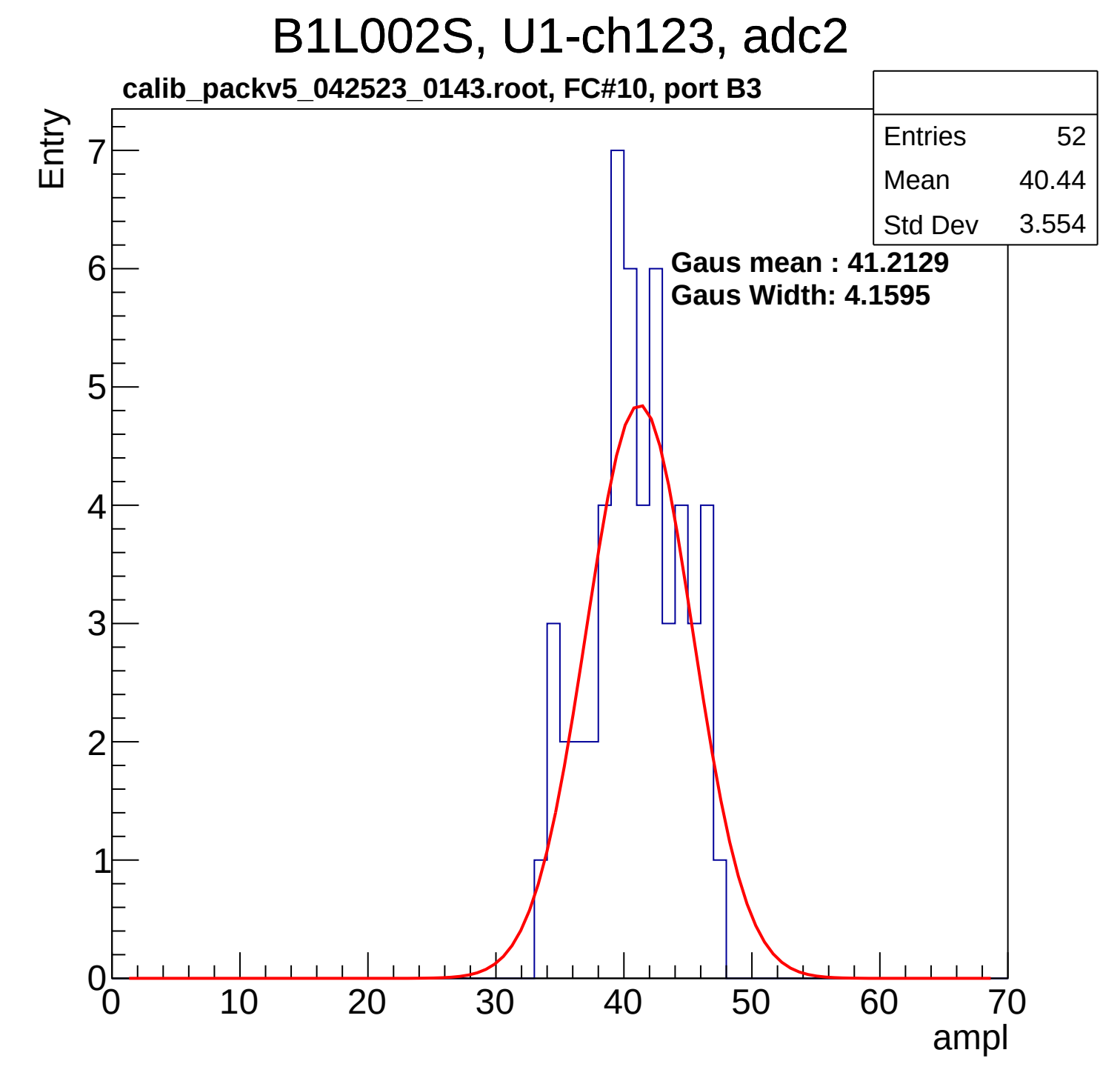
B1L002S, U1-ch123, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.44
Std Dev 3.554
Gaus mean : 41.2129
Gaus Width: 4.1595
Entry
ampl
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1
2
3
4
5
6
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0
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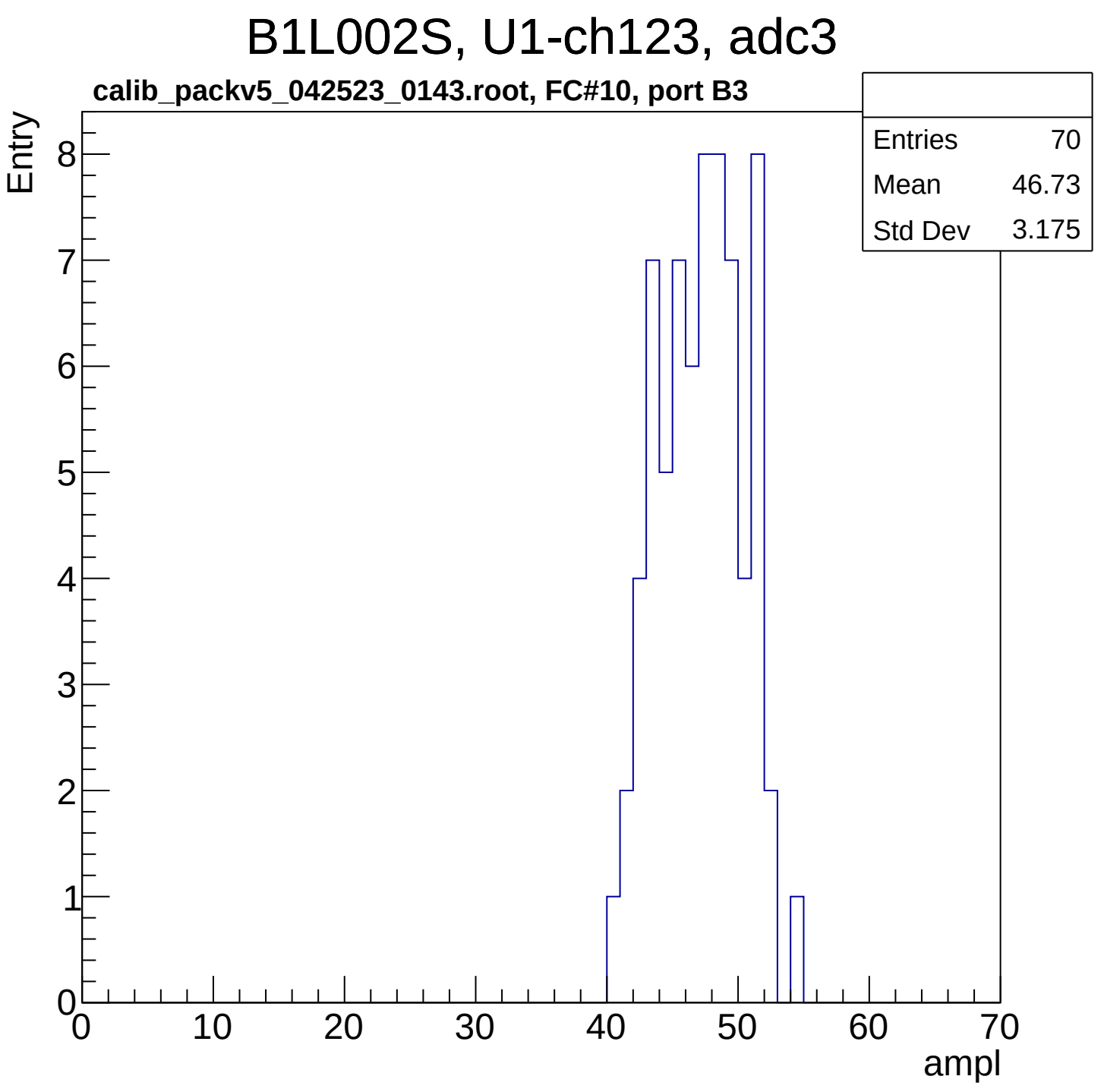
B1L002S, U1-ch123, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.73
Std Dev 3.175
Entry
ampl
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3
4
5
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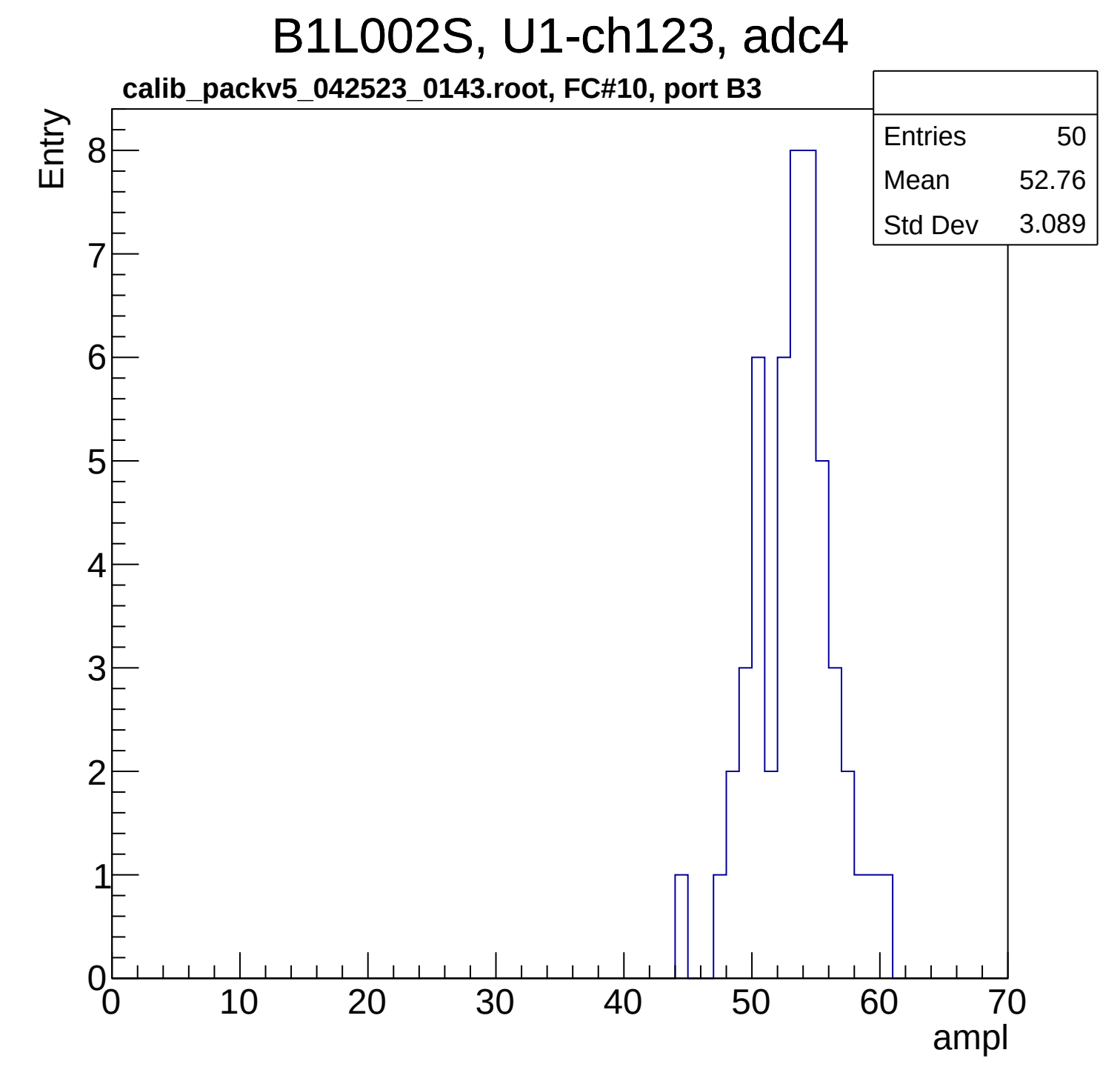
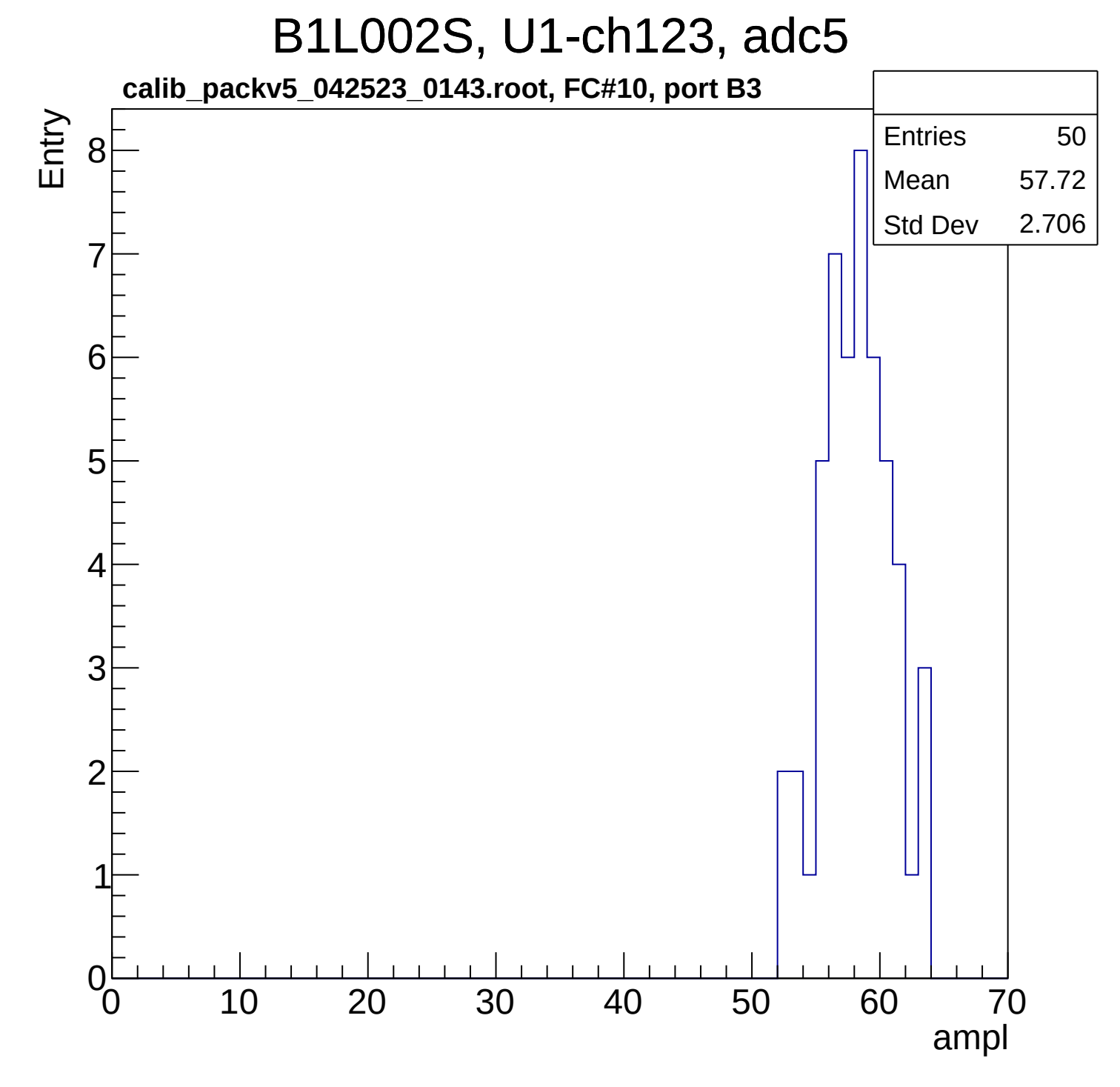

B1L002S, U1-ch123, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.76
Std Dev 3.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch123, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57.72
Std Dev 2.706
Entry
ampl
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2
3
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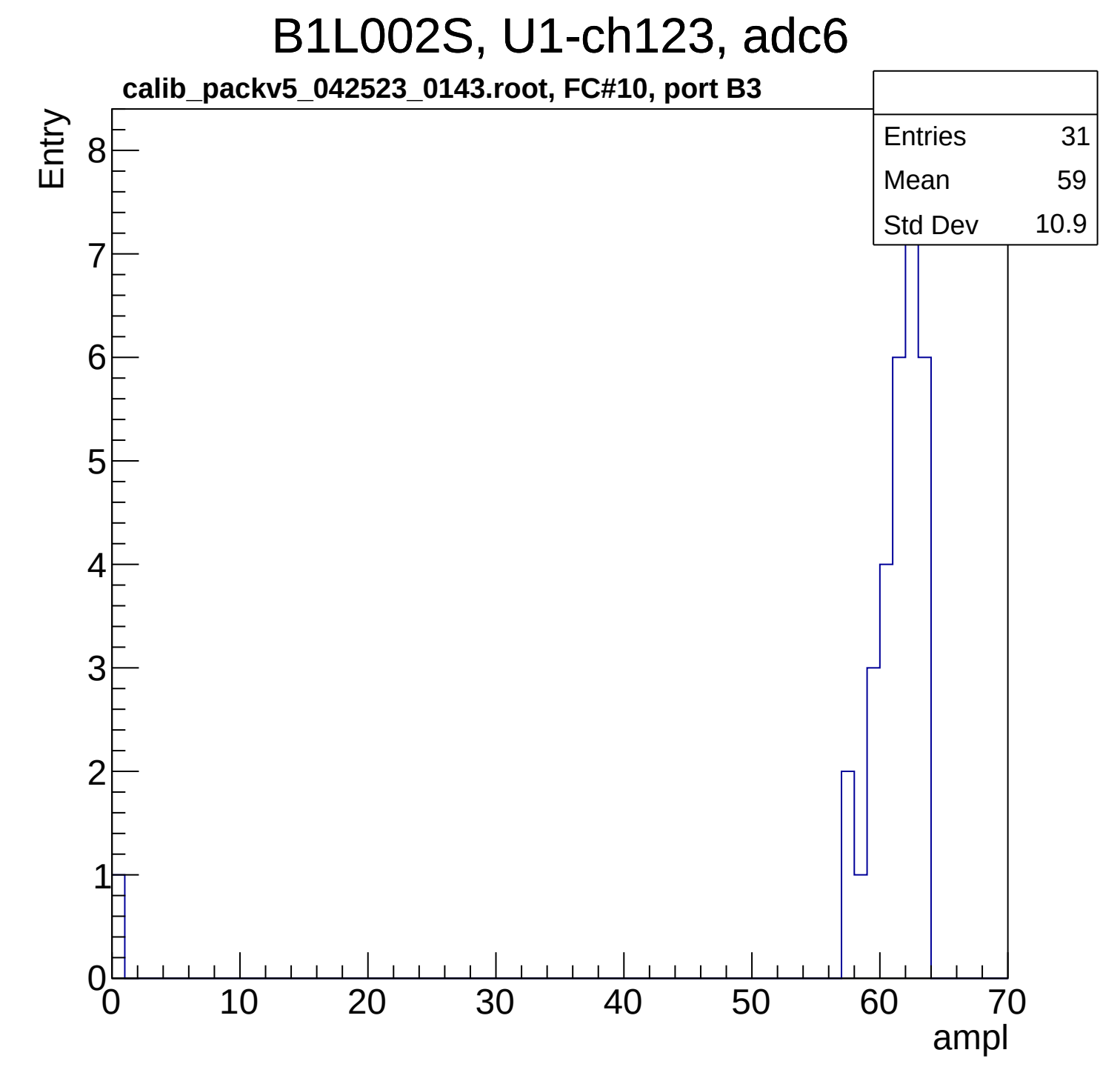
B1L002S, U1-ch123, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 59
Std Dev 10.9
Entry
ampl
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1
2
3
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5
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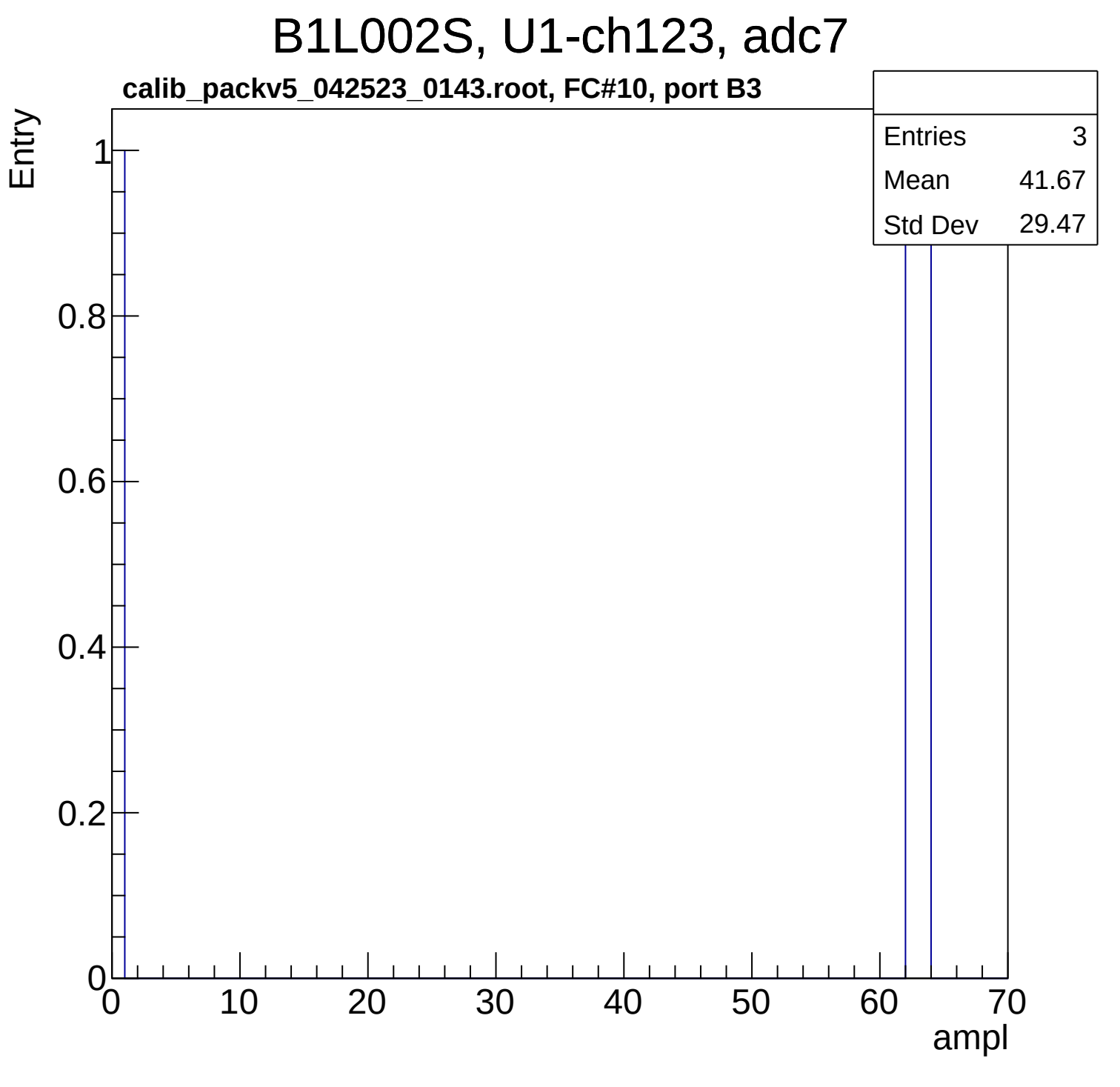
B1L002S, U1-ch123, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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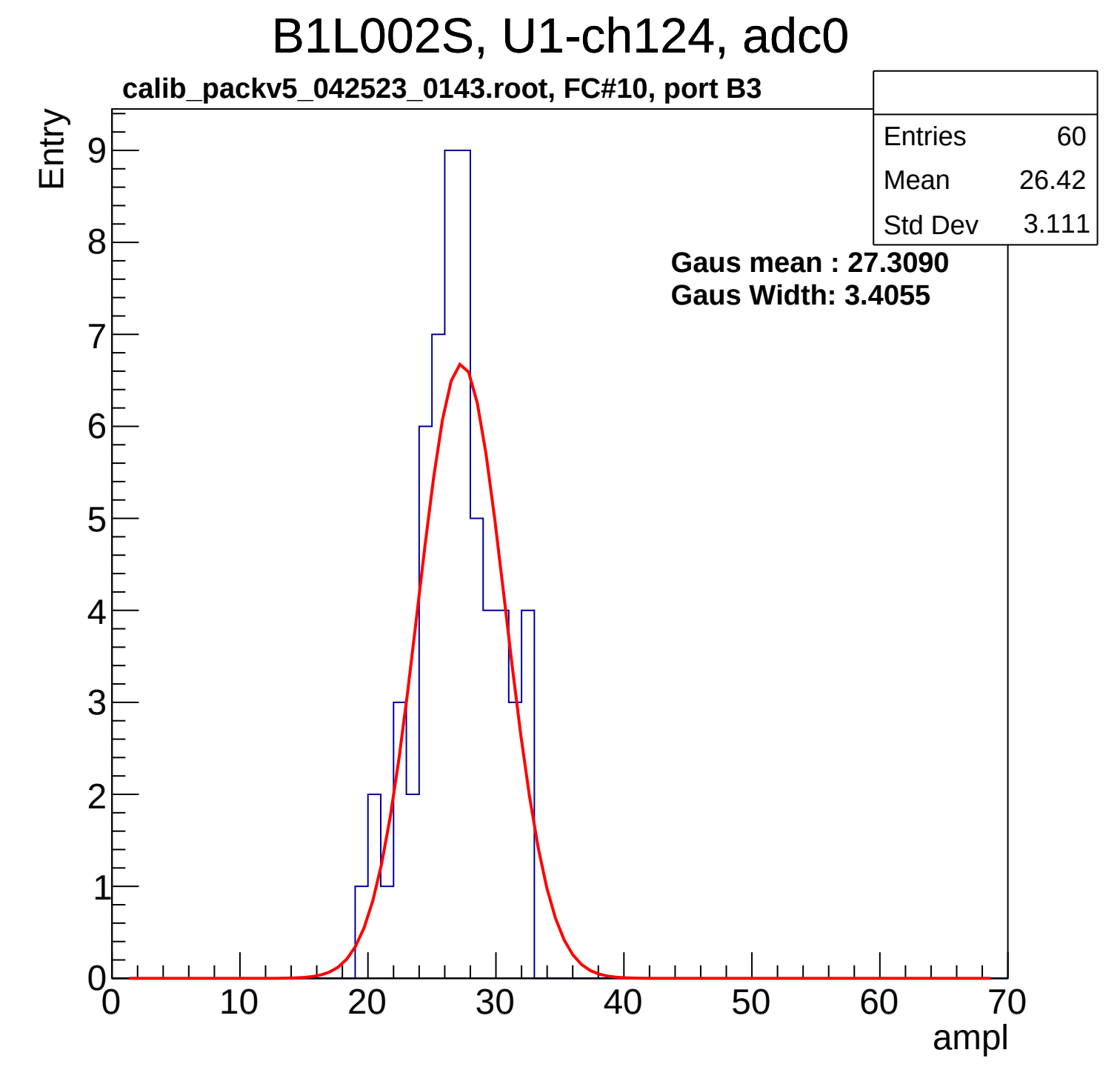
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch124, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 26.42
Std Dev 3.111
Gaus mean : 27.3090
Gaus Width: 3.4055
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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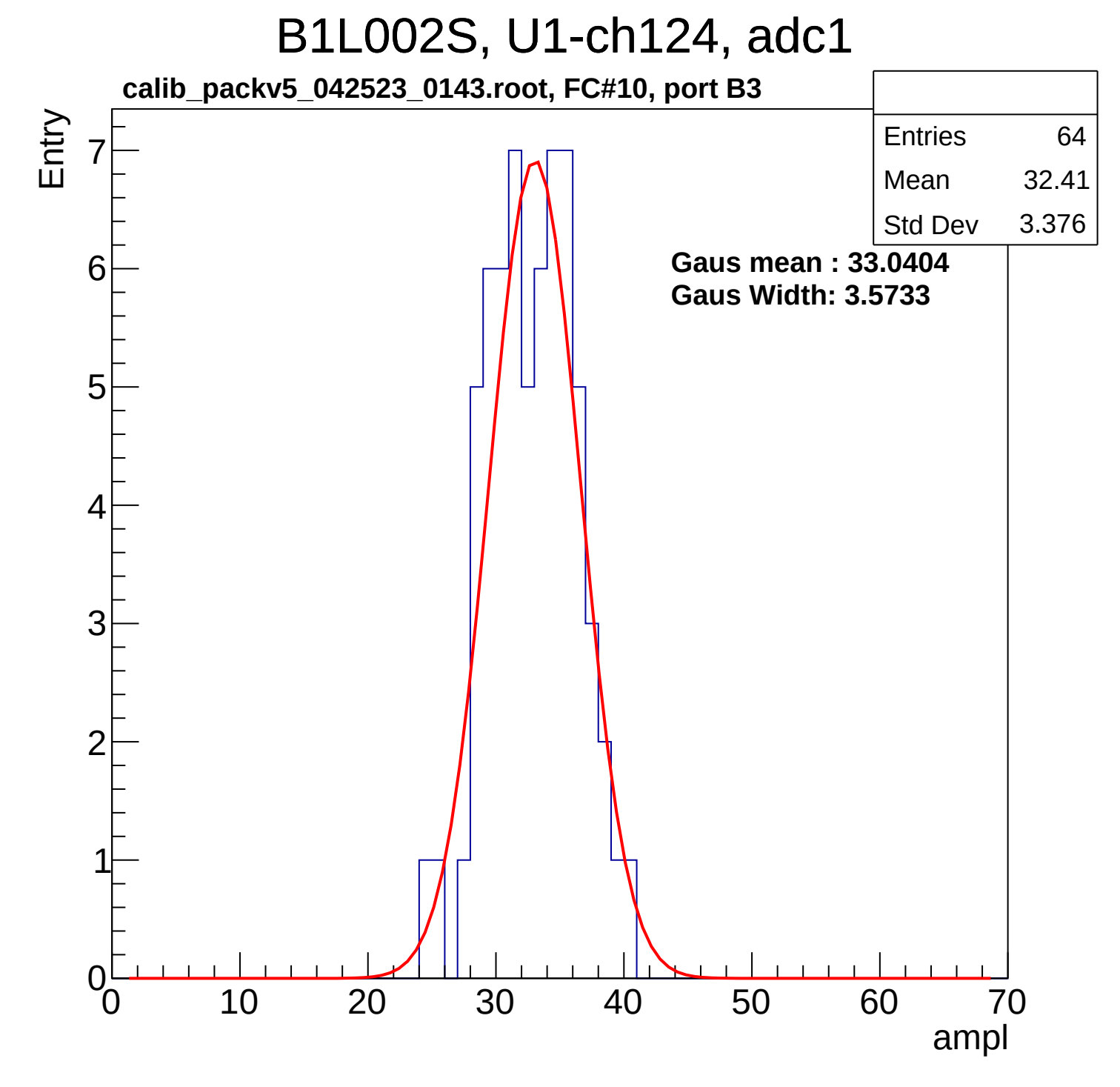
B1L002S, U1-ch124, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 32.41
Std Dev 3.376
Gaus mean : 33.0404
Gaus Width: 3.5733
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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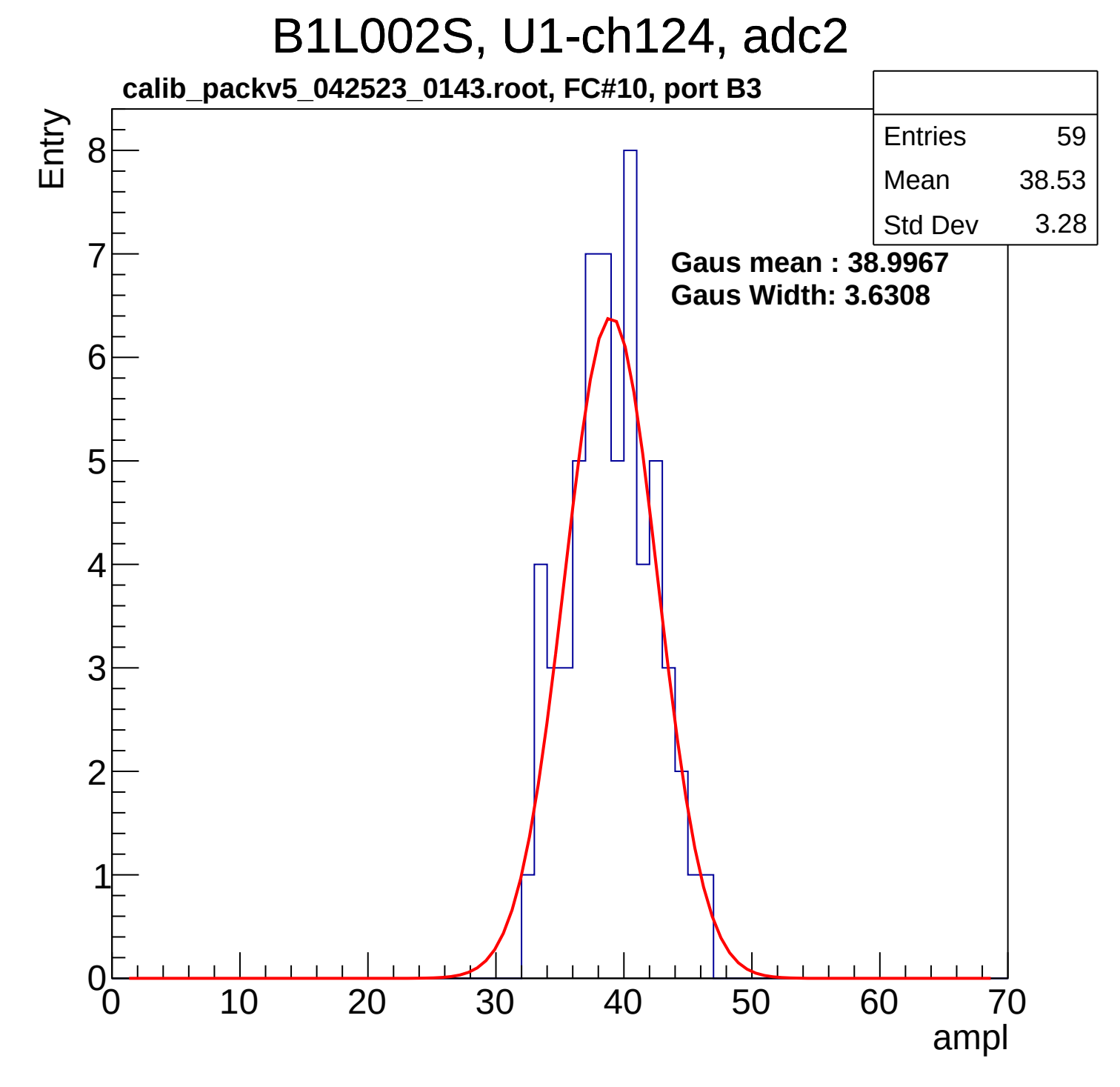
B1L002S, U1-ch124, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 38.53
Std Dev 3.28
Gaus mean : 38.9967
Gaus Width: 3.6308
Entry
ampl
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1
2
3
4
5
6
7
8
0
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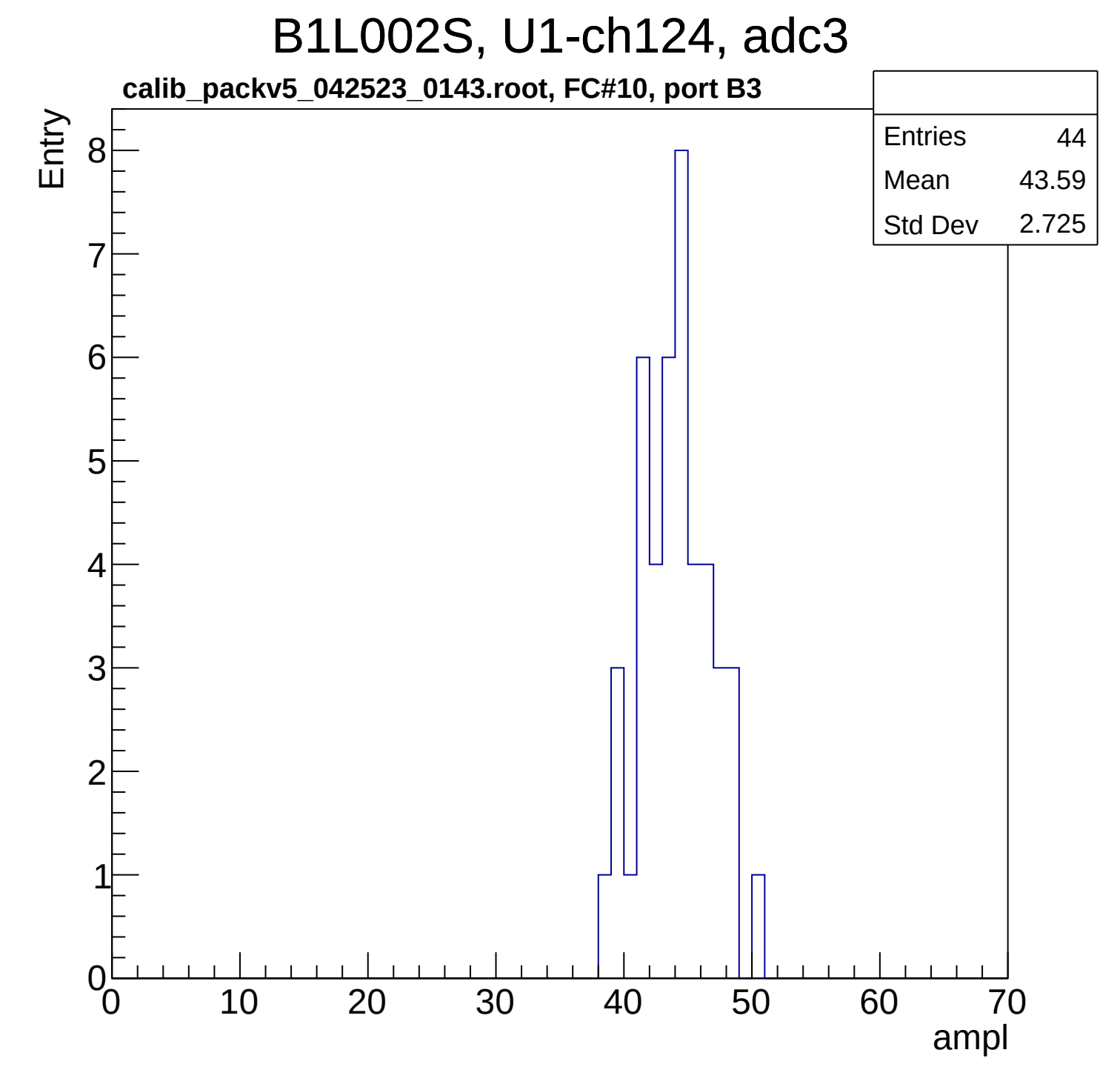
B1L002S, U1-ch124, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 43.59
Std Dev 2.725
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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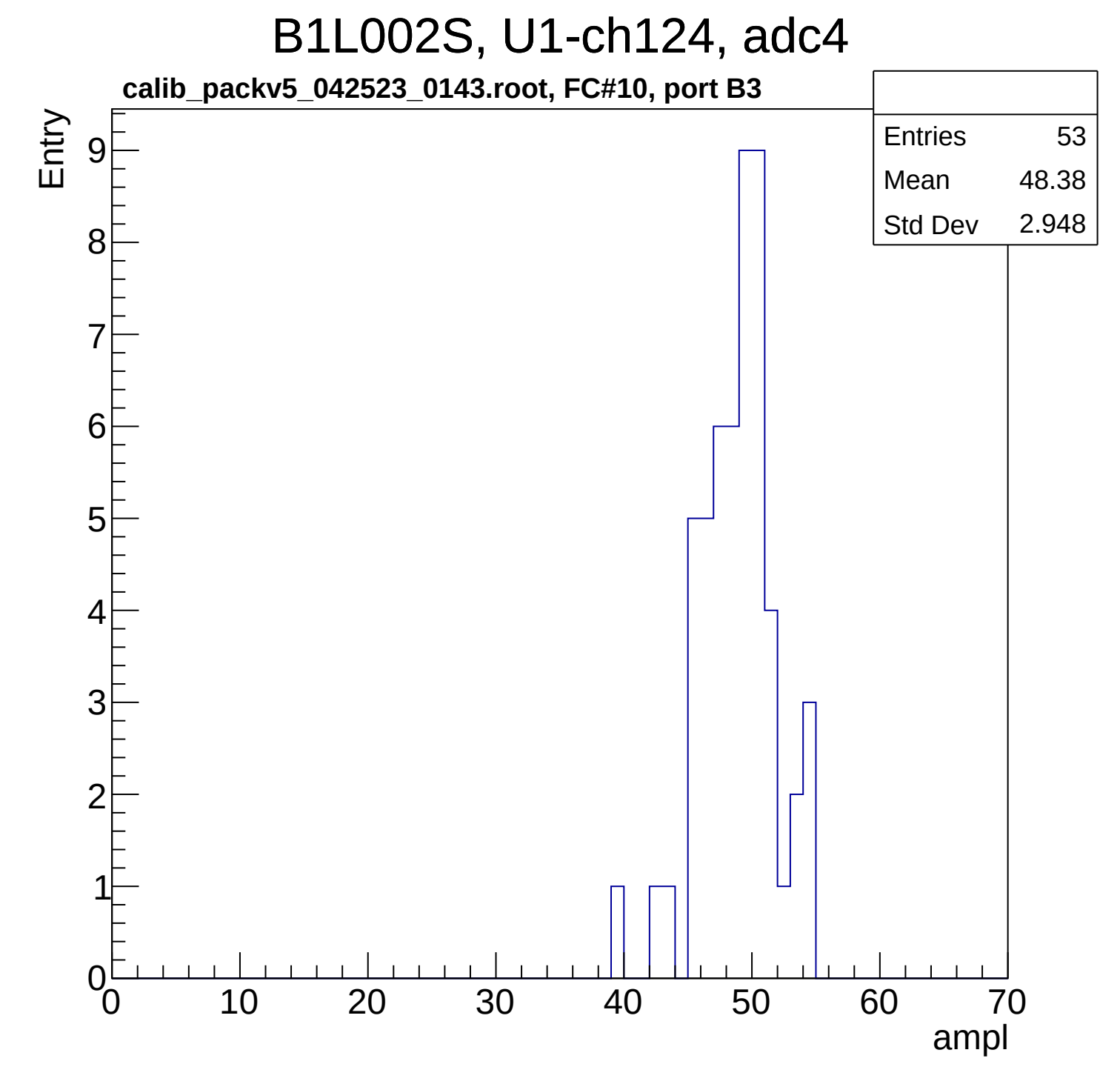
B1L002S, U1-ch124, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 48.38
Std Dev 2.948
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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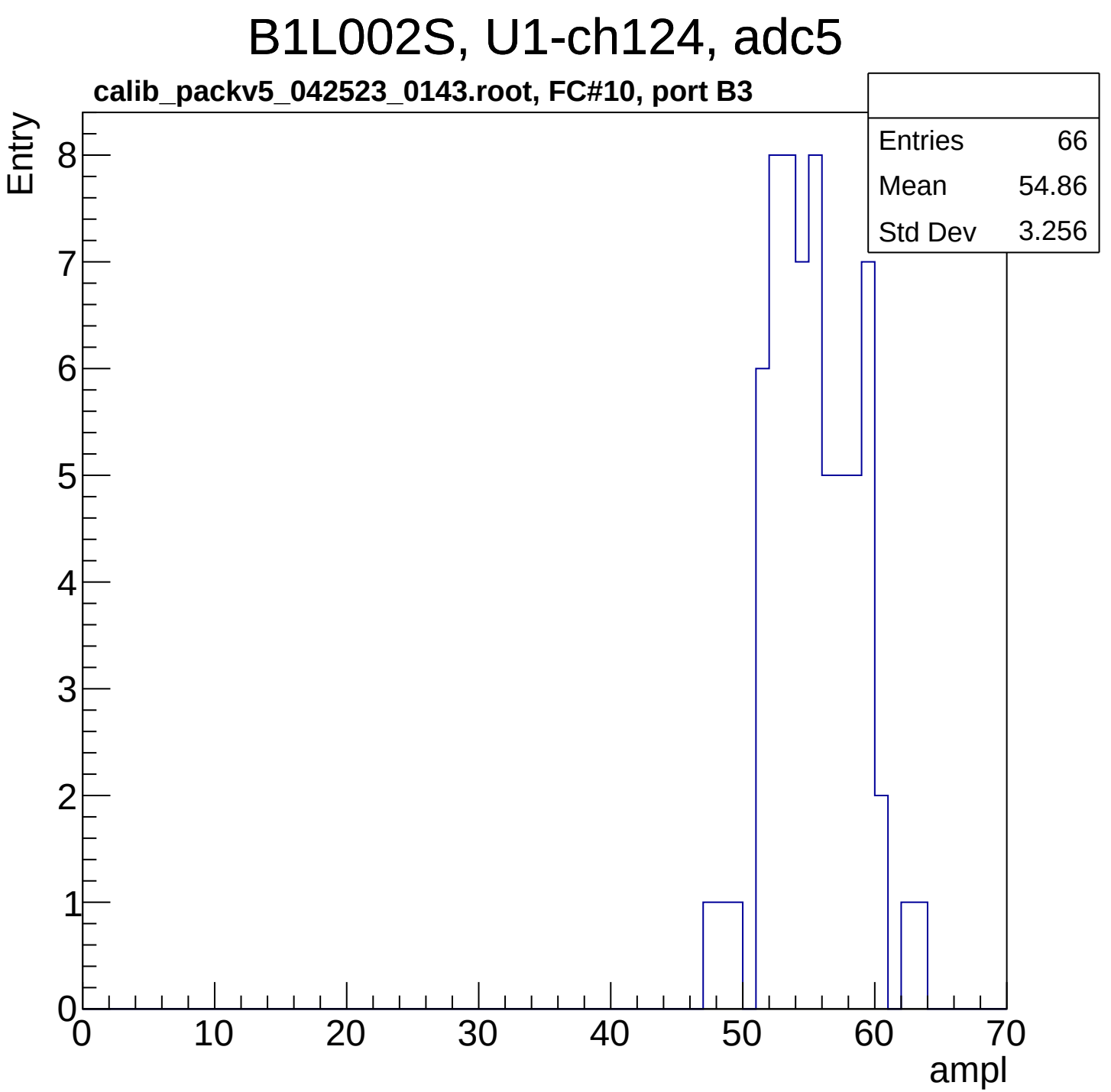
B1L002S, U1-ch124, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.86
Std Dev 3.256
Entry
ampl
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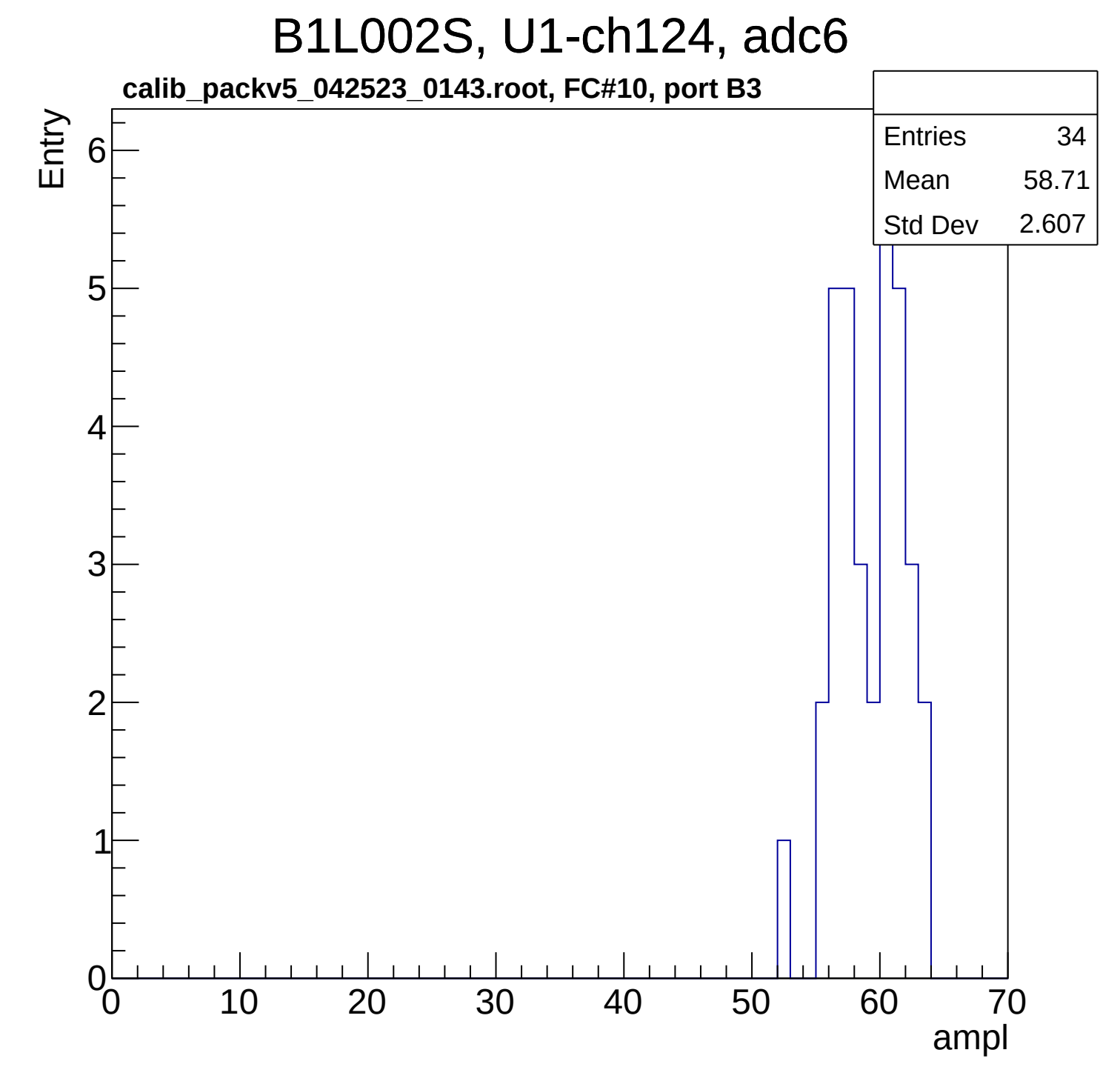
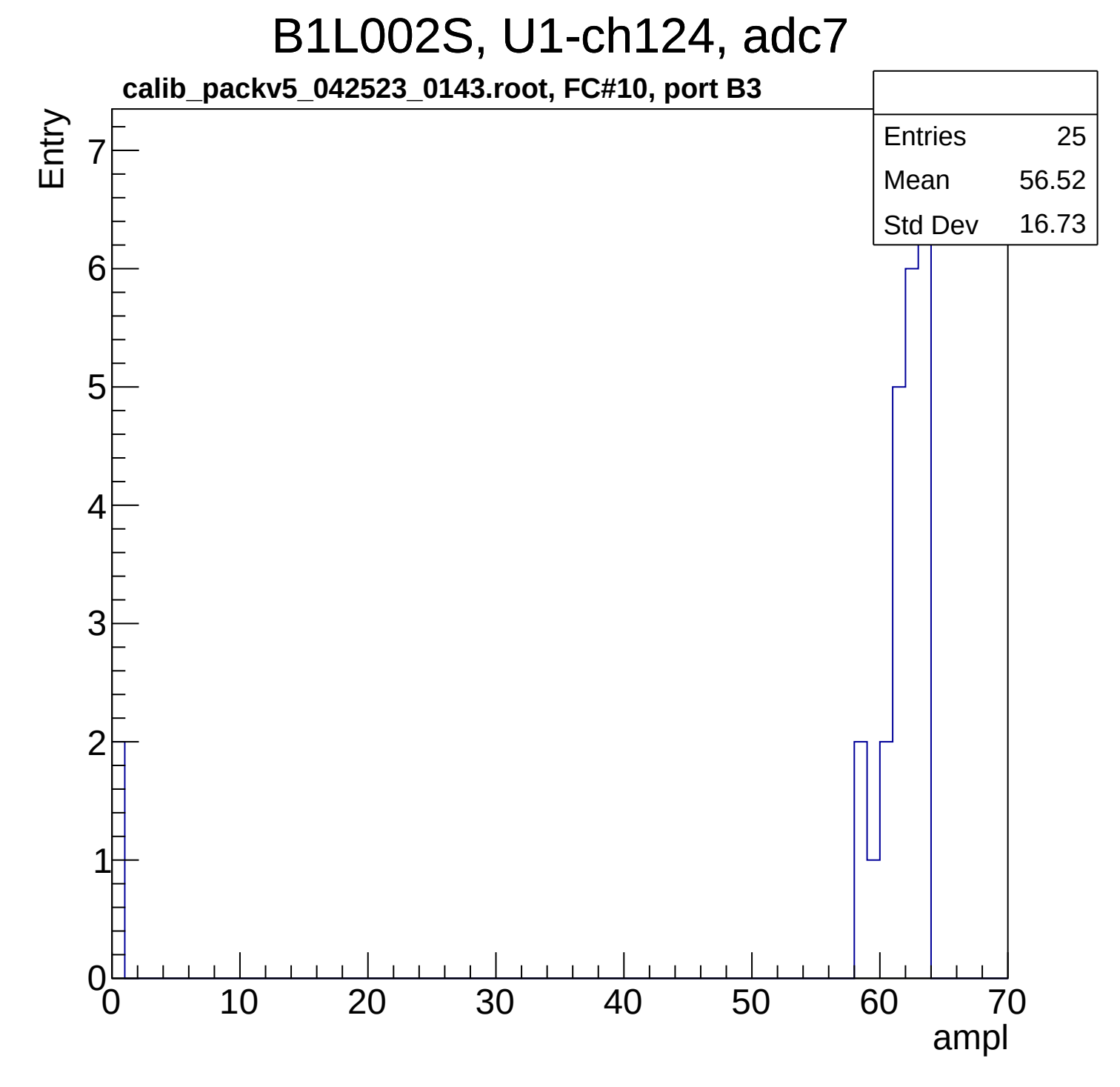
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch124, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.71
Std Dev 2.607
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch124, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 56.52
Std Dev 16.73
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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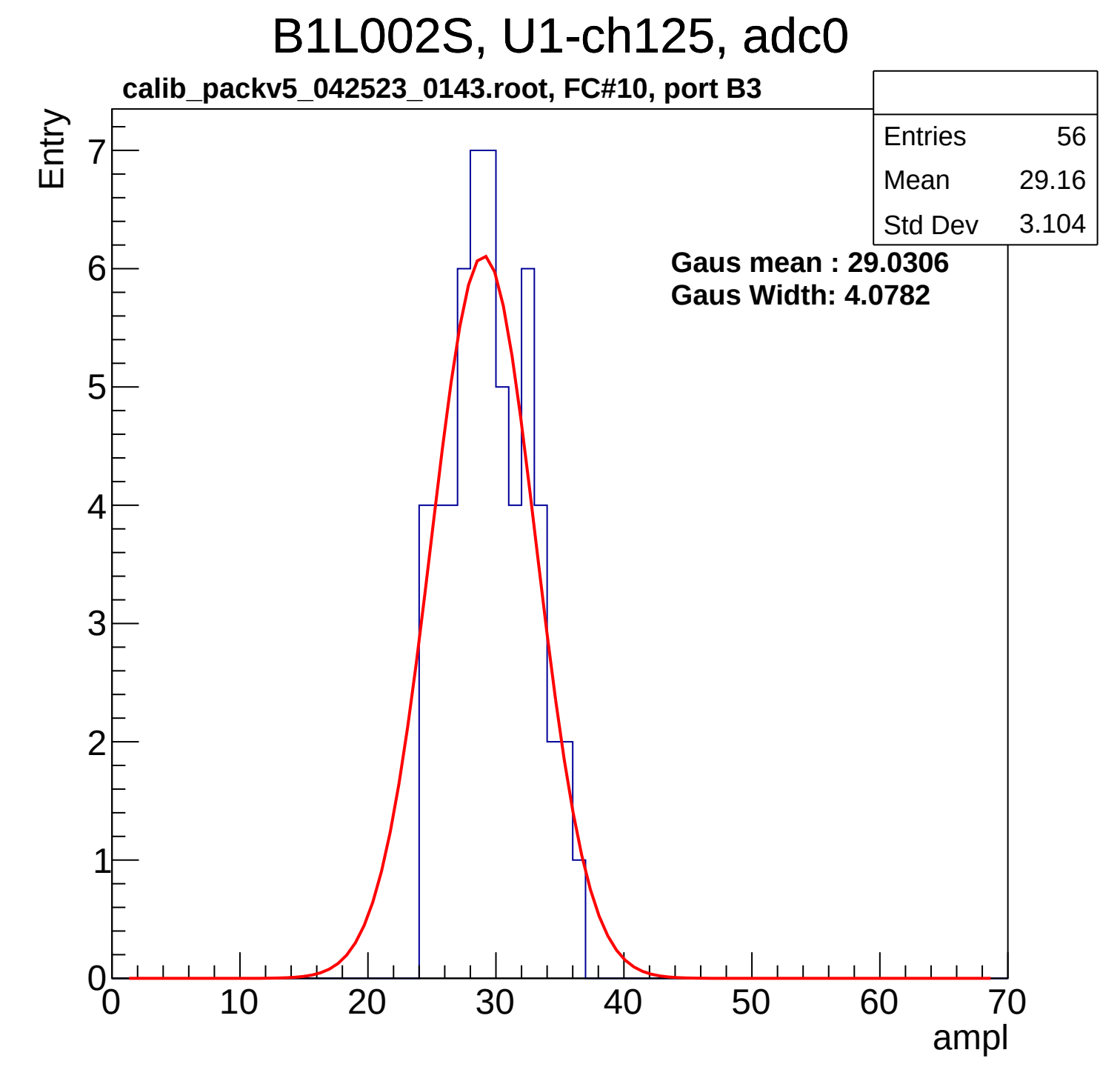
B1L002S, U1-ch125, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 29.16
Std Dev 3.104
Gaus mean : 29.0306
Gaus Width: 4.0782
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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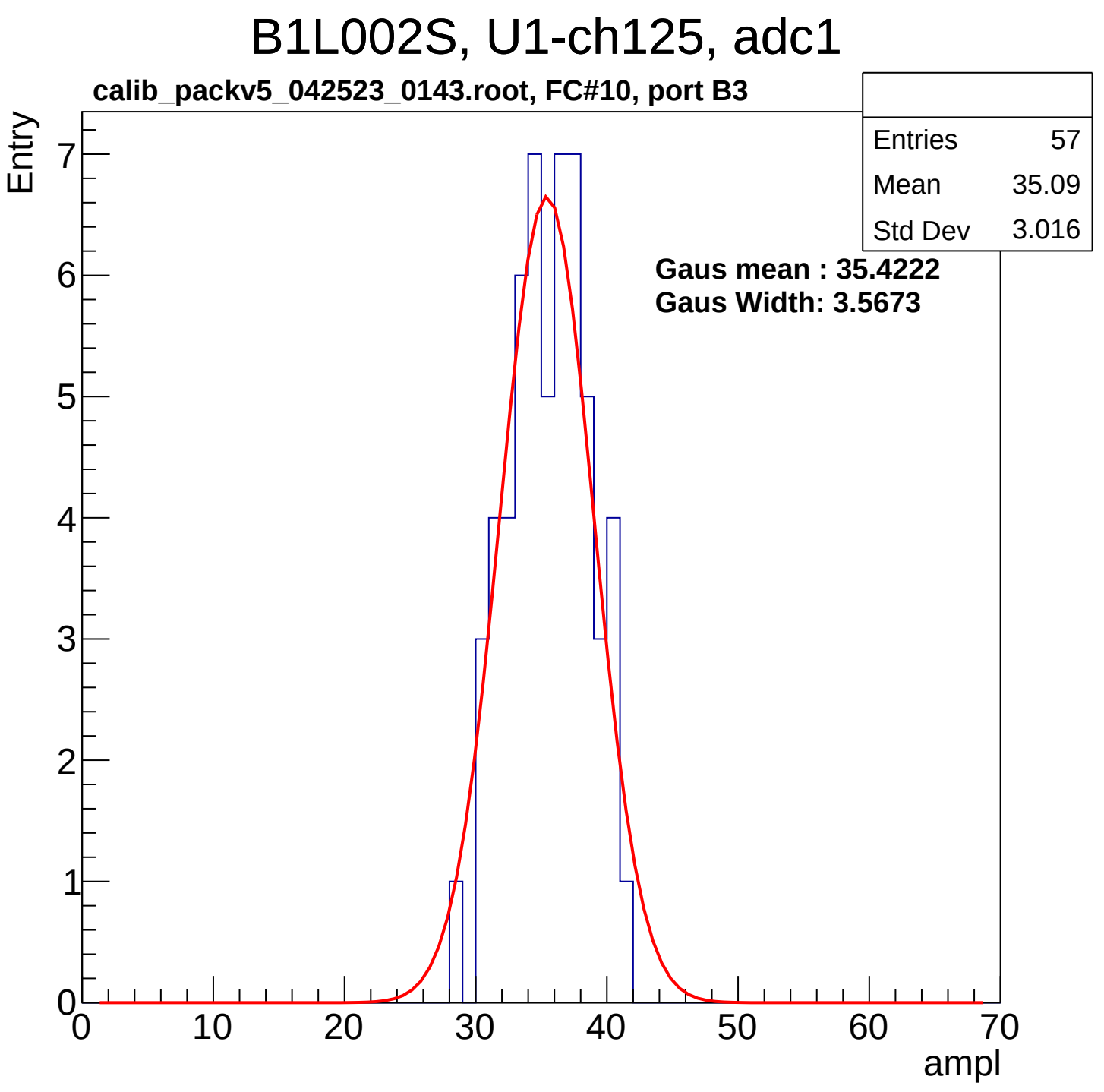
B1L002S, U1-ch125, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.09
Std Dev 3.016
Gaus mean : 35.4222
Gaus Width: 3.5673
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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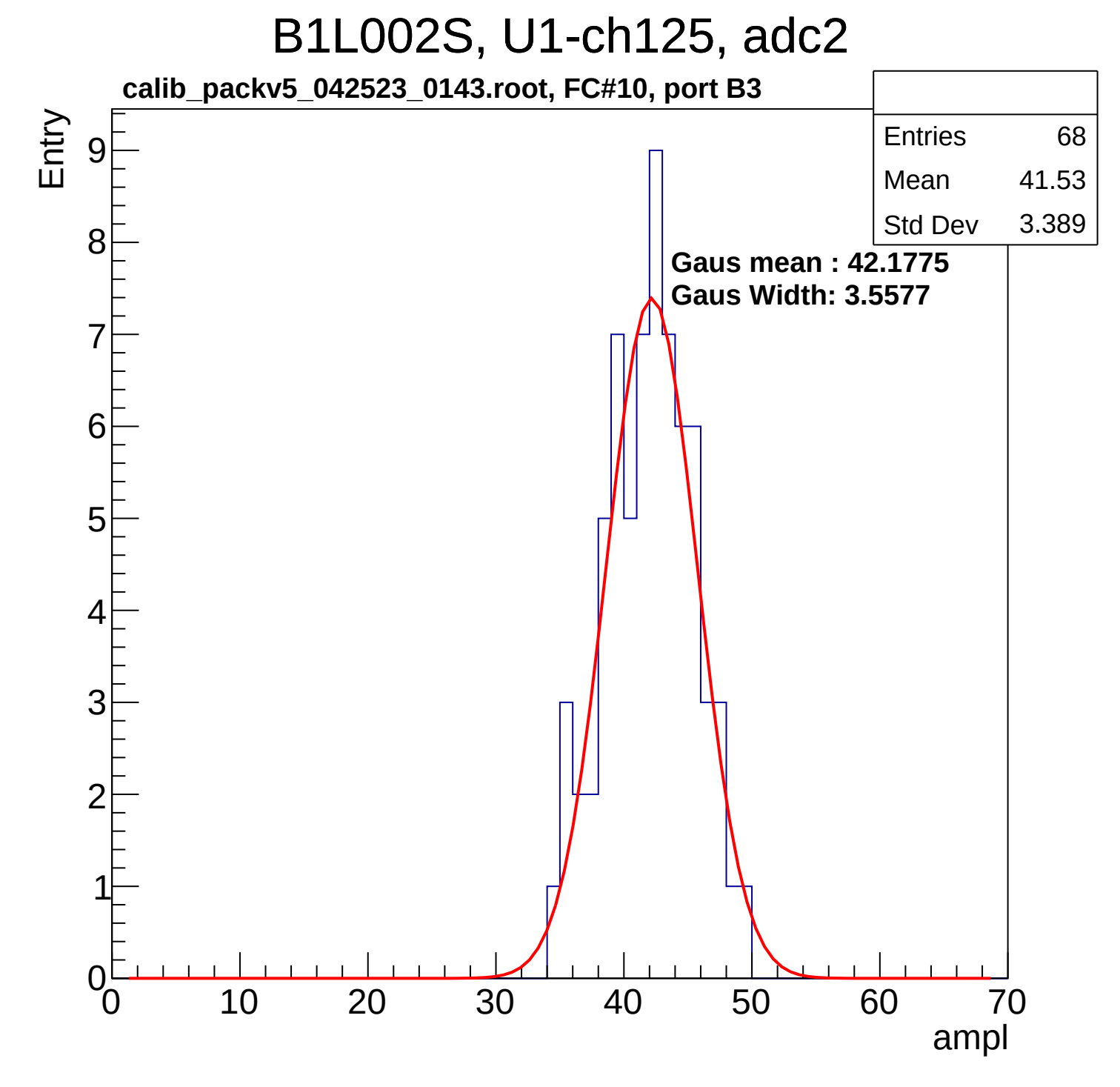
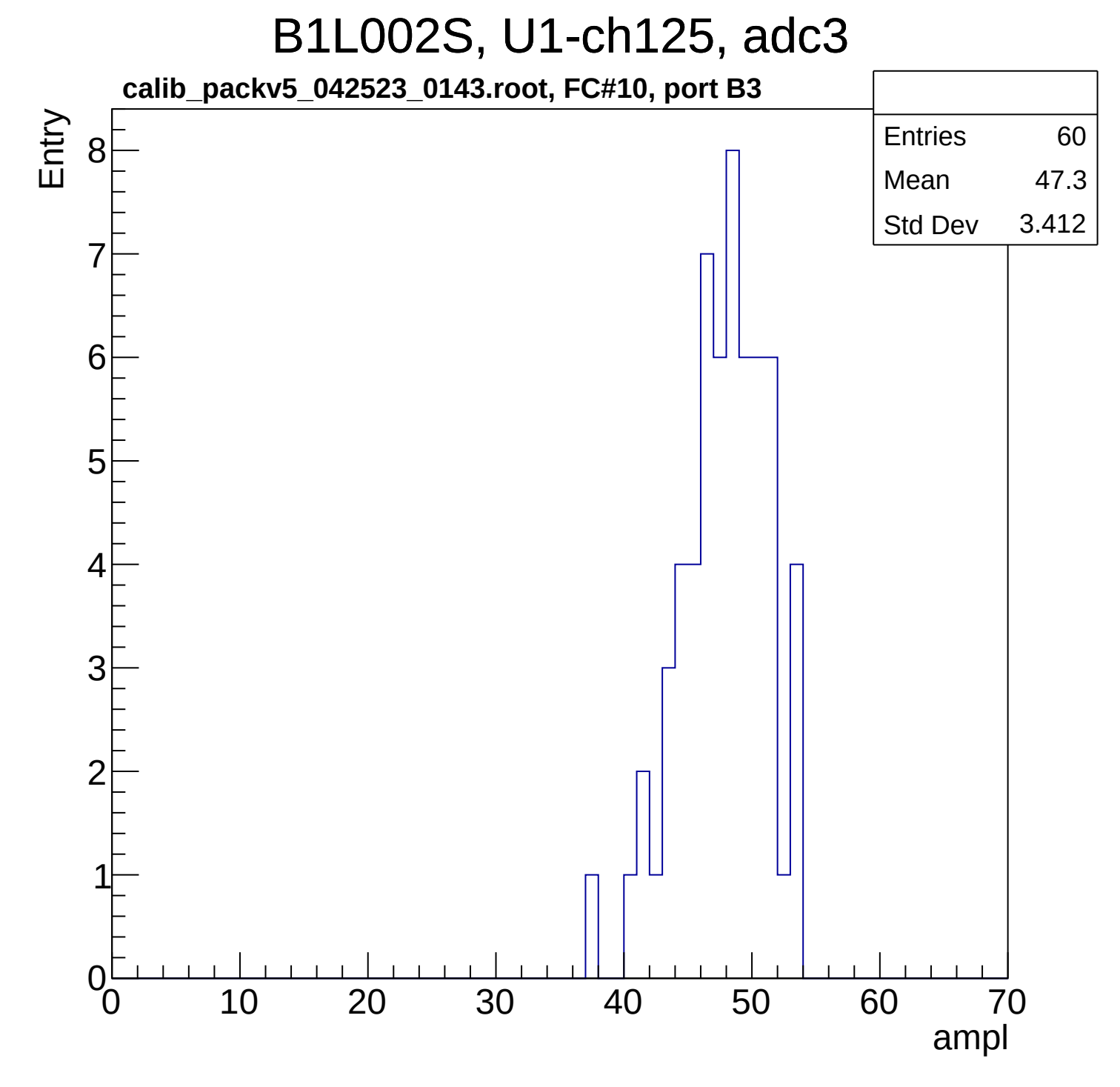

B1L002S, U1-ch125, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 41.53
Std Dev 3.389
Gaus mean : 42.1775
Gaus Width: 3.5577
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch125, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.3
Std Dev 3.412
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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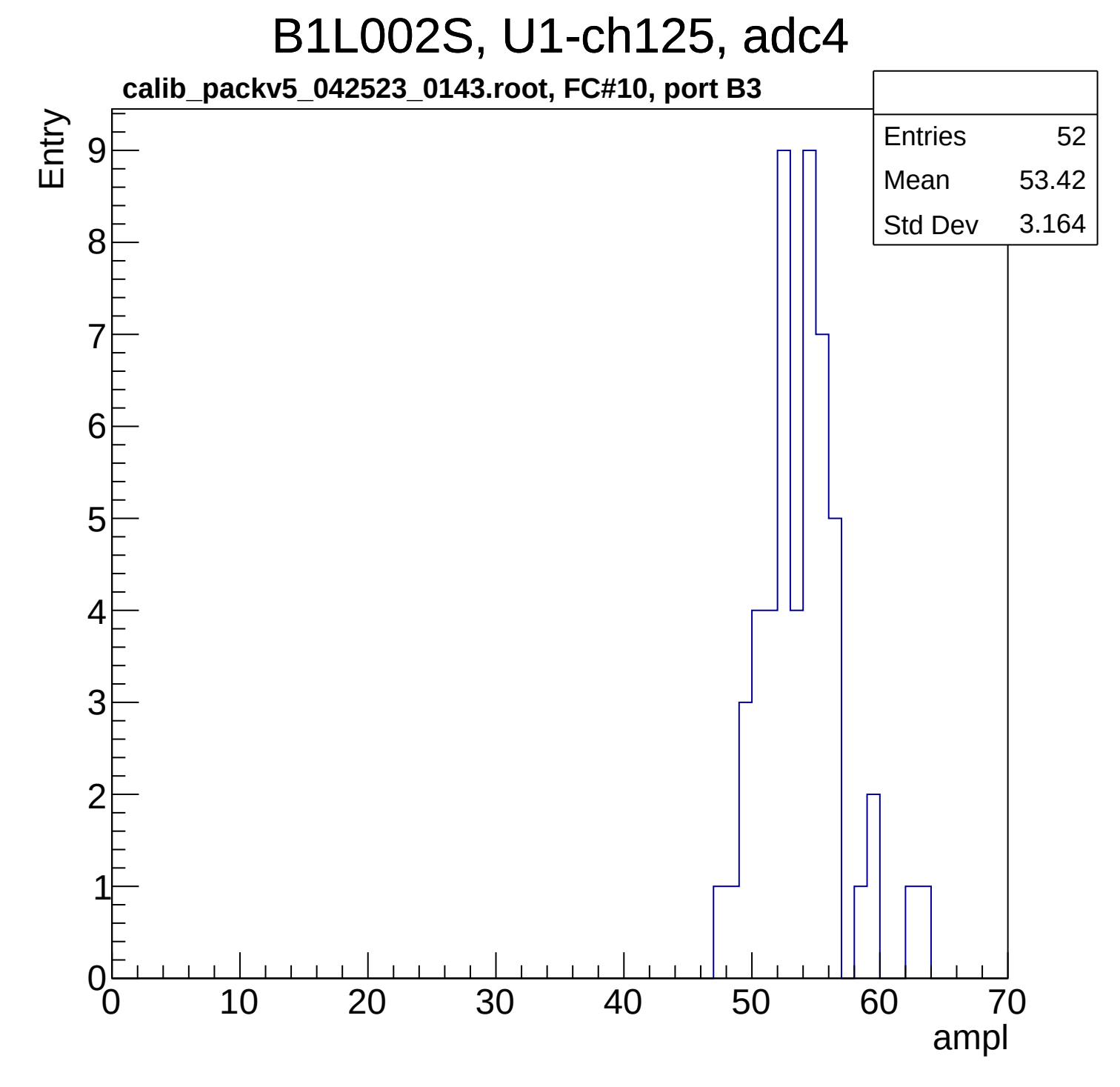
B1L002S, U1-ch125, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.42
Std Dev 3.164
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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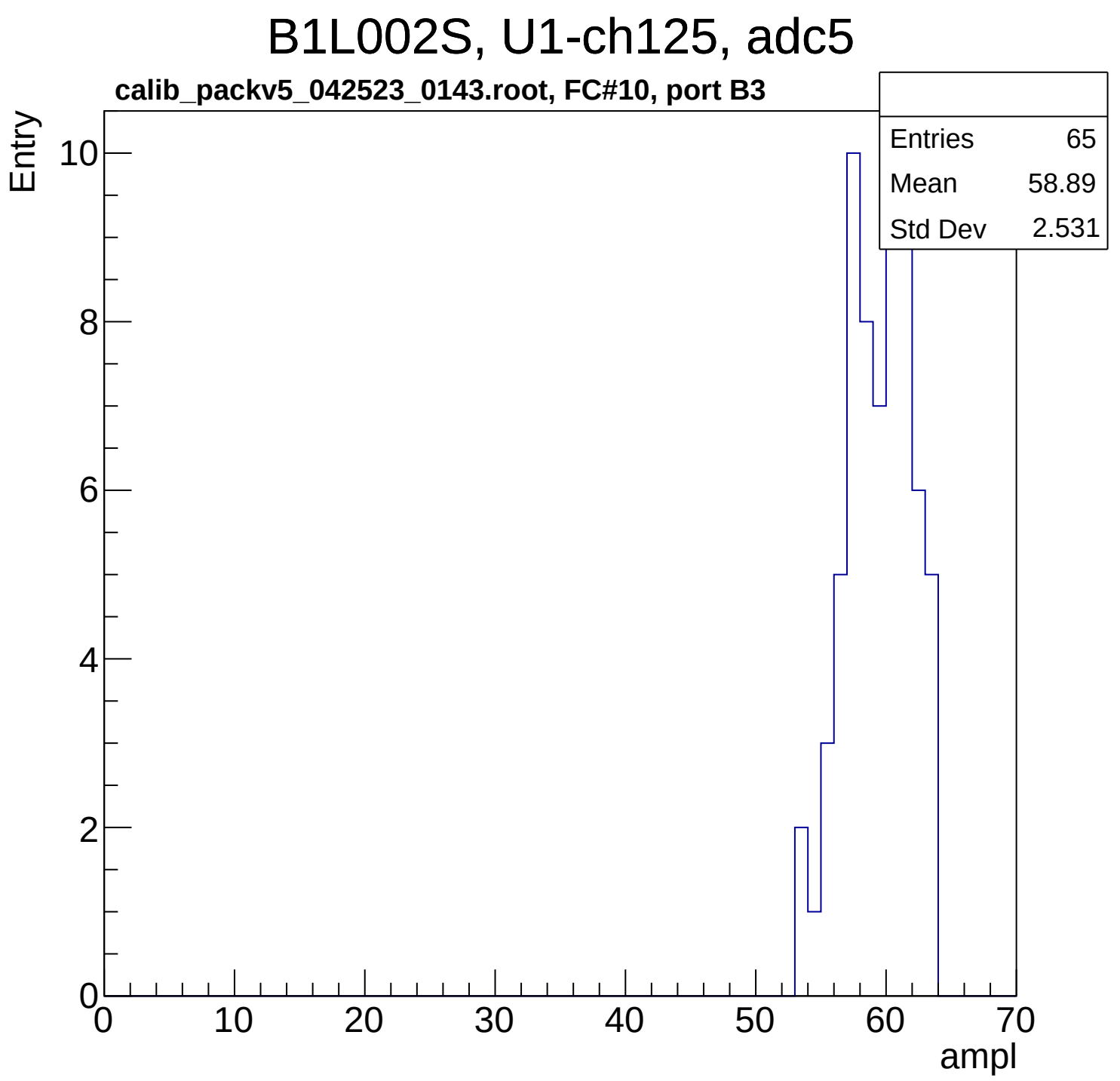
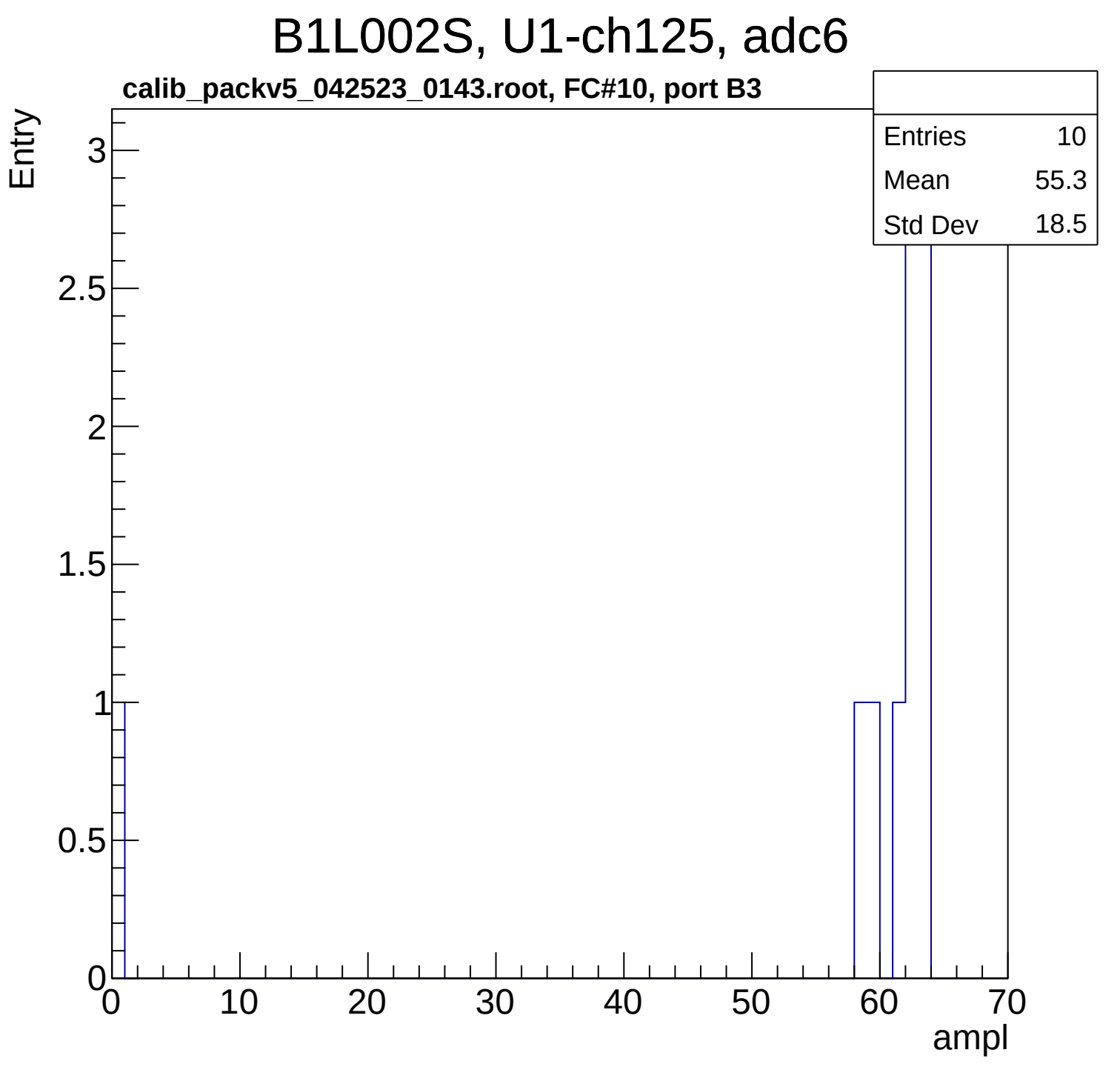

B1L002S, U1-ch125, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.89
Std Dev 2.531
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch125, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 55.3
Std Dev 18.5
Entry
ampl
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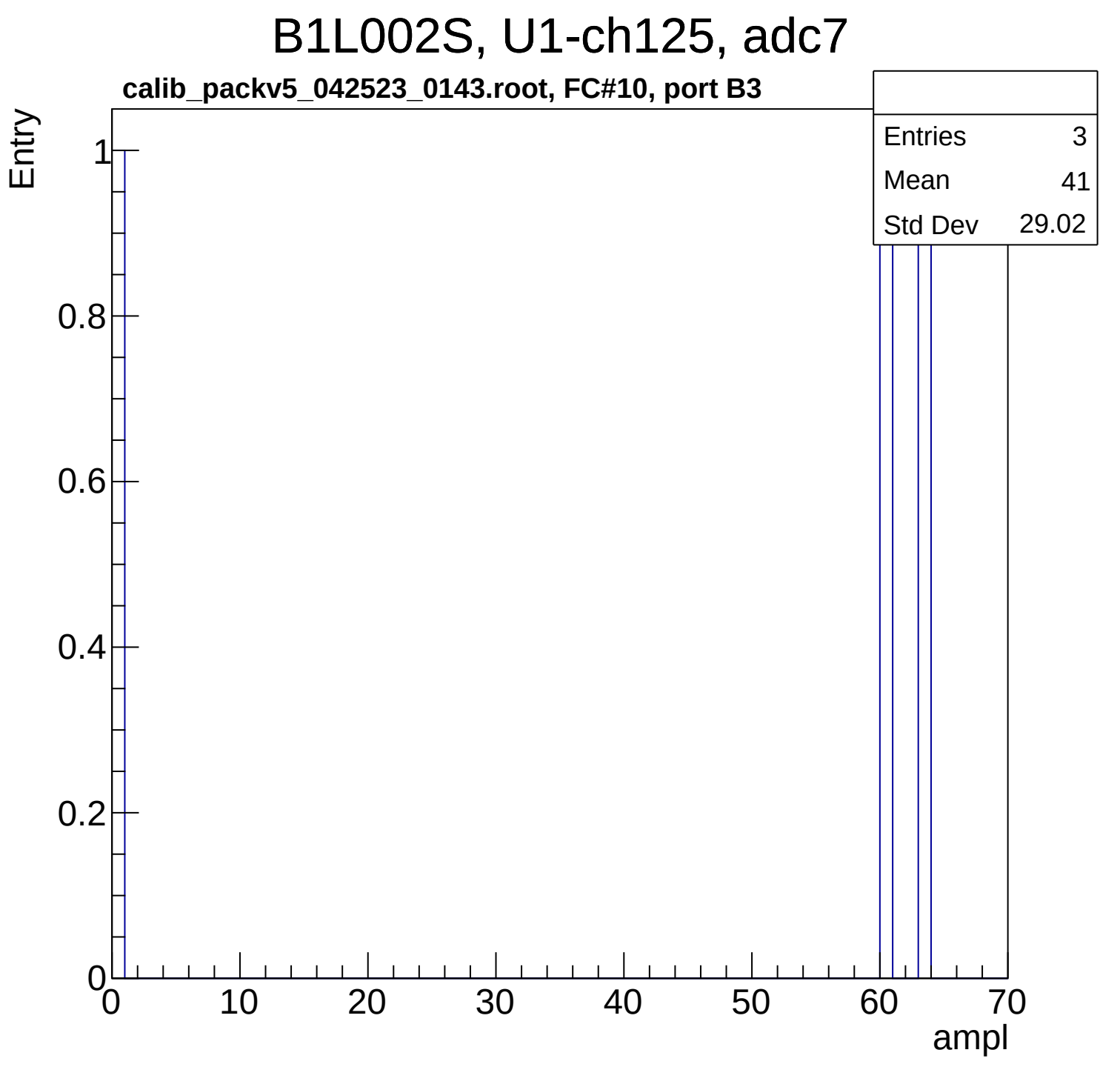
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U1-ch125, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41
Std Dev 29.02
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
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60
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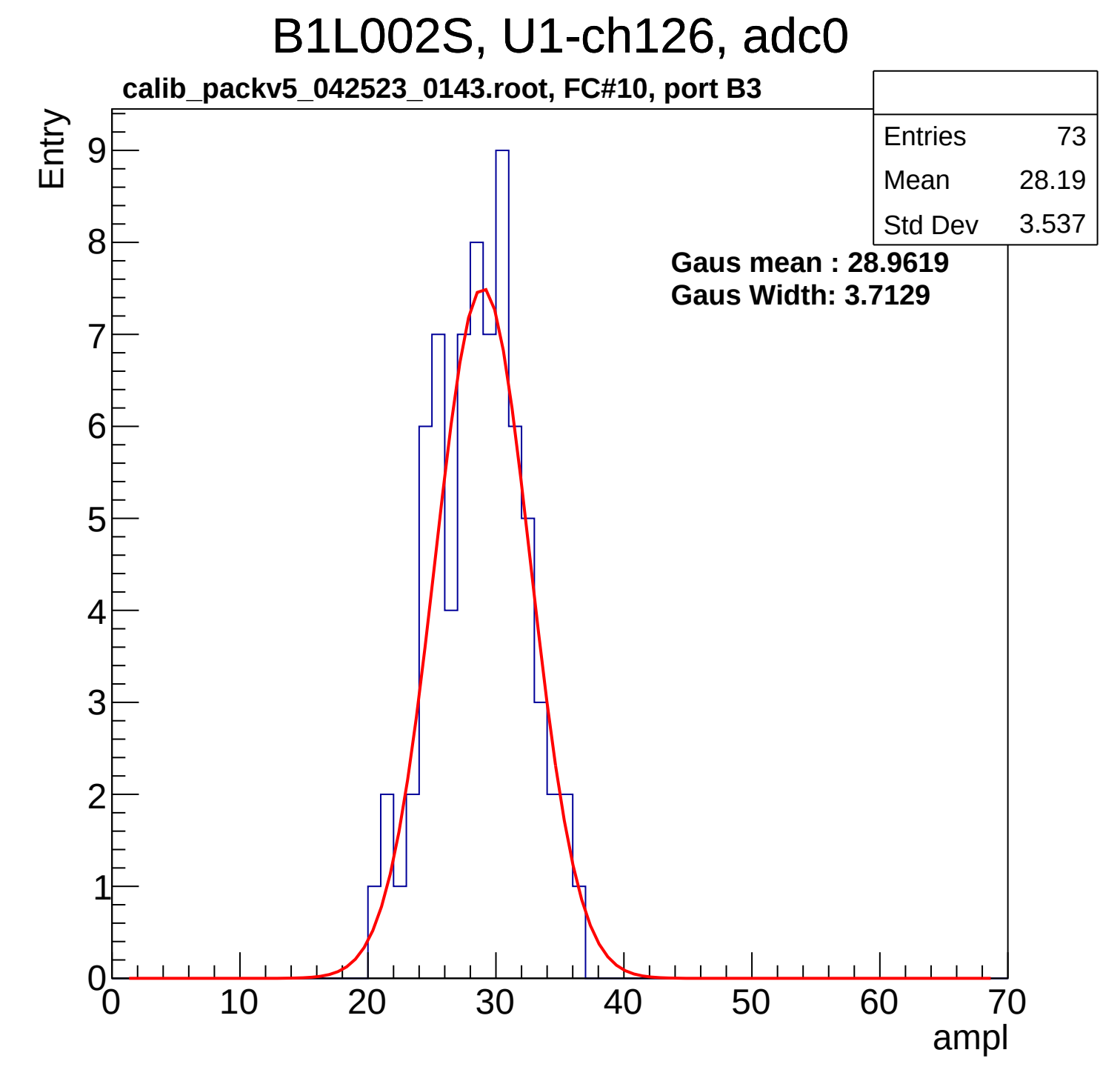
B1L002S, U1-ch126, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.19
Std Dev 3.537
Gaus mean : 28.9619
Gaus Width: 3.7129
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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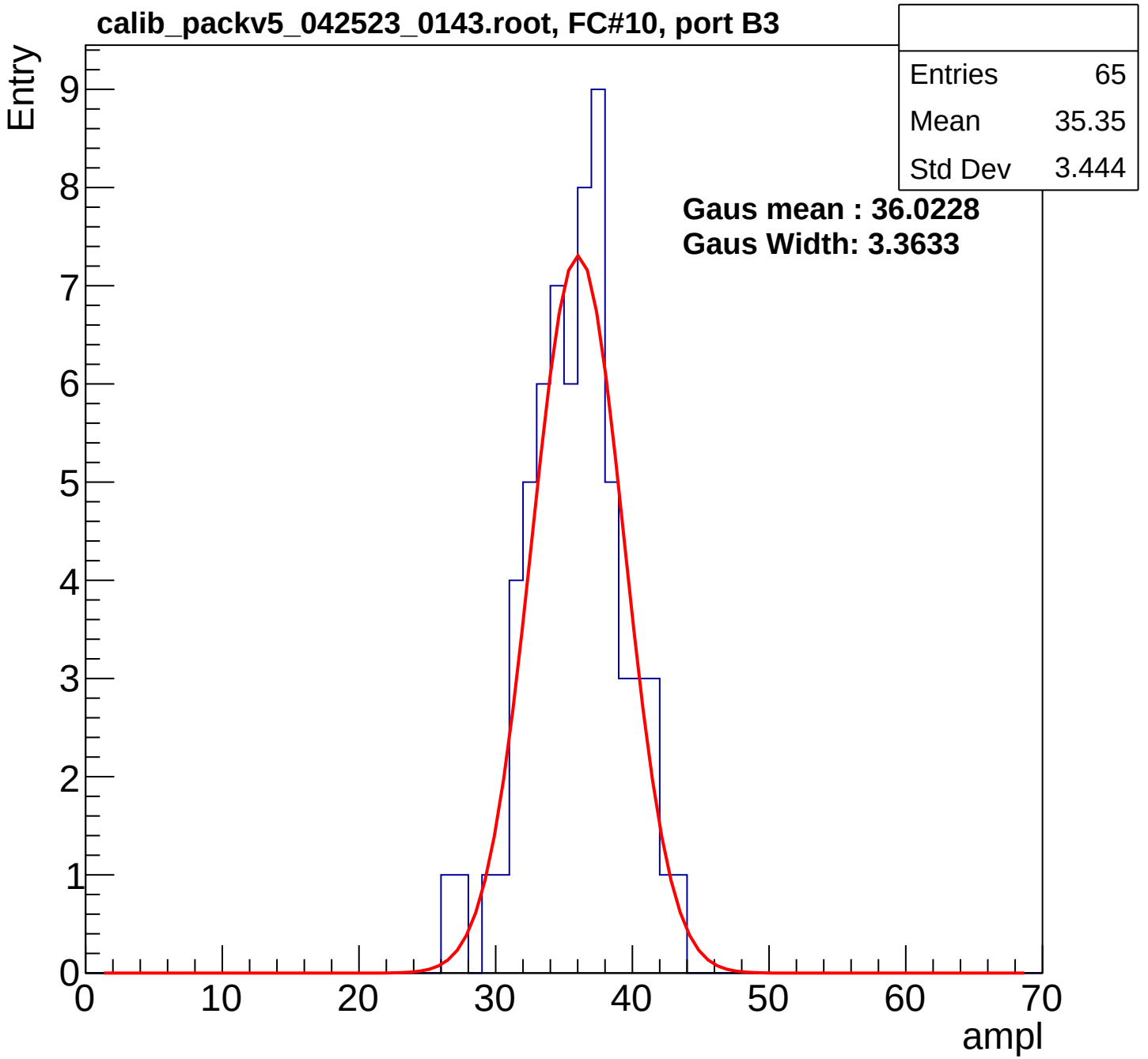
B1L002S, U1-ch126, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 35.35
Std Dev 3.444
Gaus mean : 36.0228
Gaus Width: 3.3633
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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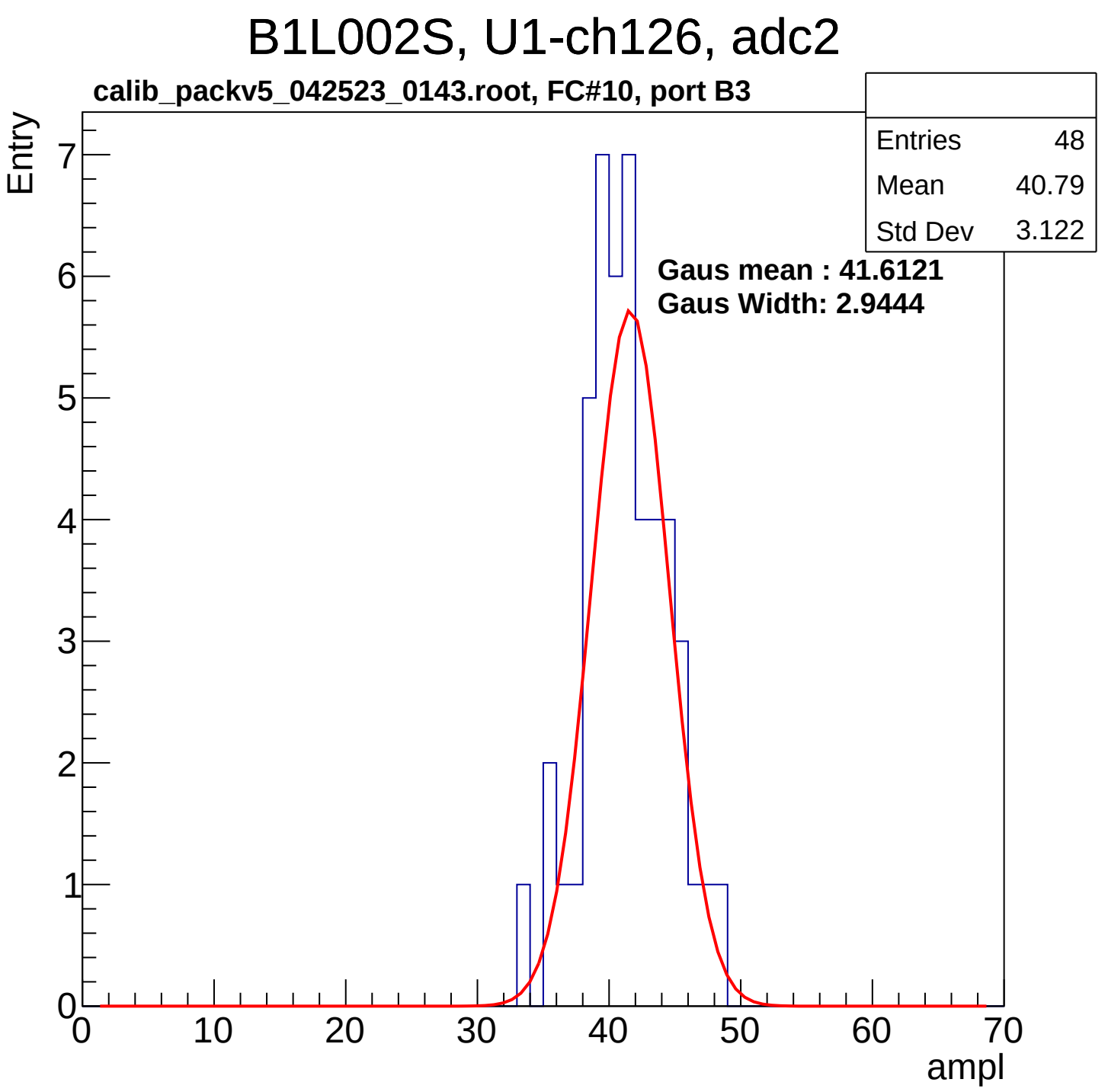
B1L002S, U1-ch126, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 40.79
Std Dev 3.122
Gaus mean : 41.6121
Gaus Width: 2.9444
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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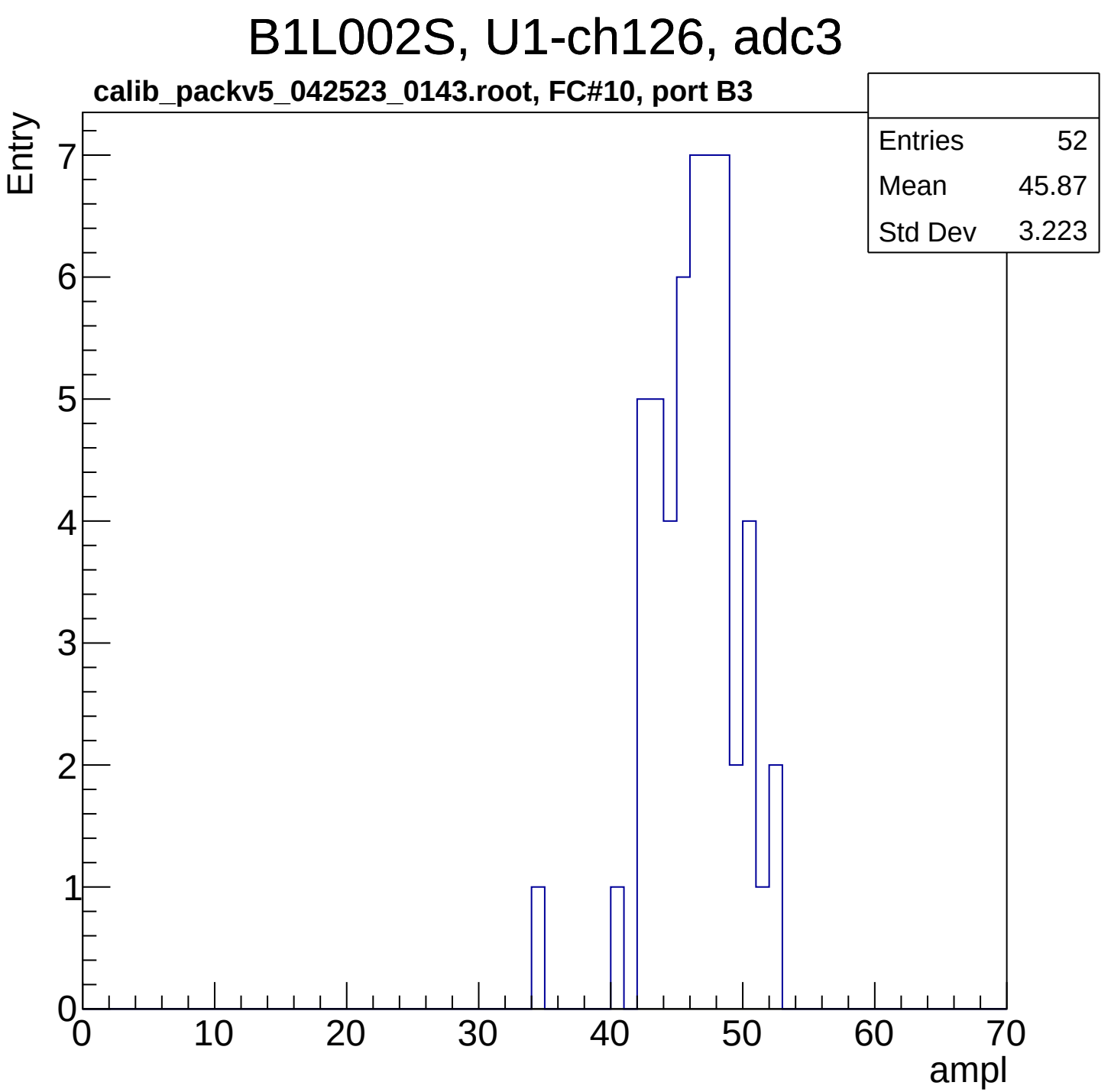
B1L002S, U1-ch126, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 45.87
Std Dev 3.223
Entry
ampl
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4
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0
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60
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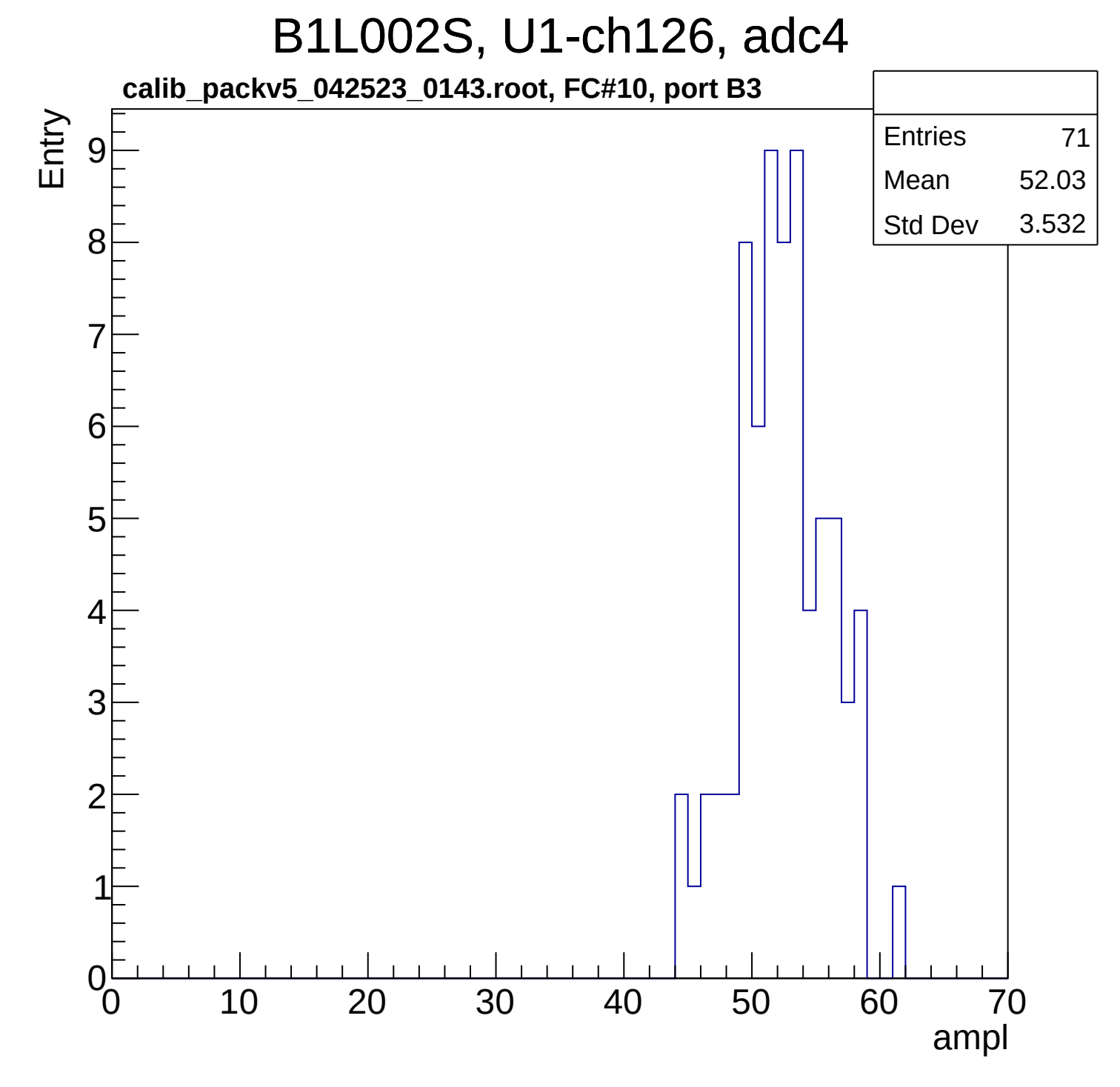
B1L002S, U1-ch126, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 52.03
Std Dev 3.532
Entry
ampl
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4
5
6
7
8
9
0
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40
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60
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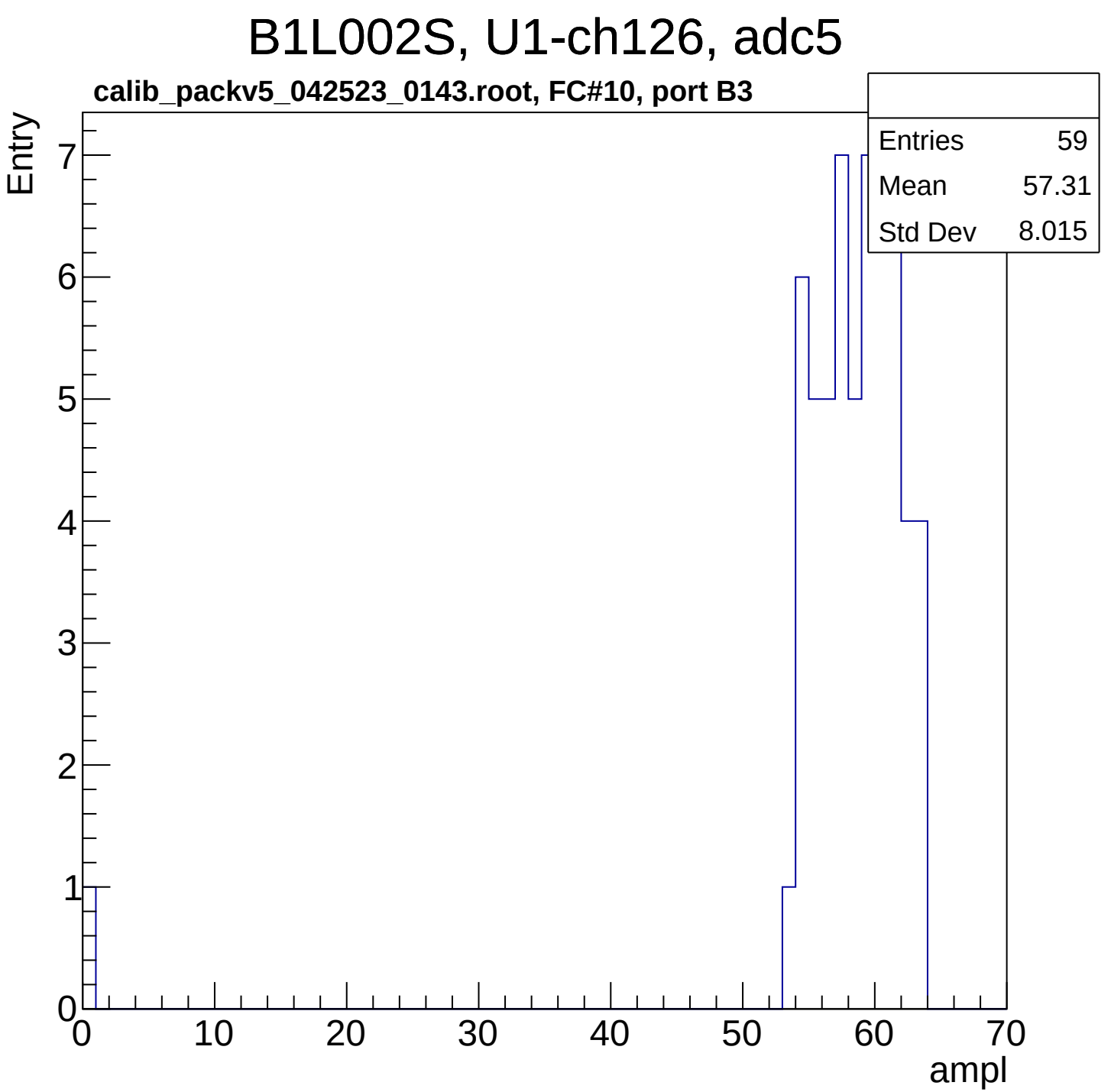
B1L002S, U1-ch126, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.31
Std Dev 8.015
Entry
ampl
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2
3
4
5
6
7
0
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20
30
40
50
60
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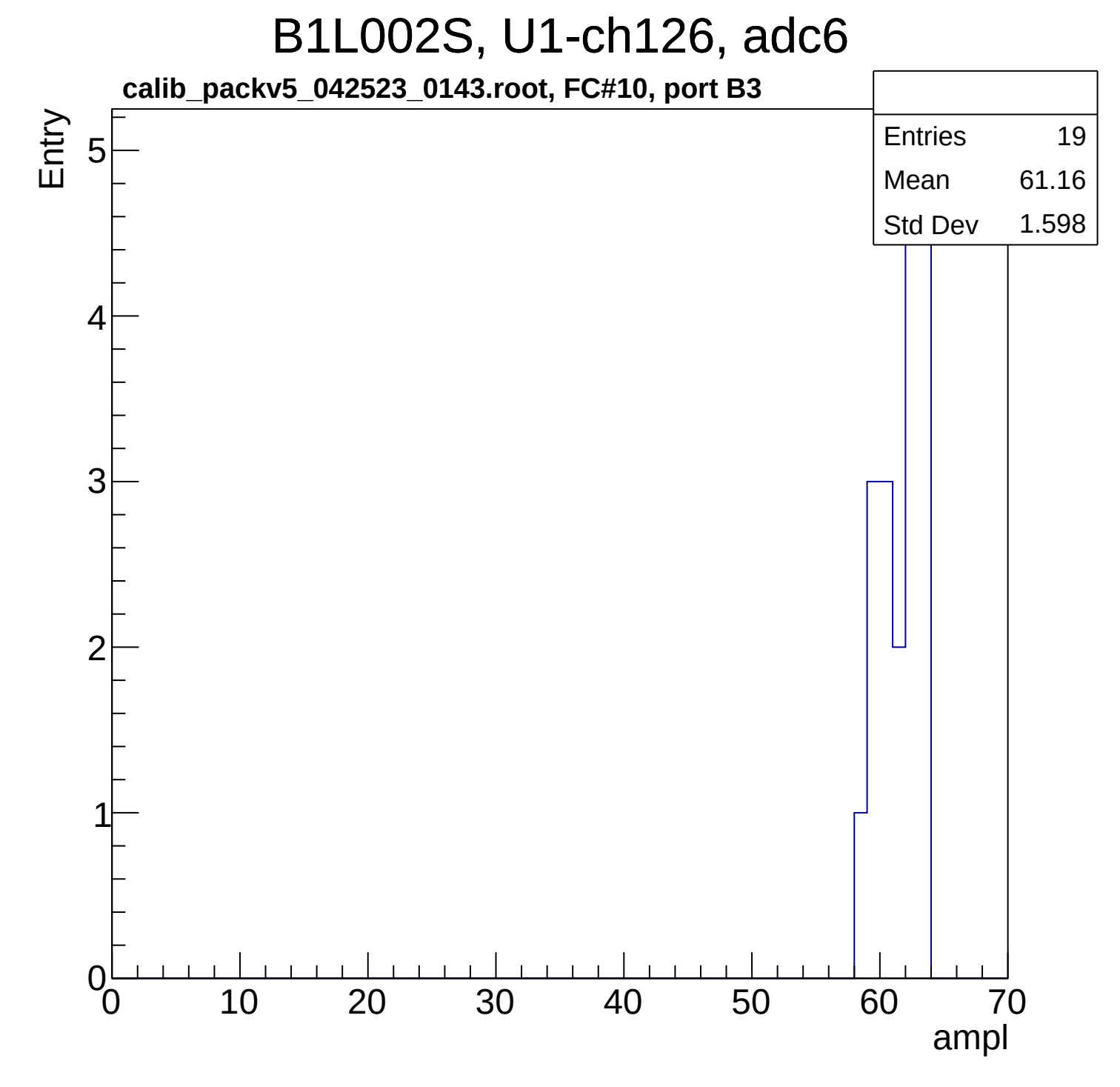
B1L002S, U1-ch126, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.16
Std Dev 1.598
Entry
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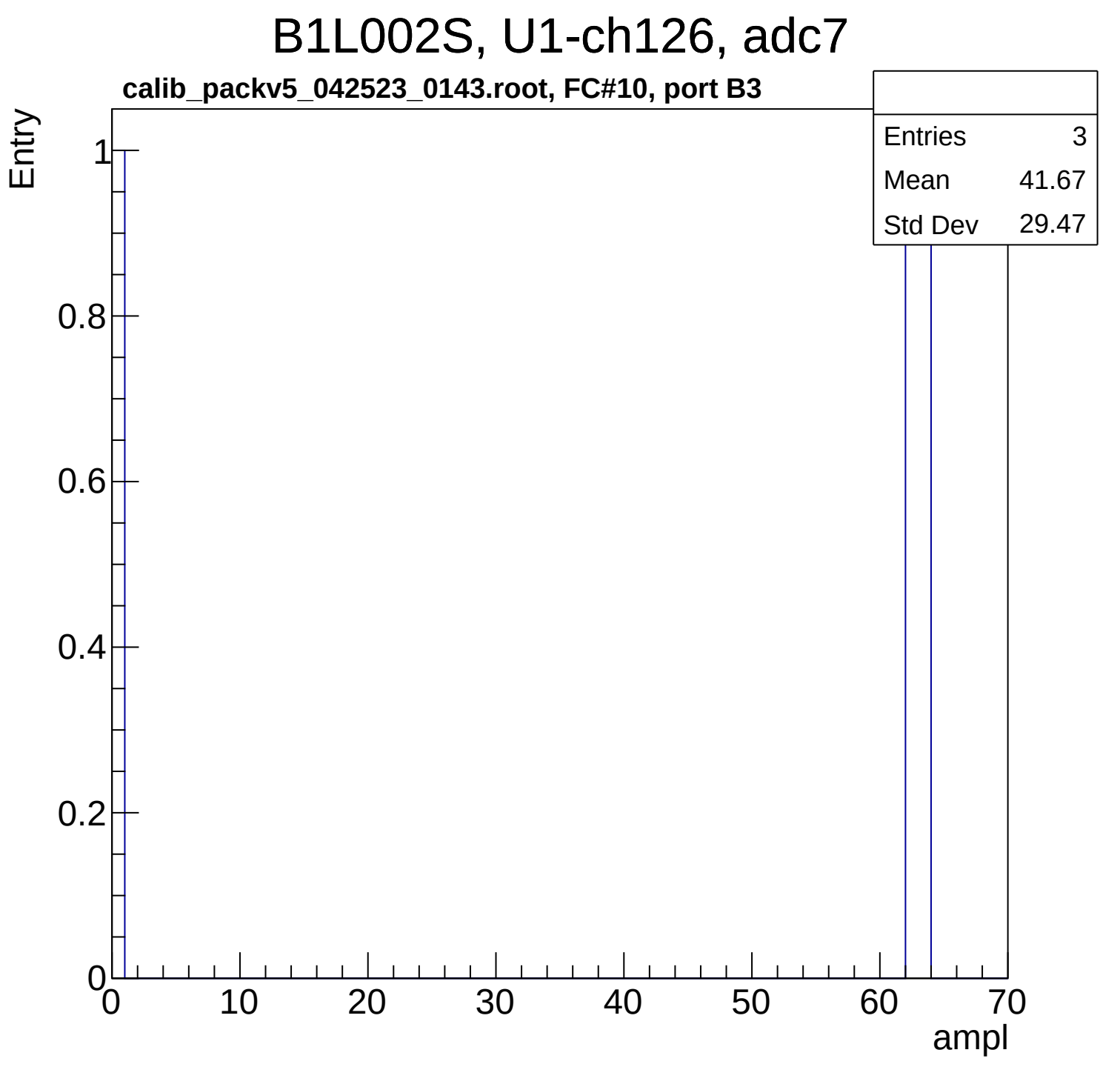
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch126, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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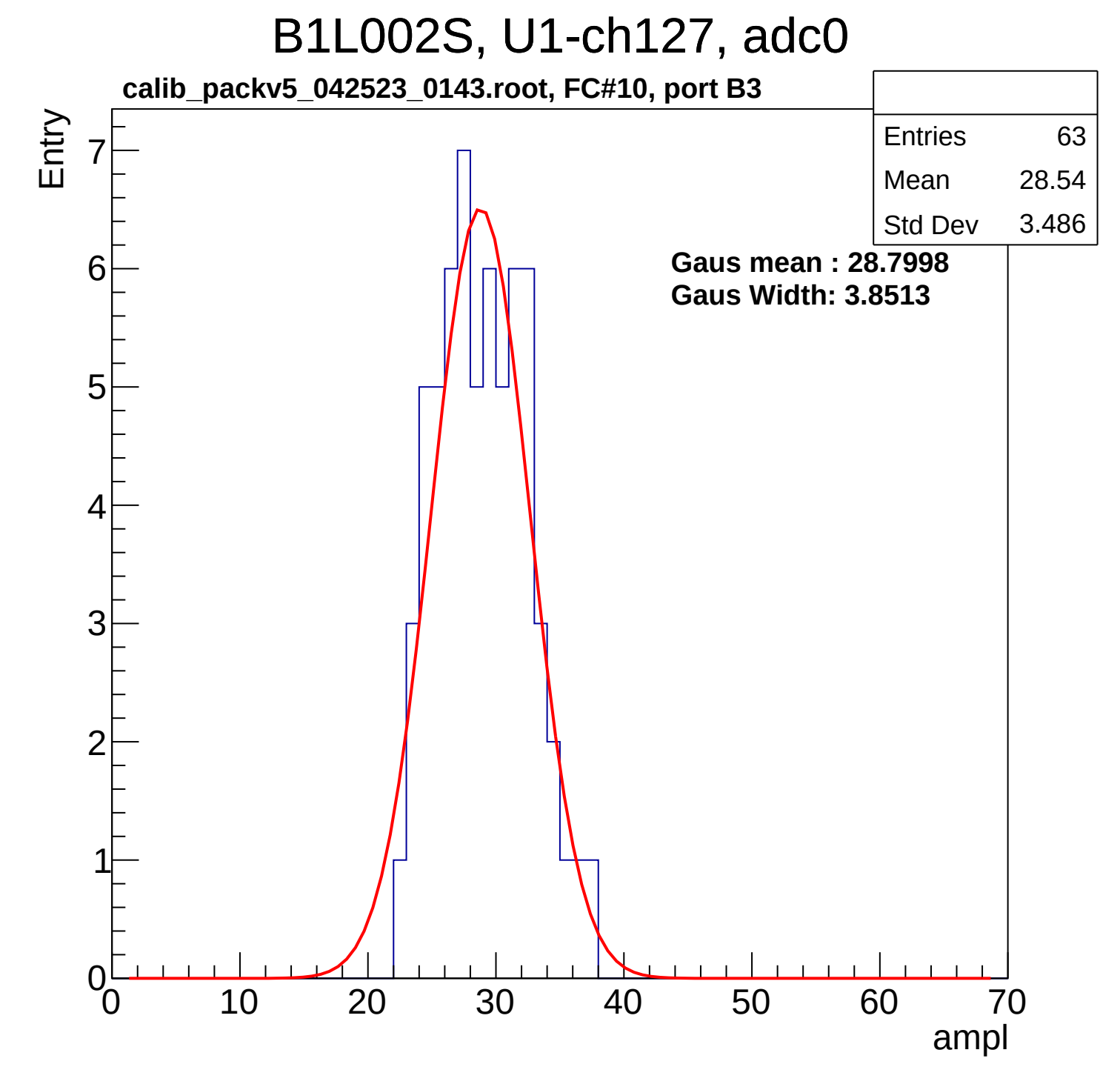
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U1-ch127, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.54
Std Dev 3.486
Gaus mean : 28.7998
Gaus Width: 3.8513
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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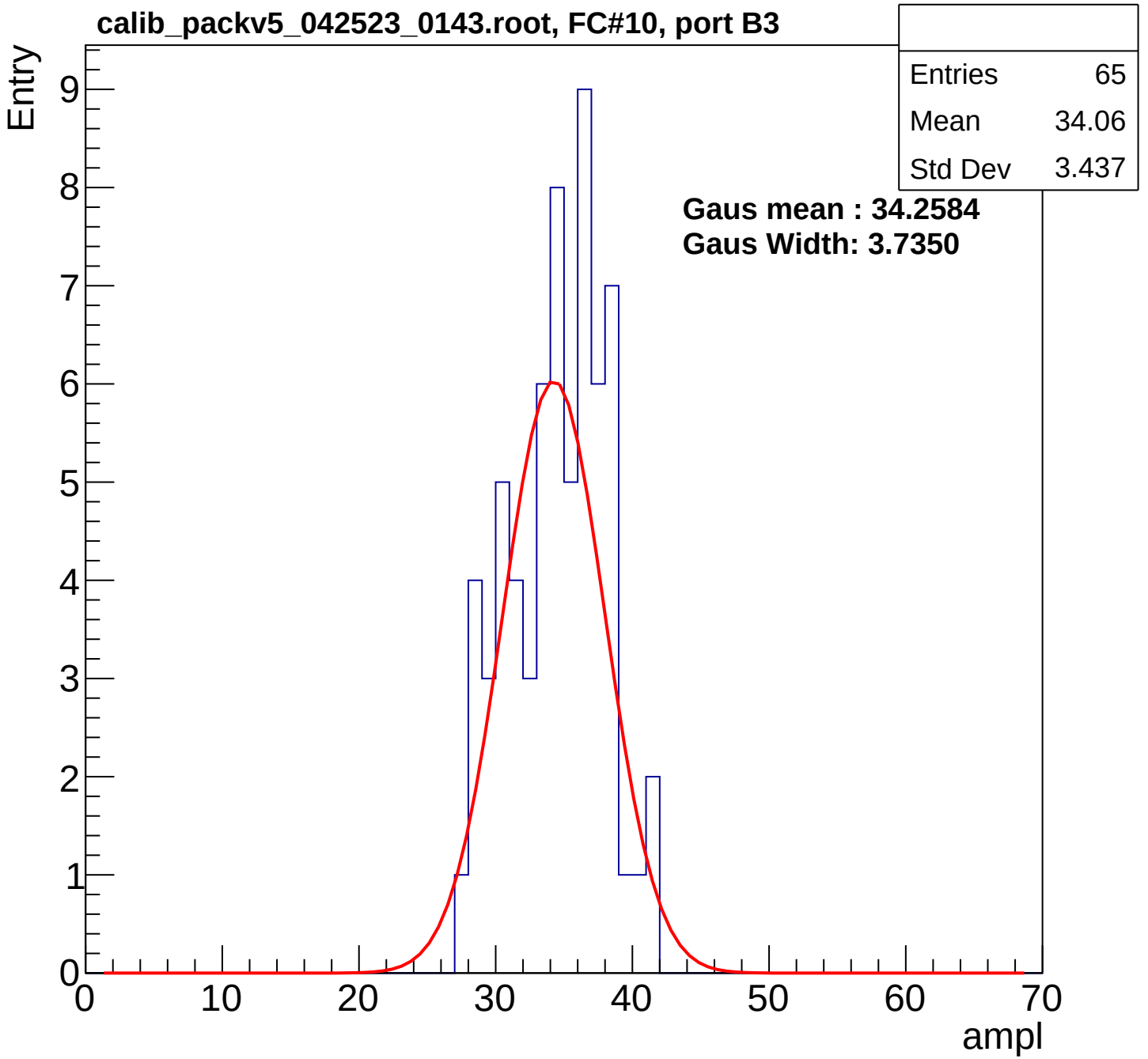
B1L002S, U1-ch127, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 34.06
Std Dev 3.437
Gaus mean : 34.2584
Gaus Width: 3.7350
Entry
ampl
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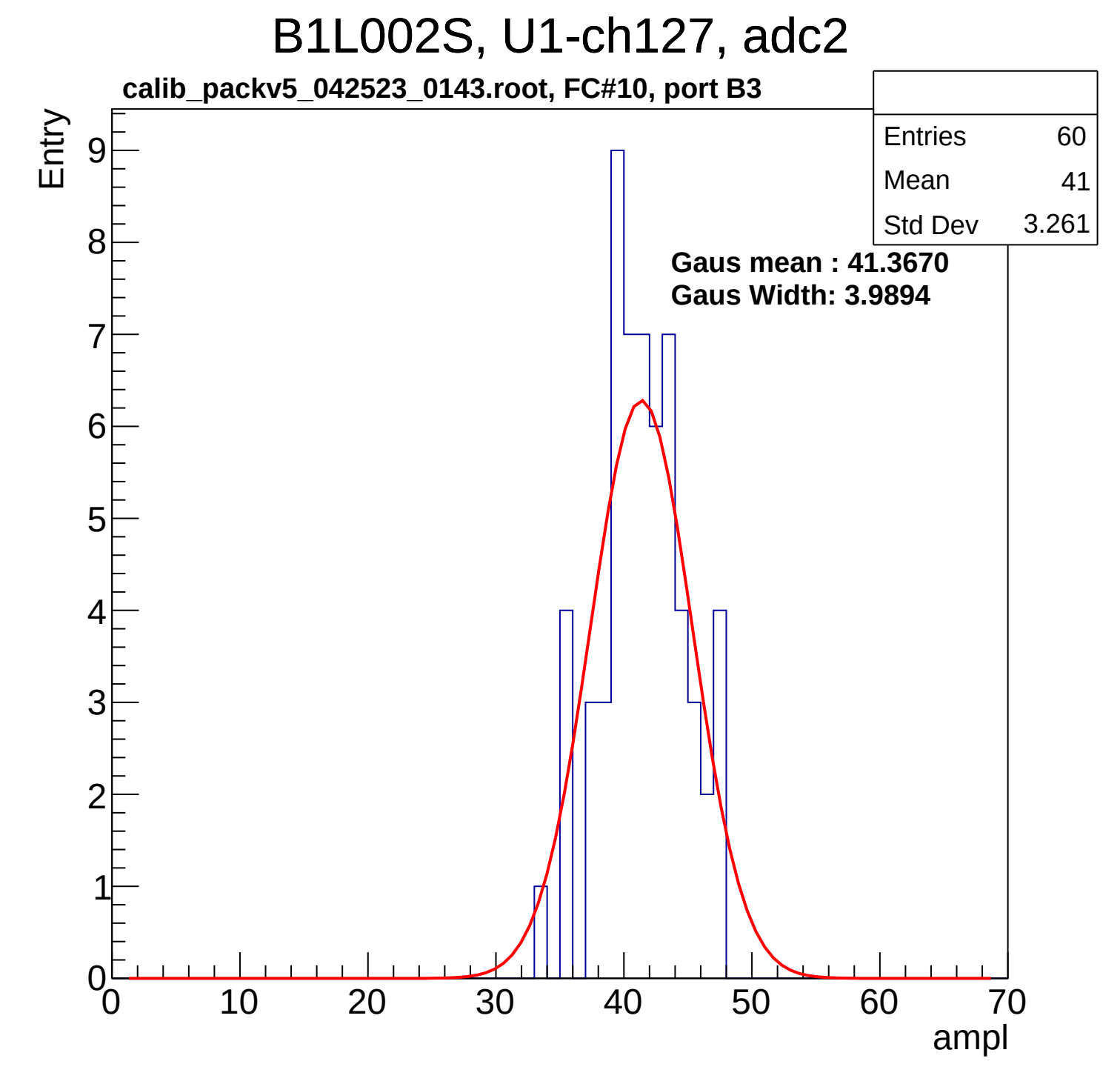
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U1-ch127, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41
Std Dev 3.261
Gaus mean : 41.3670
Gaus Width: 3.9894
Entry
ampl
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2
3
4
5
6
7
8
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0
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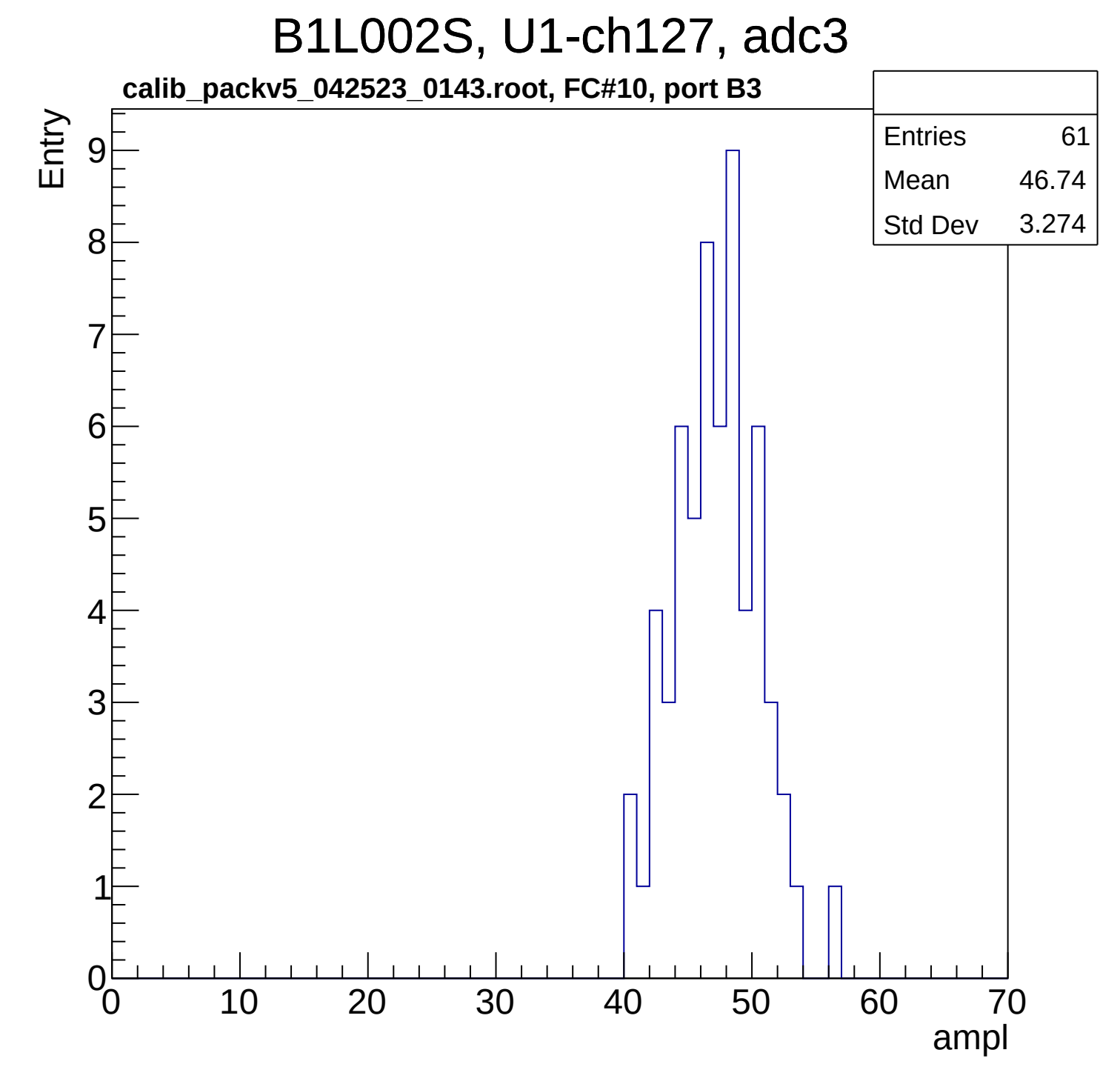
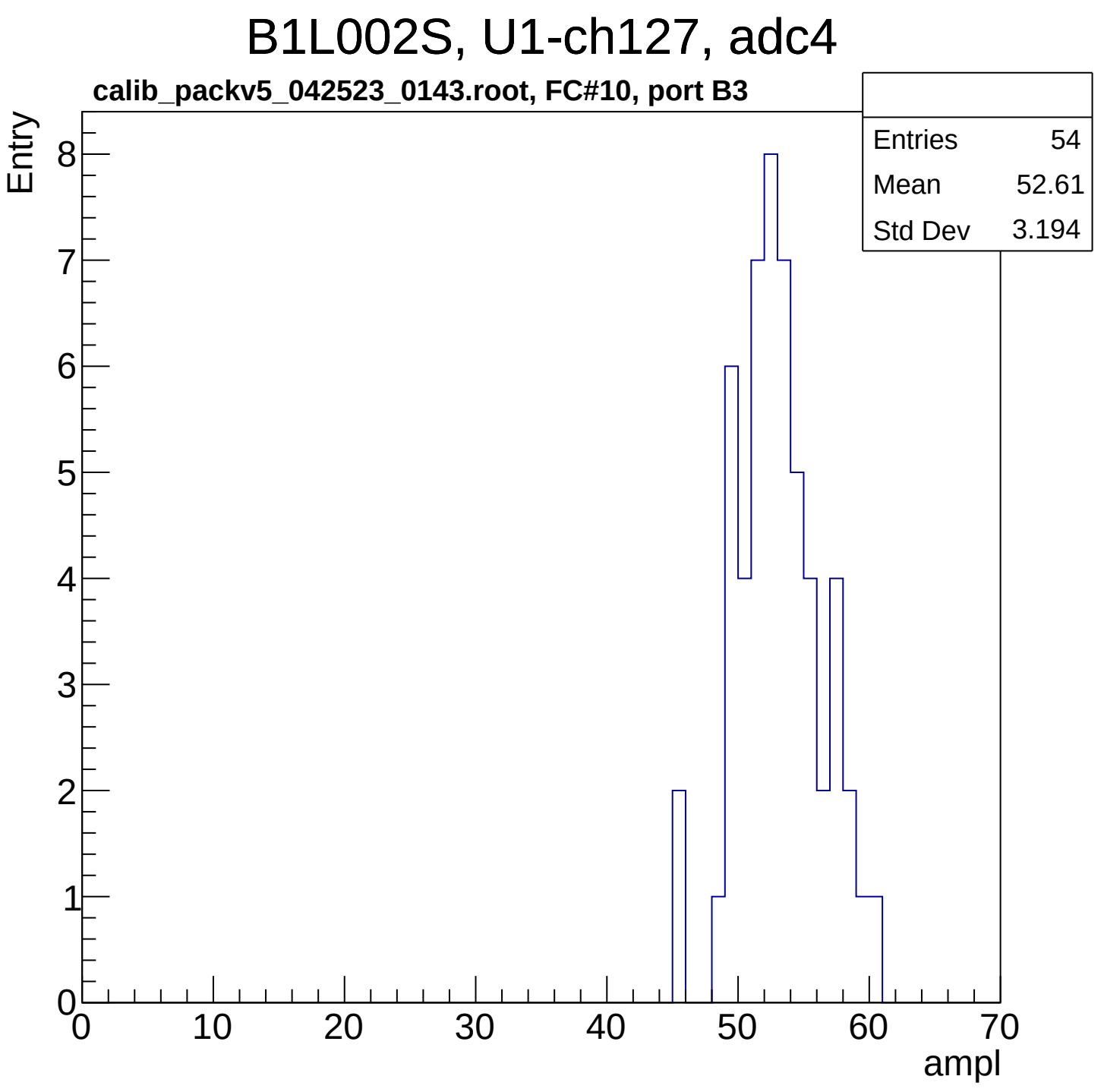
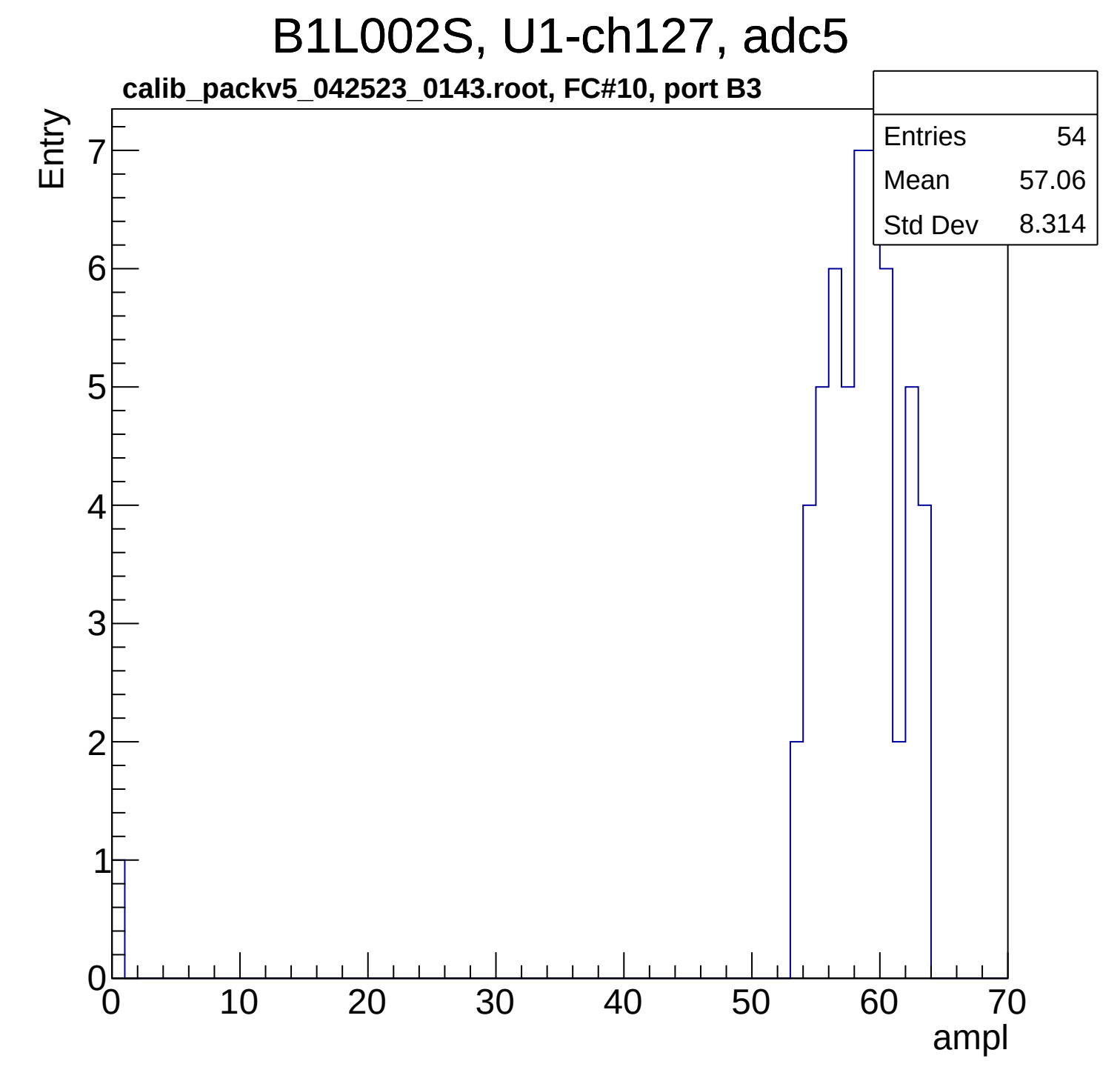
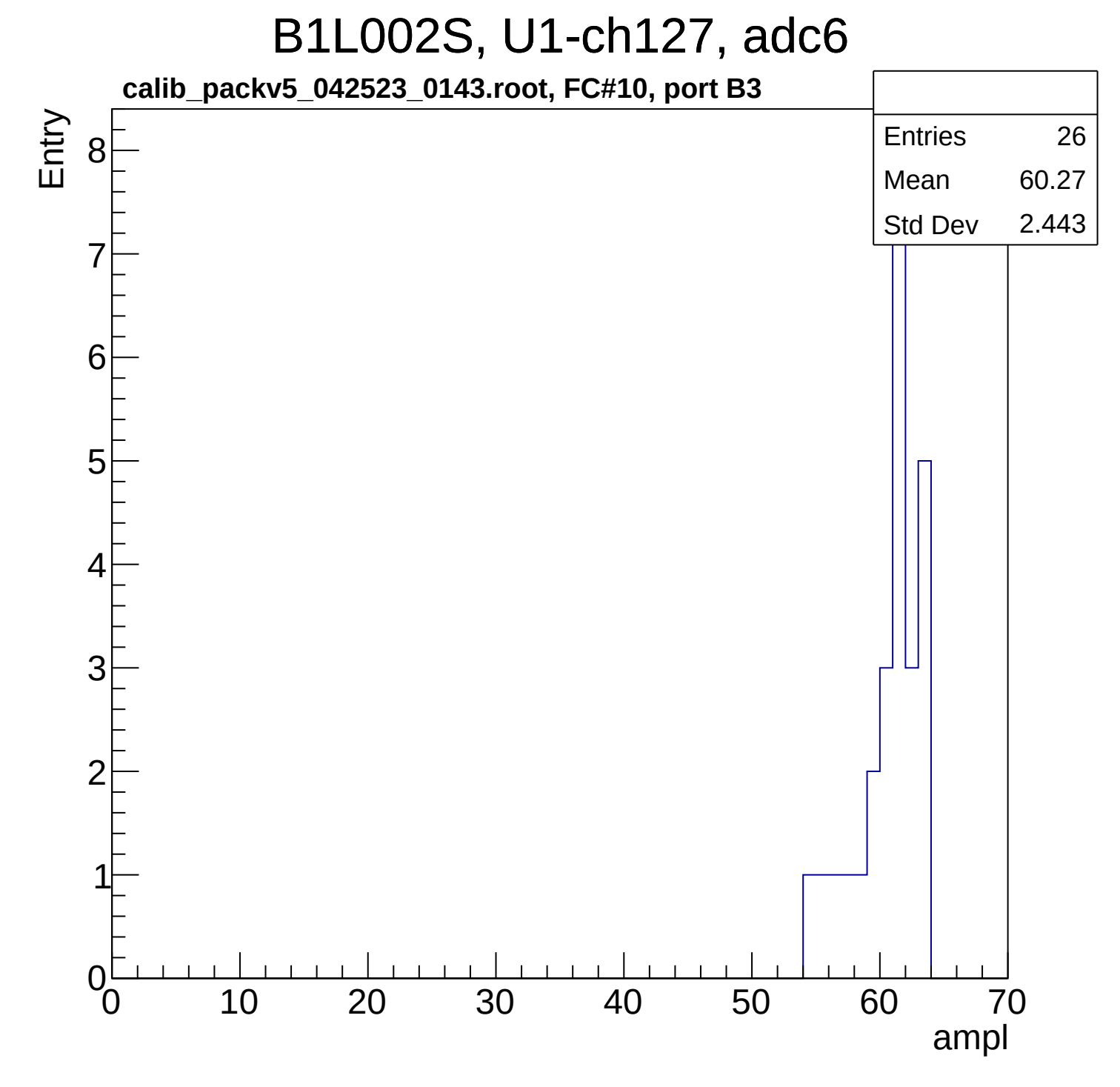
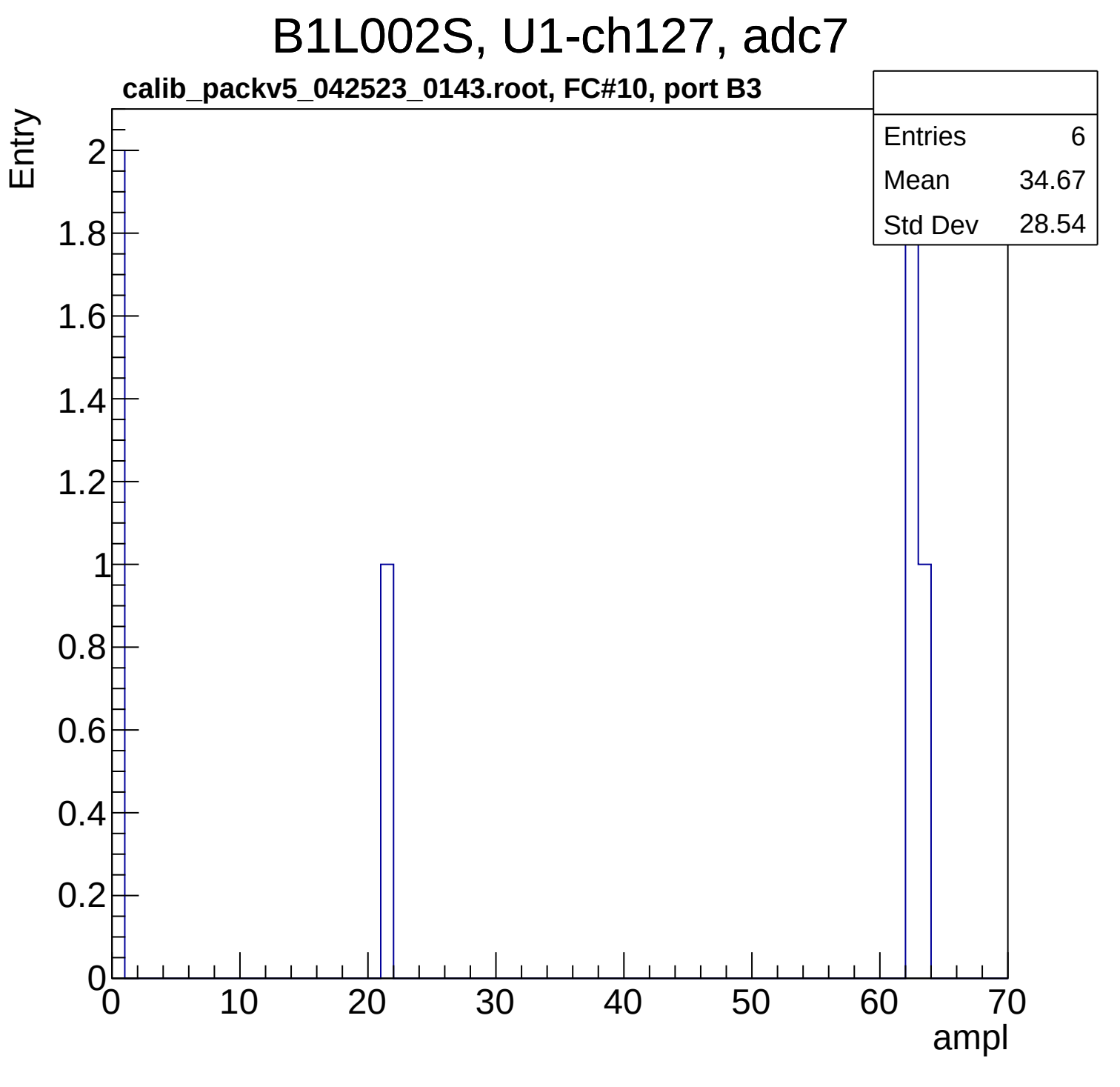
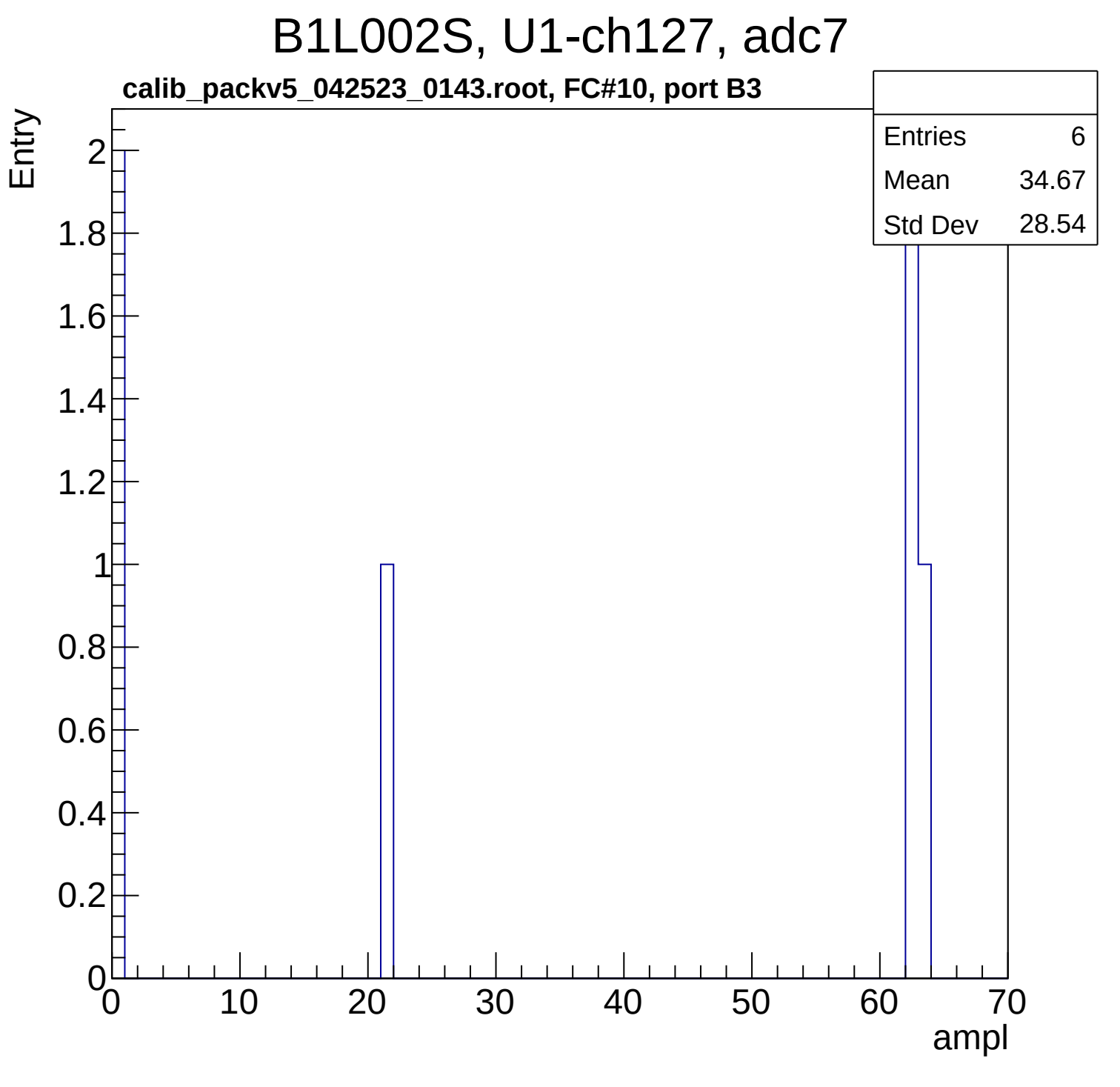

B1L002S, U1-ch127, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.74
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U1-ch127, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 52.61
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch127, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.06
Std Dev 8.314
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U1-ch127, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 60.27
Std Dev 2.443
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 34.67
Std Dev 28.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 34.67
Std Dev 28.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70