

B1L102S, U22-ch0, adc0

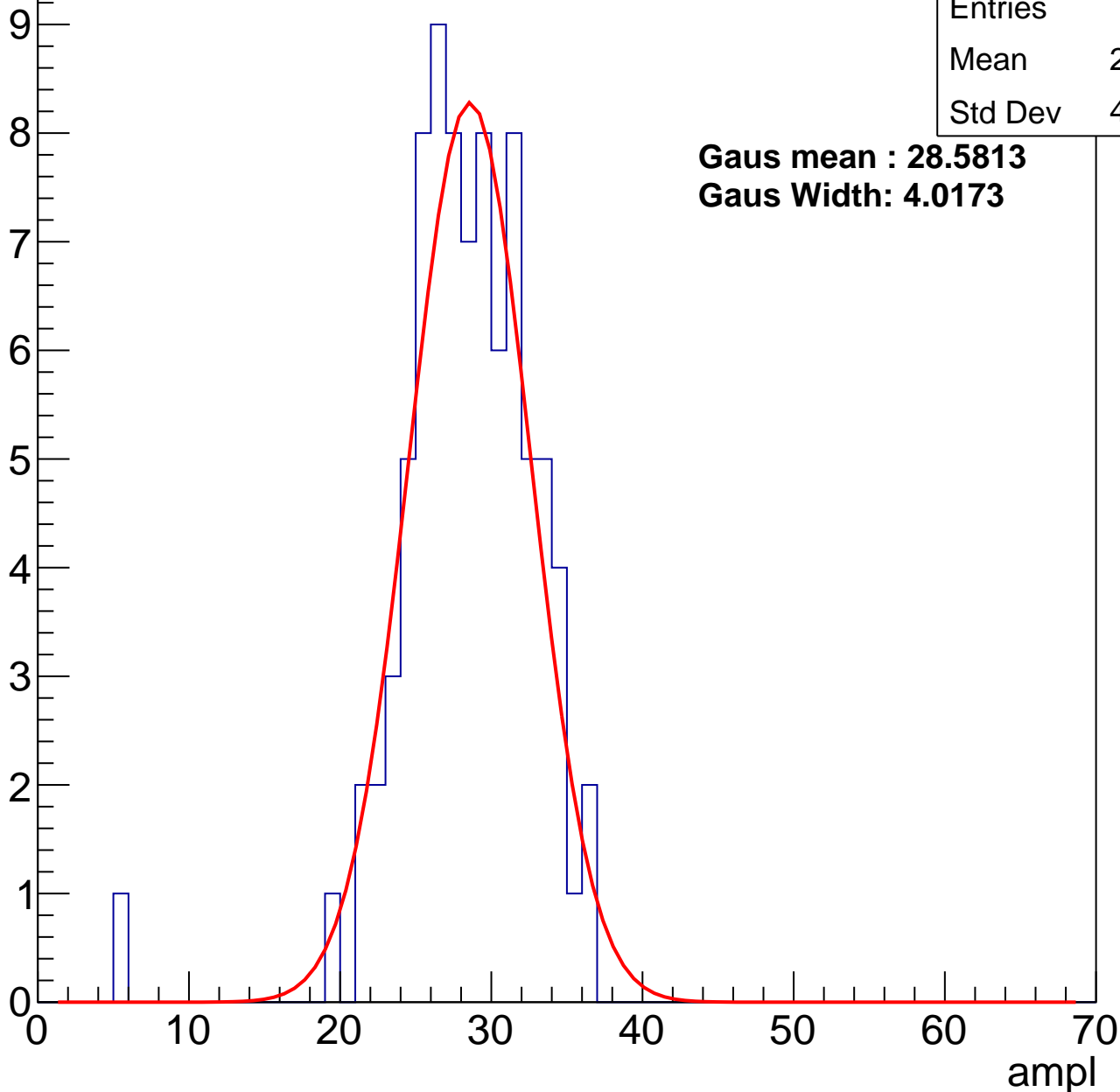
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	27.92
Std Dev	4.446

Gaus mean : 28.5813

Gaus Width: 4.0173



B1L102S, U22-ch0, adc1

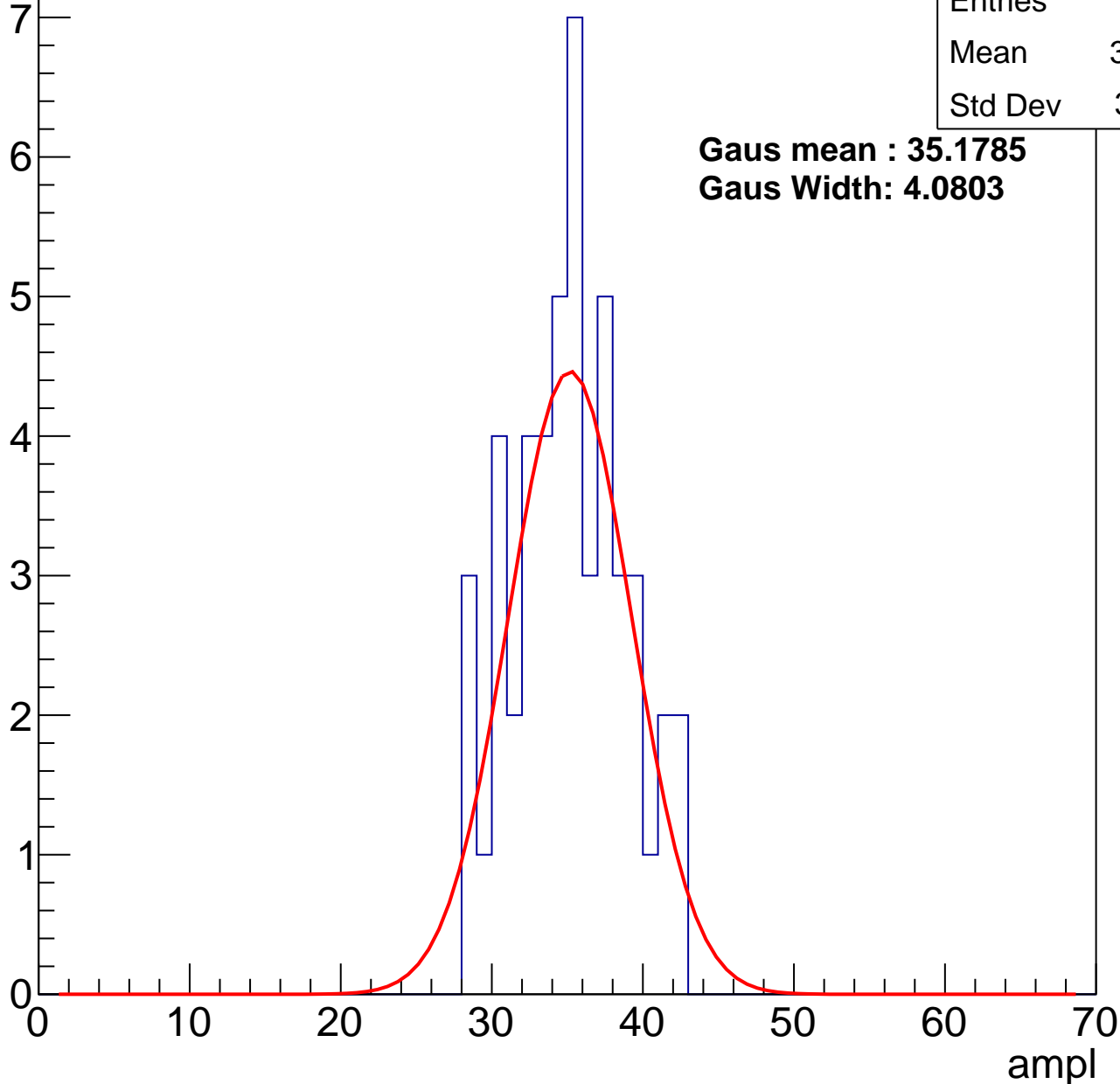
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	34.69
Std Dev	3.671

Gaus mean : 35.1785

Gaus Width: 4.0803



B1L102S, U22-ch0, adc2

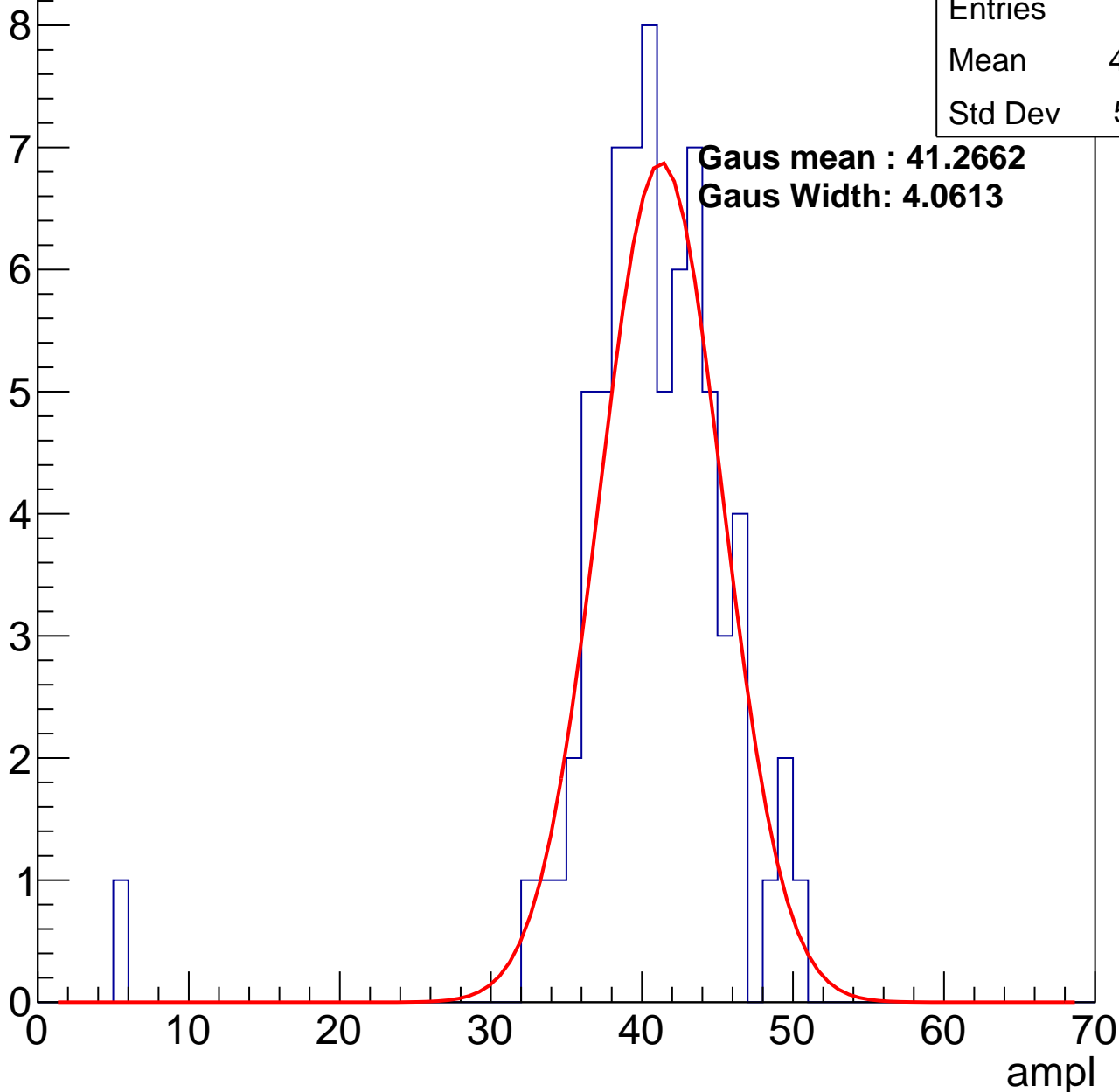
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	40.15
Std Dev	5.651

Gaus mean : 41.2662

Gaus Width: 4.0613

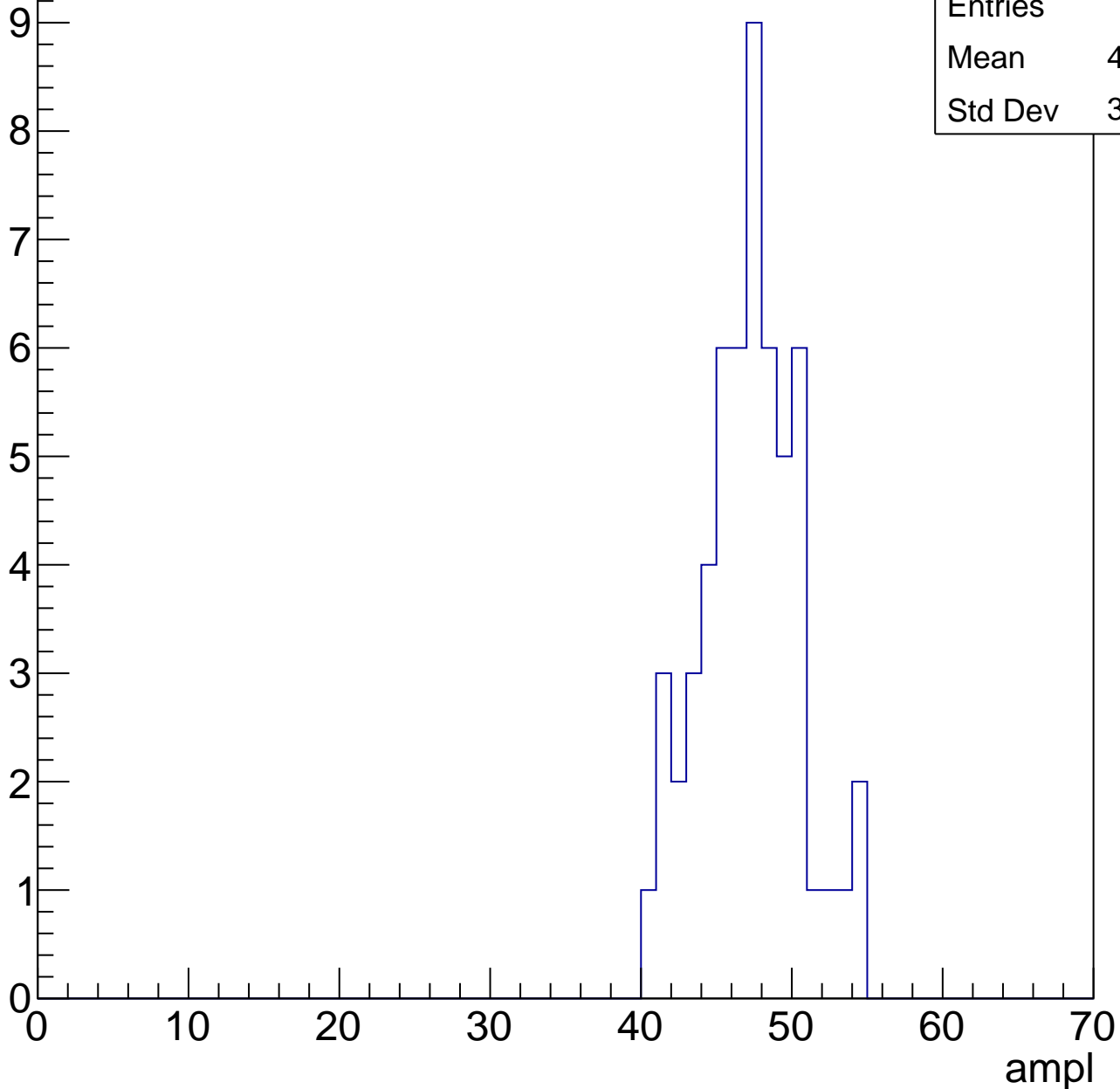


B1L102S, U22-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	46.75
Std Dev	3.197

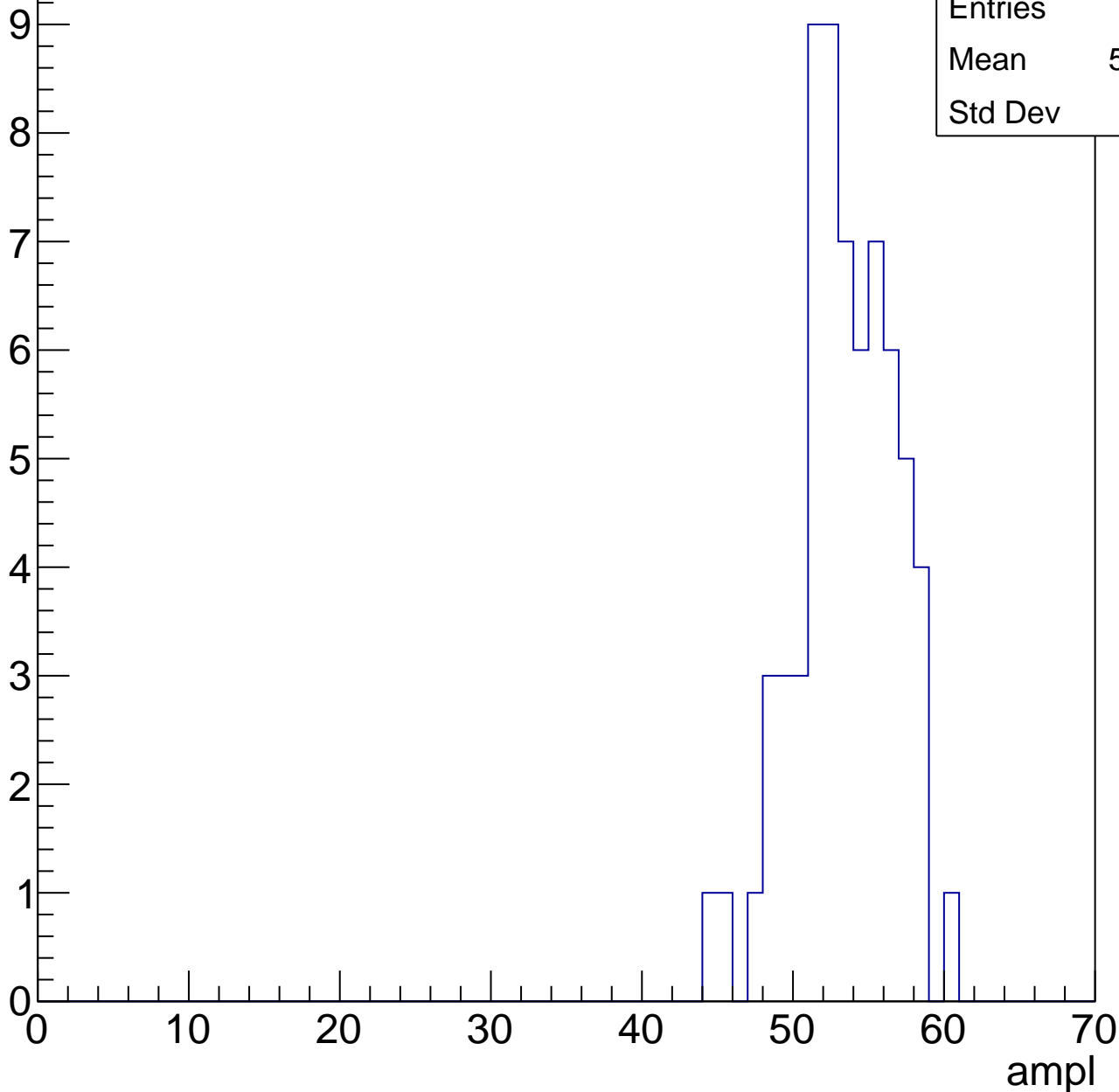


B1L102S, U22-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	52.98
Std Dev	3.24

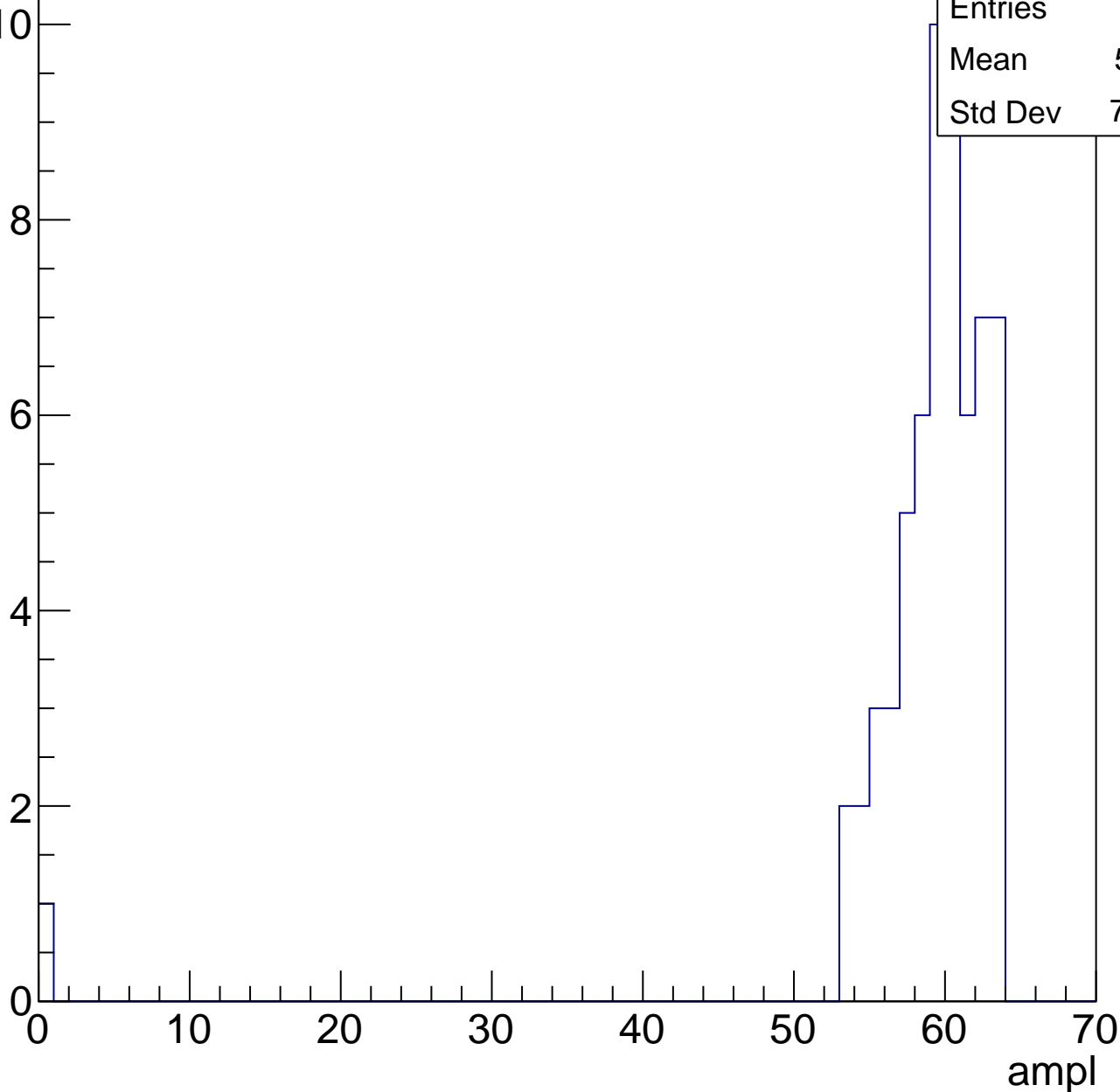


B1L102S, U22-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

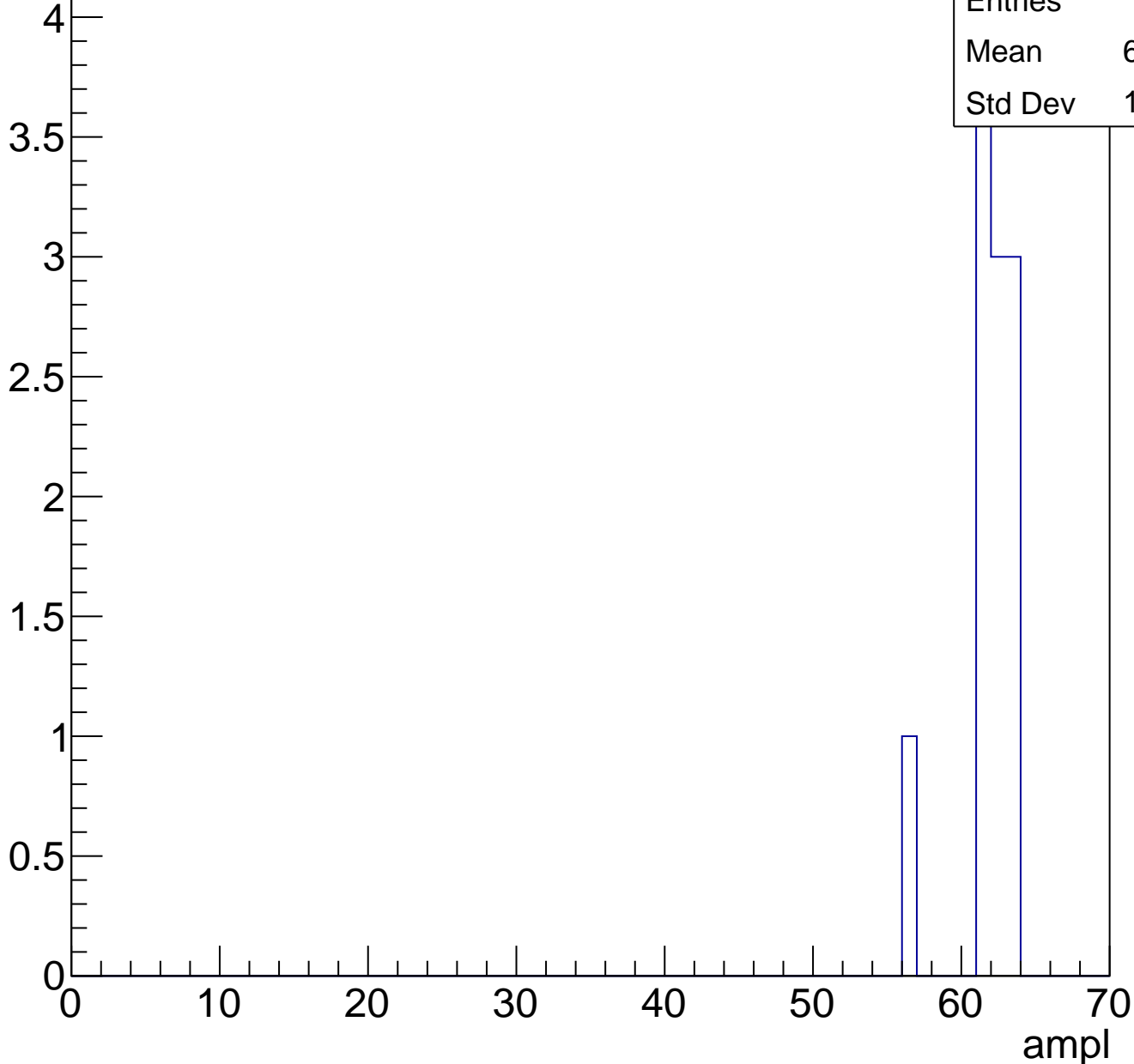
Entries	61
Mean	58.21
Std Dev	7.969



B1L102S, U22-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch1, adc0

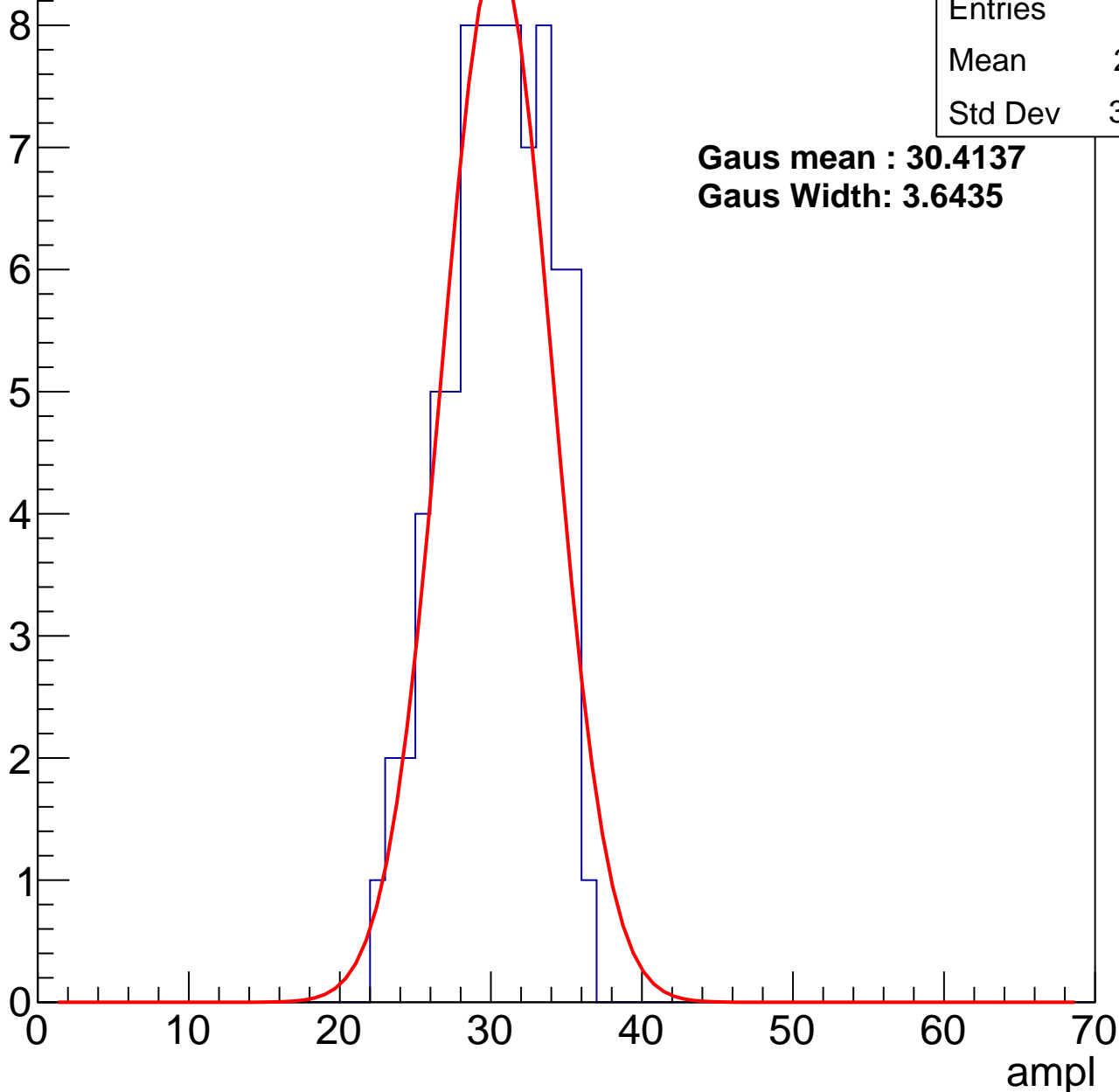
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	29.91
Std Dev	3.353

Gaus mean : 30.4137

Gaus Width: 3.6435



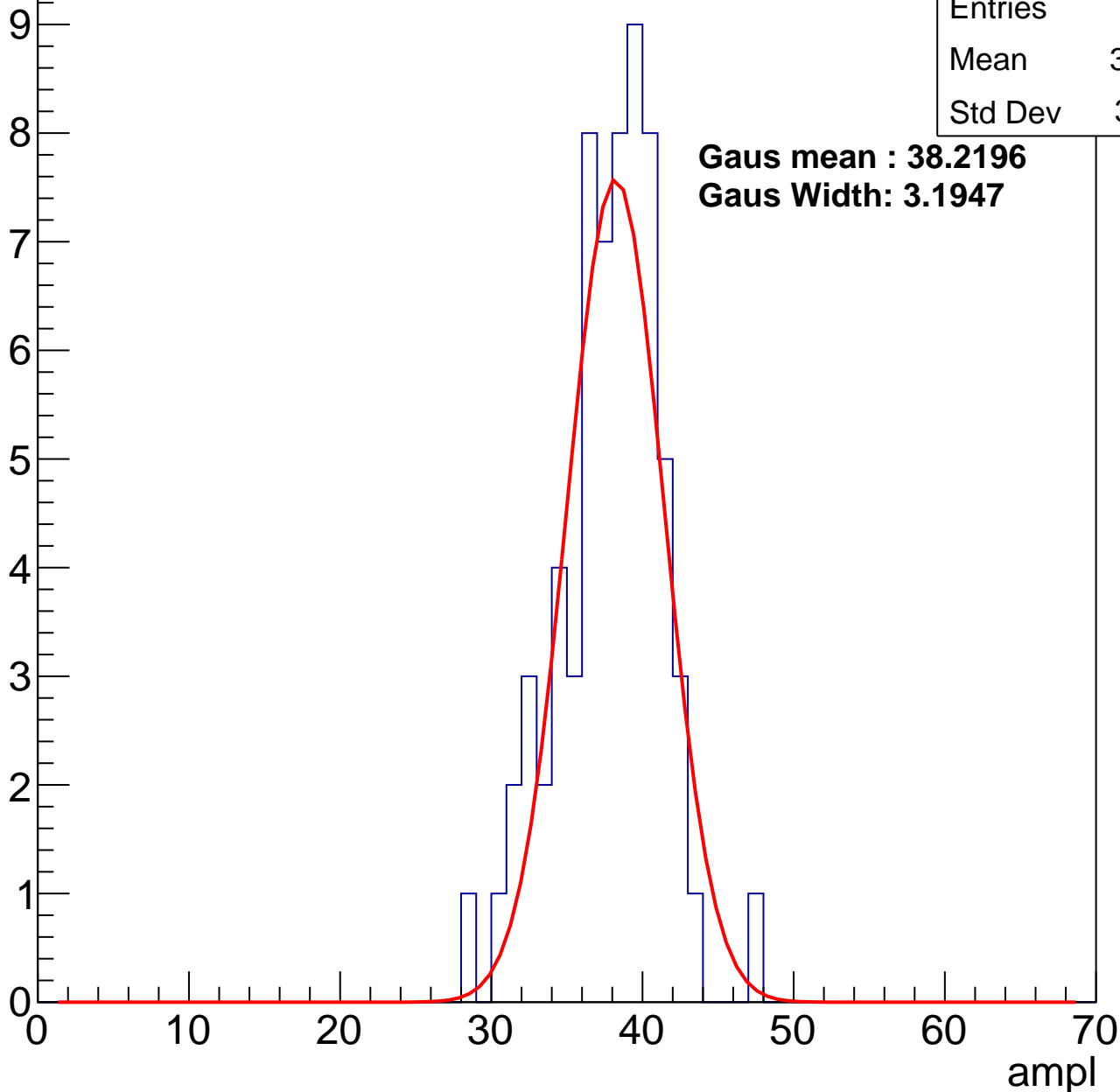
B1L102S, U22-ch1, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	37.36
Std Dev	3.401

Gaus mean : 38.2196
Gaus Width: 3.1947



B1L102S, U22-ch1, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	43.87
Std Dev	3.584

Gaus mean : 44.8066

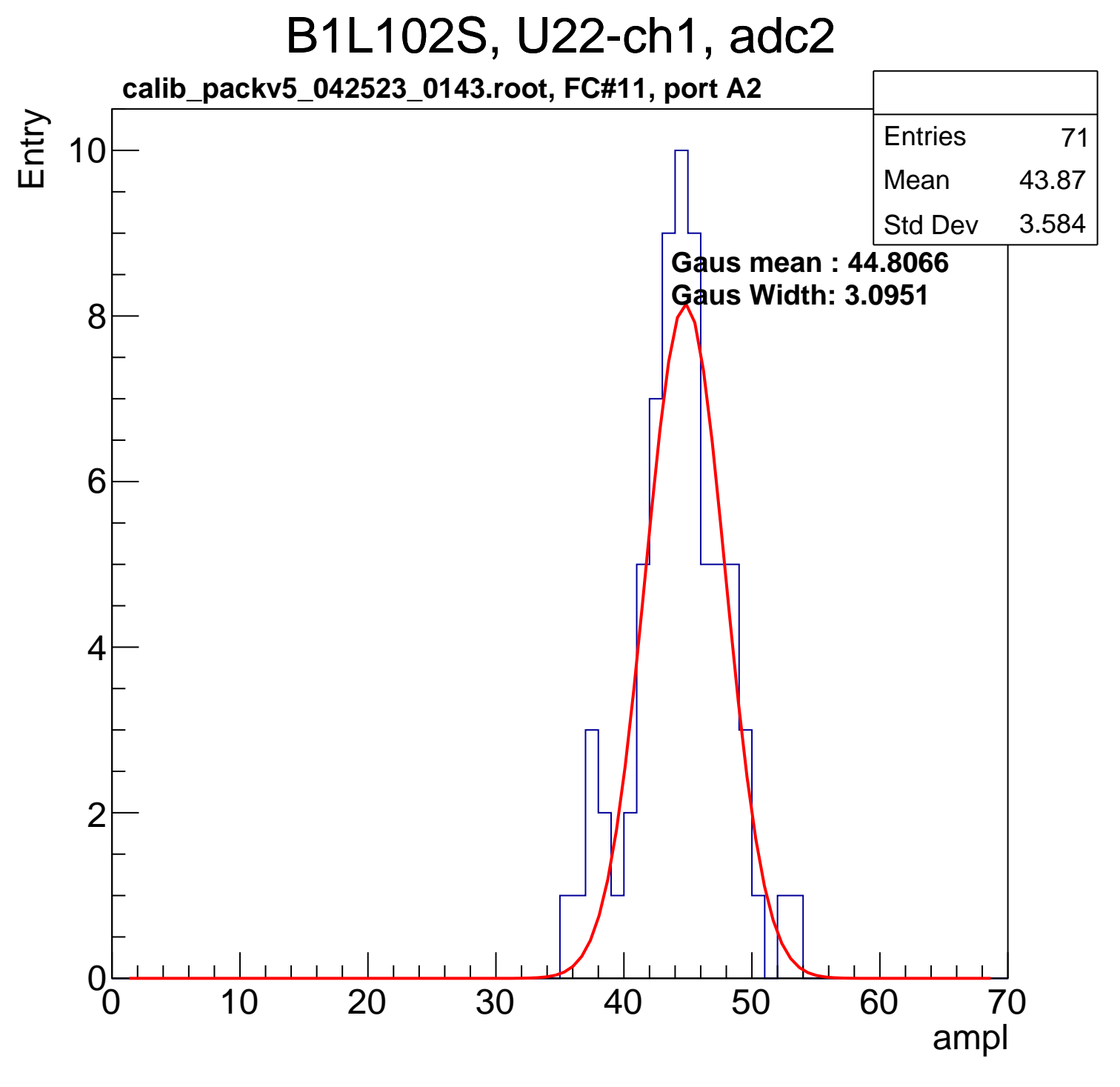
Gaus Width: 3.0951

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

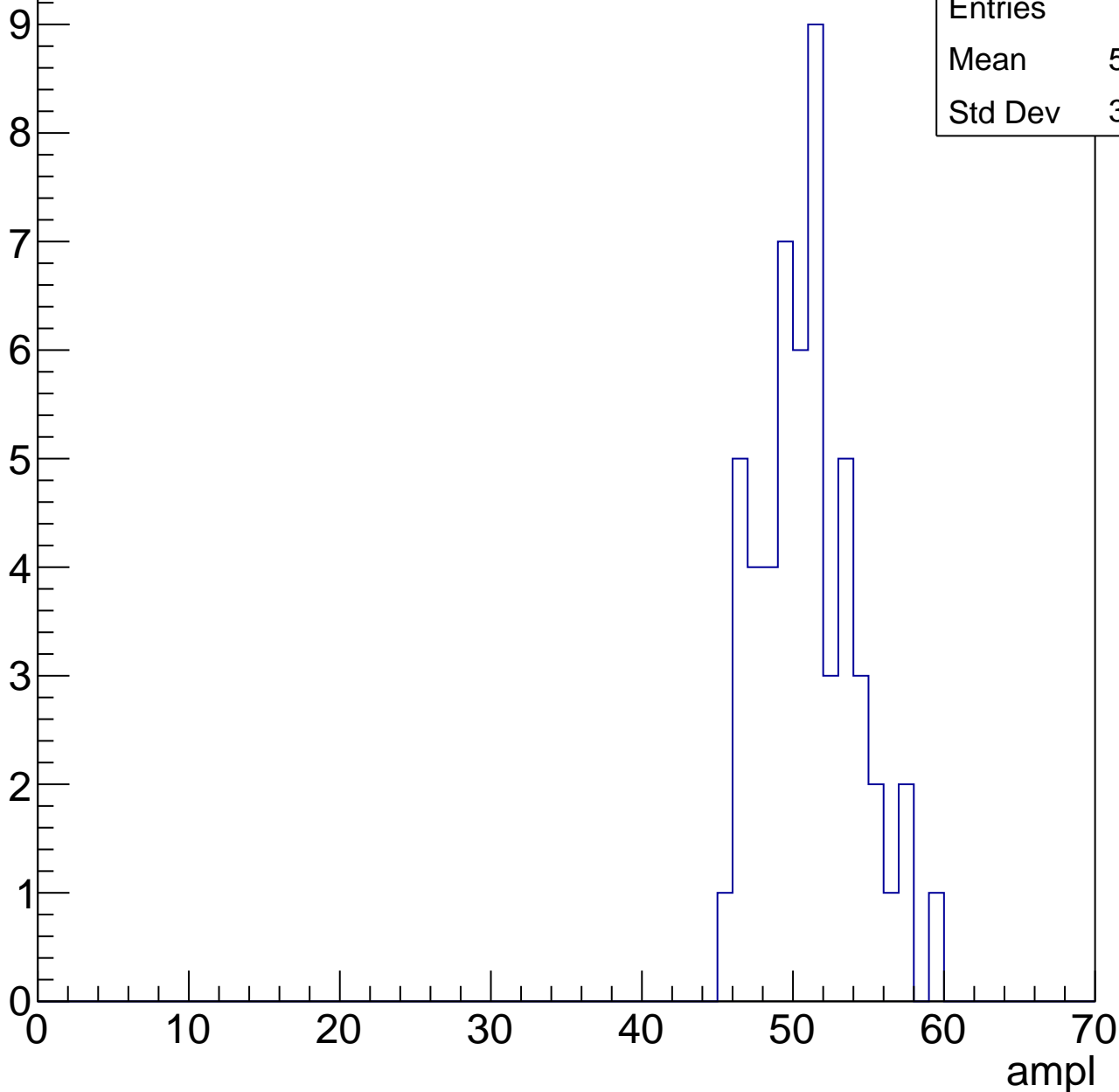


B1L102S, U22-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	50.55
Std Dev	3.154

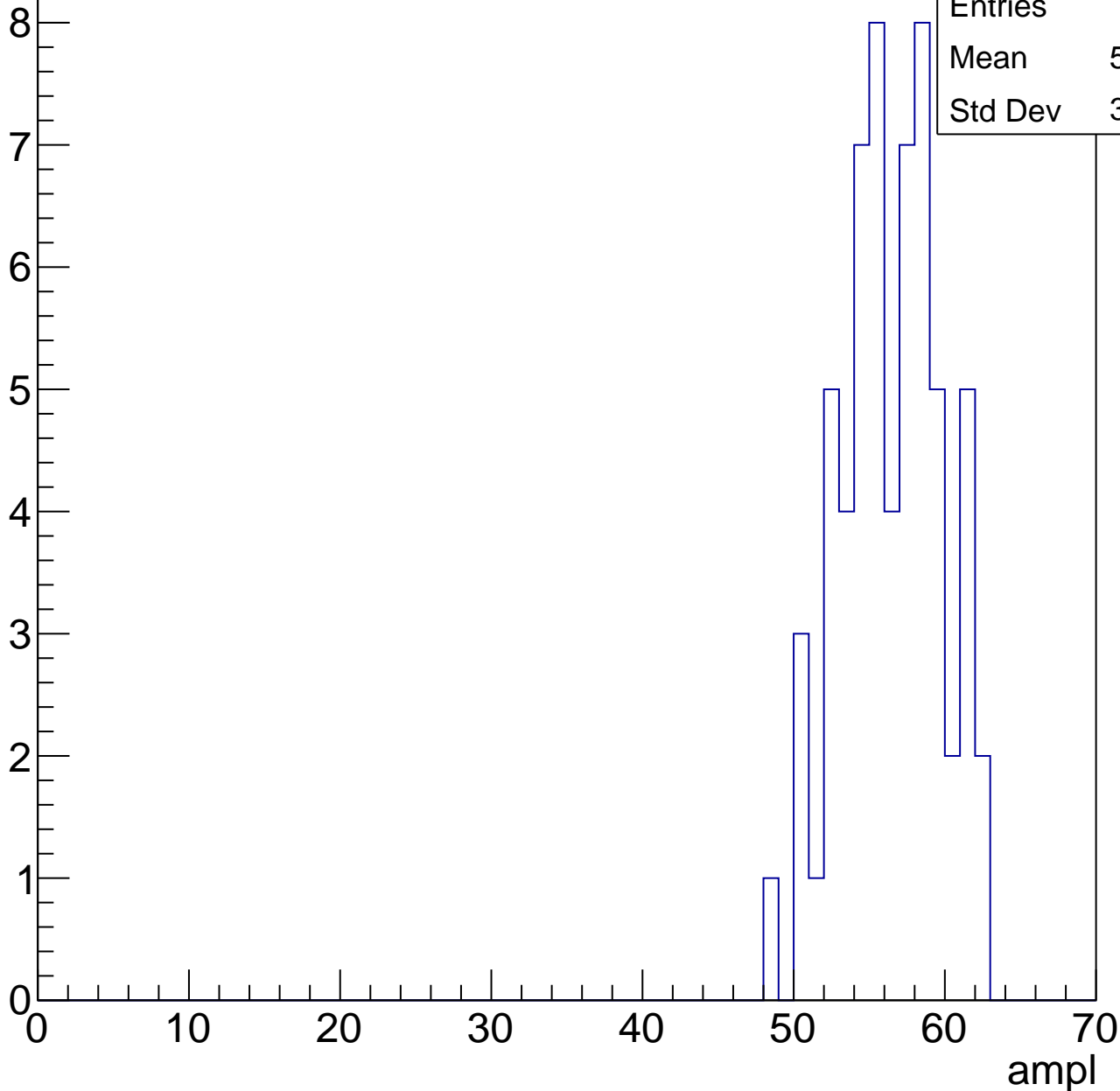


B1L102S, U22-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.97
Std Dev	3.267

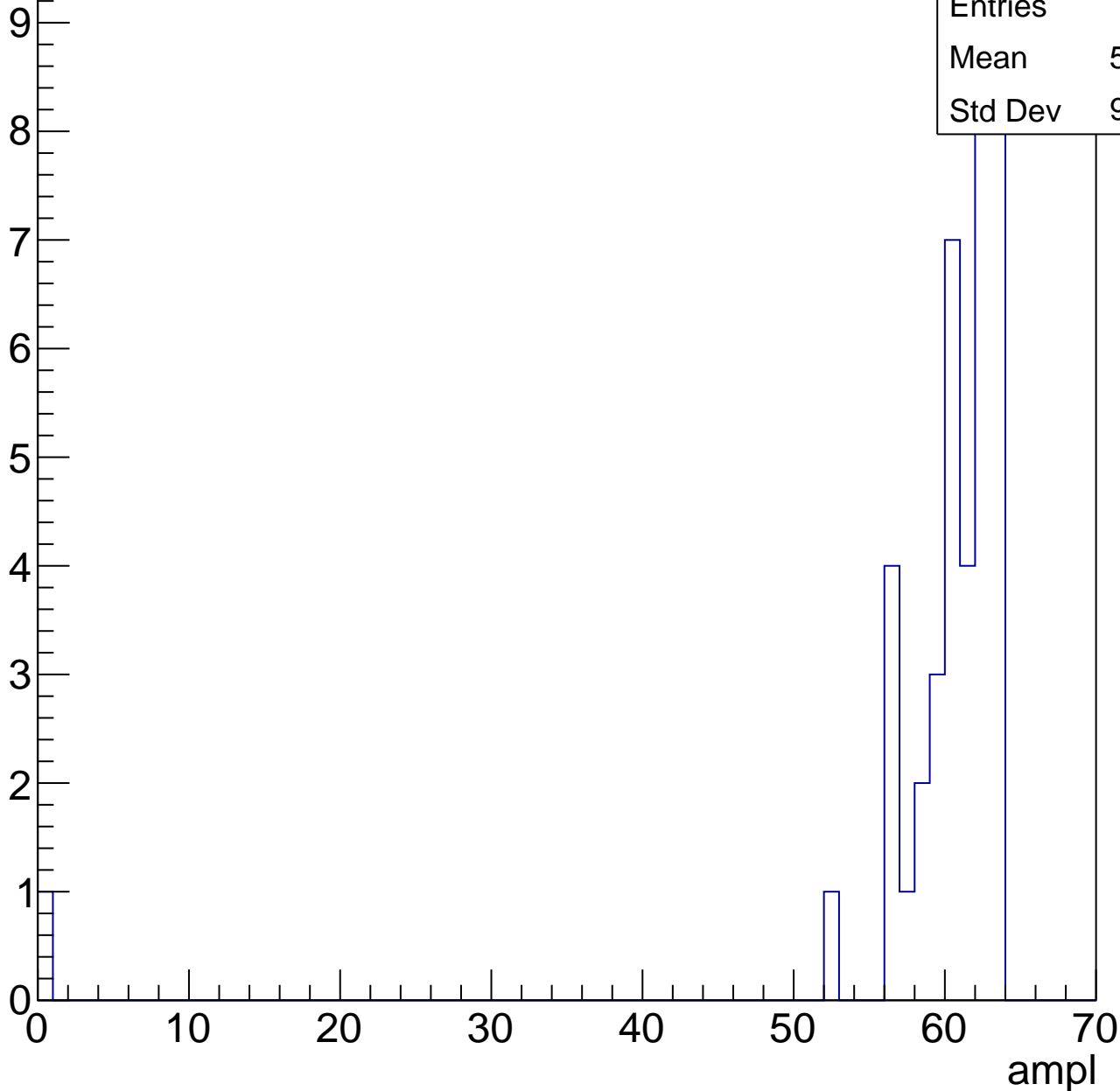


B1L102S, U22-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.83
Std Dev	9.759



B1L102S, U22-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	59.8
Std Dev	2.315

ampl

B1L102S, U22-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L102S, U22-ch2, adc0

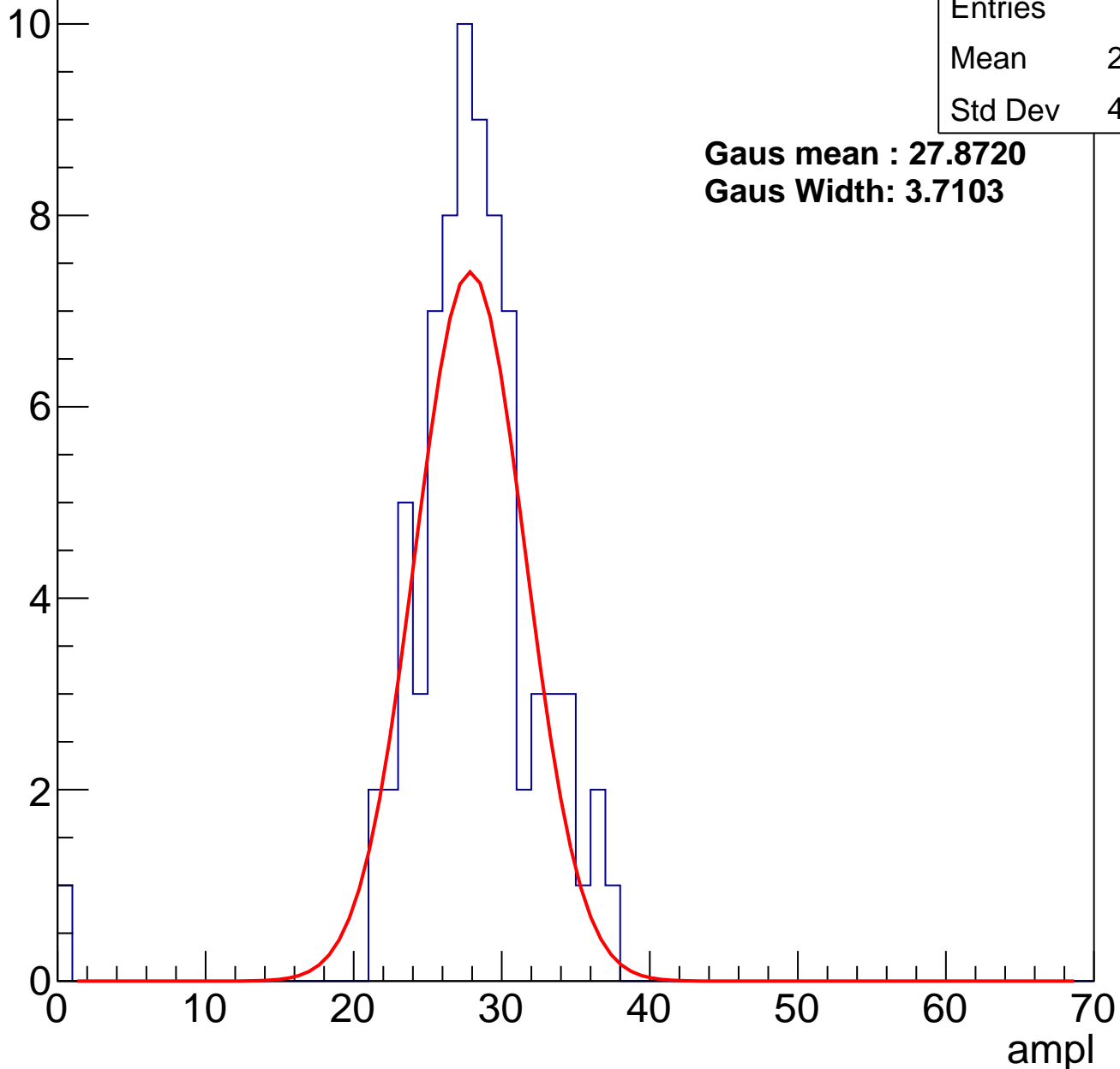
calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	27.57
Std Dev	4.782

Gaus mean : 27.8720

Gaus Width: 3.7103

Entry



B1L102S, U22-ch2, adc1

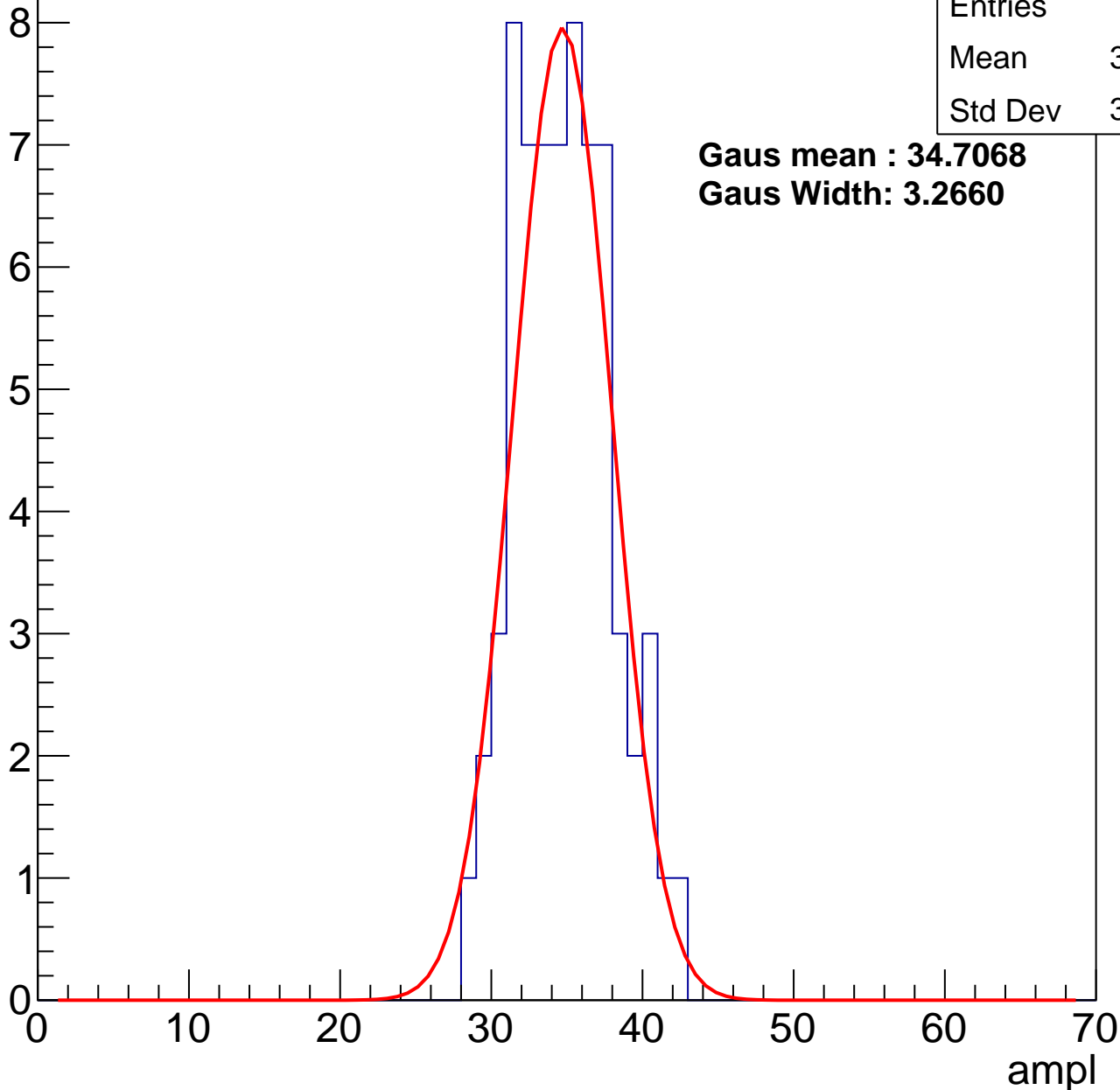
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.37
Std Dev	3.114

Gaus mean : 34.7068

Gaus Width: 3.2660



B1L102S, U22-ch2, adc2

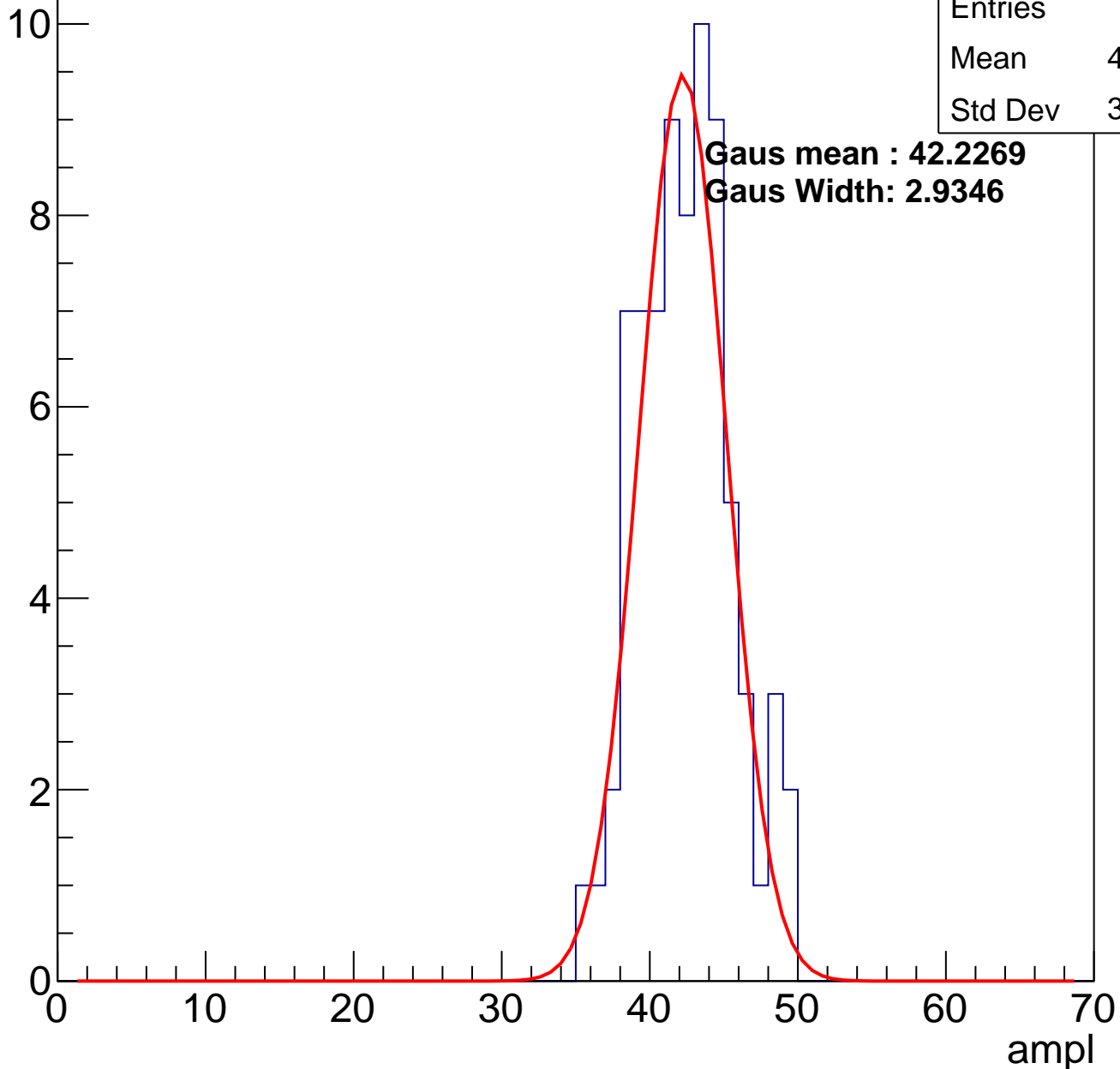
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	41.96
Std Dev	3.092

Gaus mean : 42.2269

Gaus Width: 2.9346

Entry

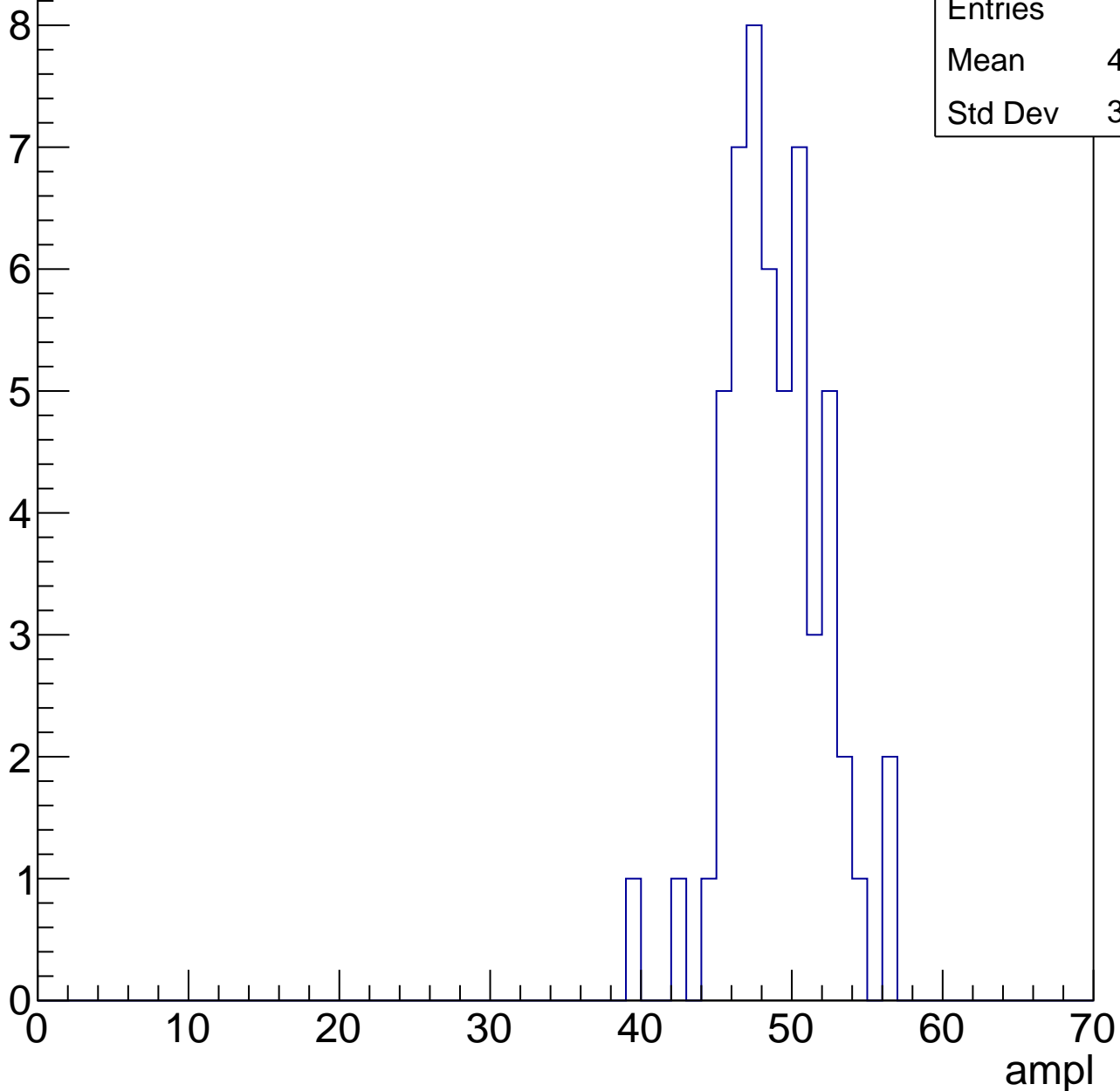


B1L102S, U22-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	48.44
Std Dev	3.213

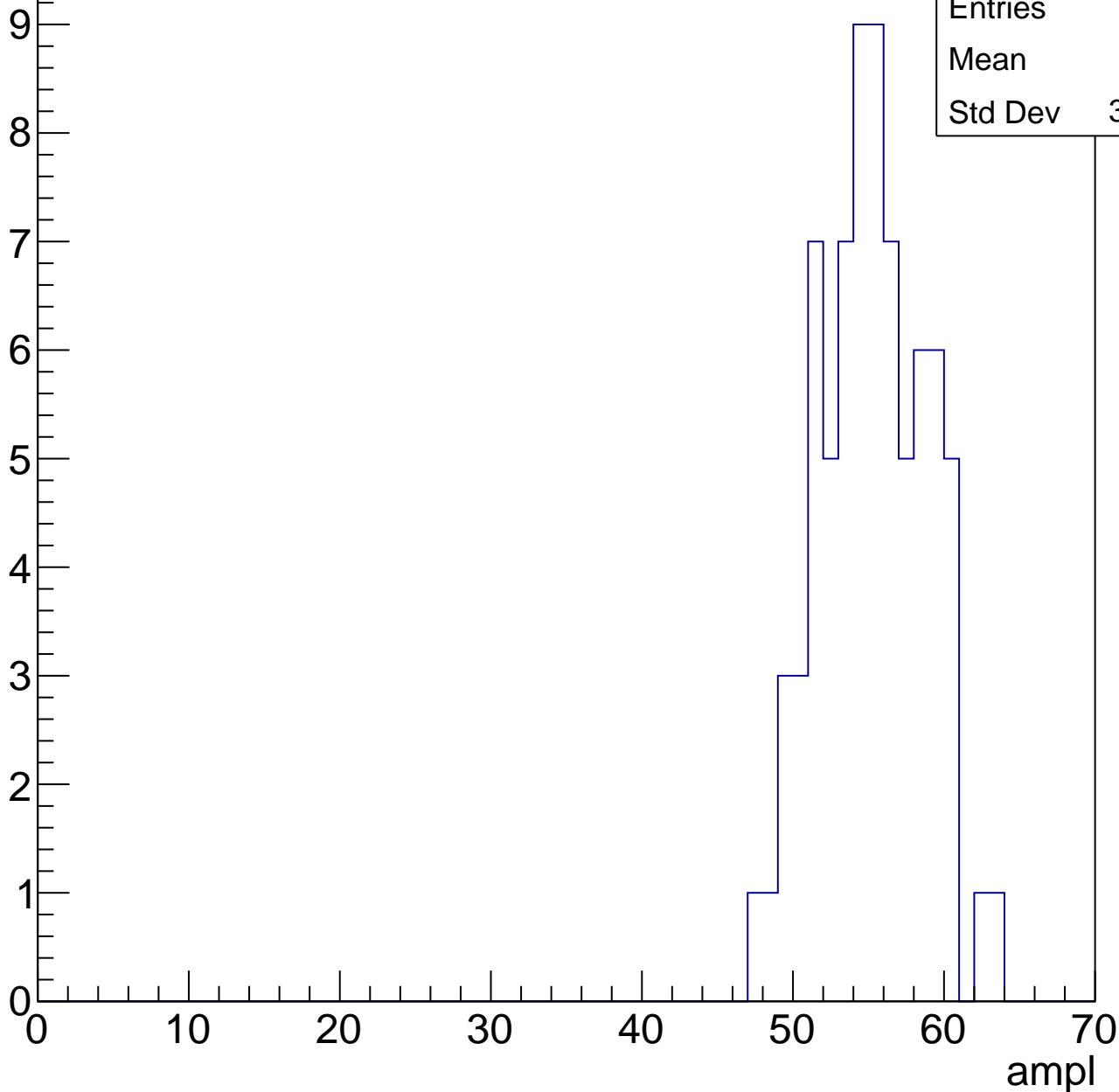


B1L102S, U22-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	54.8
Std Dev	3.445

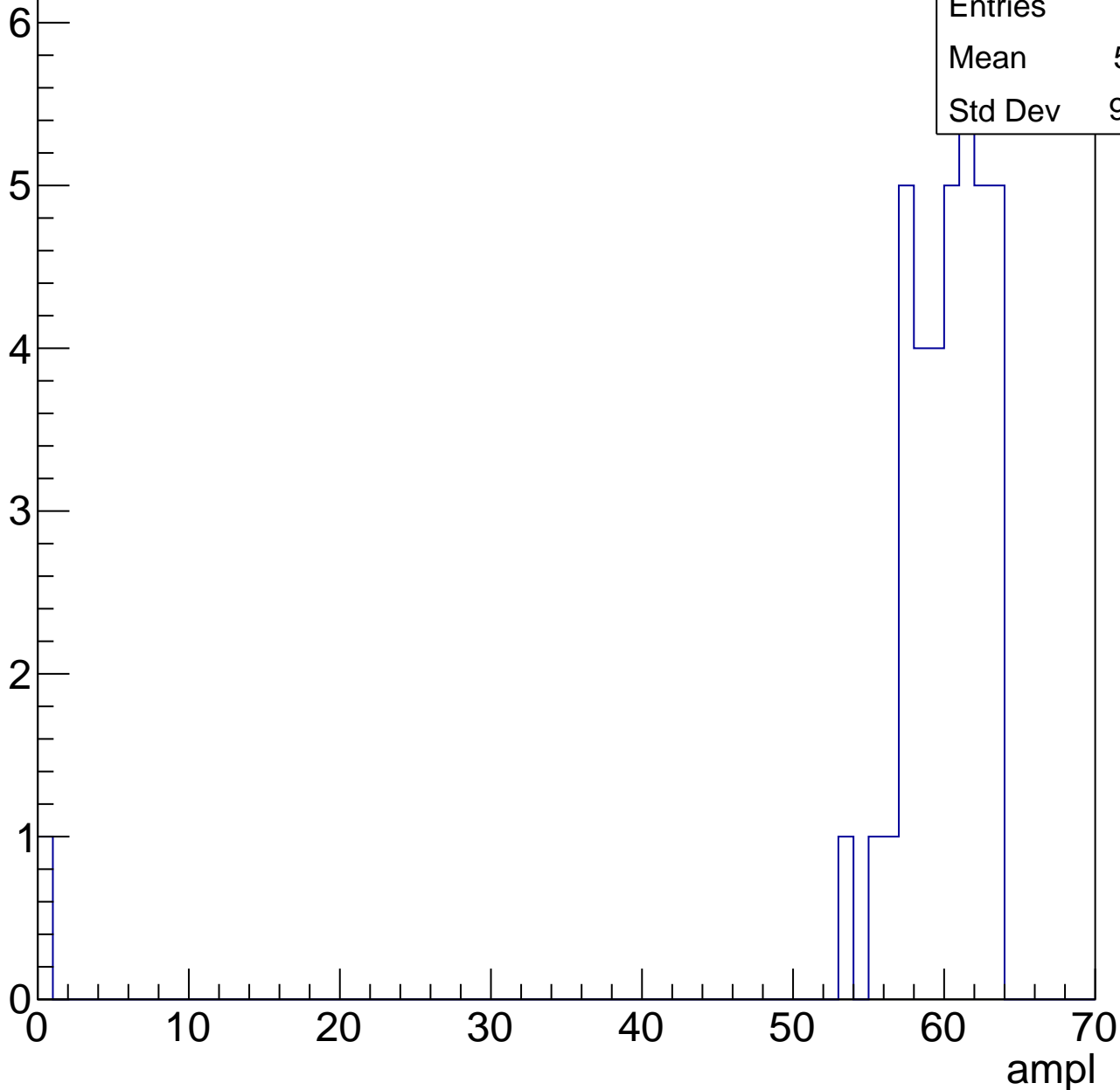


B1L102S, U22-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

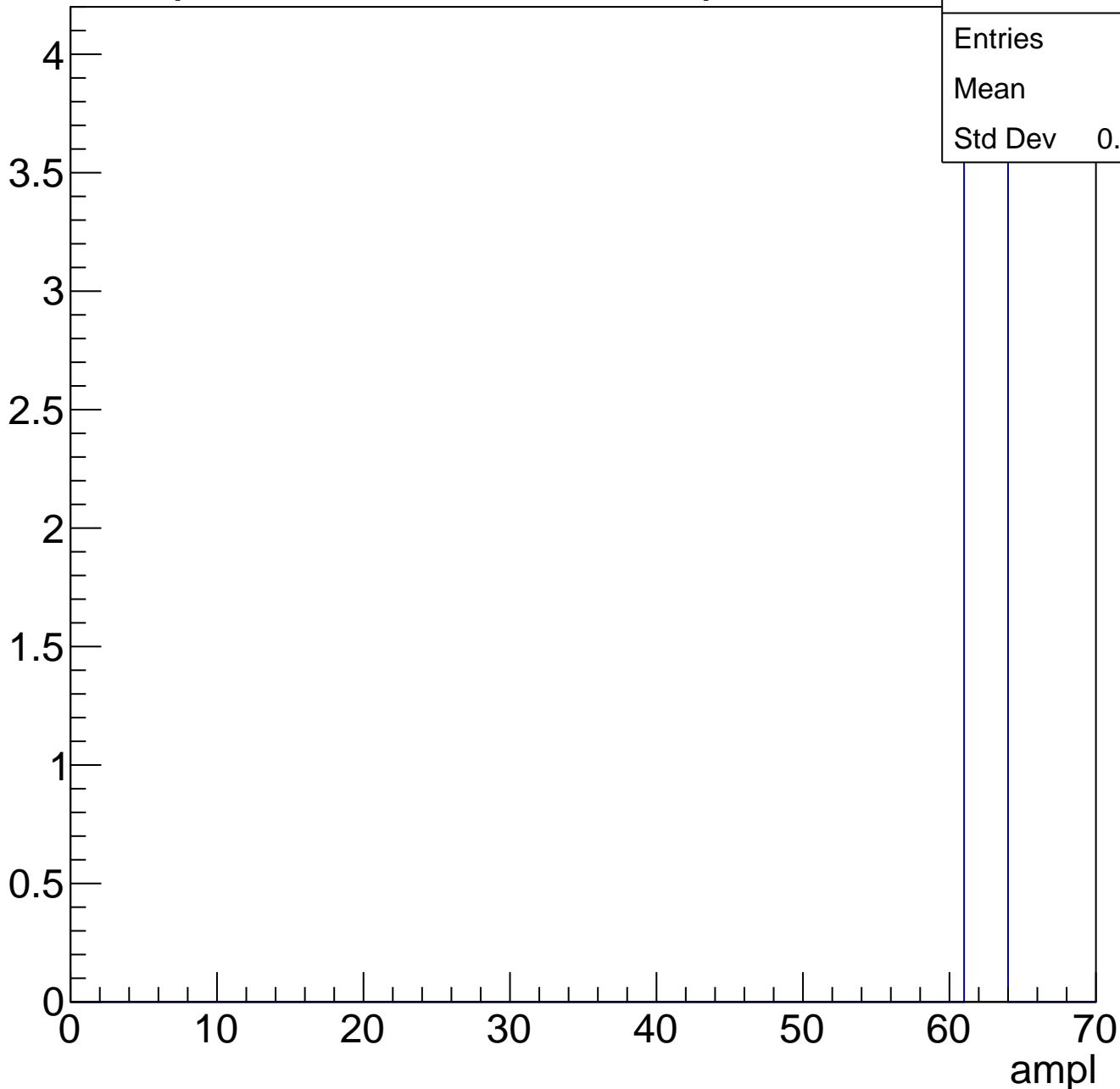
Entries	38
Mean	58.11
Std Dev	9.854



B1L102S, U22-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch3, adc0

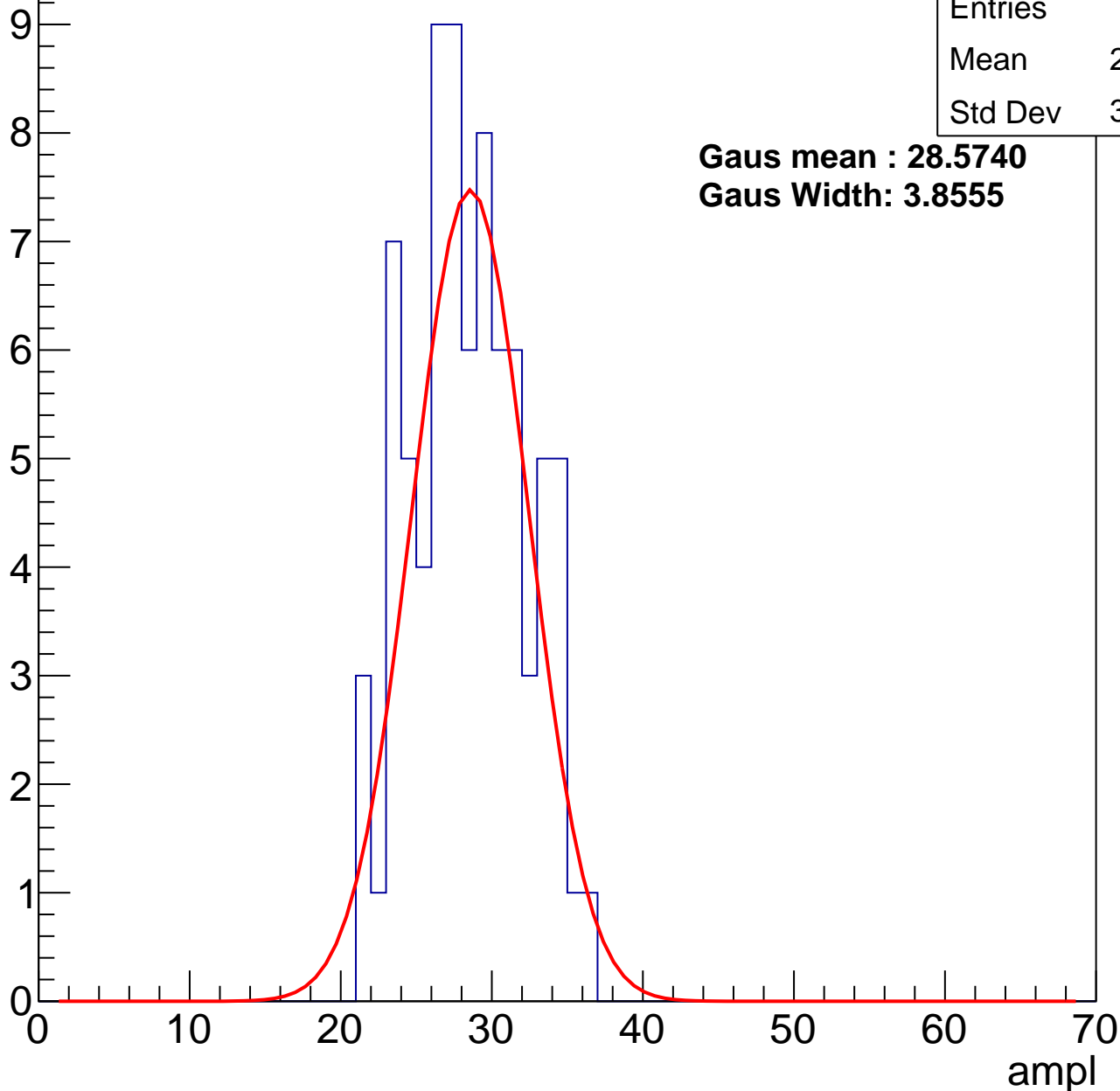
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	27.99
Std Dev	3.682

Gaus mean : 28.5740

Gaus Width: 3.8555



B1L102S, U22-ch3, adc1

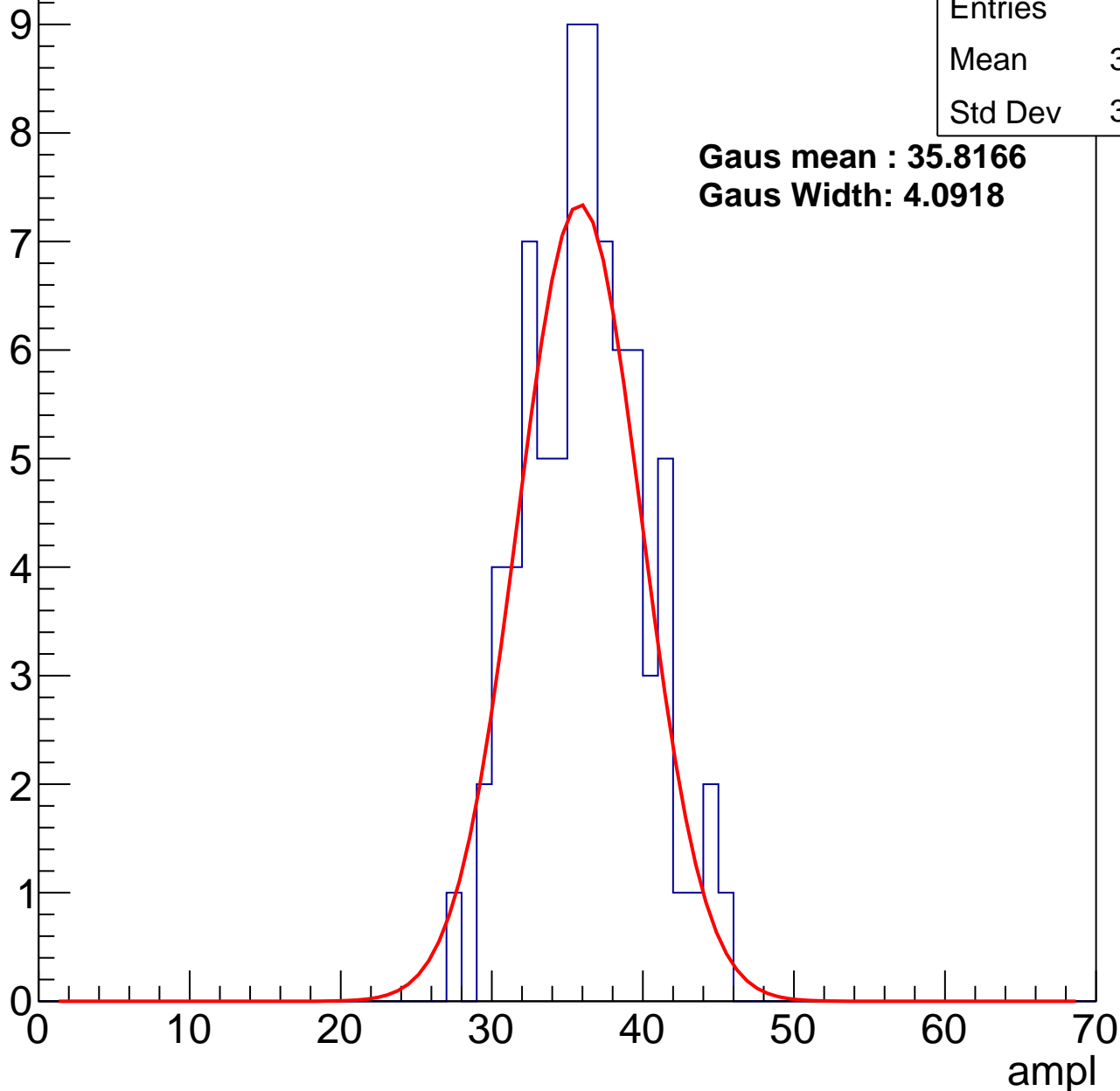
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.78
Std Dev	3.852

Gaus mean : 35.8166

Gaus Width: 4.0918



B1L102S, U22-ch3, adc2

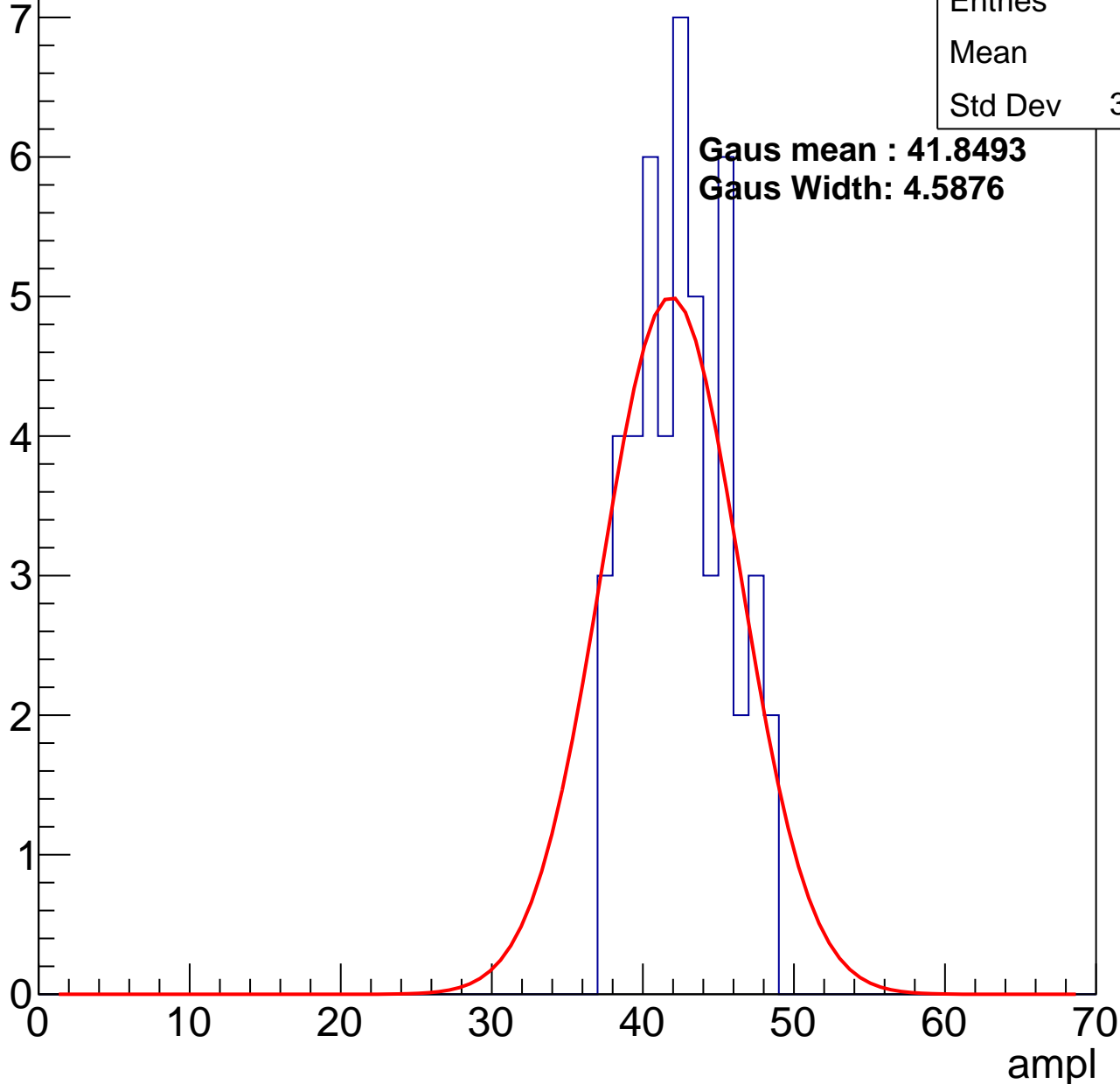
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.1
Std Dev	3.039

Gaus mean : 41.8493

Gaus Width: 4.5876

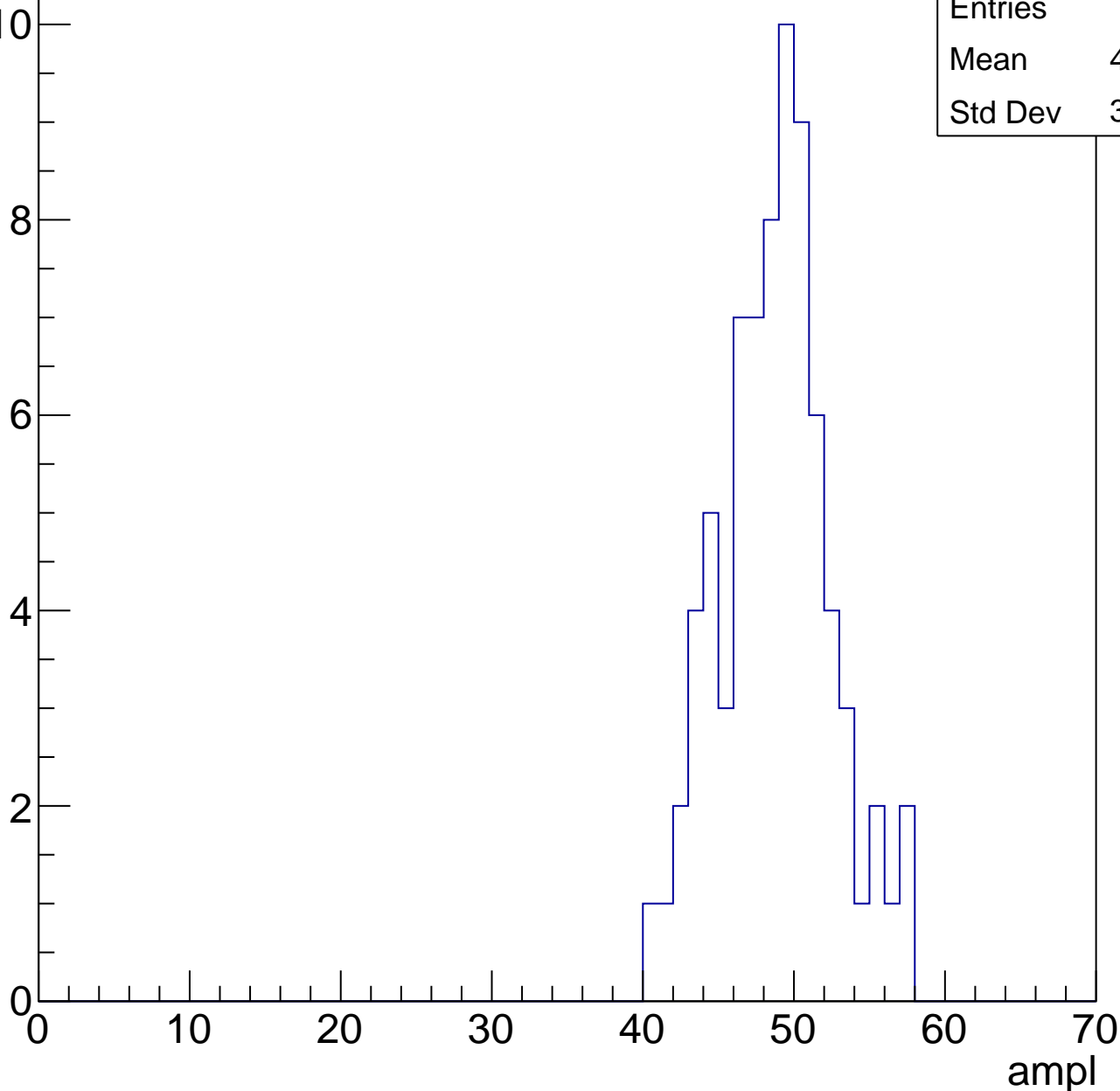


B1L102S, U22-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	48.34
Std Dev	3.658

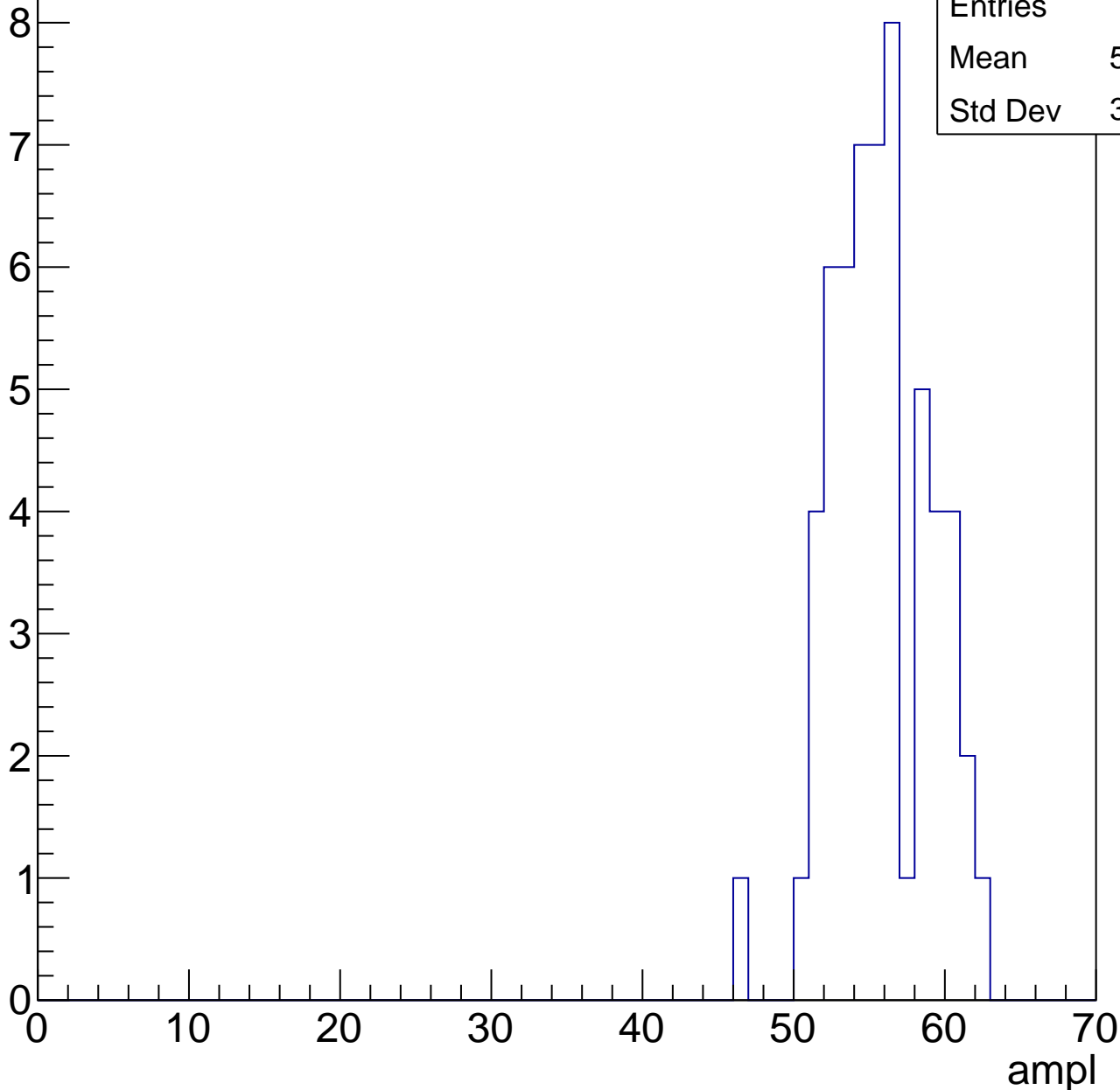


B1L102S, U22-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.23
Std Dev	3.228

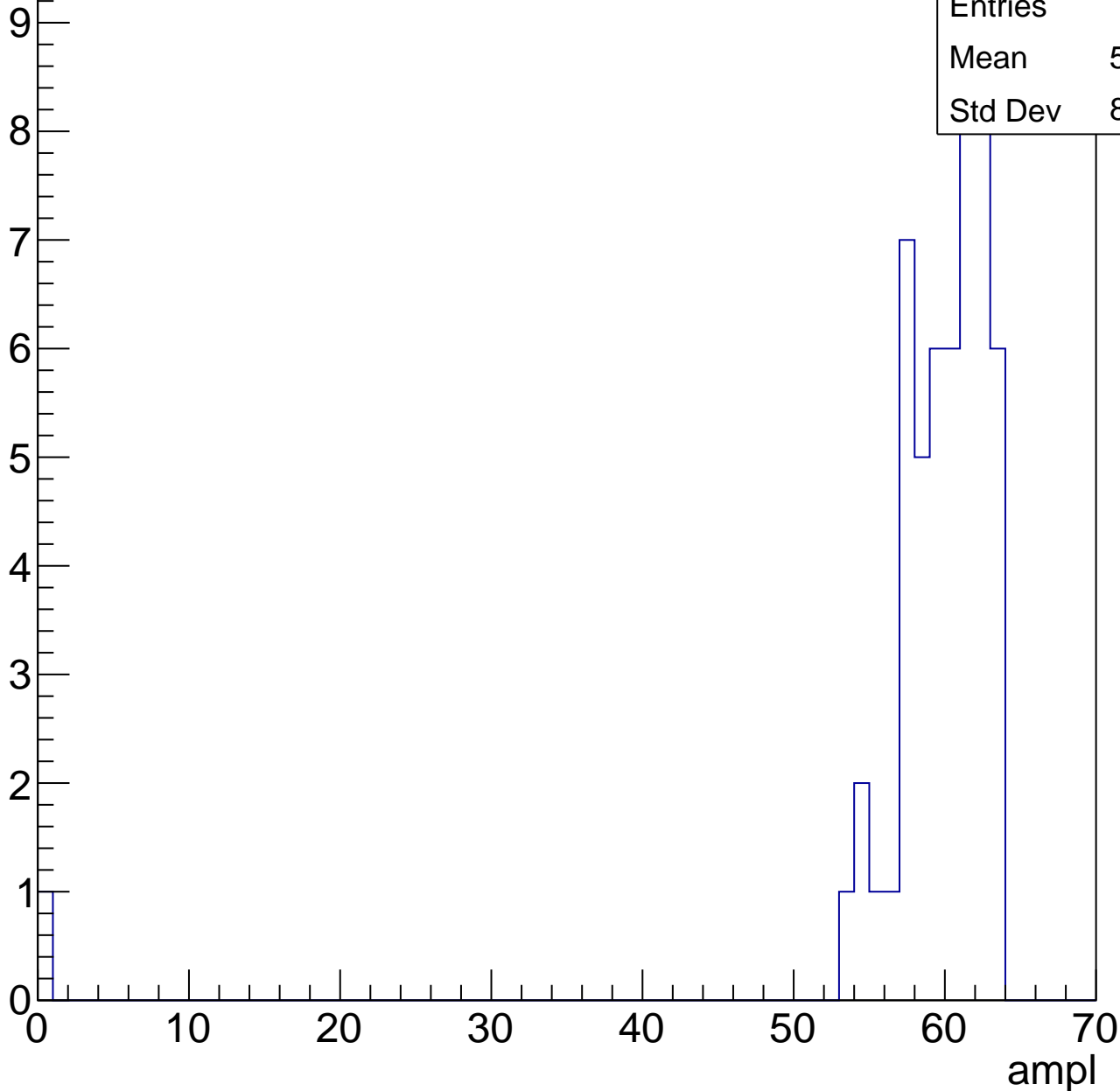


B1L102S, U22-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

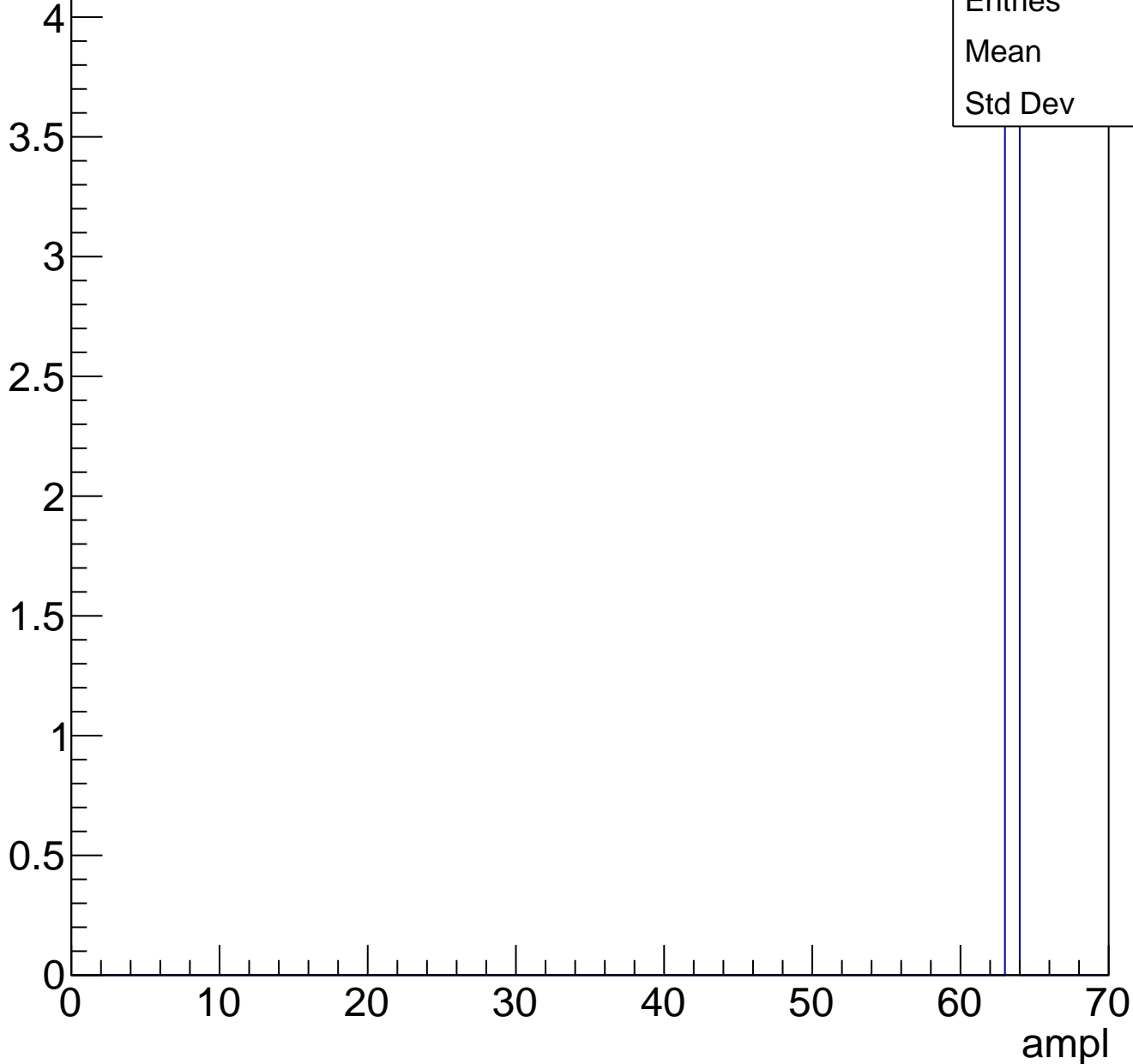
Entries	53
Mean	58.47
Std Dev	8.496



B1L102S, U22-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

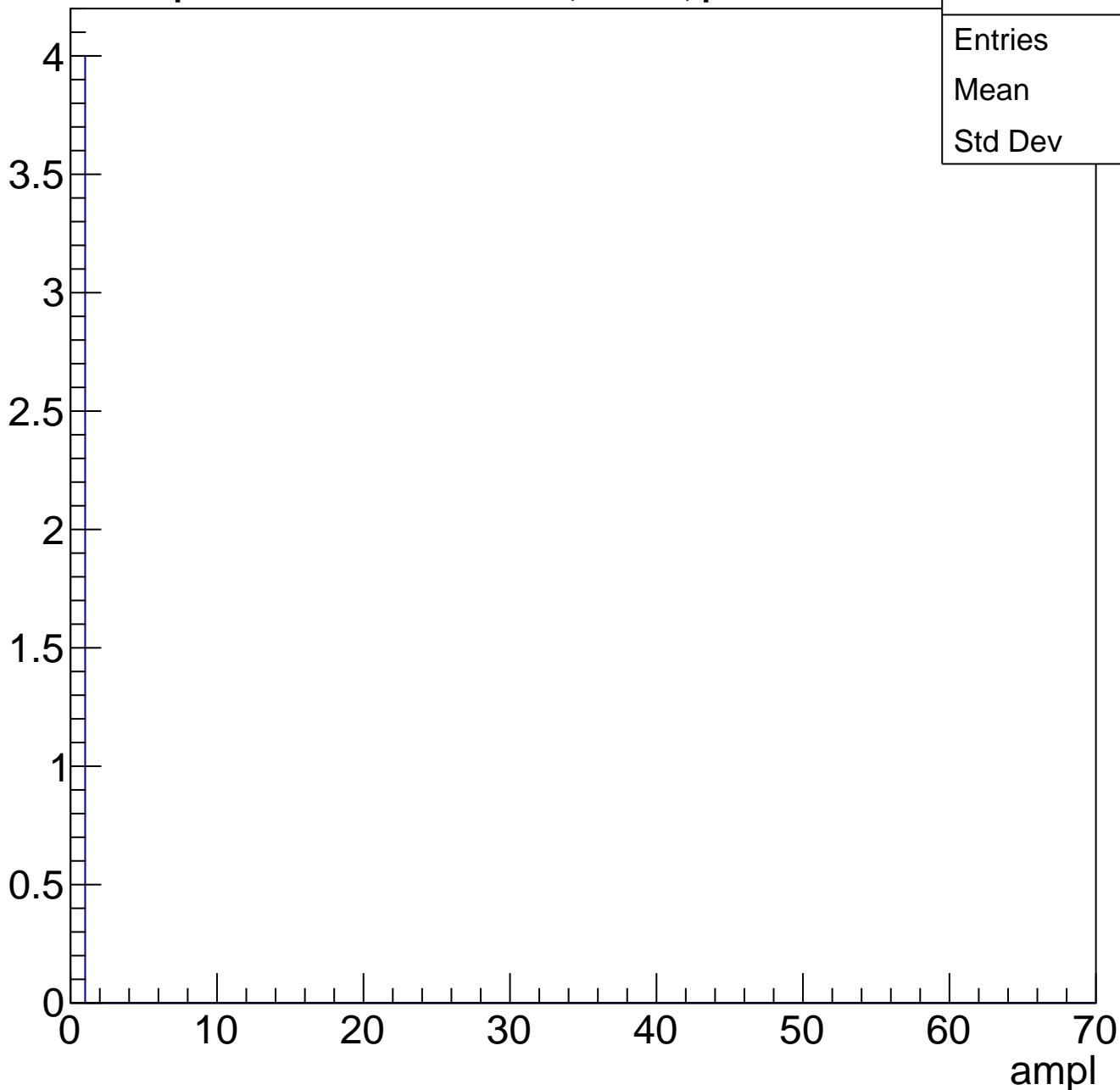
Entry



B1L102S, U22-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L102S, U22-ch4, adc0

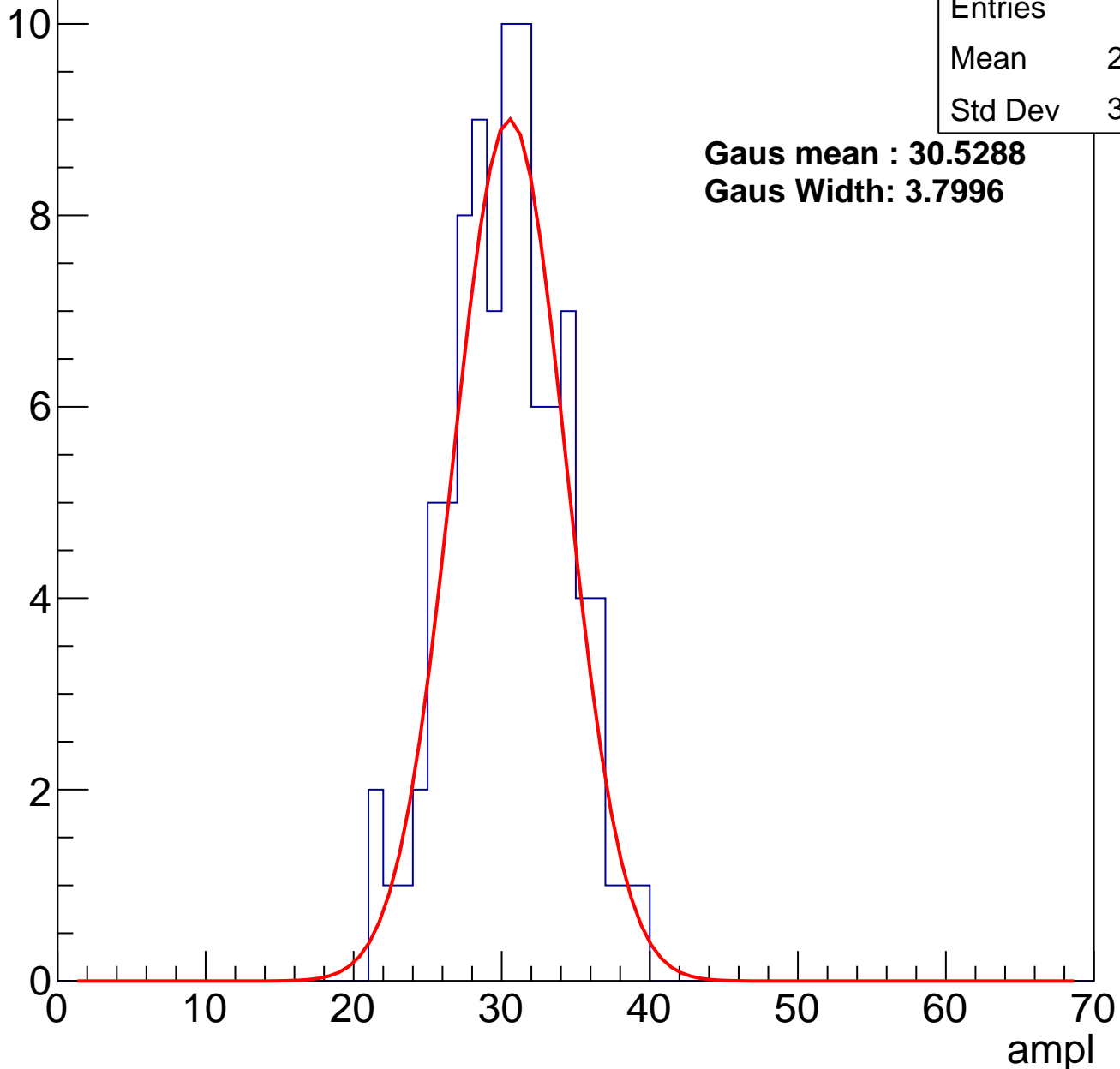
calib_packv5_042523_0143.root, FC#11, port A2

Entries	90
Mean	29.97
Std Dev	3.808

Gaus mean : 30.5288

Gaus Width: 3.7996

Entry



B1L102S, U22-ch4, adc1

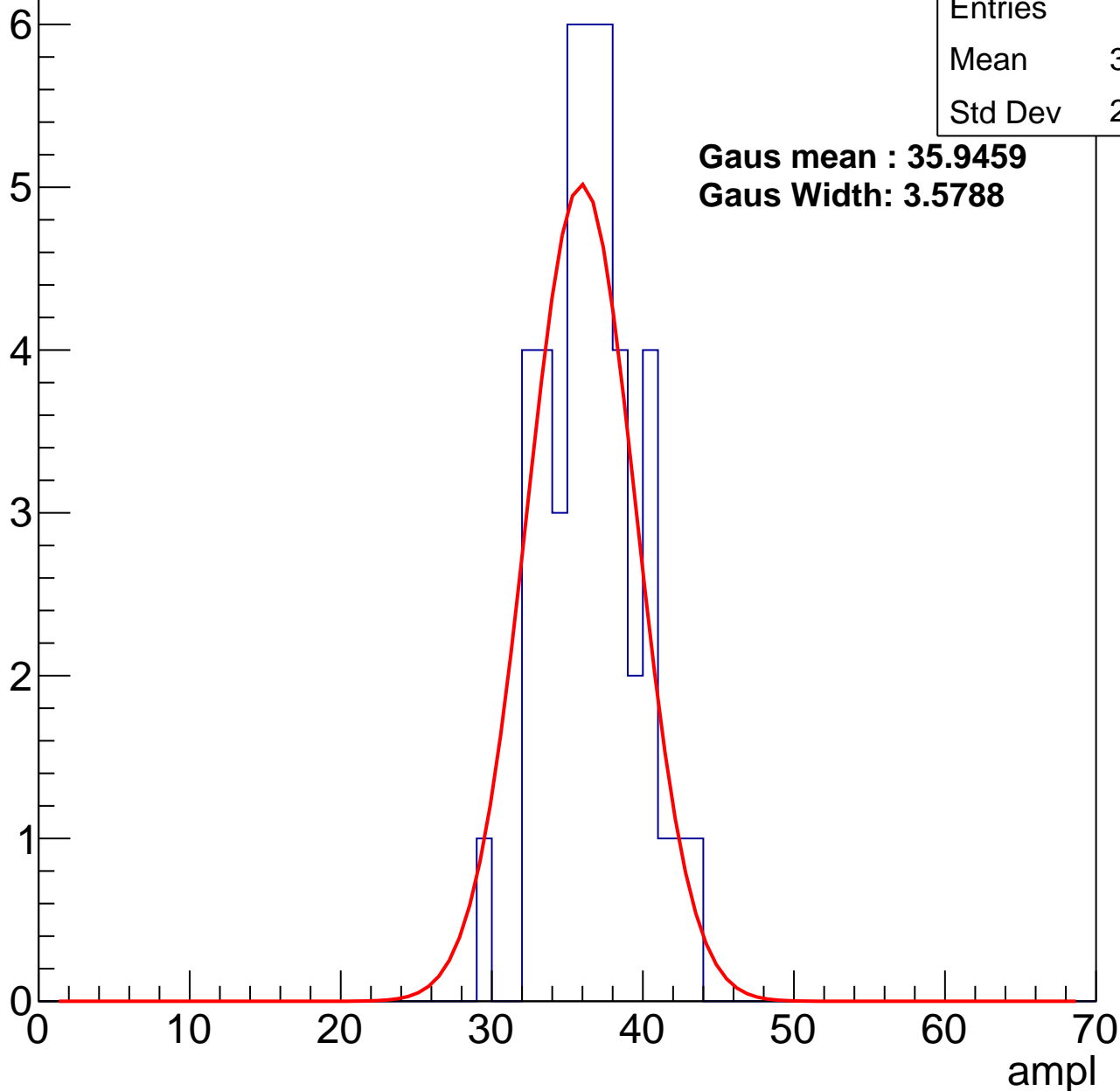
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	36.16
Std Dev	2.972

Gaus mean : 35.9459

Gaus Width: 3.5788



B1L102S, U22-ch4, adc2

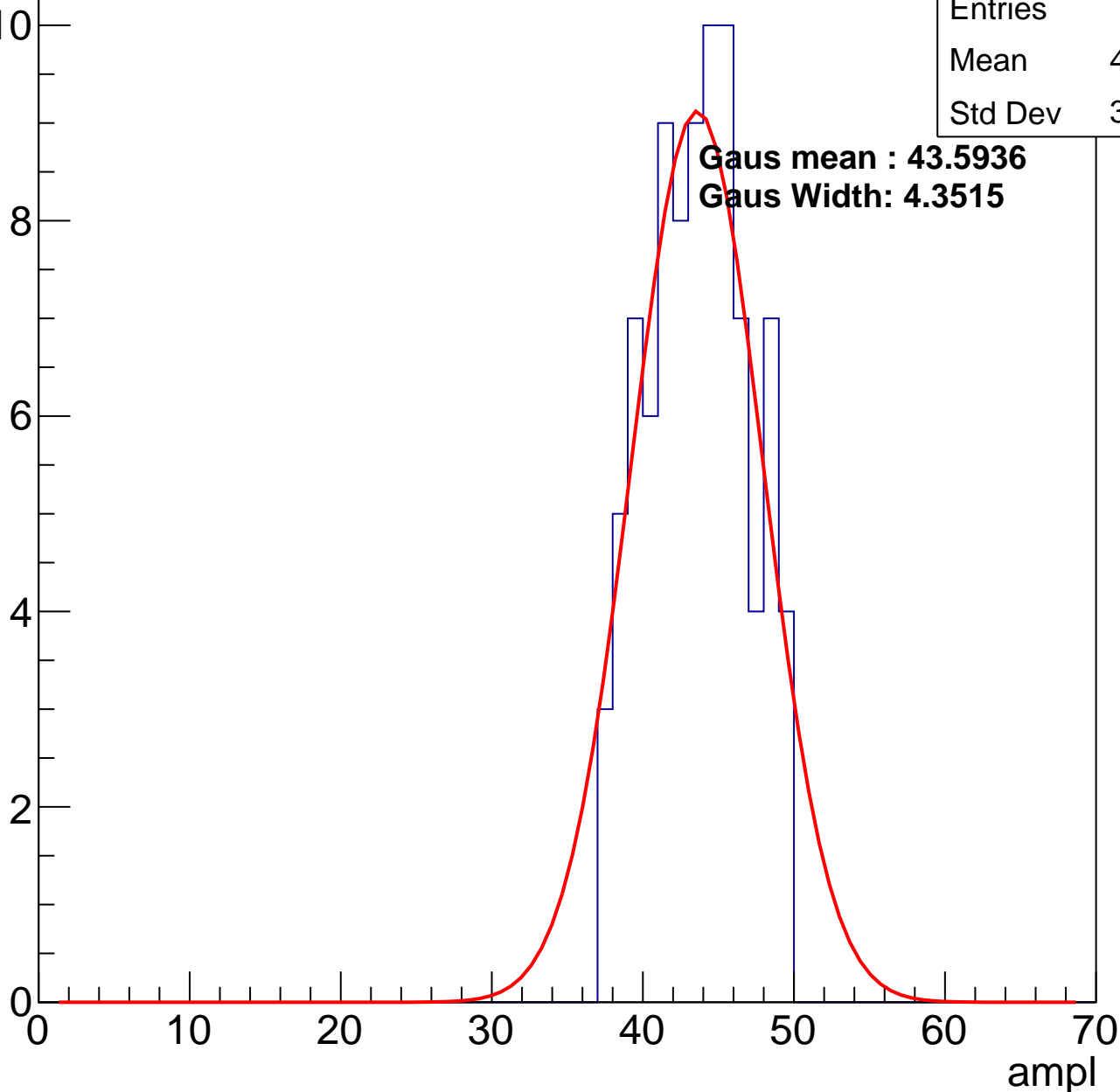
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	43.12
Std Dev	3.246

Gaus mean : 43.5936

Gaus Width: 4.3515



B1L102S, U22-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

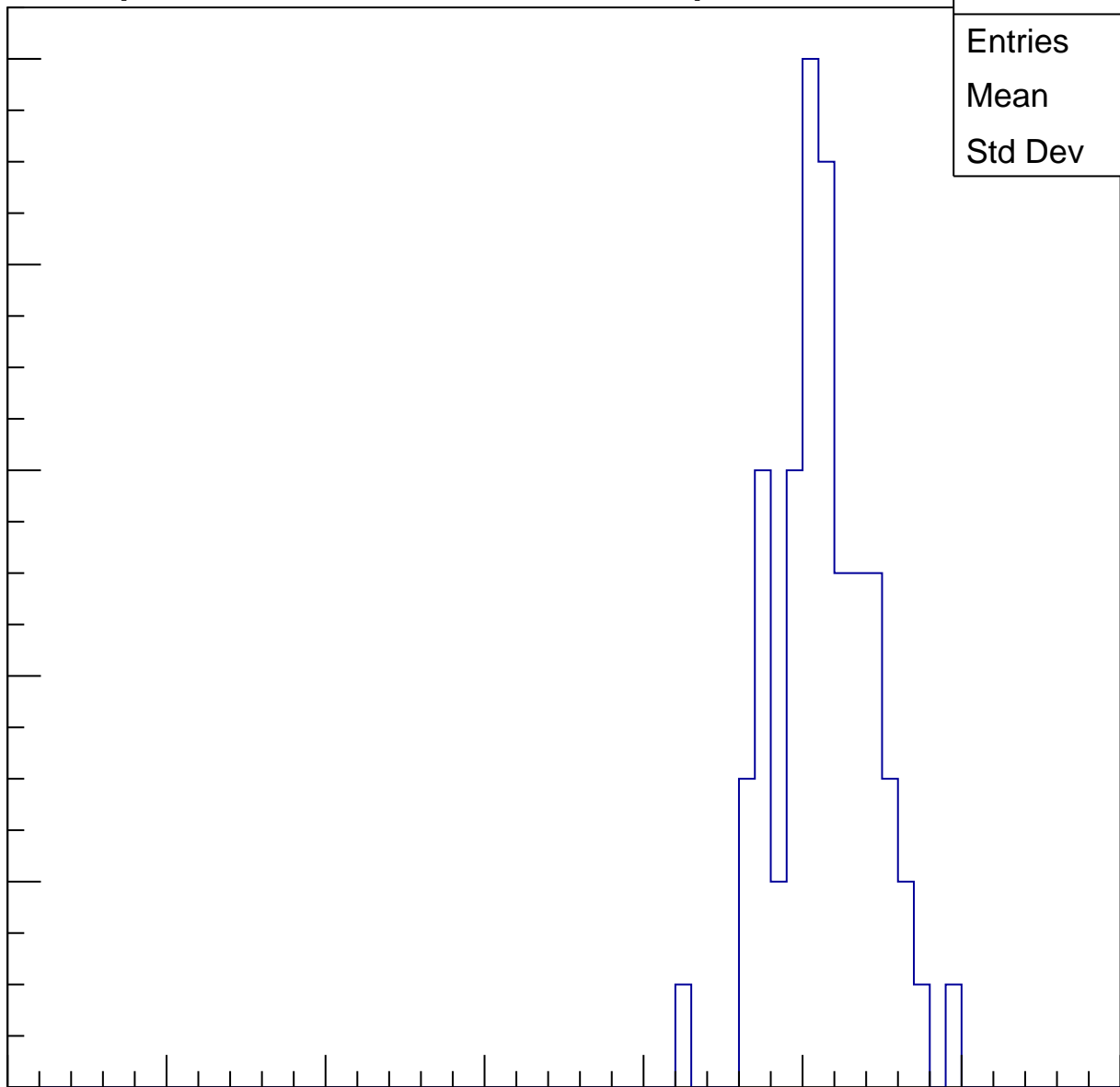
Entries	59
Mean	50.83
Std Dev	3.109

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

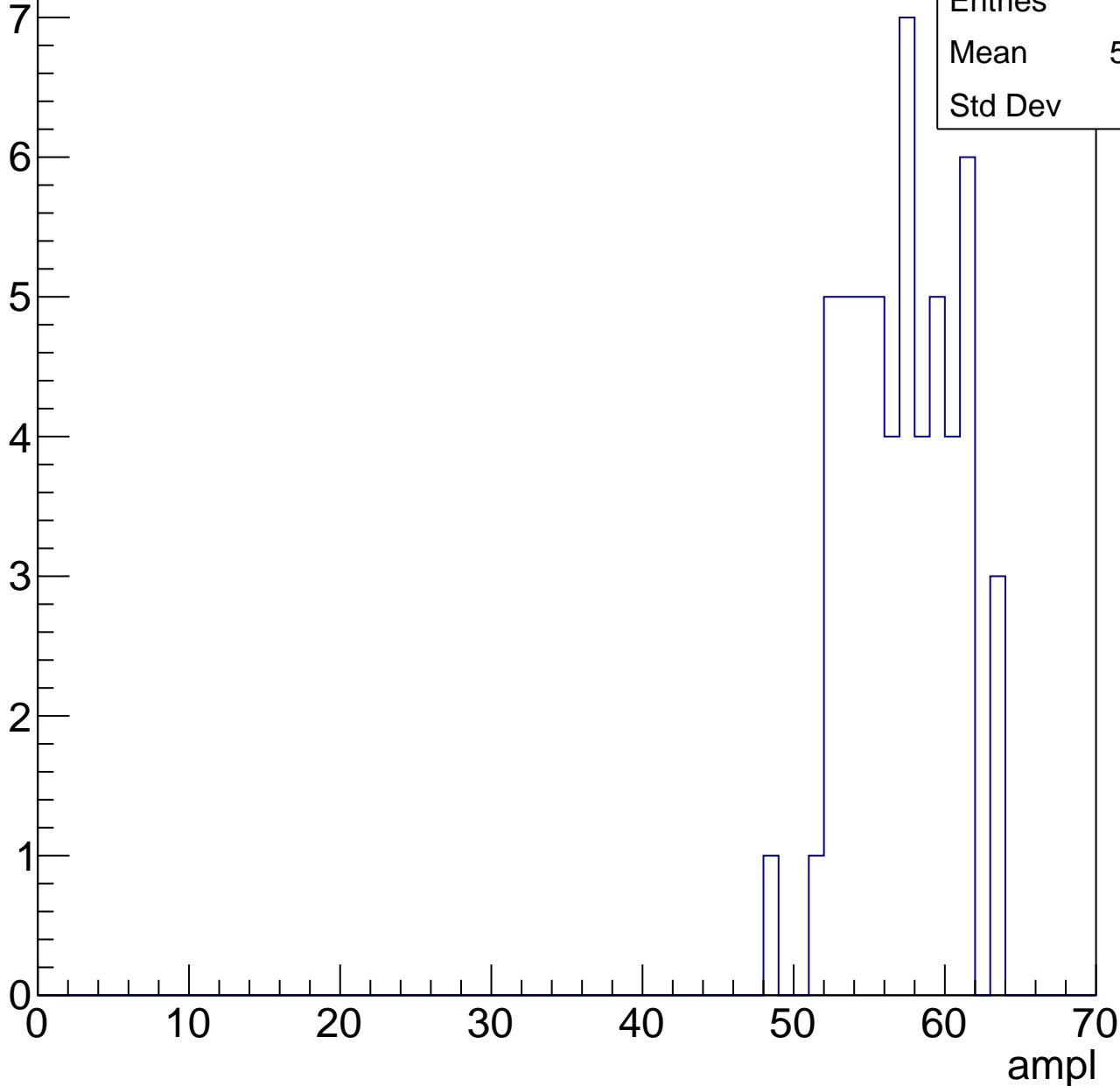


B1L102S, U22-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.62
Std Dev	3.43

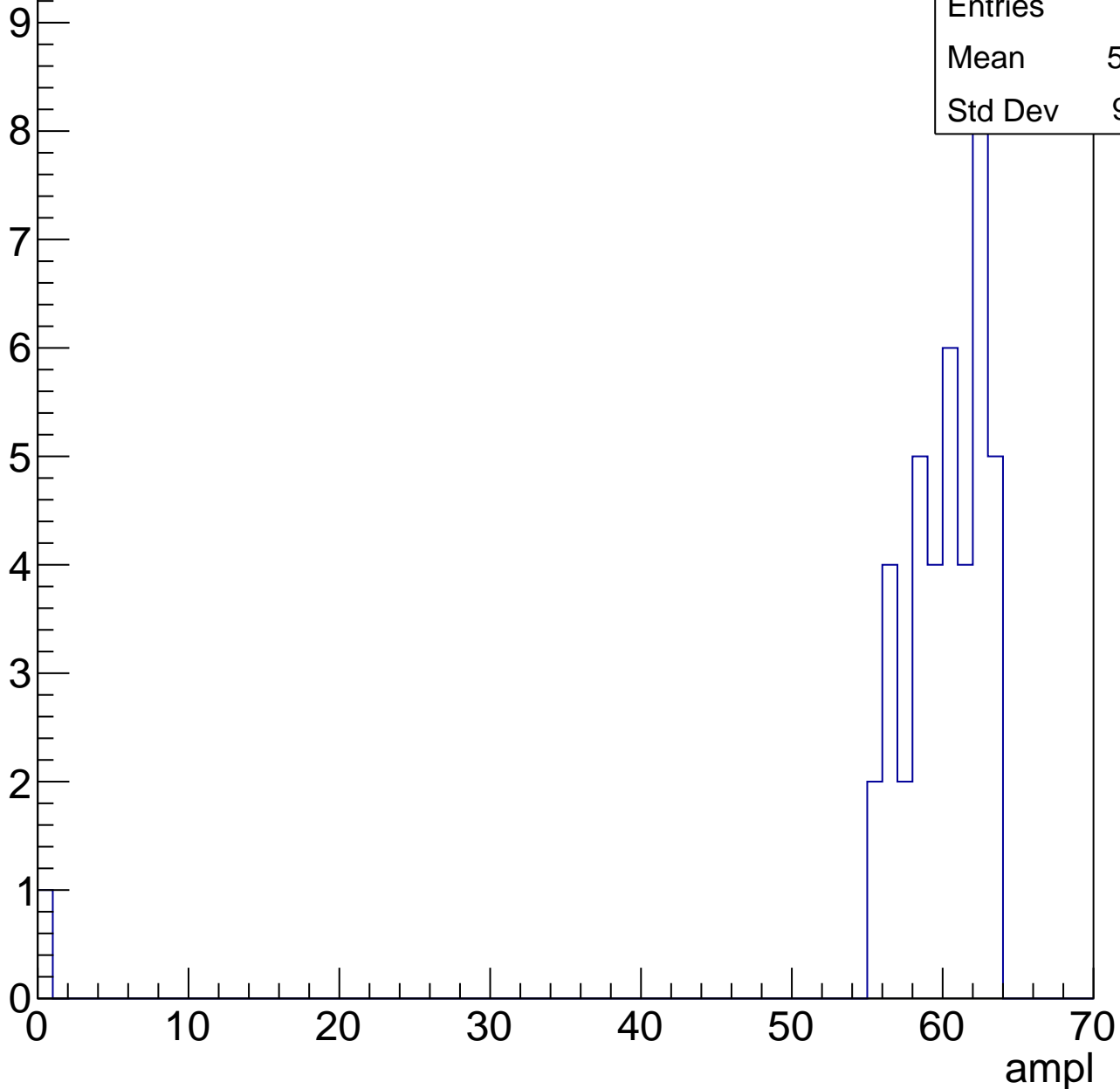


B1L102S, U22-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.36
Std Dev	9.421



B1L102S, U22-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch5, adc0

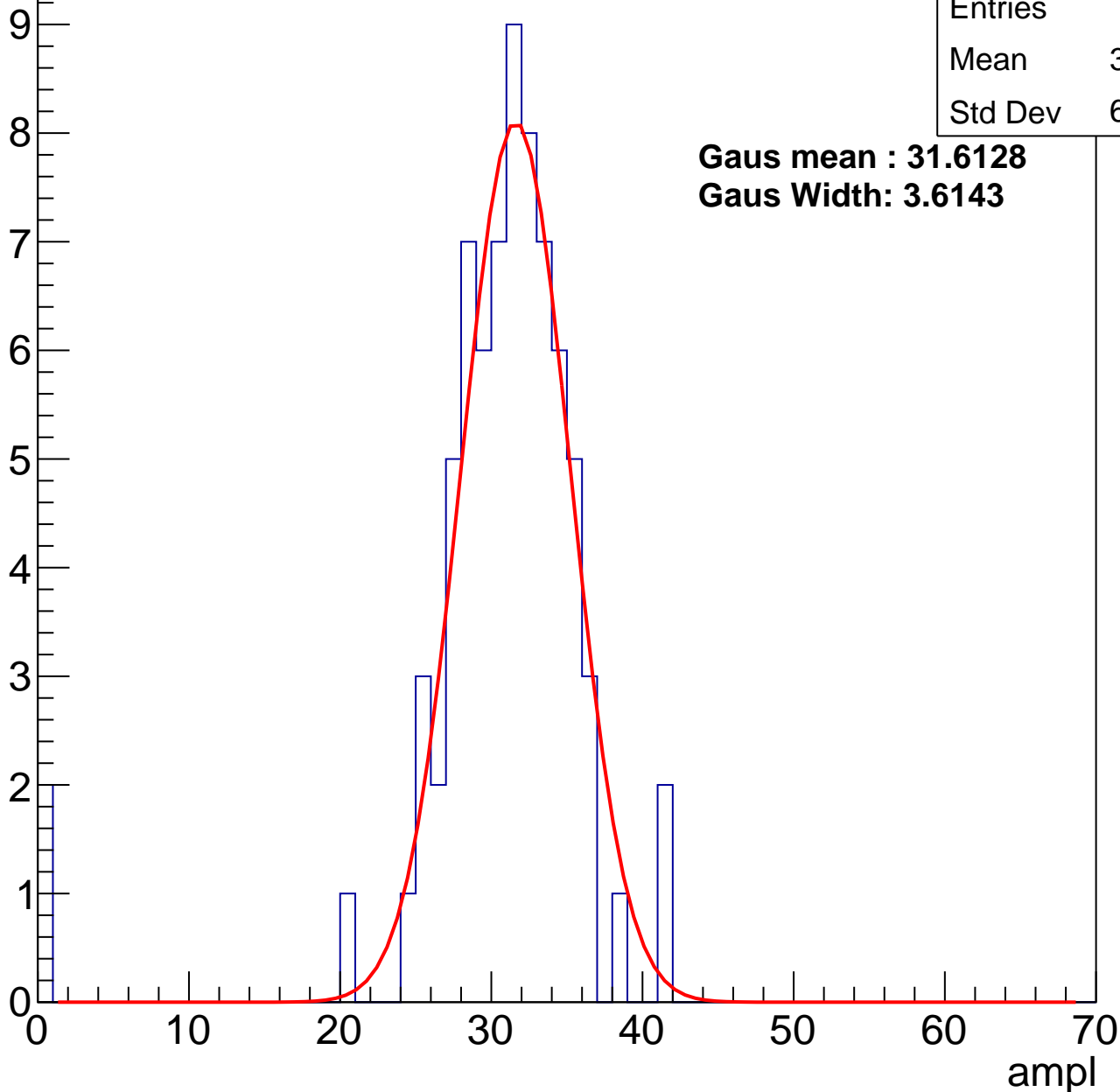
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	30.12
Std Dev	6.175

Gaus mean : 31.6128

Gaus Width: 3.6143



B1L102S, U22-ch5, adc1

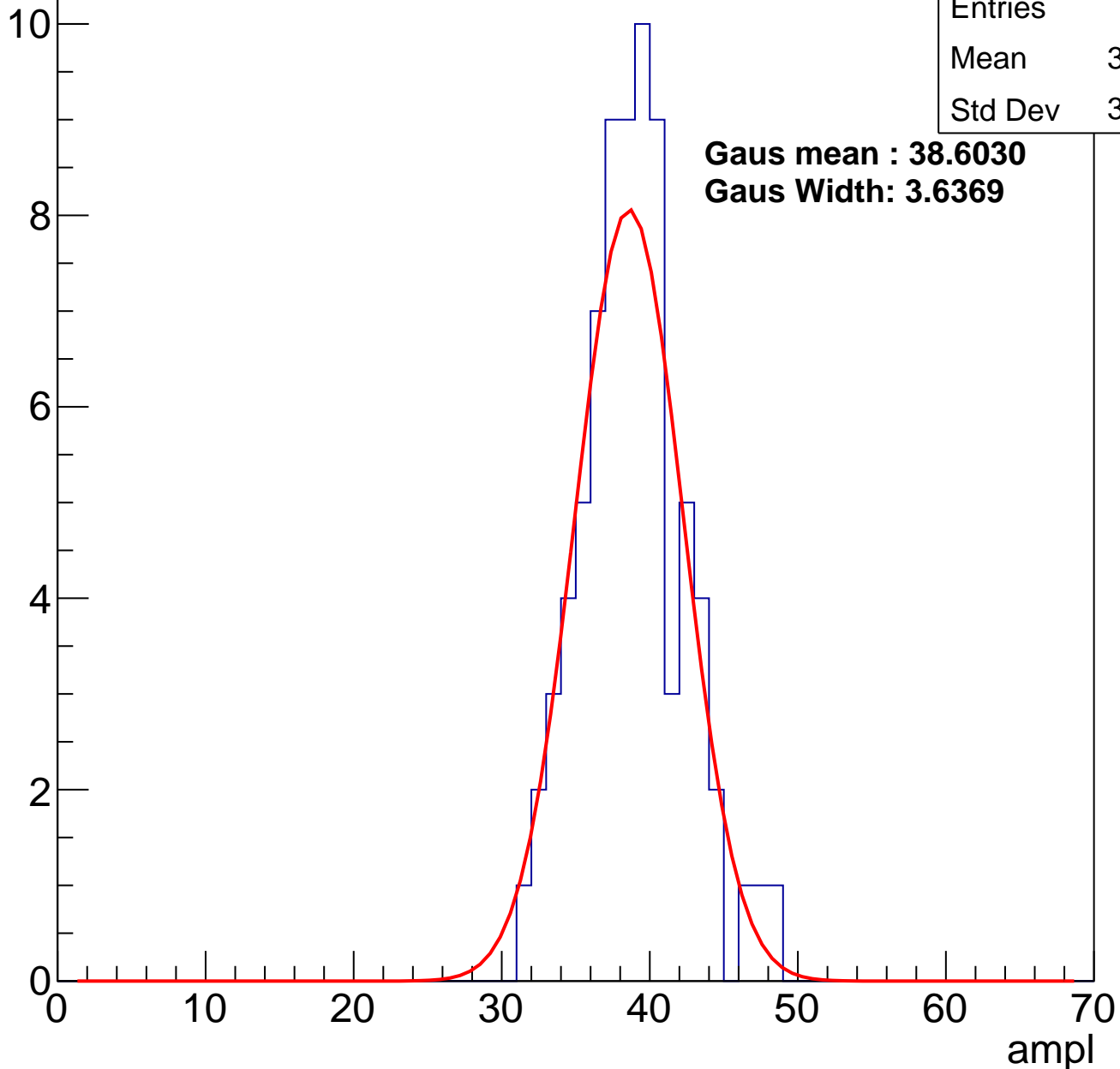
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	38.37
Std Dev	3.444

Gaus mean : 38.6030

Gaus Width: 3.6369

Entry



B1L102S, U22-ch5, adc2

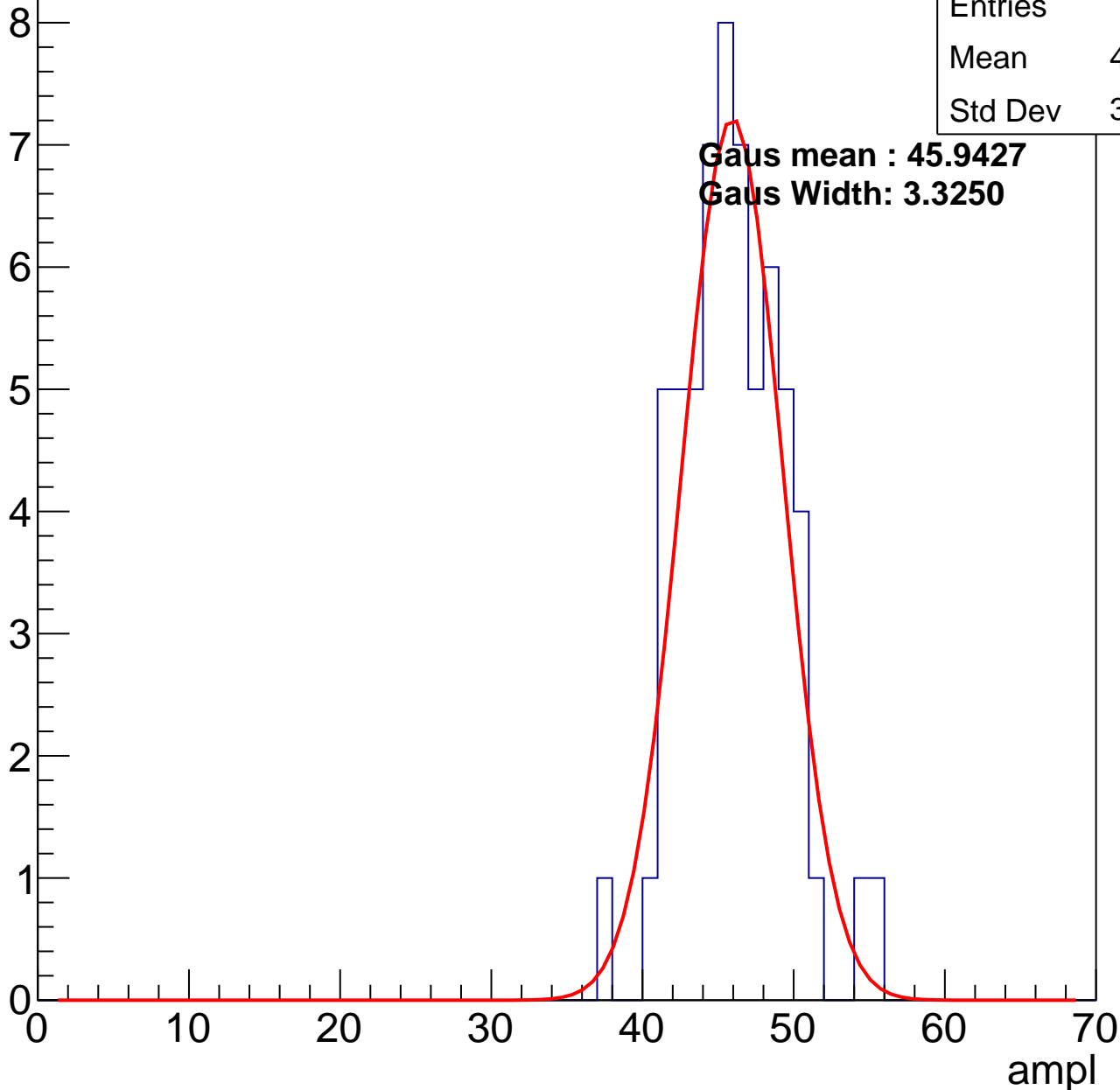
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	45.56
Std Dev	3.358

Gaus mean : 45.9427

Gaus Width: 3.3250

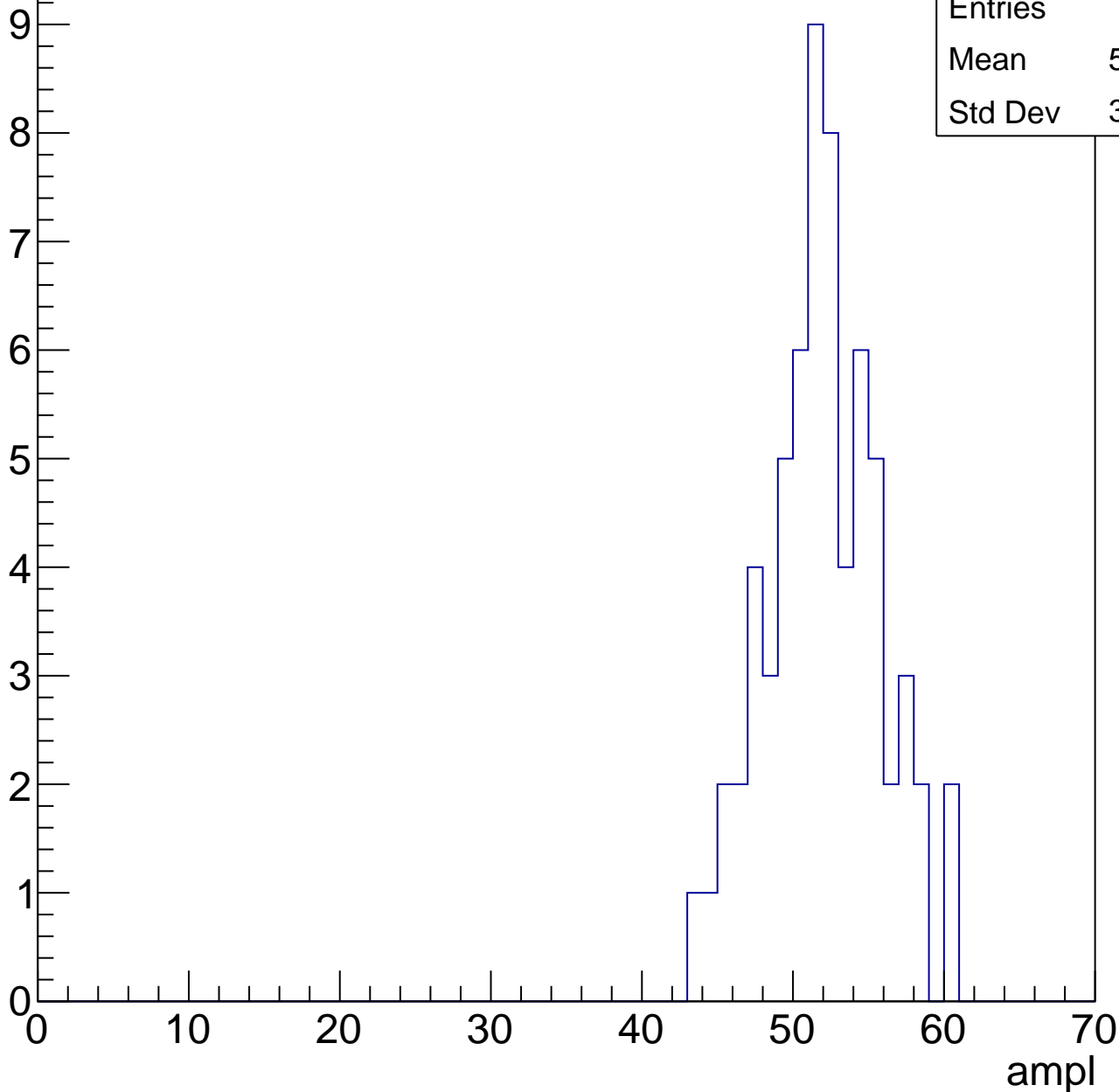


B1L102S, U22-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	51.55
Std Dev	3.738

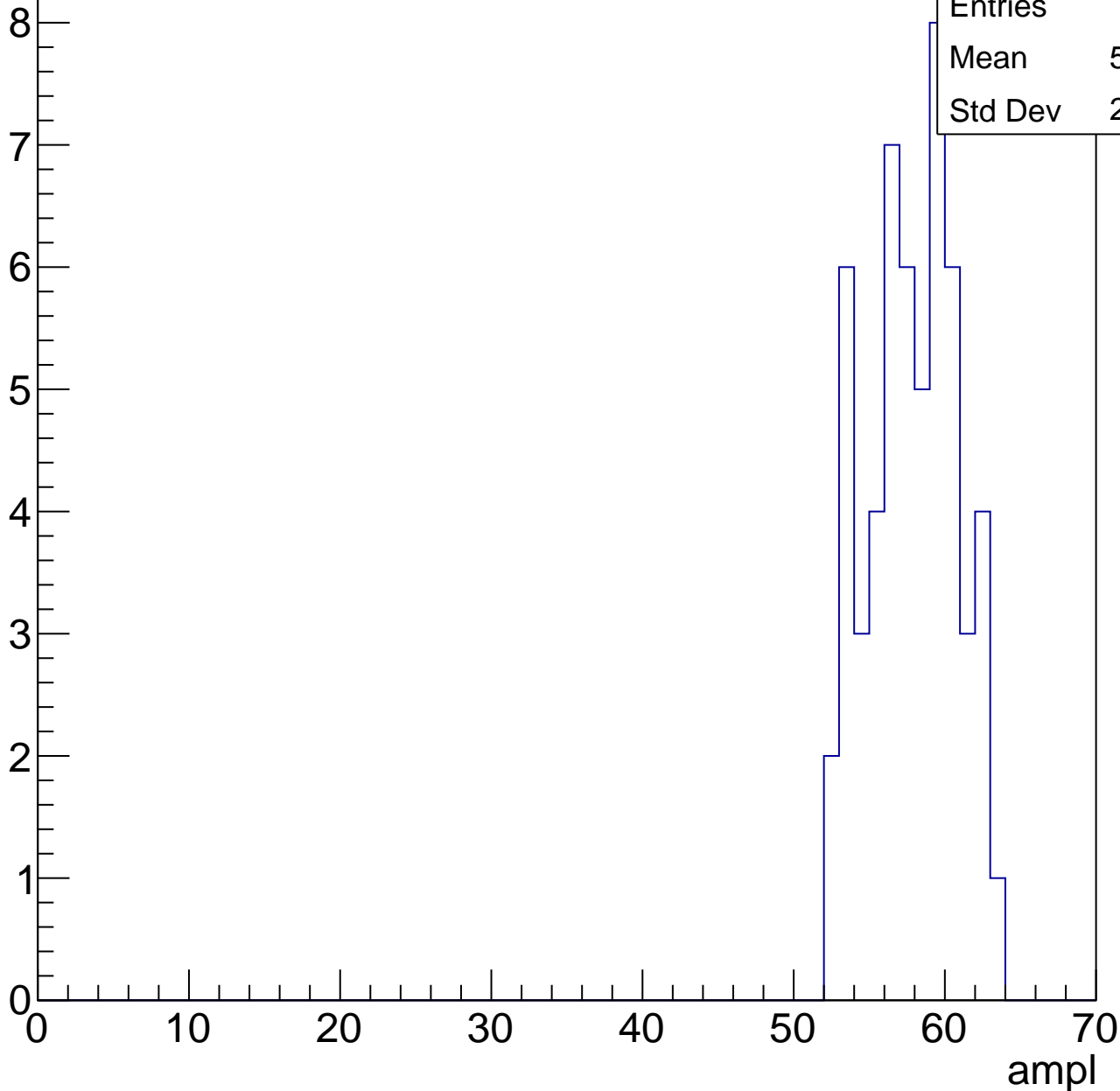


B1L102S, U22-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	57.35
Std Dev	2.906

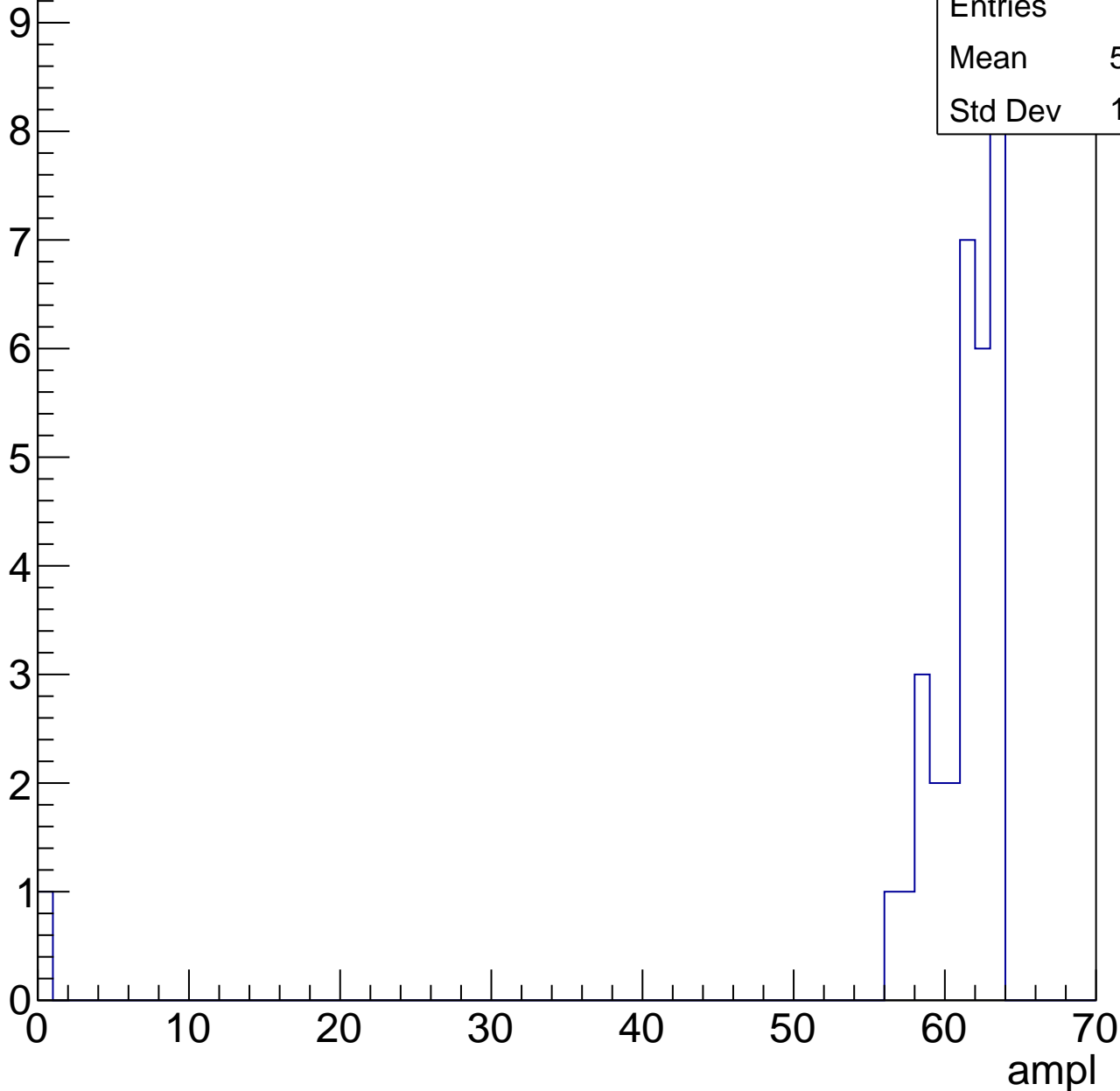


B1L102S, U22-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	32
Mean	59.09
Std Dev	10.79



B1L102S, U22-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch6, adc0

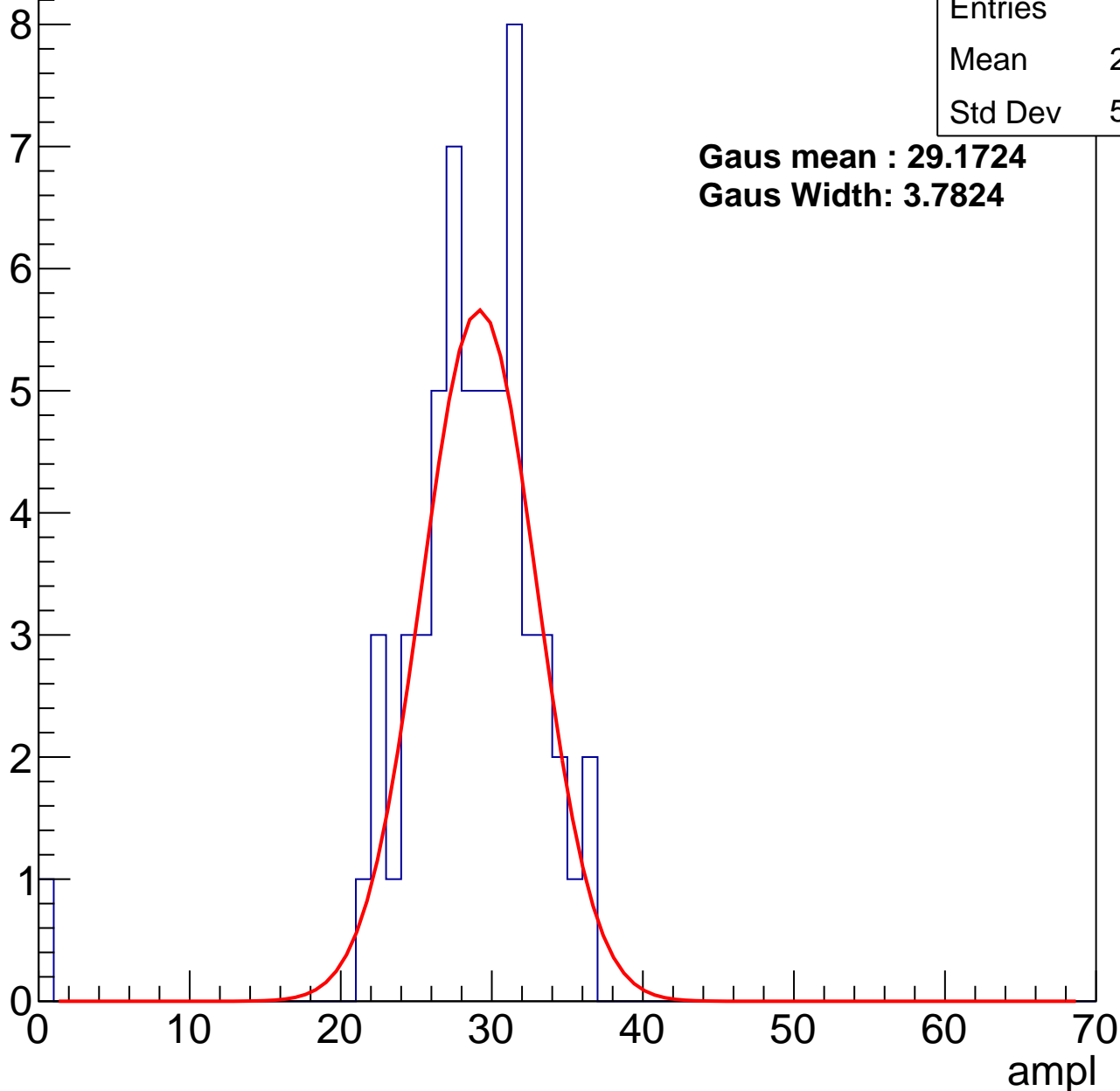
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.09
Std Dev	5.147

Gaus mean : 29.1724

Gaus Width: 3.7824



B1L102S, U22-ch6, adc1

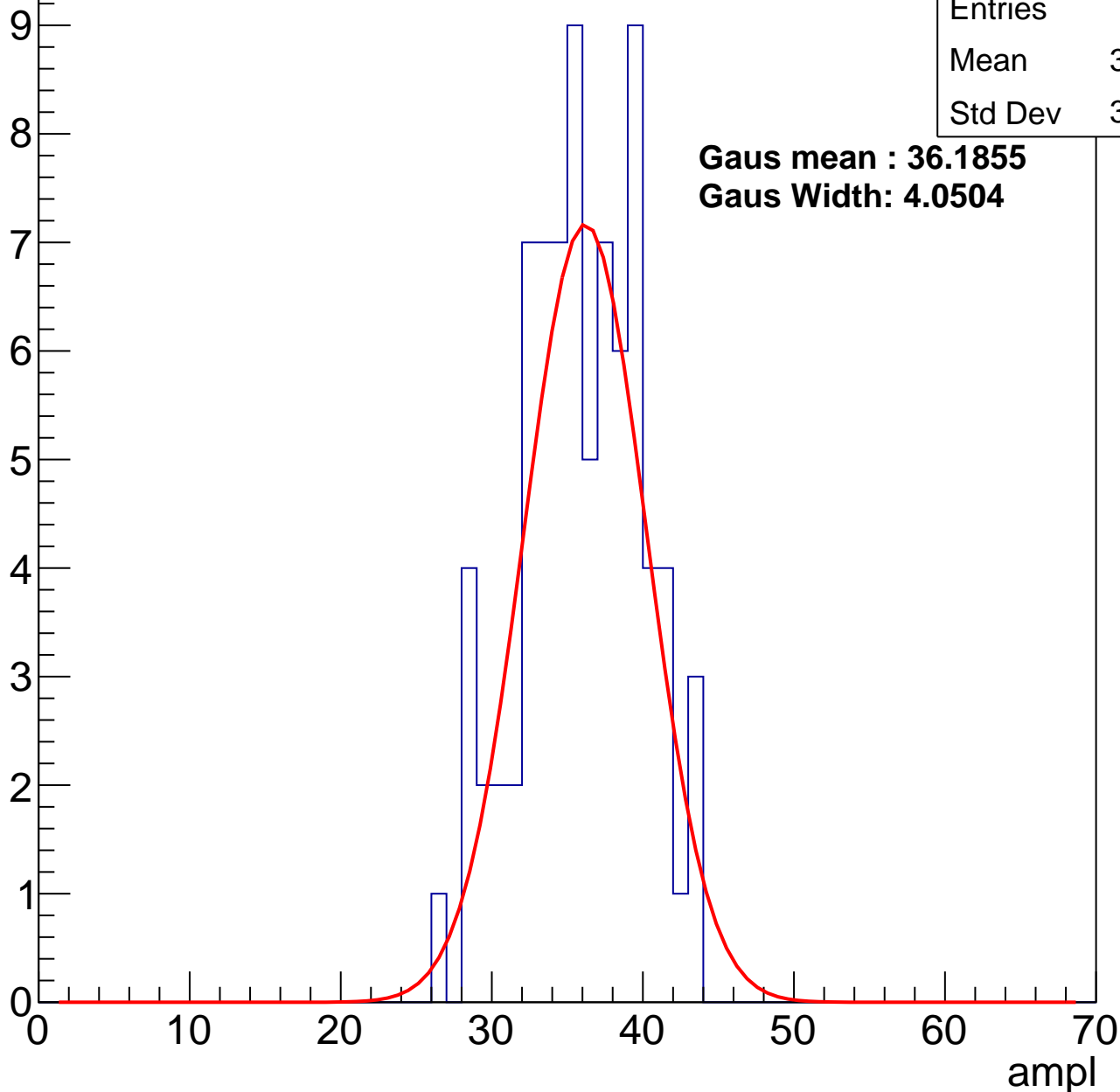
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.49
Std Dev	3.915

Gaus mean : 36.1855

Gaus Width: 4.0504



B1L102S, U22-ch6, adc2

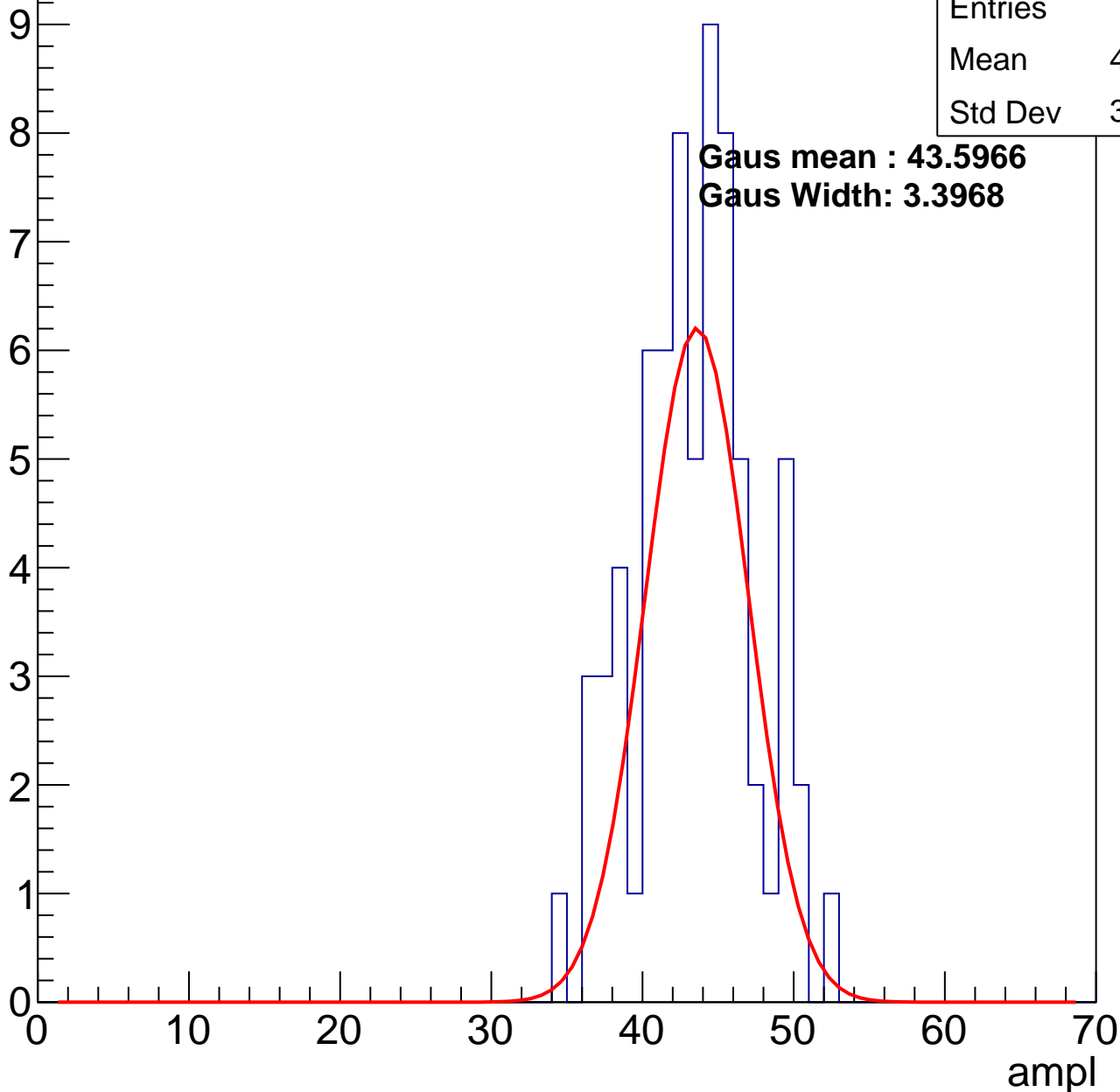
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.94
Std Dev	3.854

Gaus mean : 43.5966

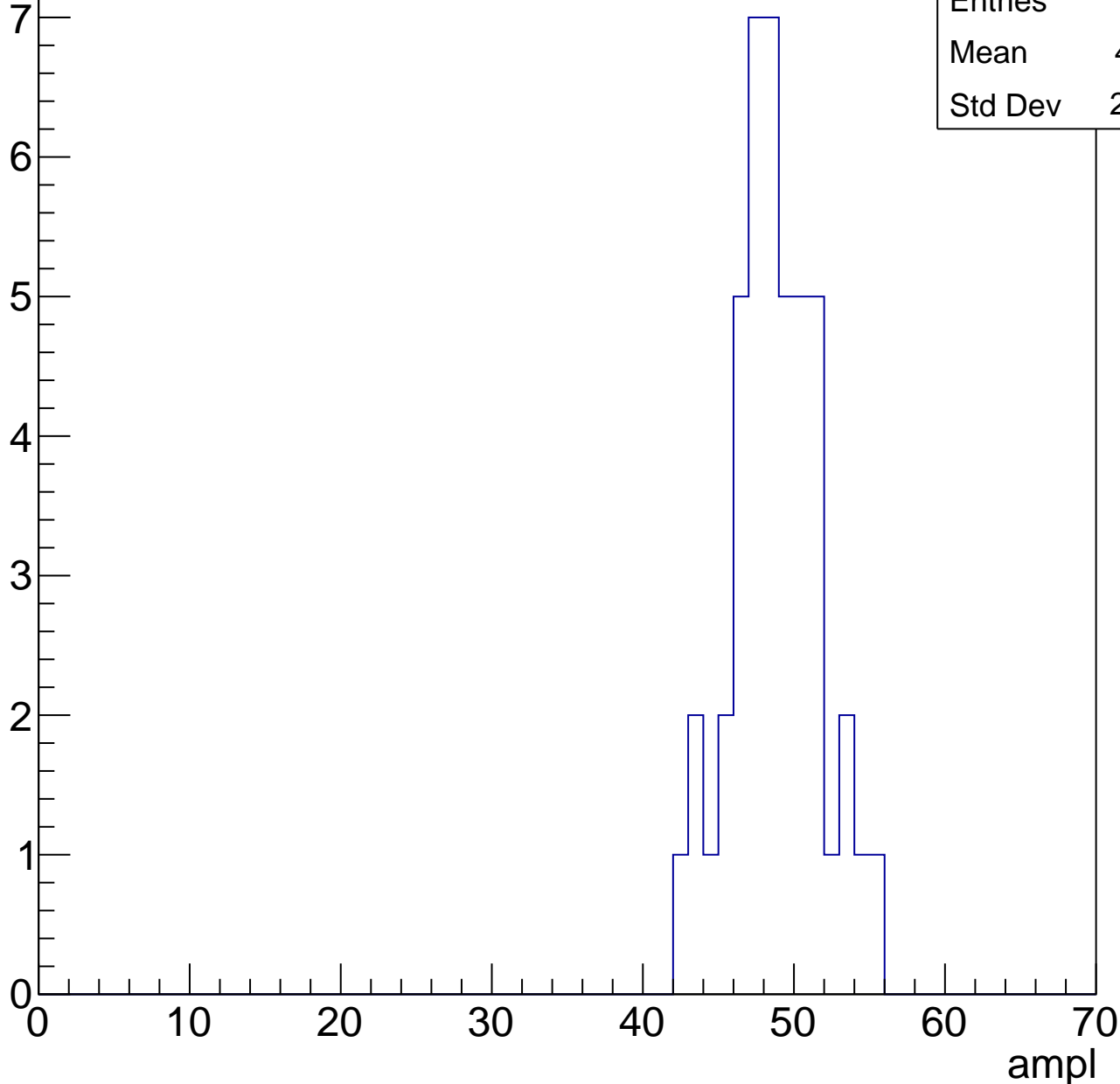
Gaus Width: 3.3968



B1L102S, U22-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



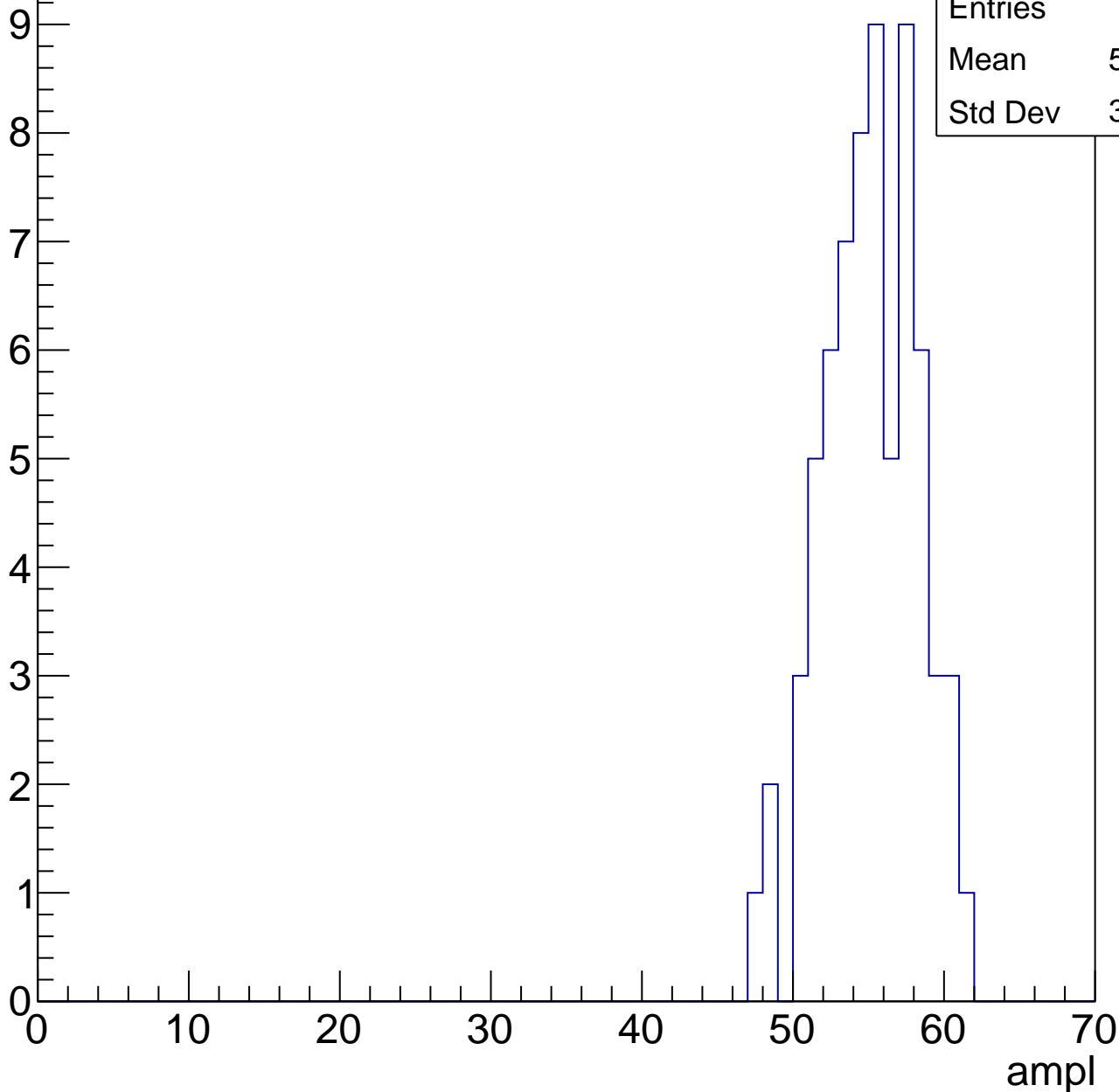
Entries	45
Mean	48.31
Std Dev	2.843

B1L102S, U22-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.66
Std Dev	3.099

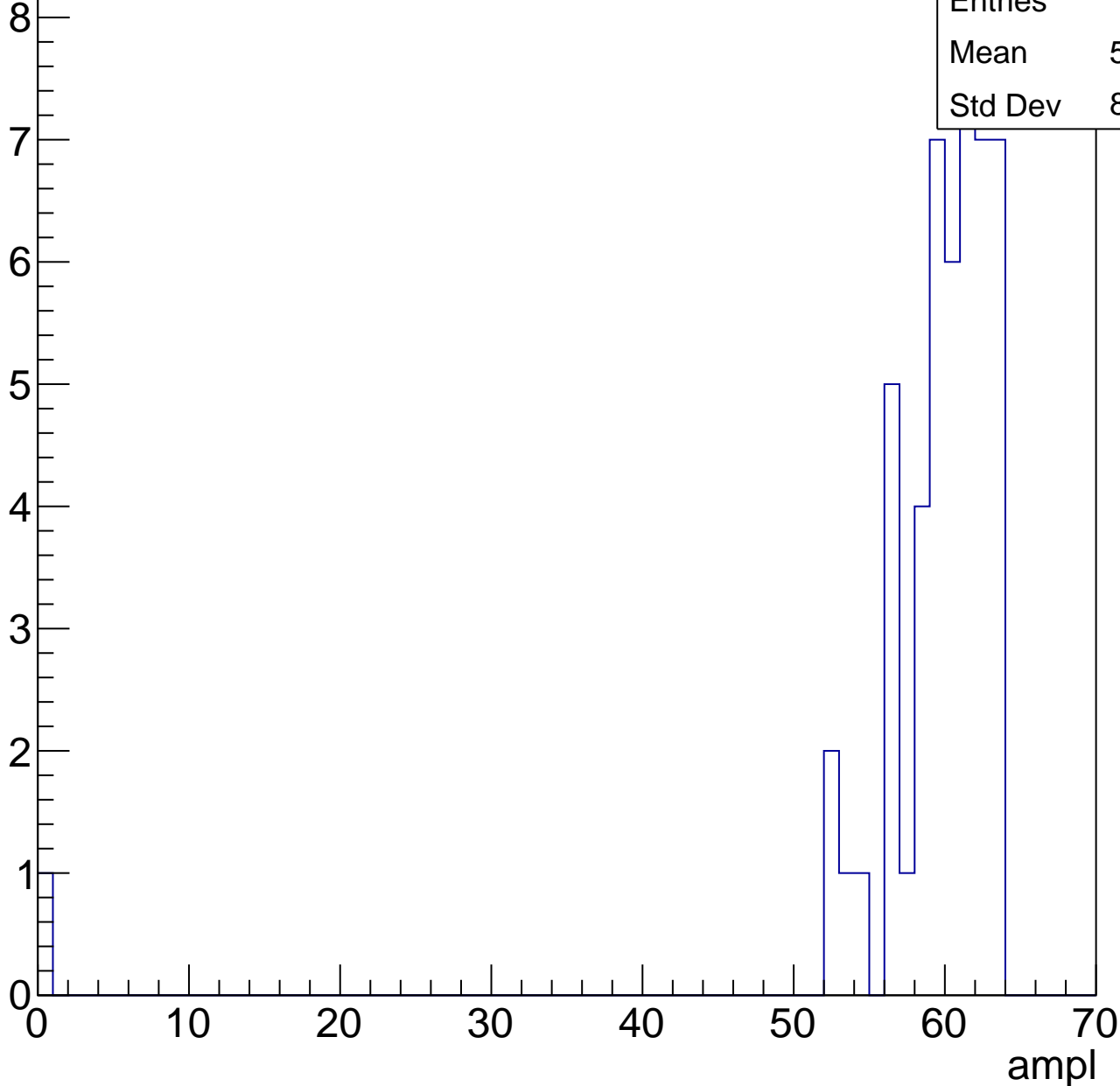


B1L102S, U22-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.32
Std Dev	8.812



B1L102S, U22-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

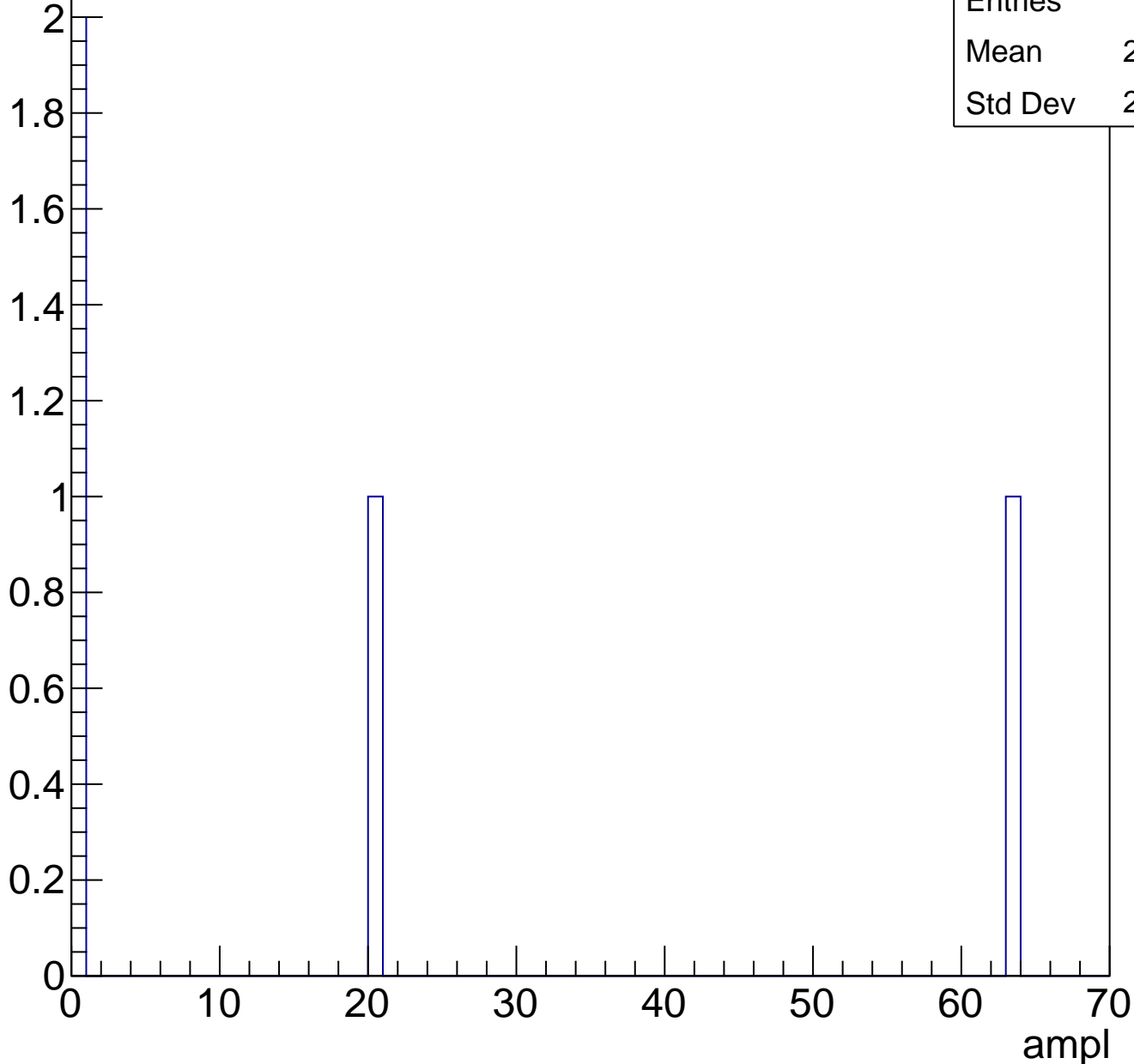
Entry



B1L102S, U22-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	20.75
Std Dev	25.72

B1L102S, U22-ch7, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	31.25
Std Dev	4.82

Gaus mean : 32.4816

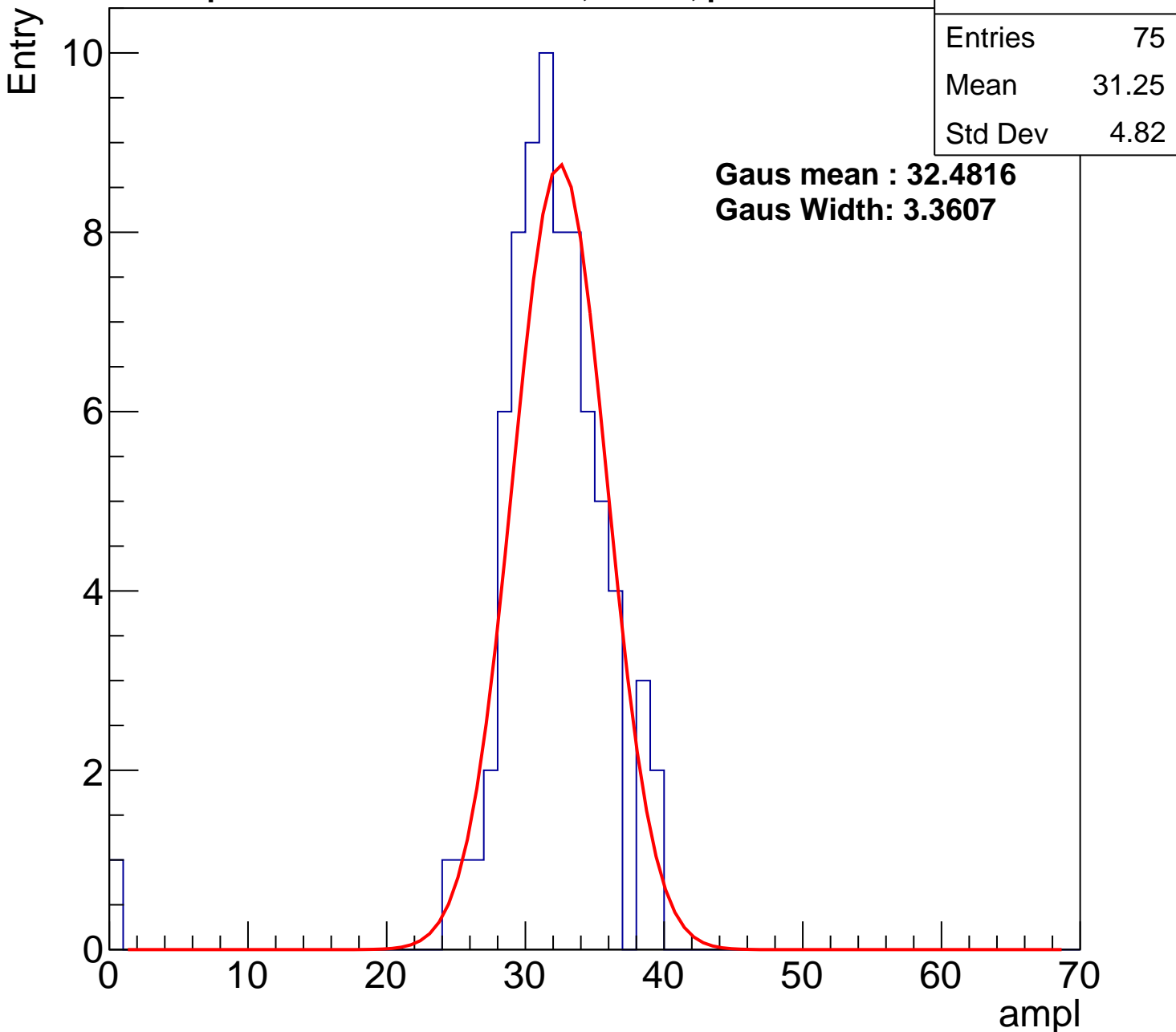
Gaus Width: 3.3607

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch7, adc1

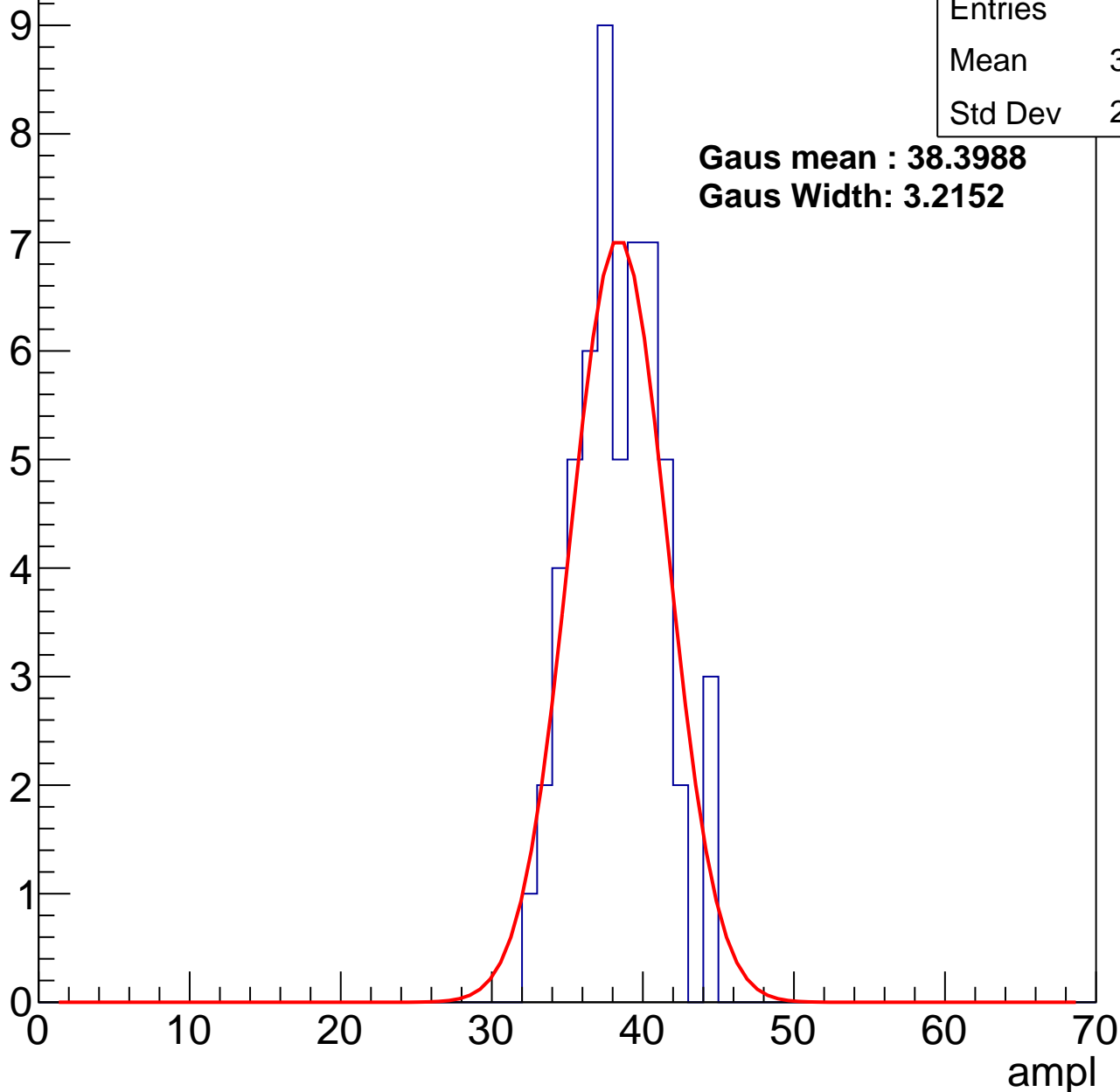
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	37.89
Std Dev	2.826

Gaus mean : 38.3988

Gaus Width: 3.2152



B1L102S, U22-ch7, adc2

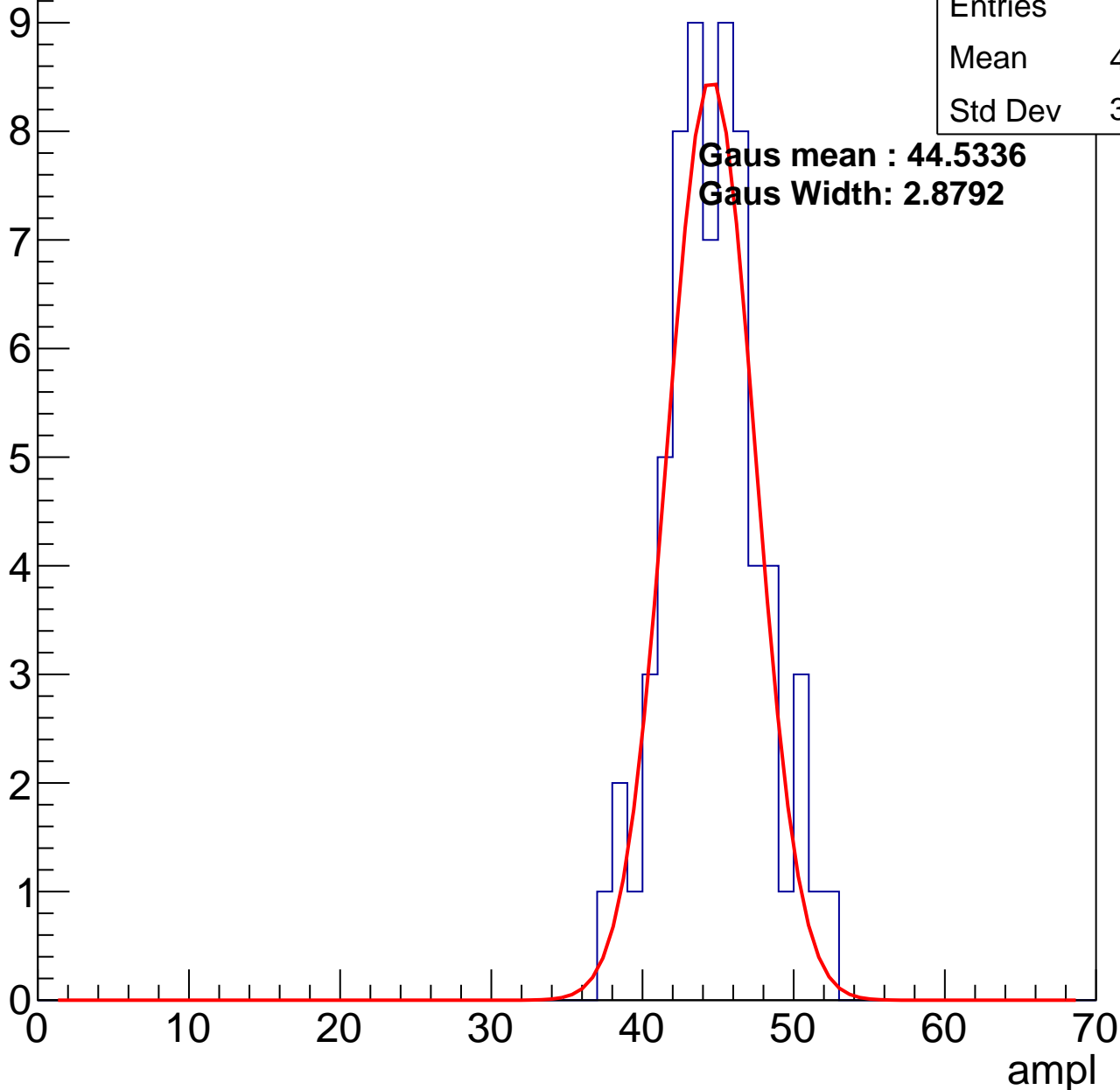
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.22
Std Dev	3.147

Gaus mean : 44.5336

Gaus Width: 2.8792

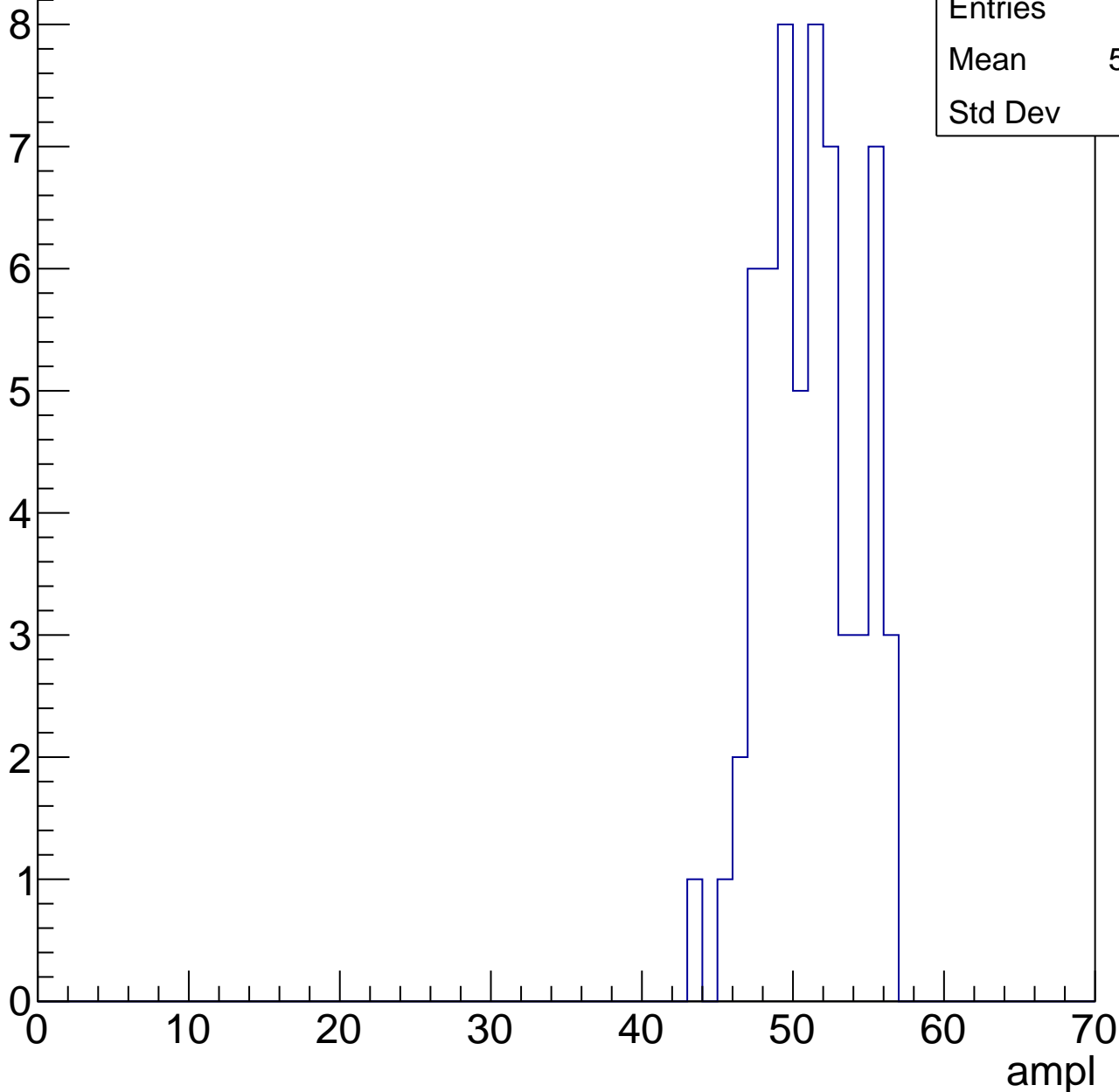


B1L102S, U22-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	50.63
Std Dev	3.06

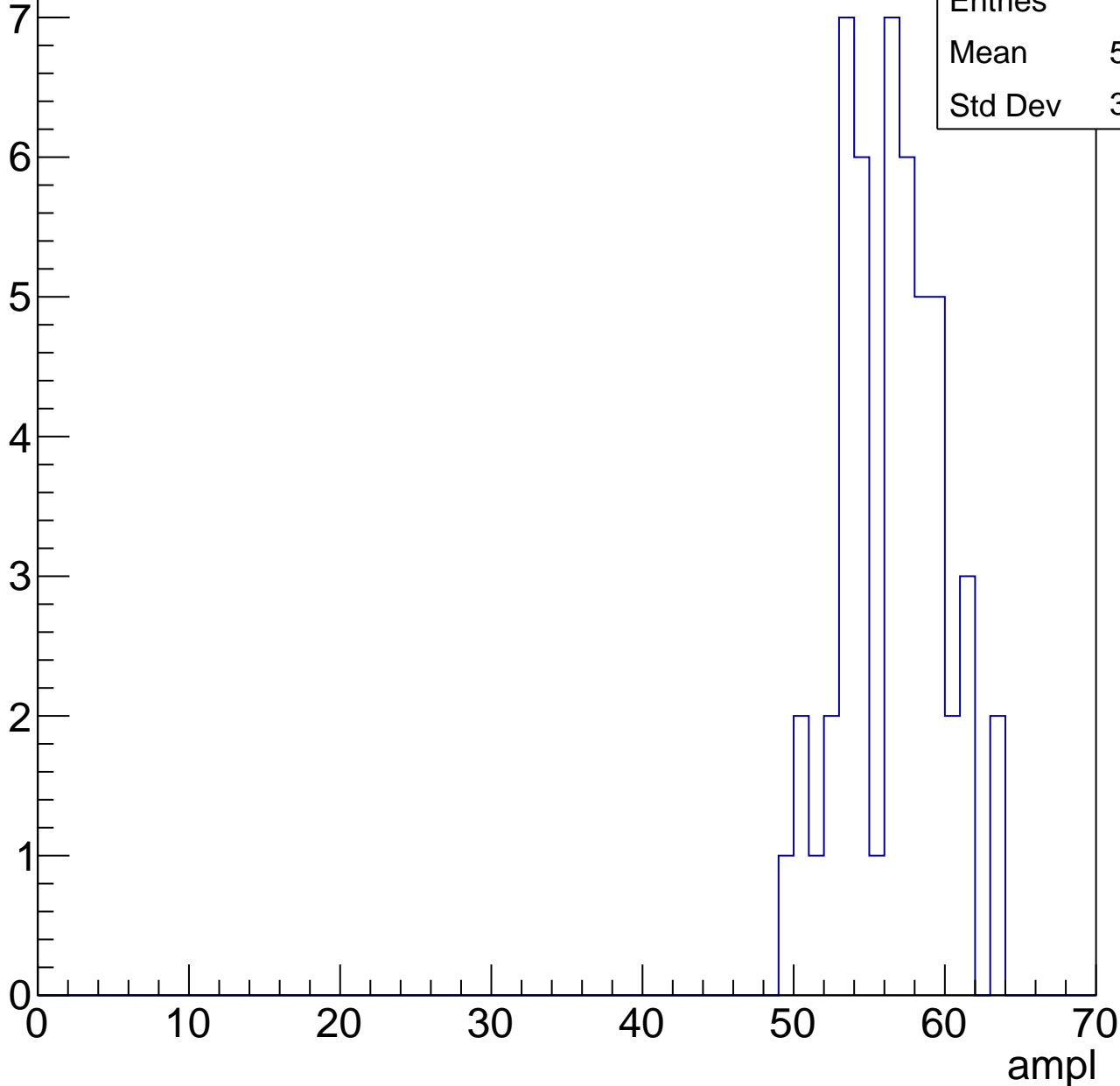


B1L102S, U22-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	56.04
Std Dev	3.292



B1L102S, U22-ch7, adc5

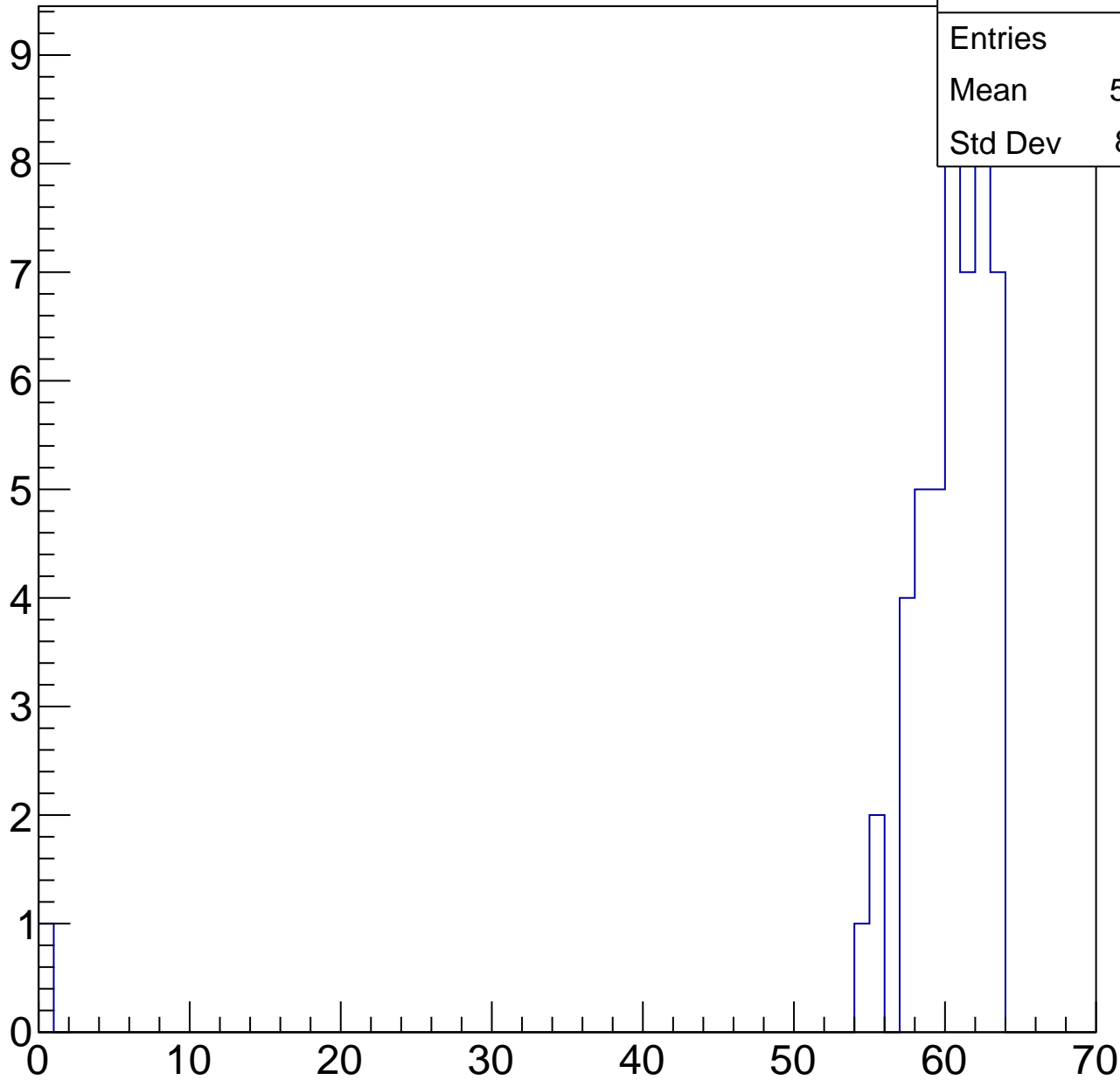
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.84
Std Dev	8.791

ampl



B1L102S, U22-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch8, adc0

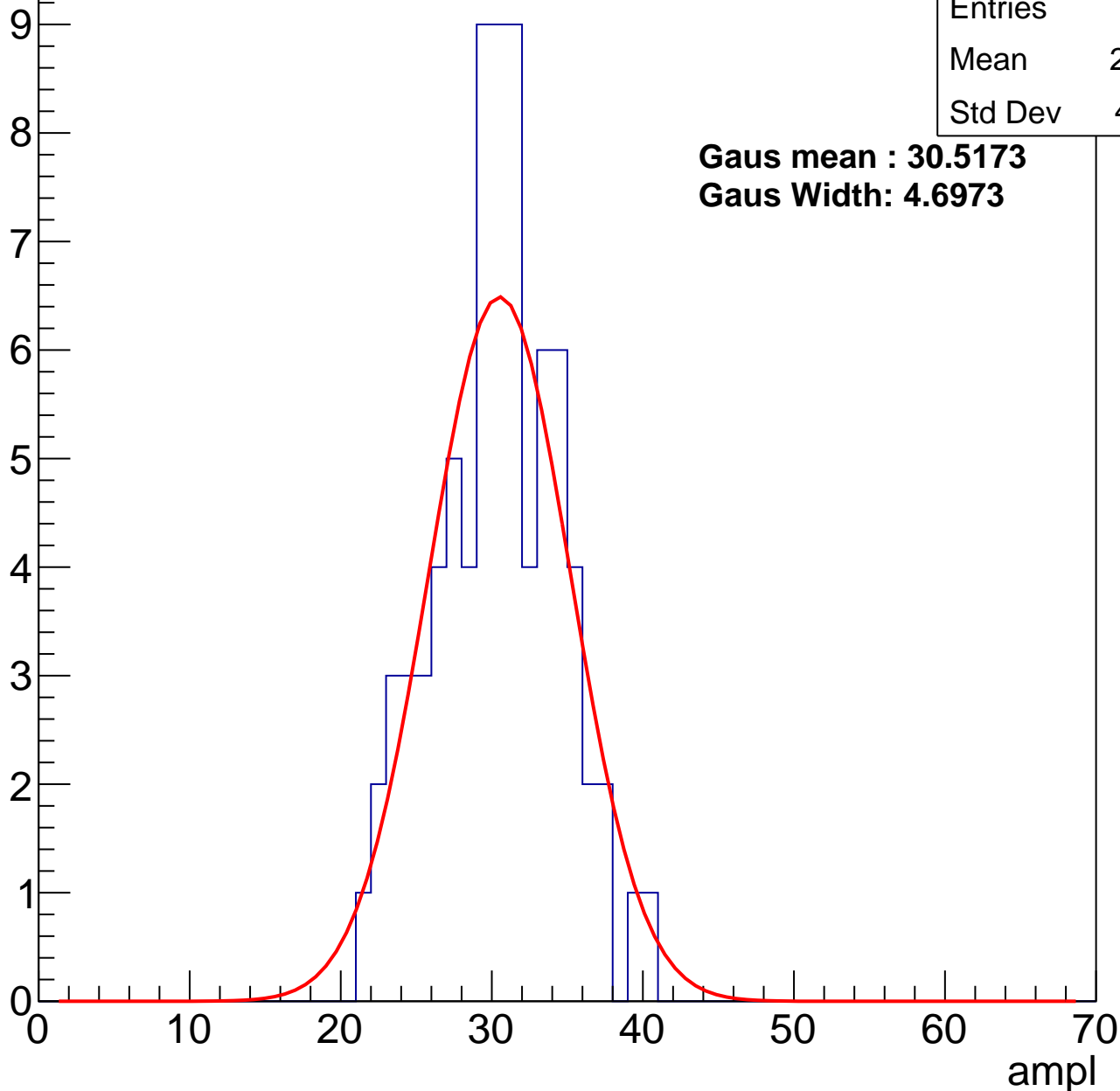
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.96
Std Dev	4.081

Gaus mean : 30.5173

Gaus Width: 4.6973



B1L102S, U22-ch8, adc1

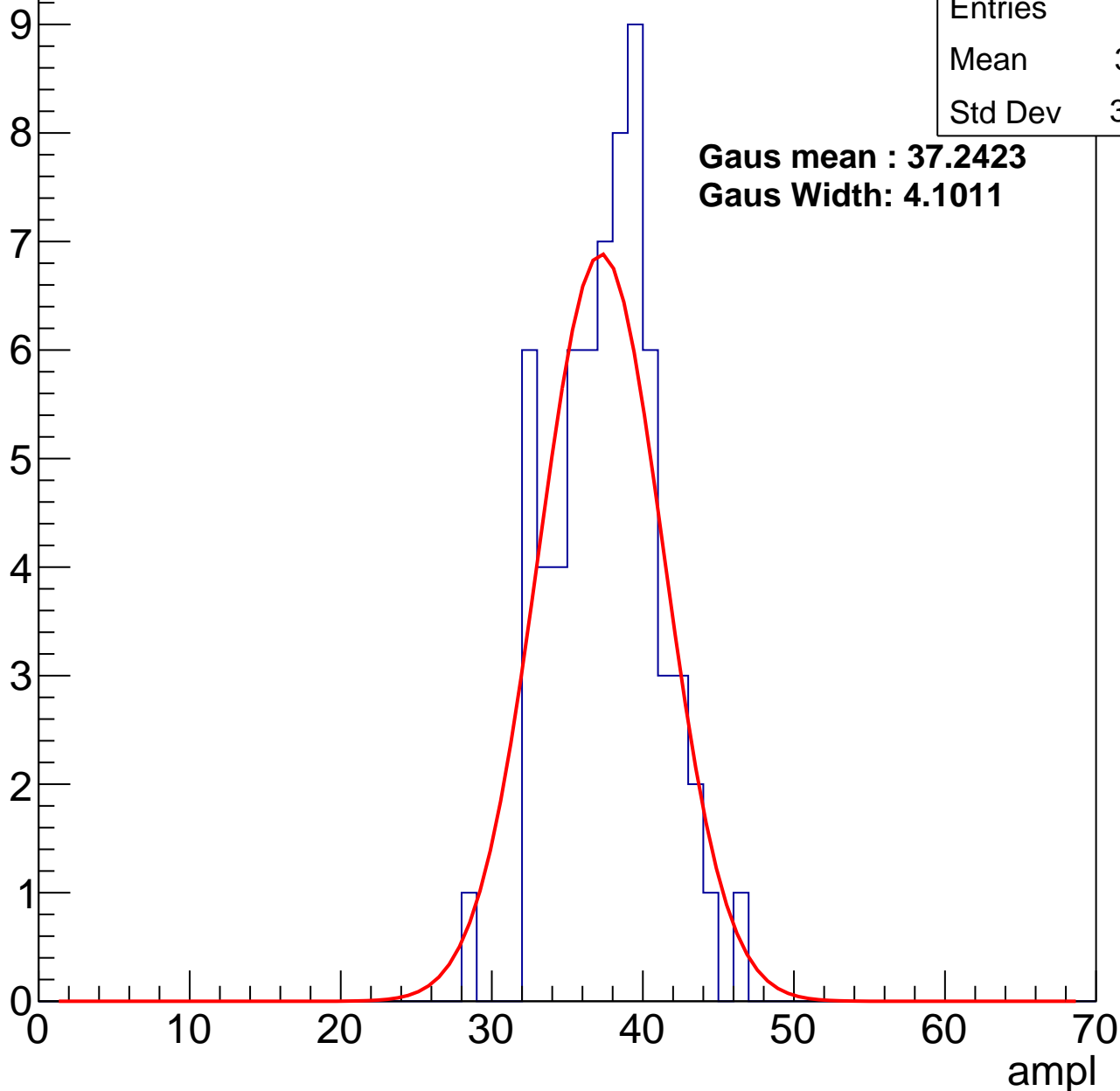
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	37.21
Std Dev	3.414

Gaus mean : 37.2423

Gaus Width: 4.1011



B1L102S, U22-ch8, adc2

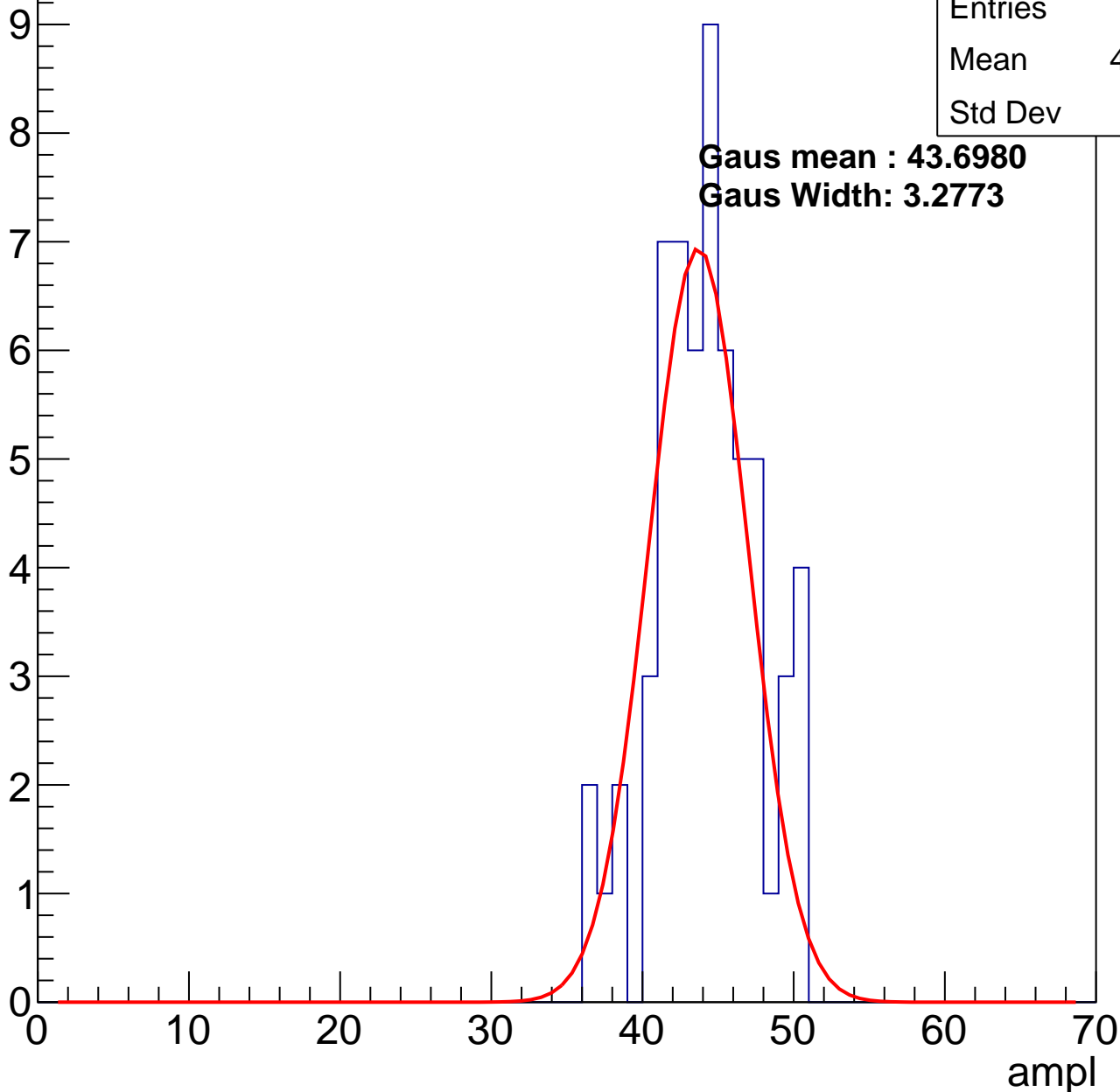
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.77
Std Dev	3.38

Gaus mean : 43.6980

Gaus Width: 3.2773

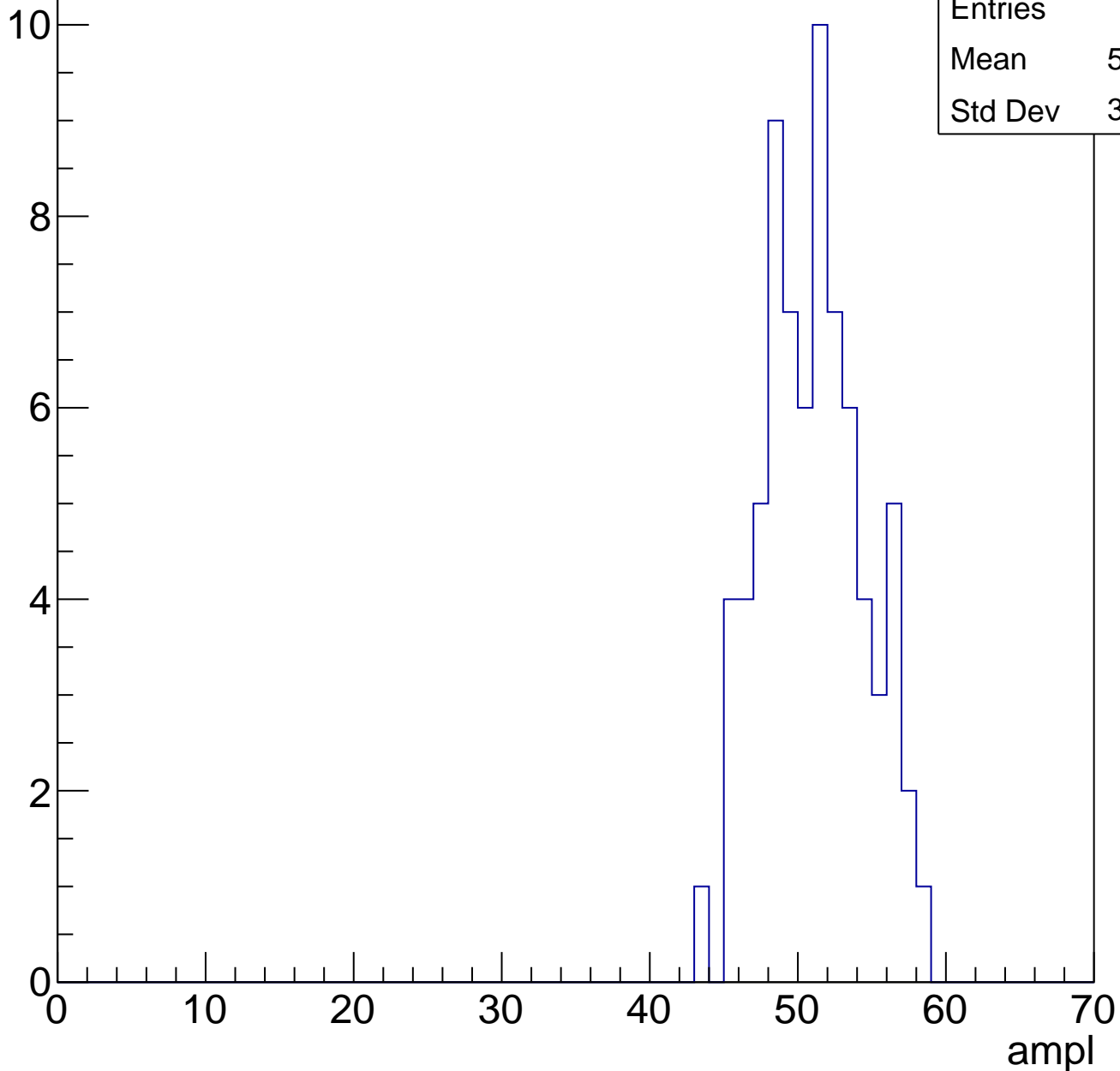


B1L102S, U22-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	50.57
Std Dev	3.389

Entry

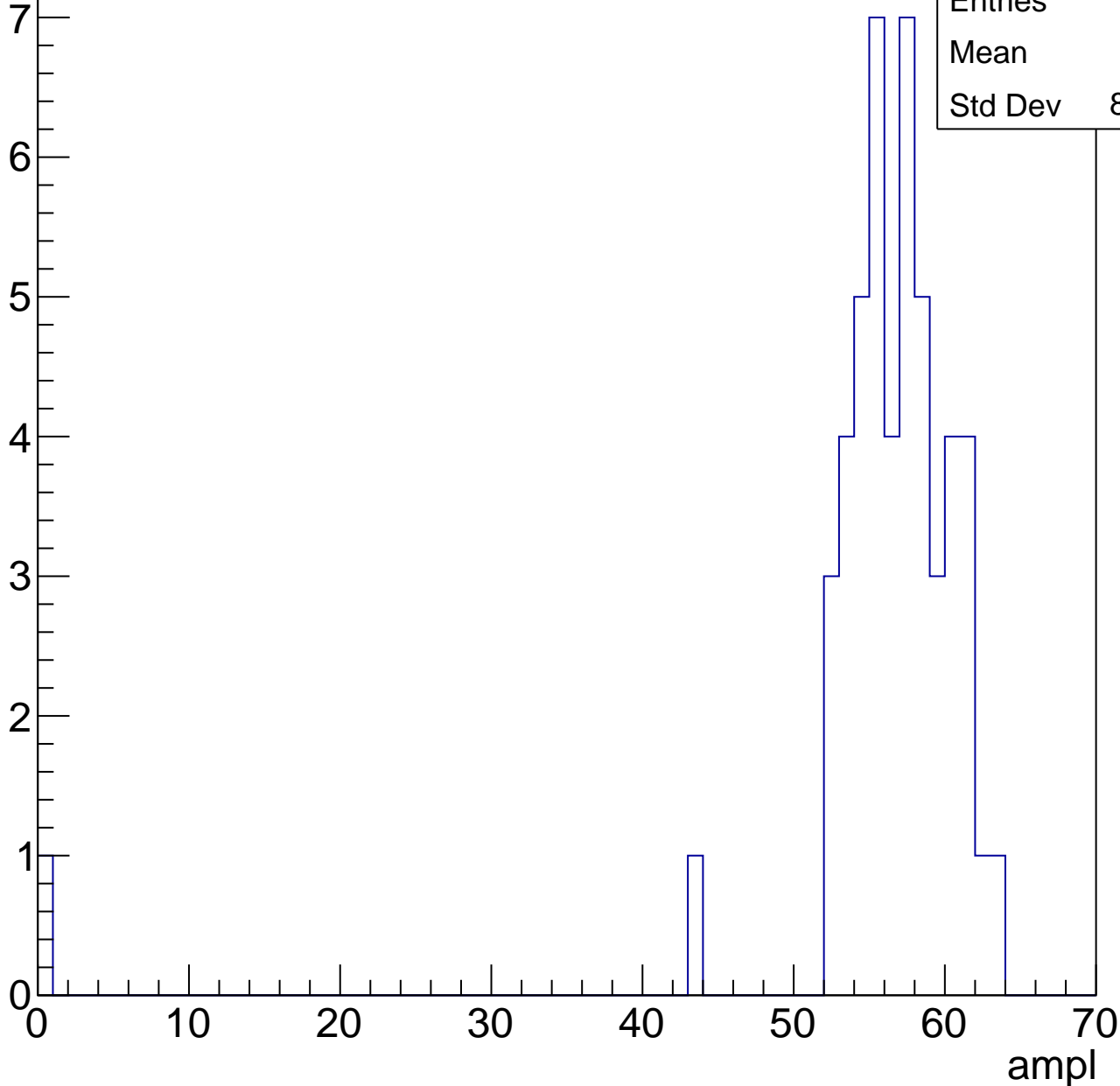


B1L102S, U22-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.3
Std Dev	8.596



B1L102S, U22-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	40
Mean	60.33
Std Dev	2.102

ampl

0

10

20

30

40

50

60

70

B1L102S, U22-ch8, adc6

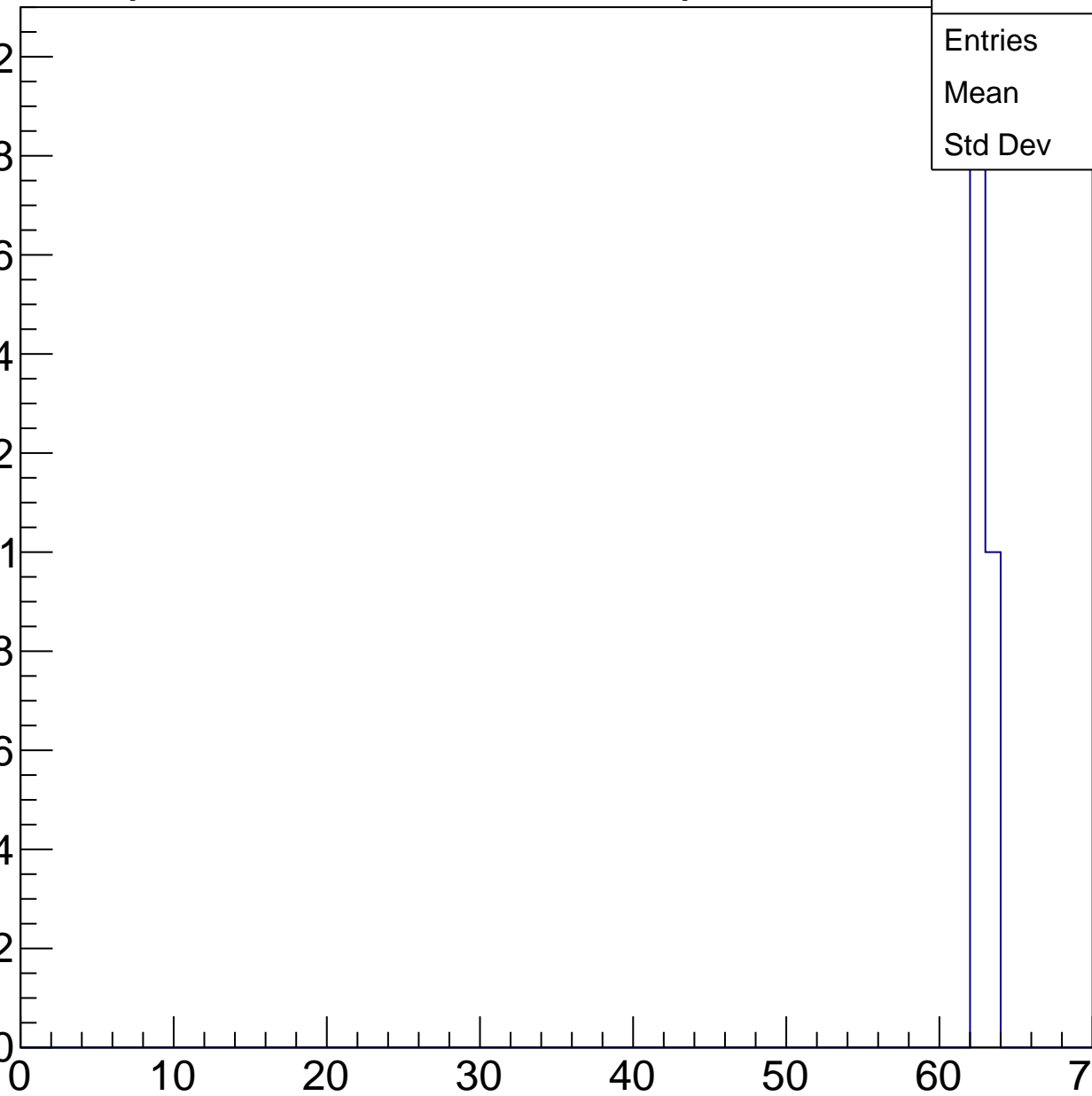
calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L102S, U22-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch9, adc0

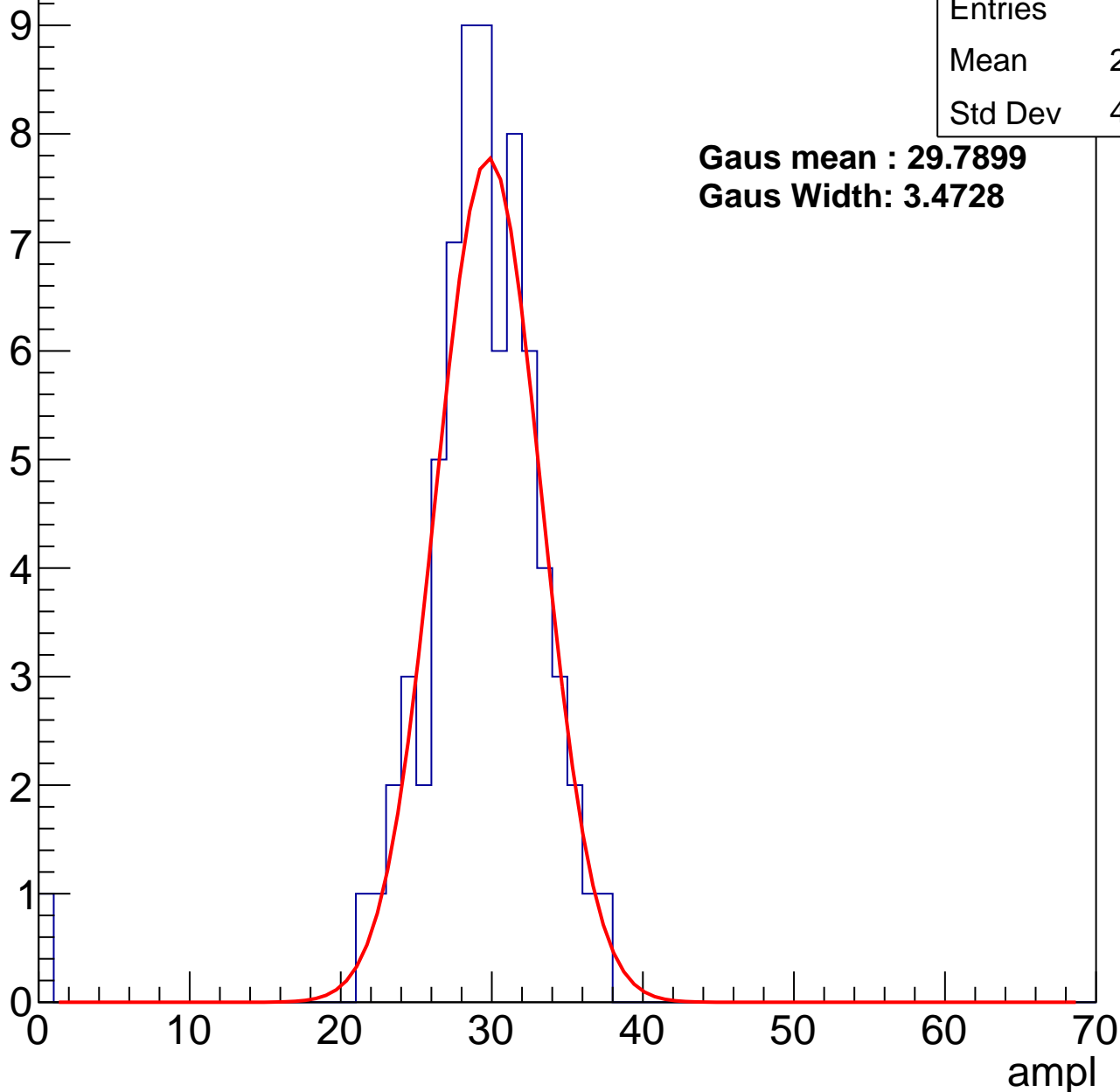
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.73
Std Dev	4.785

Gaus mean : 29.7899

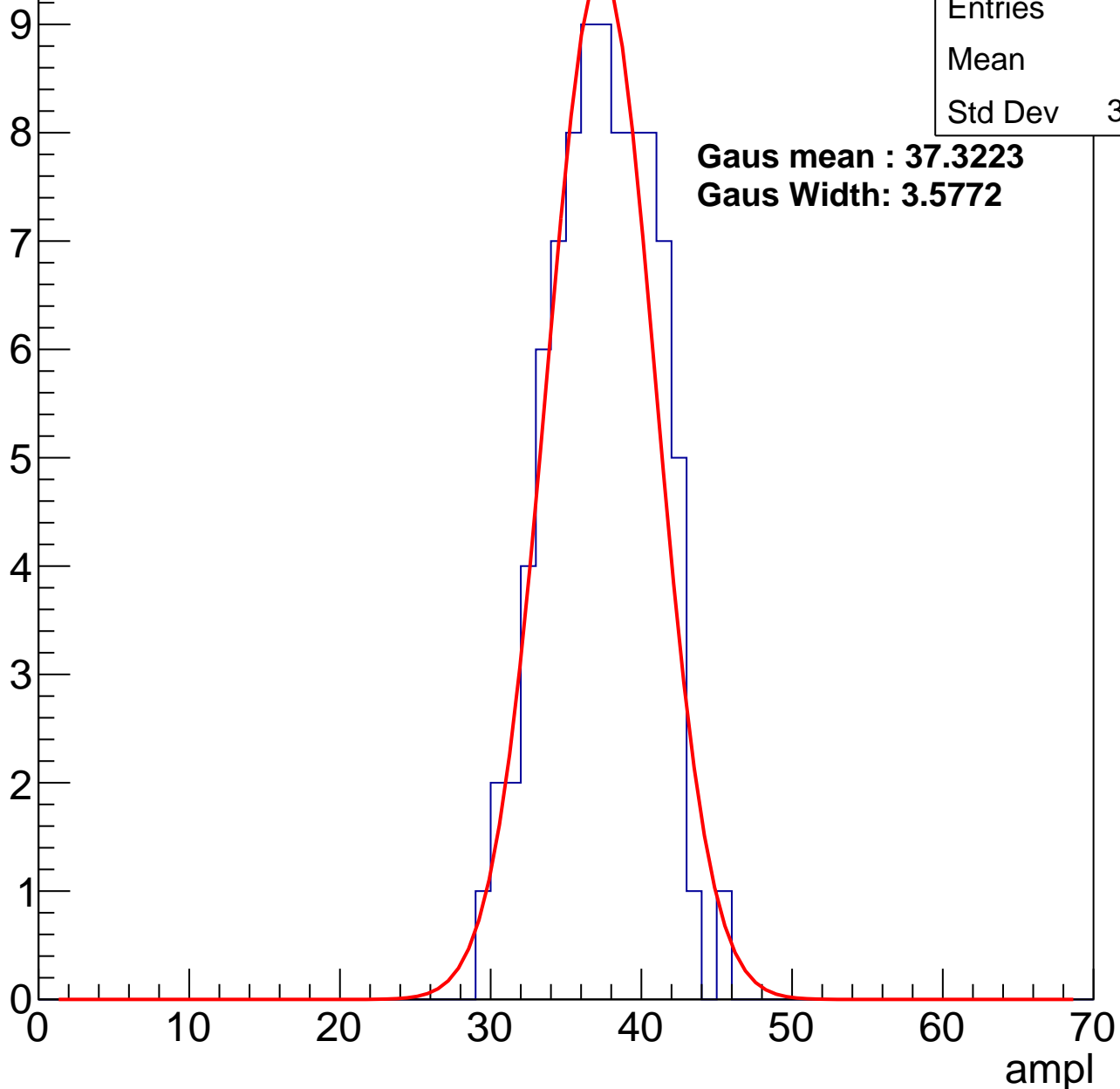
Gaus Width: 3.4728



B1L102S, U22-ch9, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch9, adc2

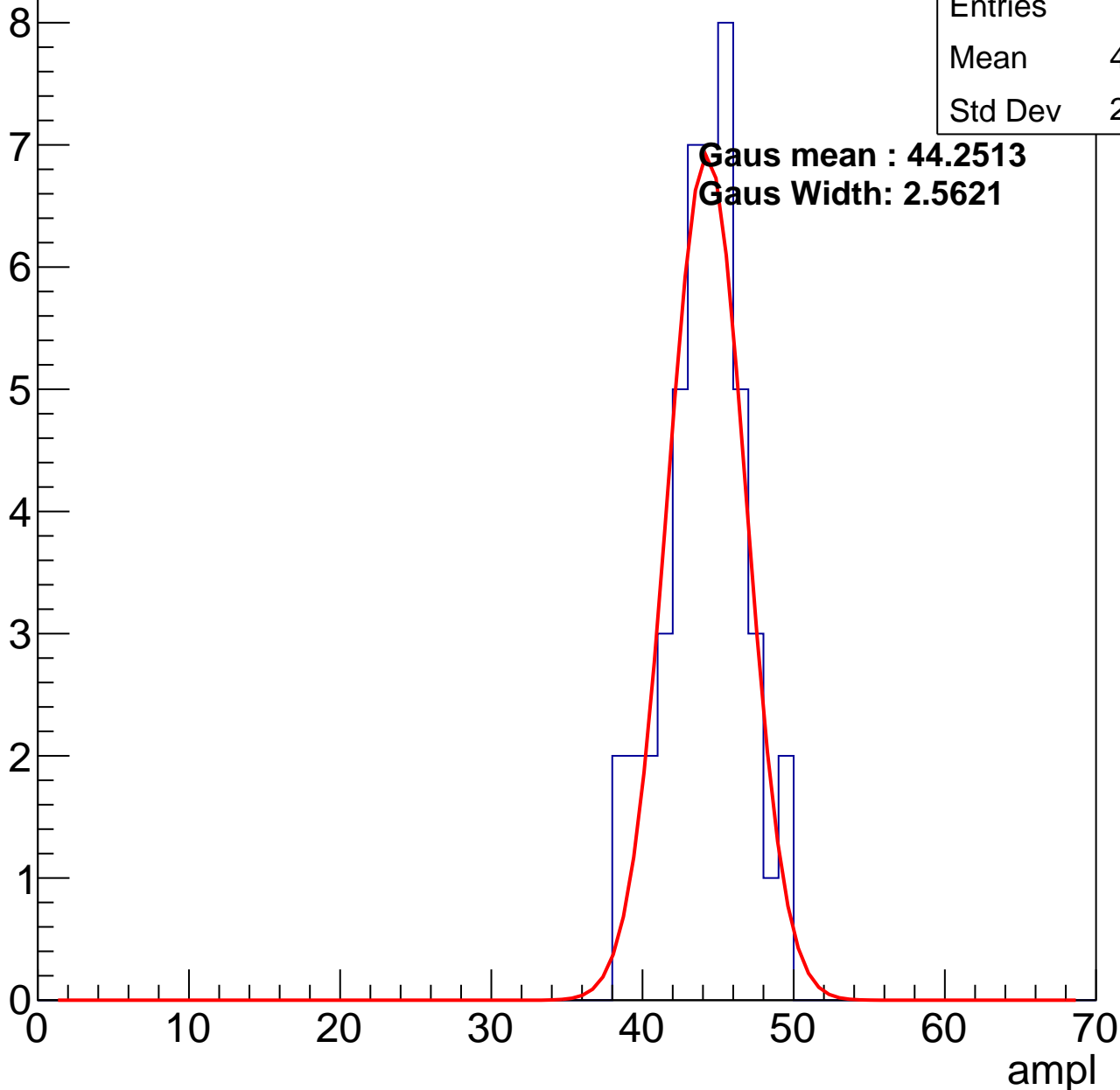
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	43.68
Std Dev	2.626

Gaus mean : 44.2513

Gaus Width: 2.5621

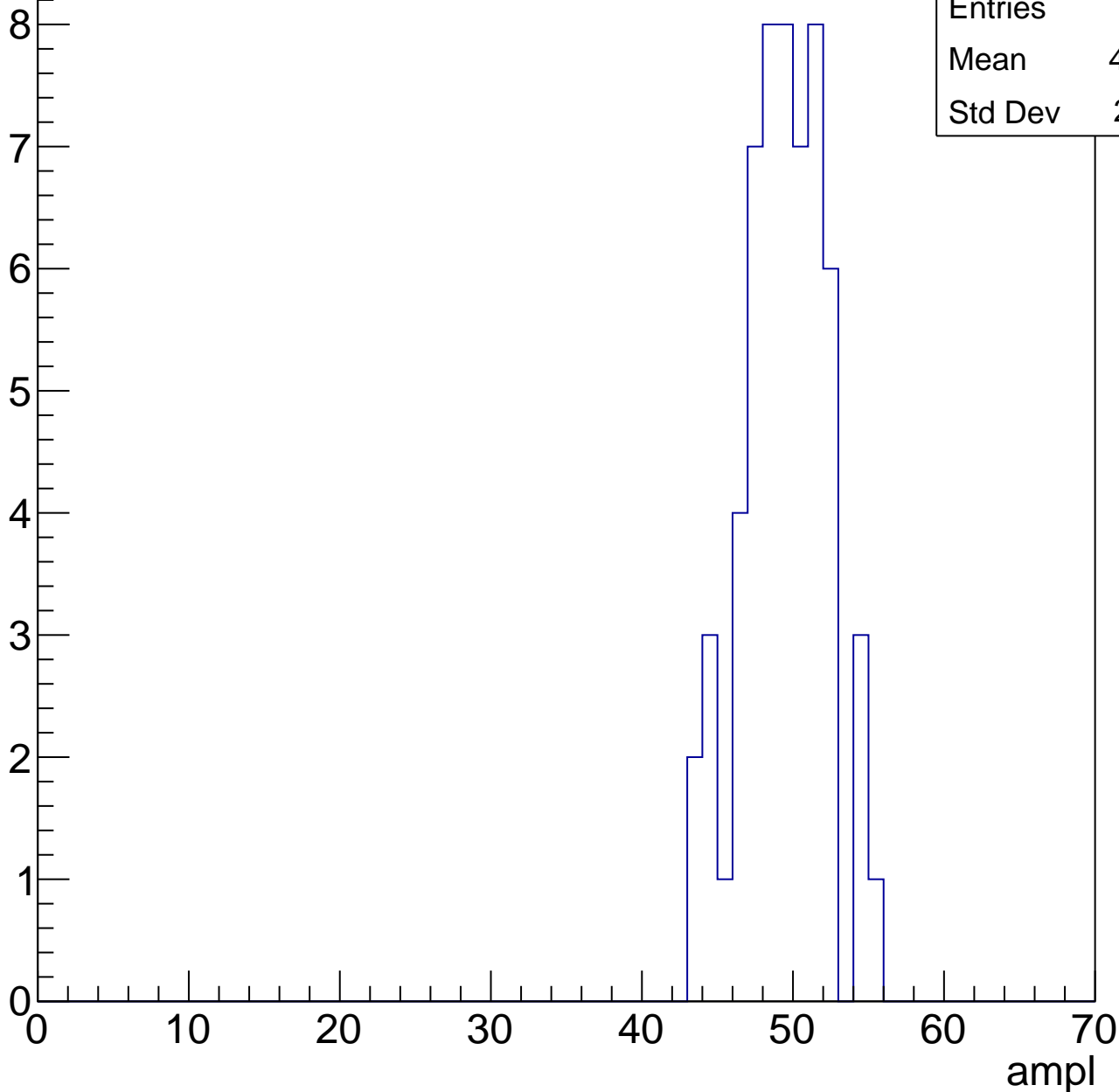


B1L102S, U22-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.95
Std Dev	2.751

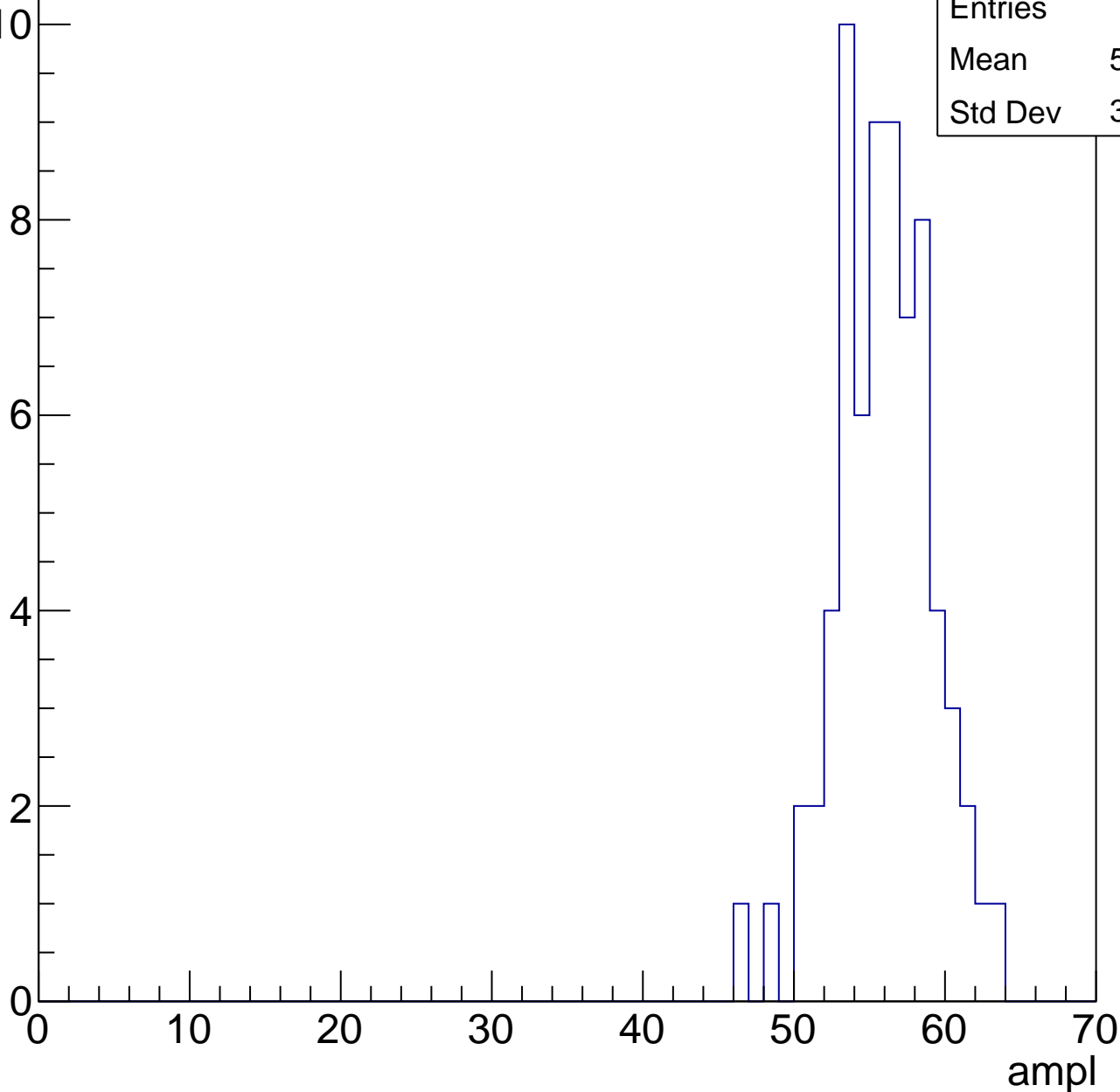


B1L102S, U22-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	55.47
Std Dev	3.188

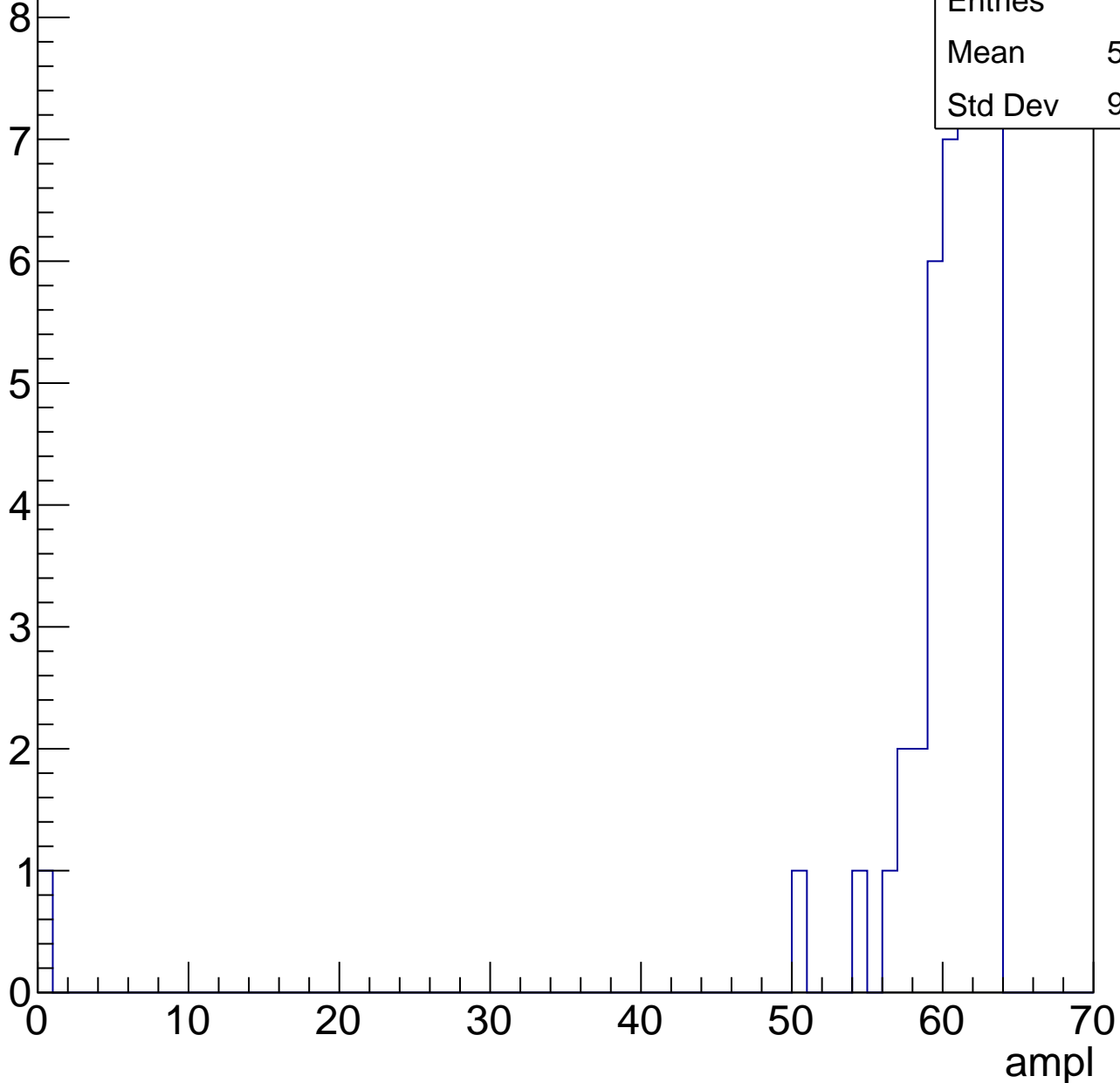


B1L102S, U22-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.93
Std Dev	9.243



B1L102S, U22-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

60.67

Std Dev

2.625

B1L102S, U22-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch10, adc0

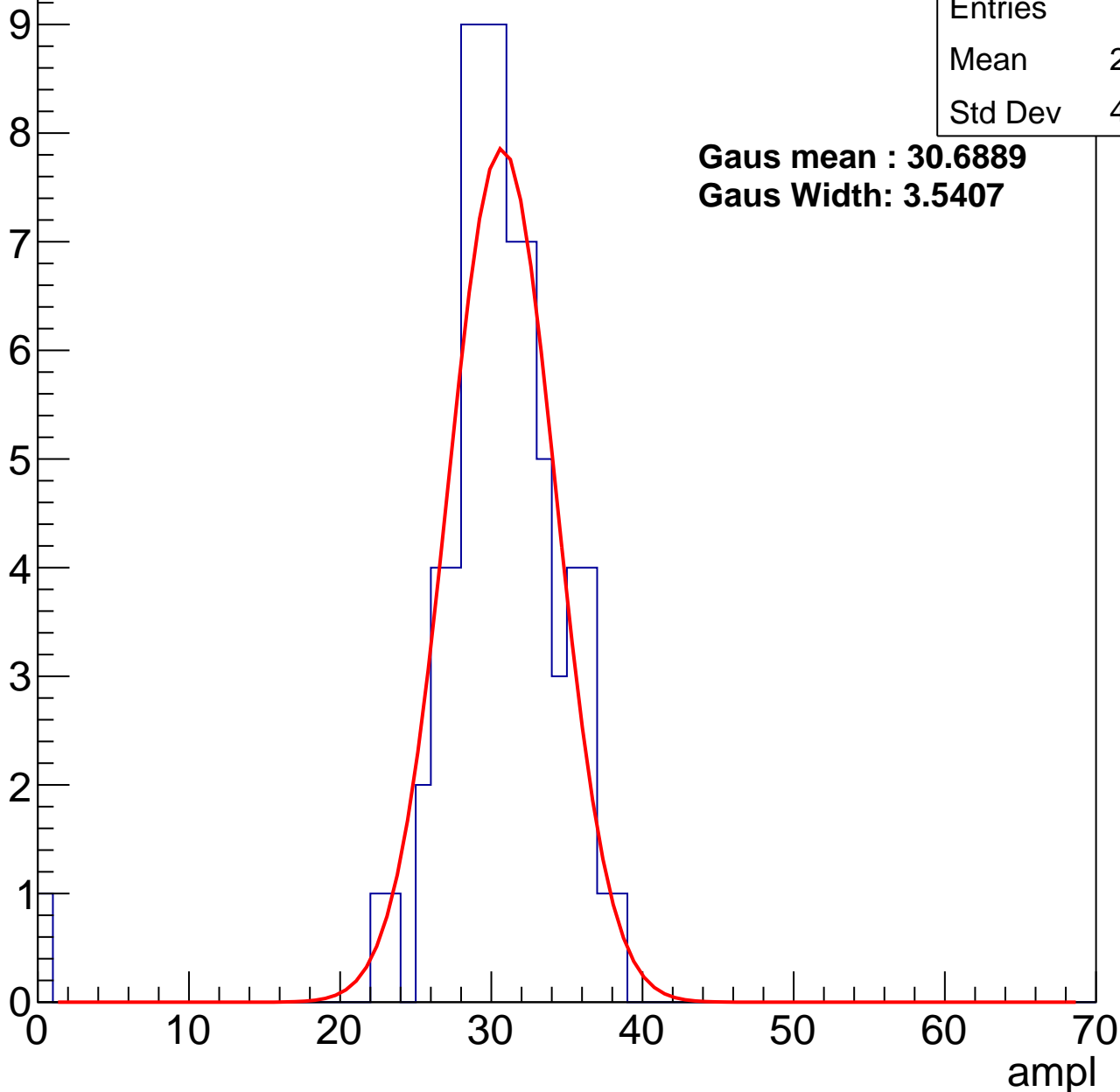
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.96
Std Dev	4.849

Gaus mean : 30.6889

Gaus Width: 3.5407



B1L102S, U22-ch10, adc1

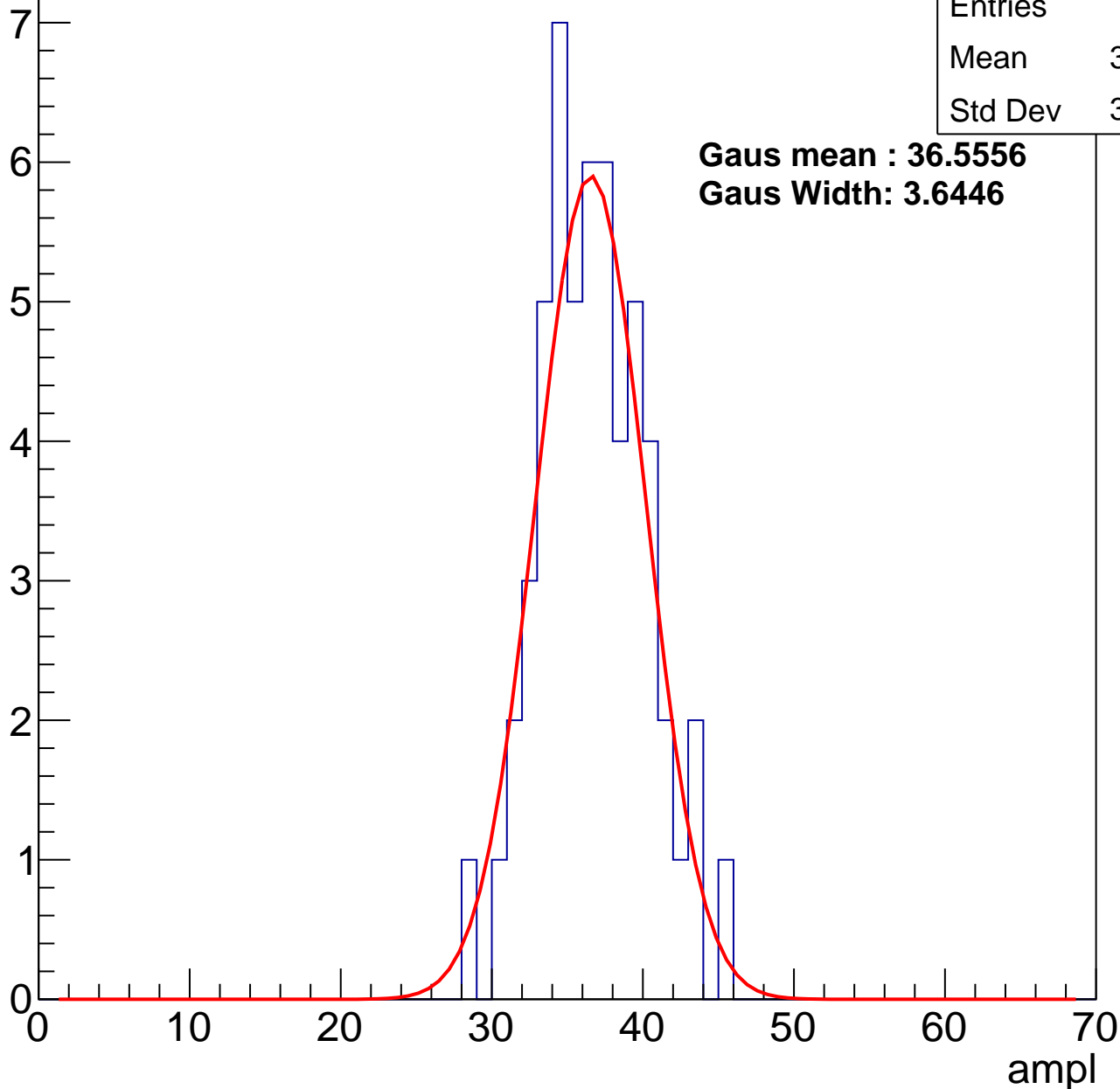
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	36.25
Std Dev	3.486

Gaus mean : 36.5556

Gaus Width: 3.6446



B1L102S, U22-ch10, adc2

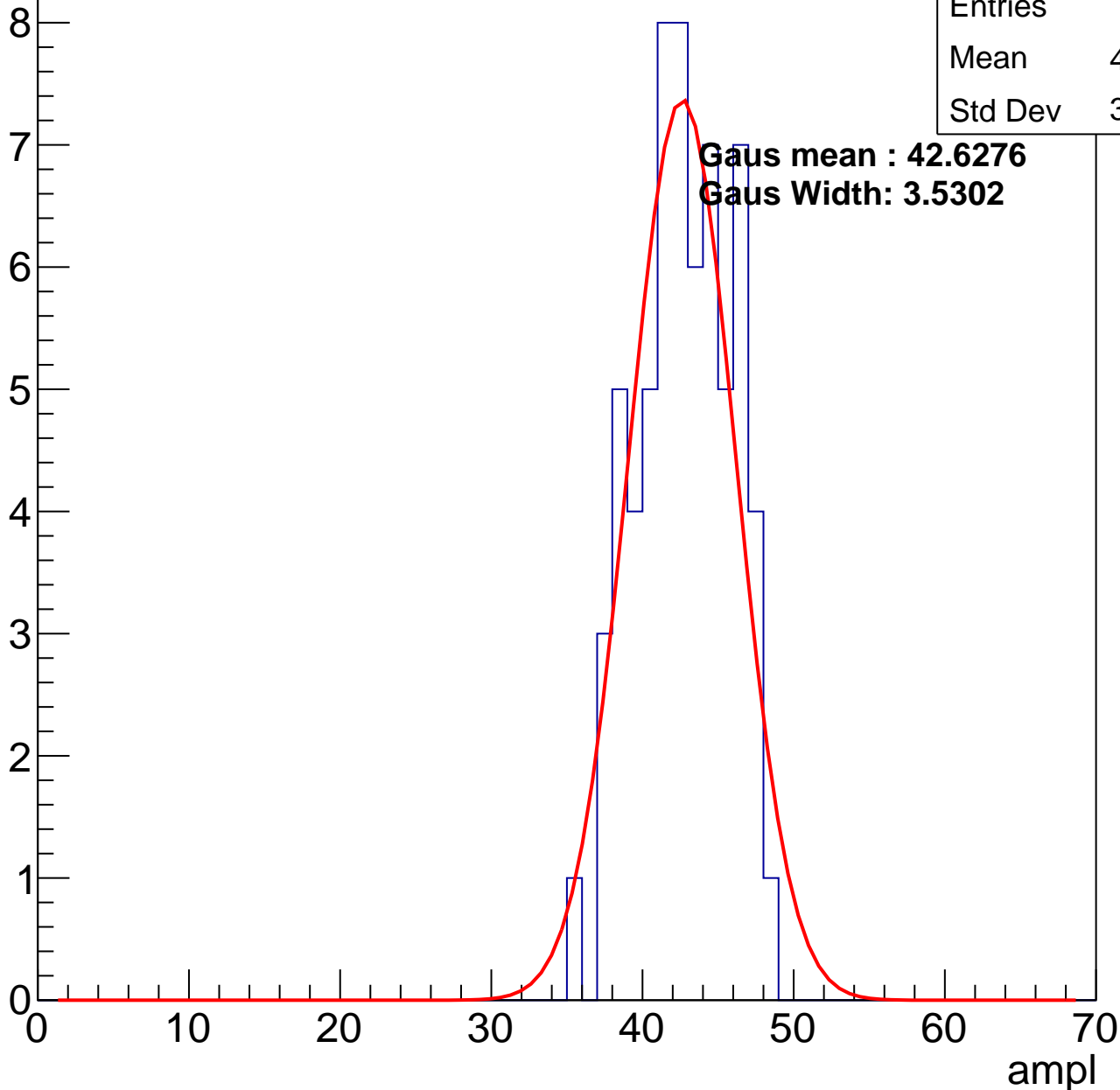
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.27
Std Dev	3.037

Gaus mean : 42.6276

Gaus Width: 3.5302



B1L102S, U22-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

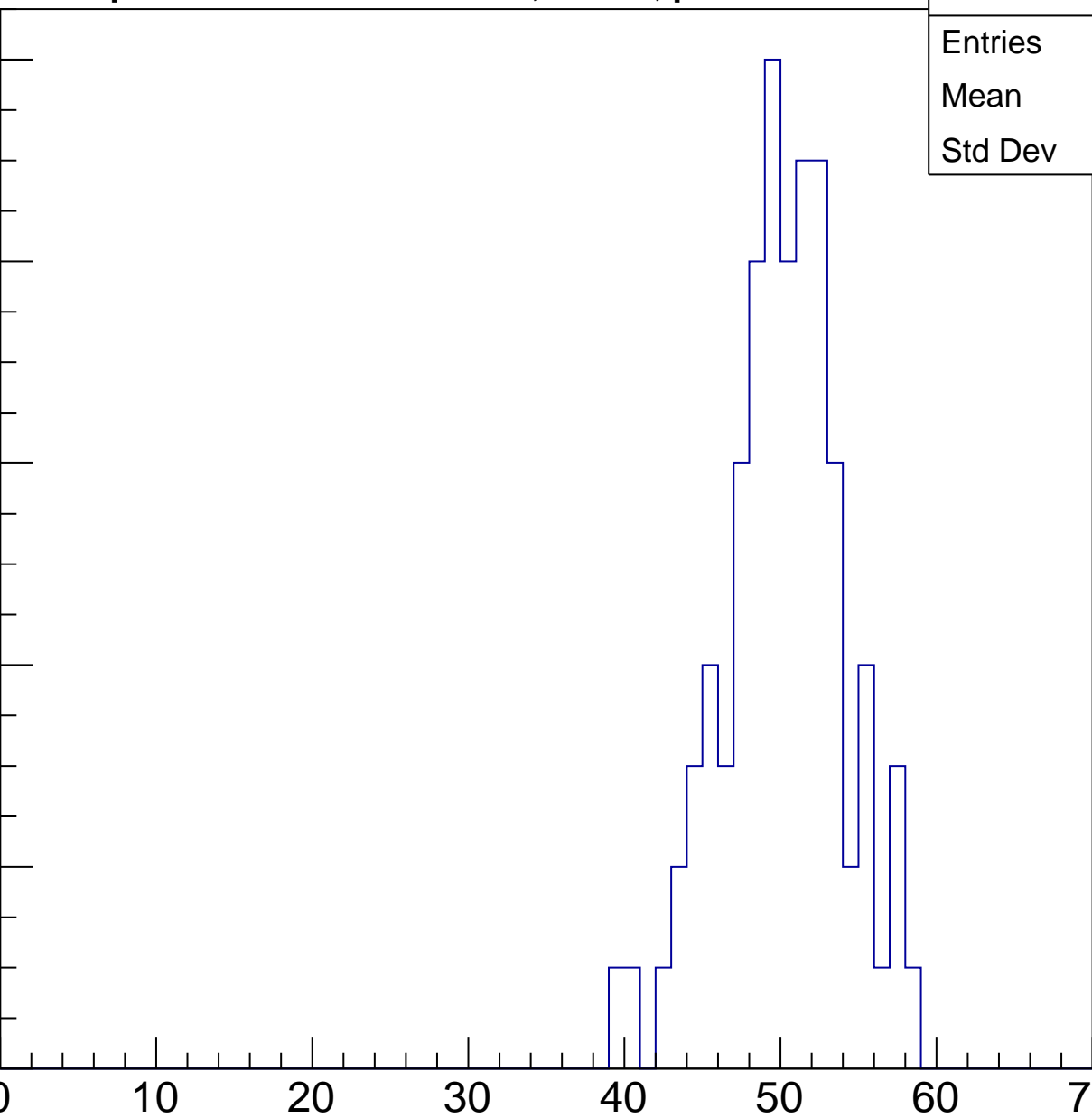
Entries	82
Mean	49.65
Std Dev	3.83

Entry

10
8
6
4
2
0

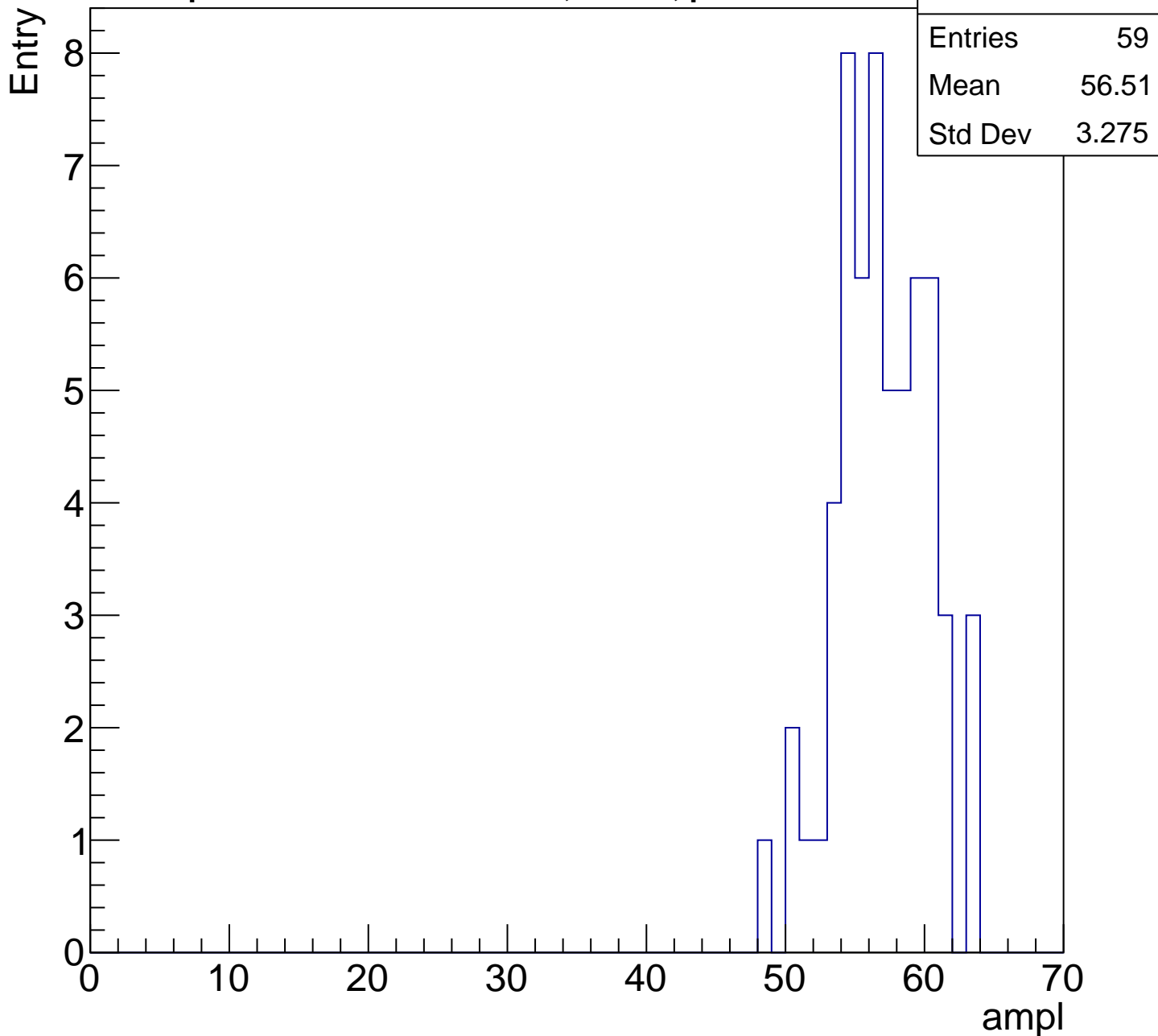
0 10 20 30 40 50 60 70

ampl



B1L102S, U22-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

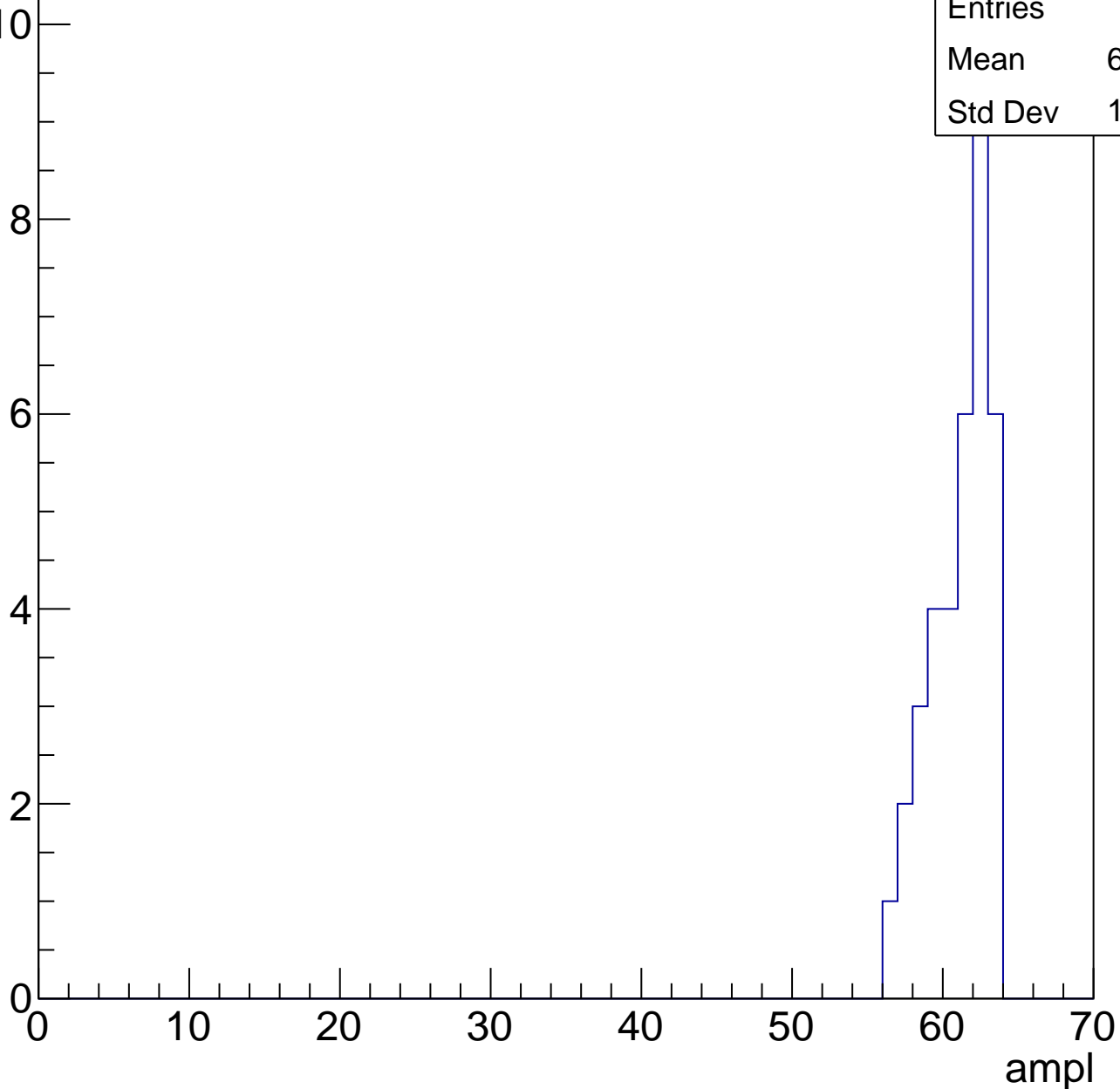


B1L102S, U22-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

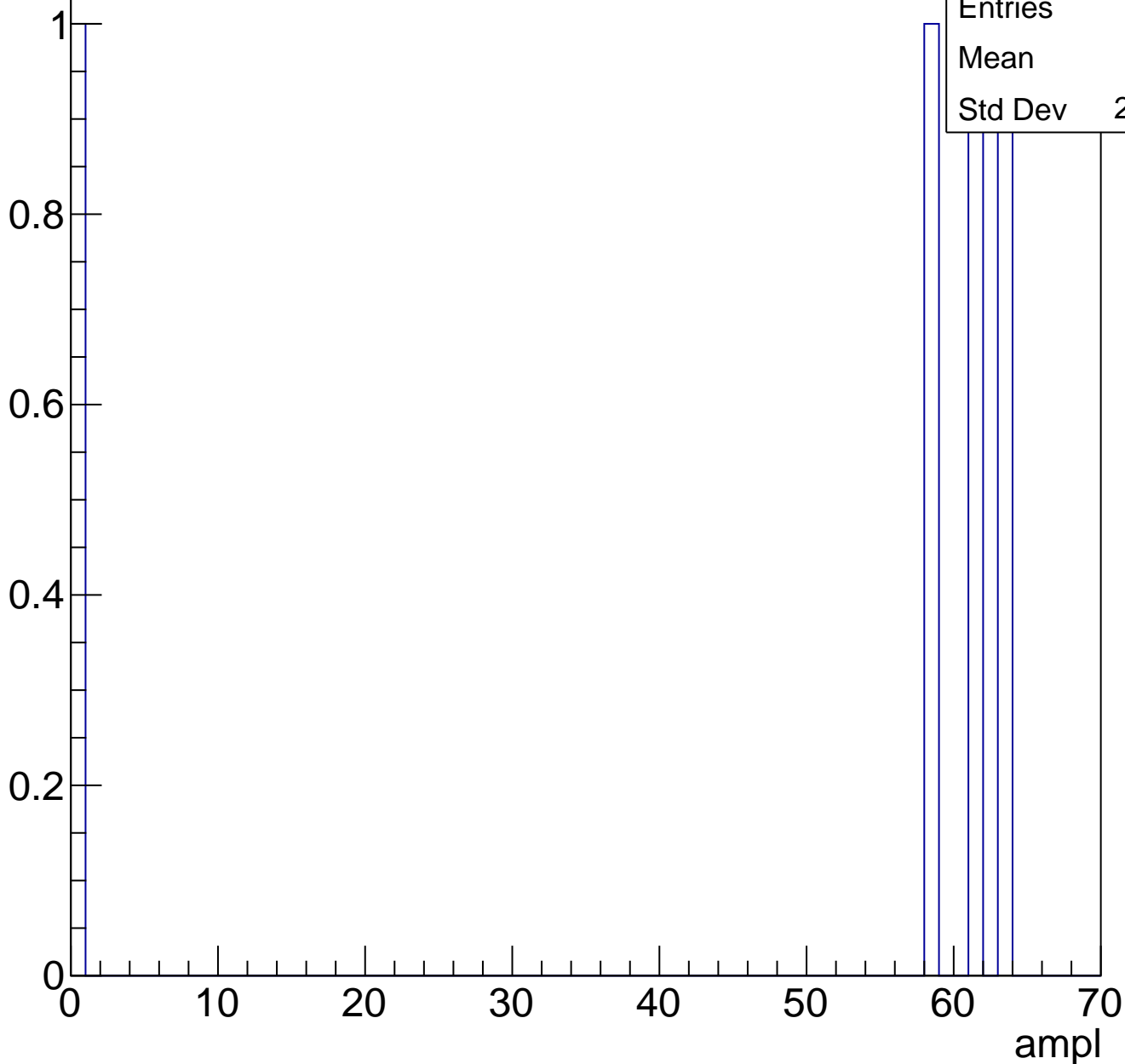
Entries	36
Mean	60.67
Std Dev	1.929



B1L102S, U22-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch11, adc0

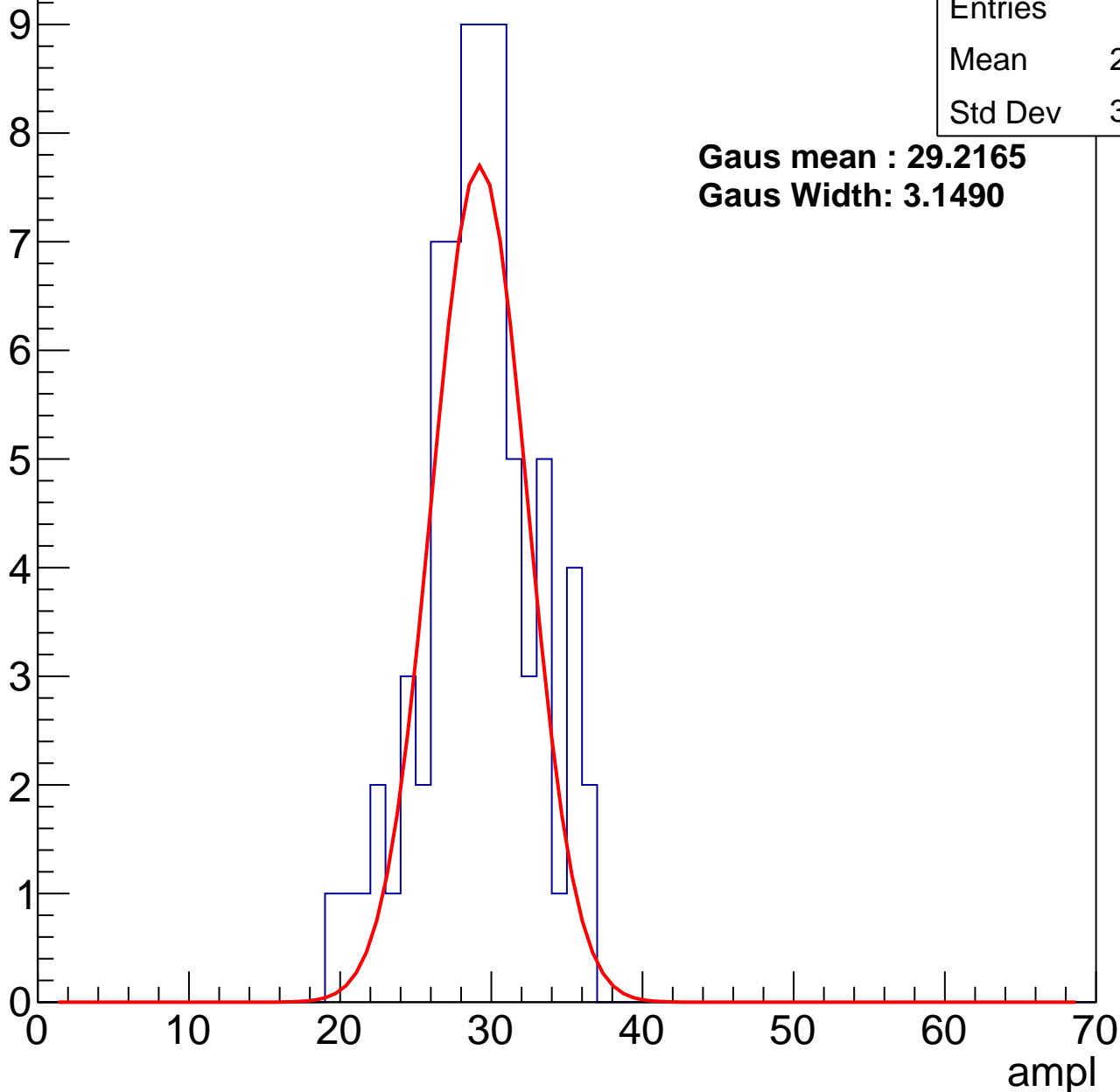
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.68
Std Dev	3.692

Gaus mean : 29.2165

Gaus Width: 3.1490



B1L102S, U22-ch11, adc1

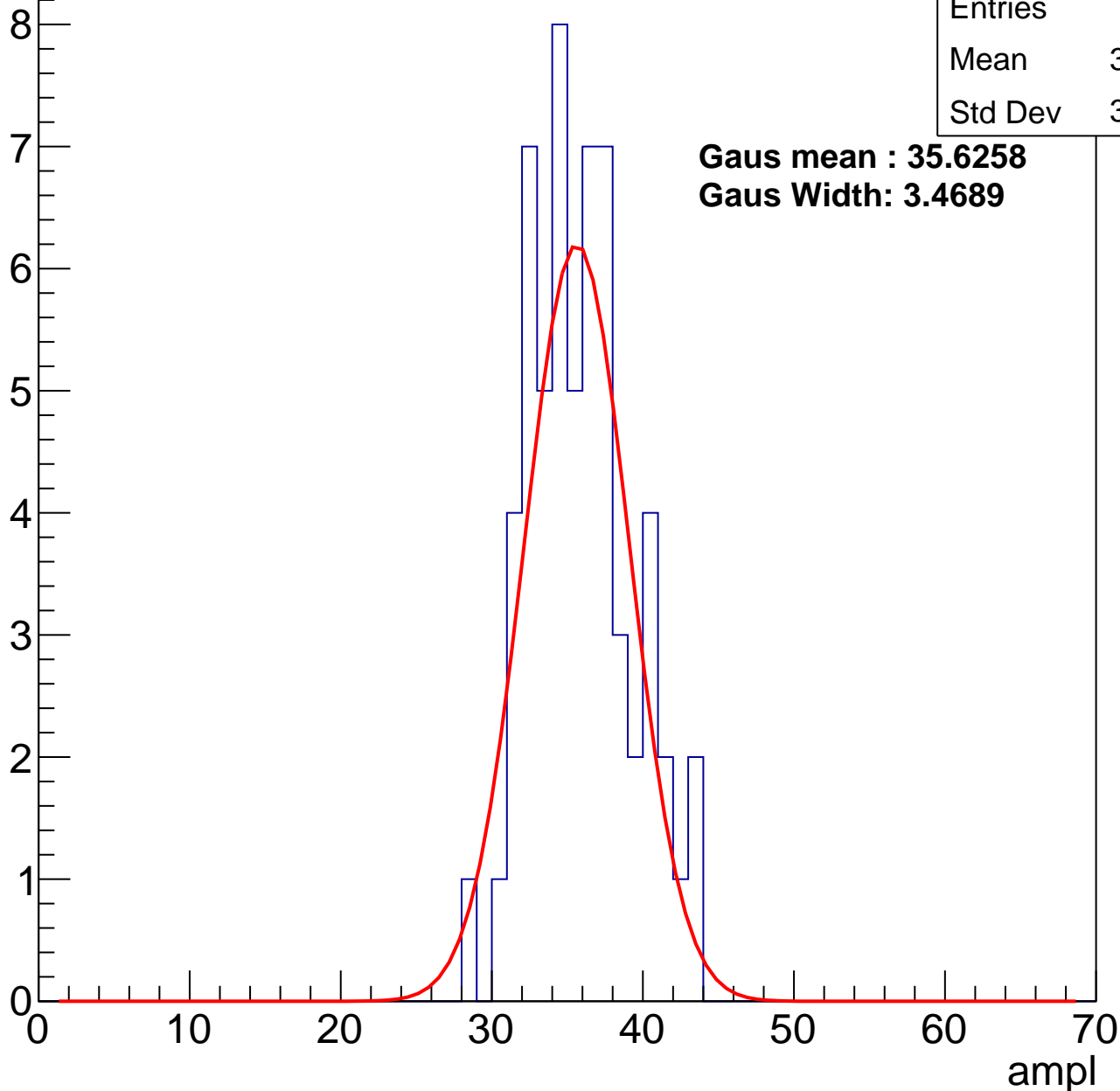
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.44
Std Dev	3.346

Gaus mean : 35.6258

Gaus Width: 3.4689



B1L102S, U22-ch11, adc2

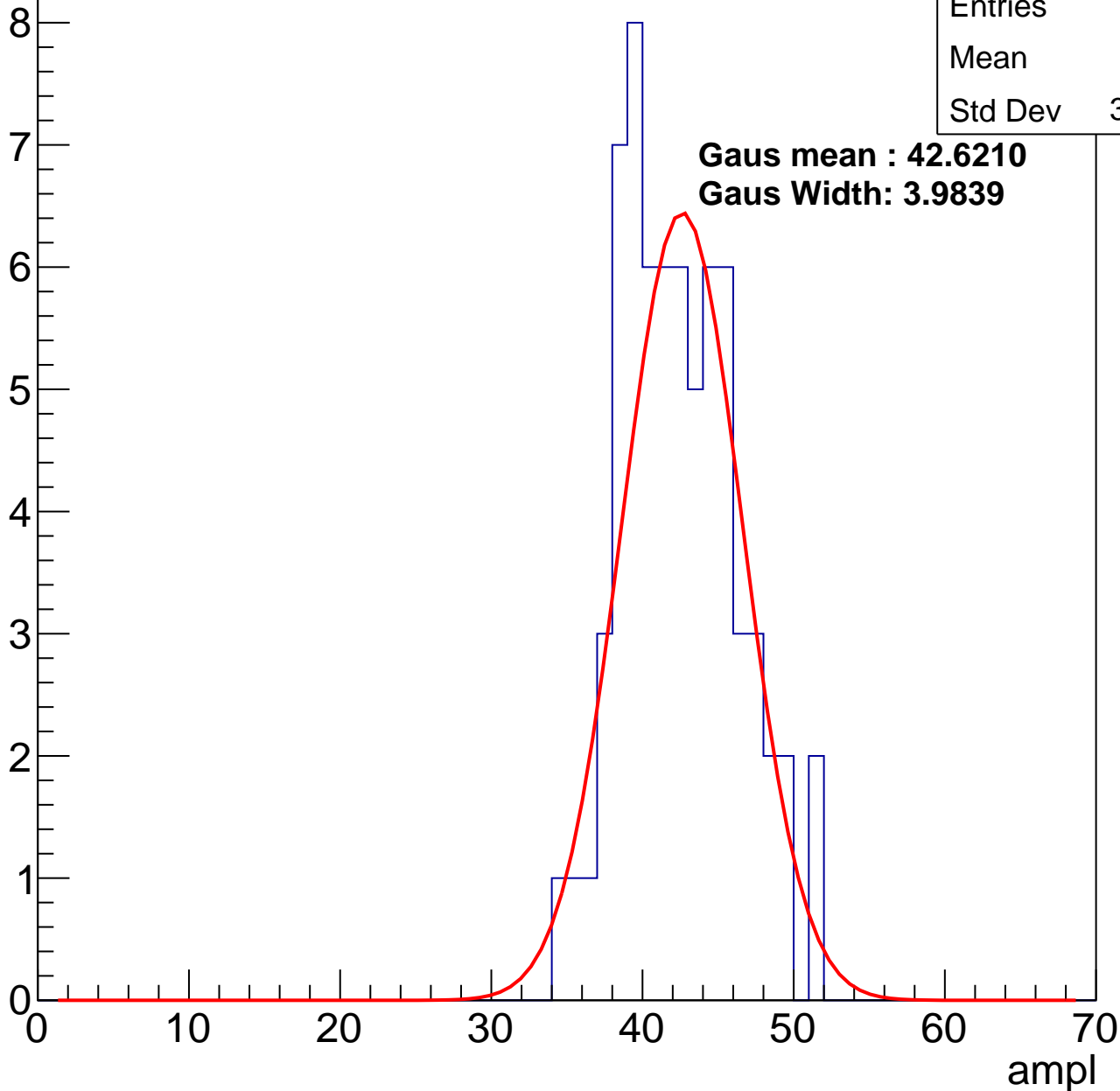
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42
Std Dev	3.789

Gaus mean : 42.6210

Gaus Width: 3.9839

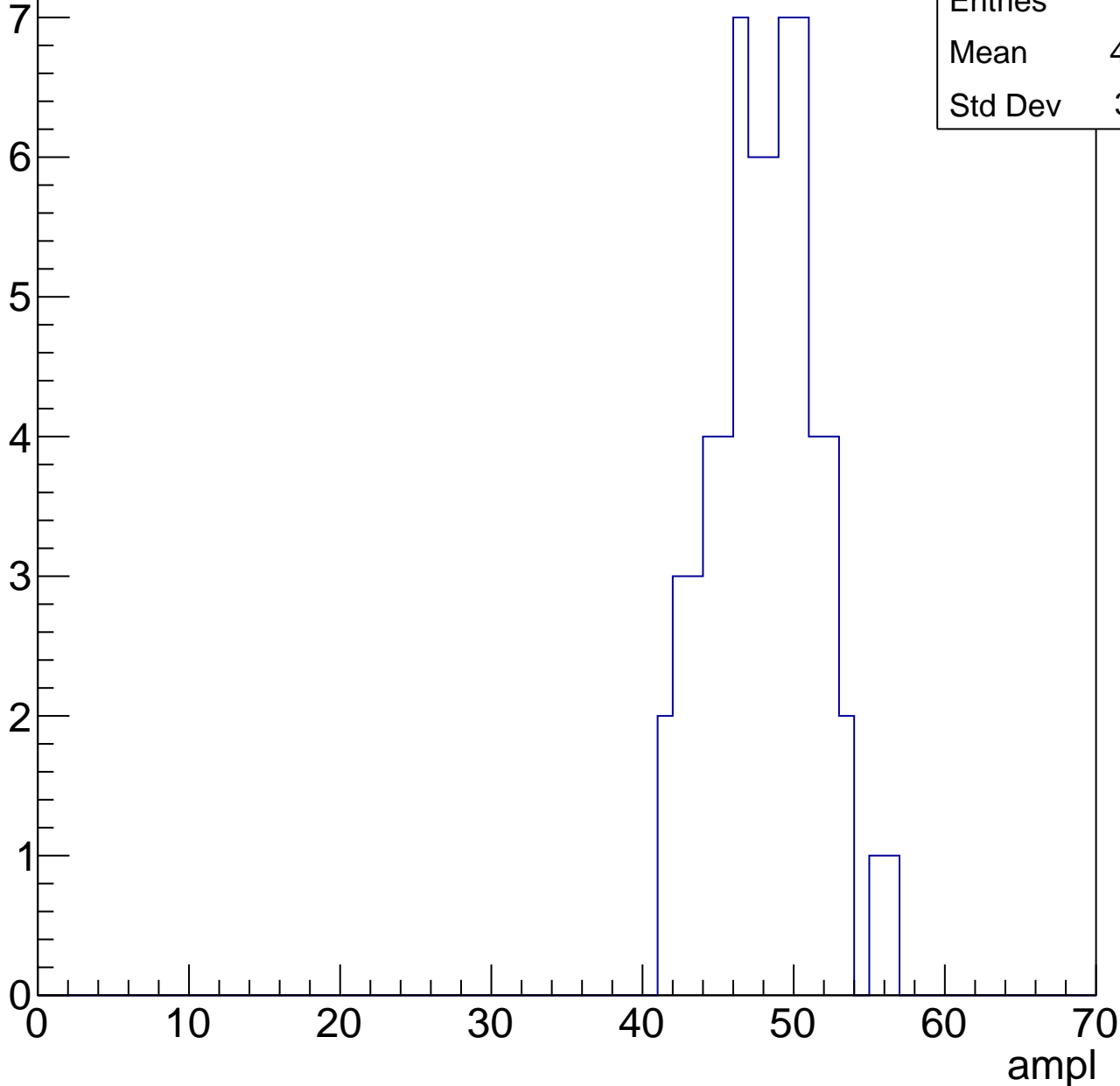


B1L102S, U22-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.66
Std Dev	3.401

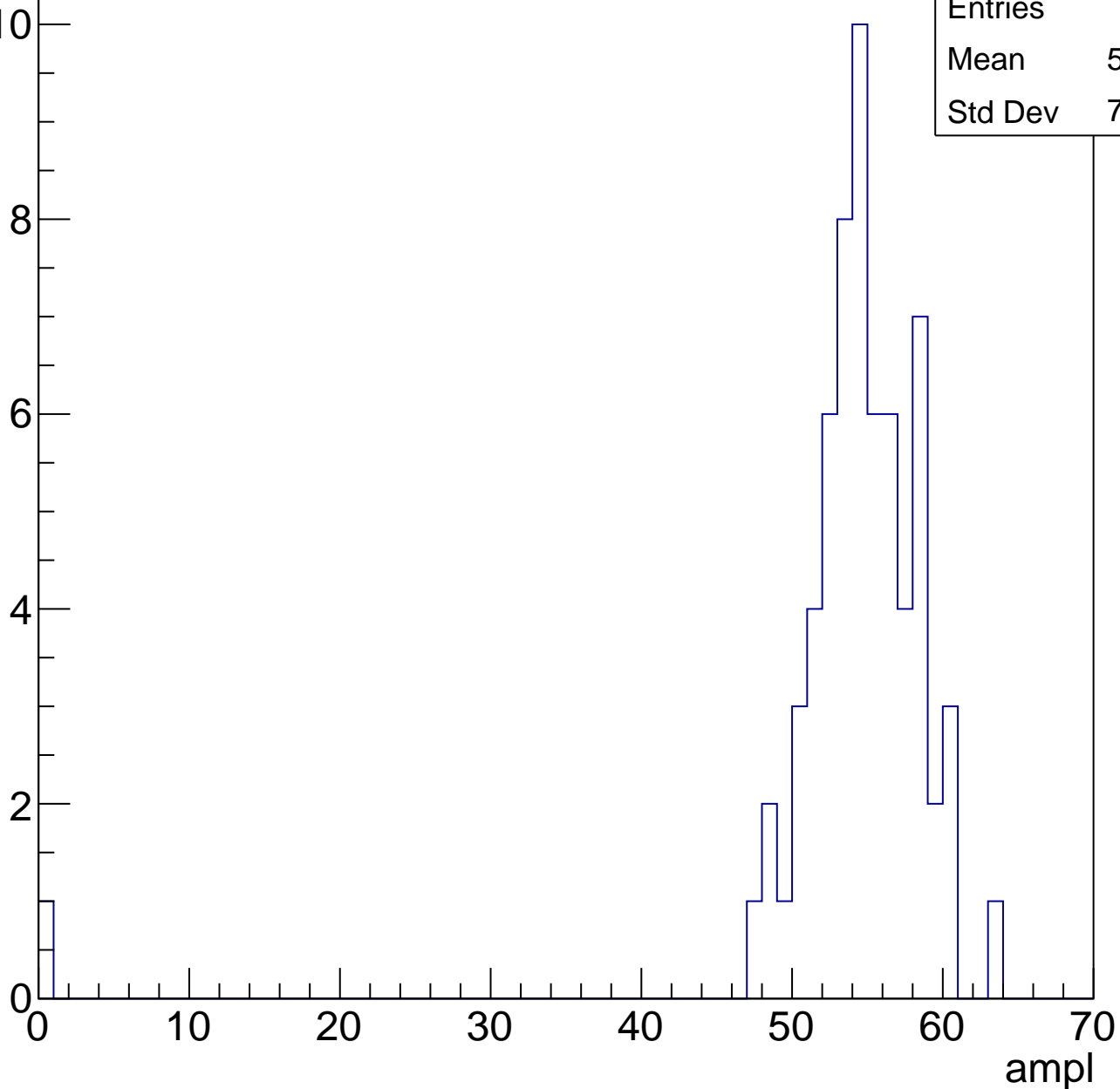


B1L102S, U22-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	53.58
Std Dev	7.425



B1L102S, U22-ch11, adc5

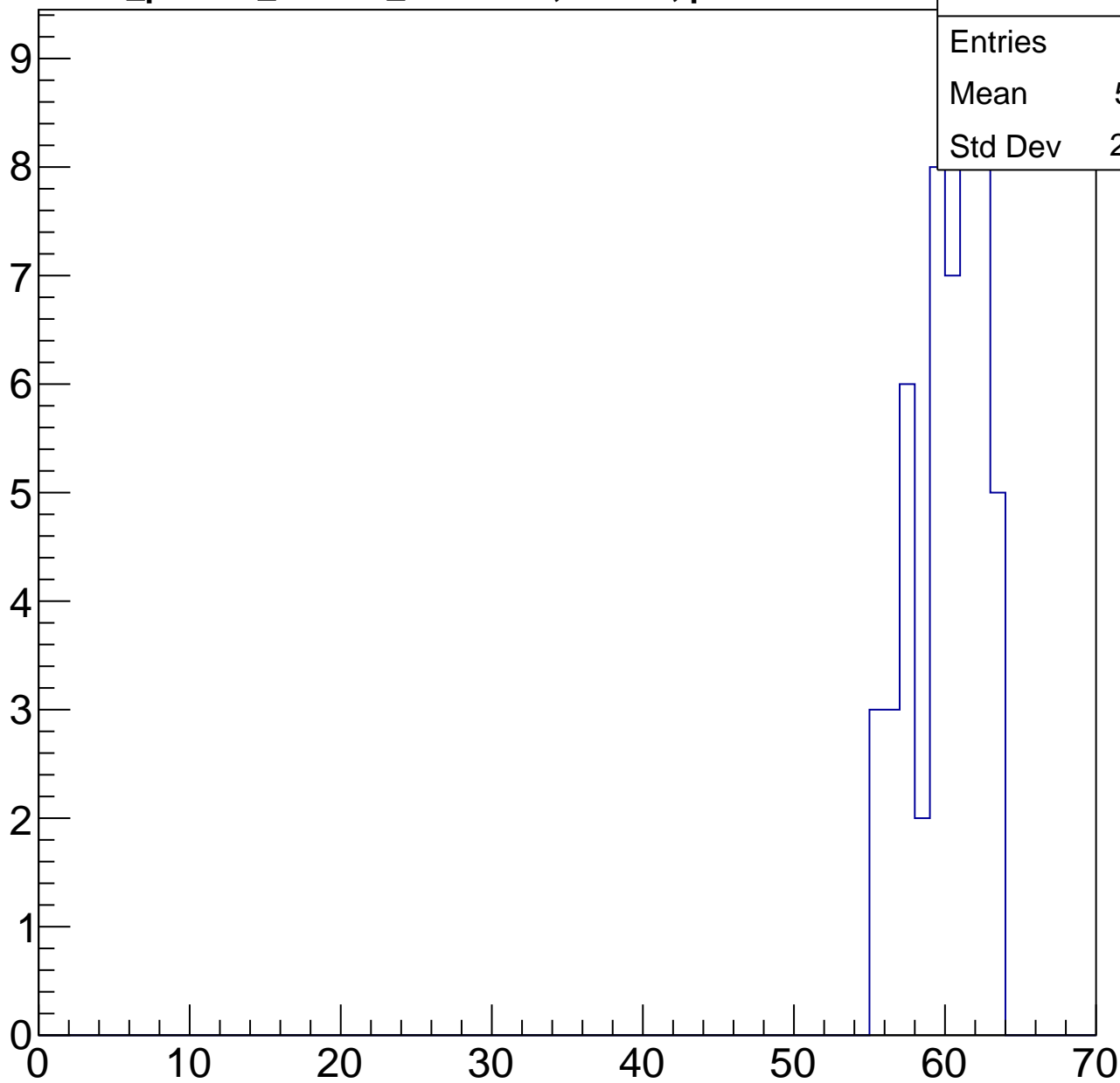
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.71
Std Dev	2.315

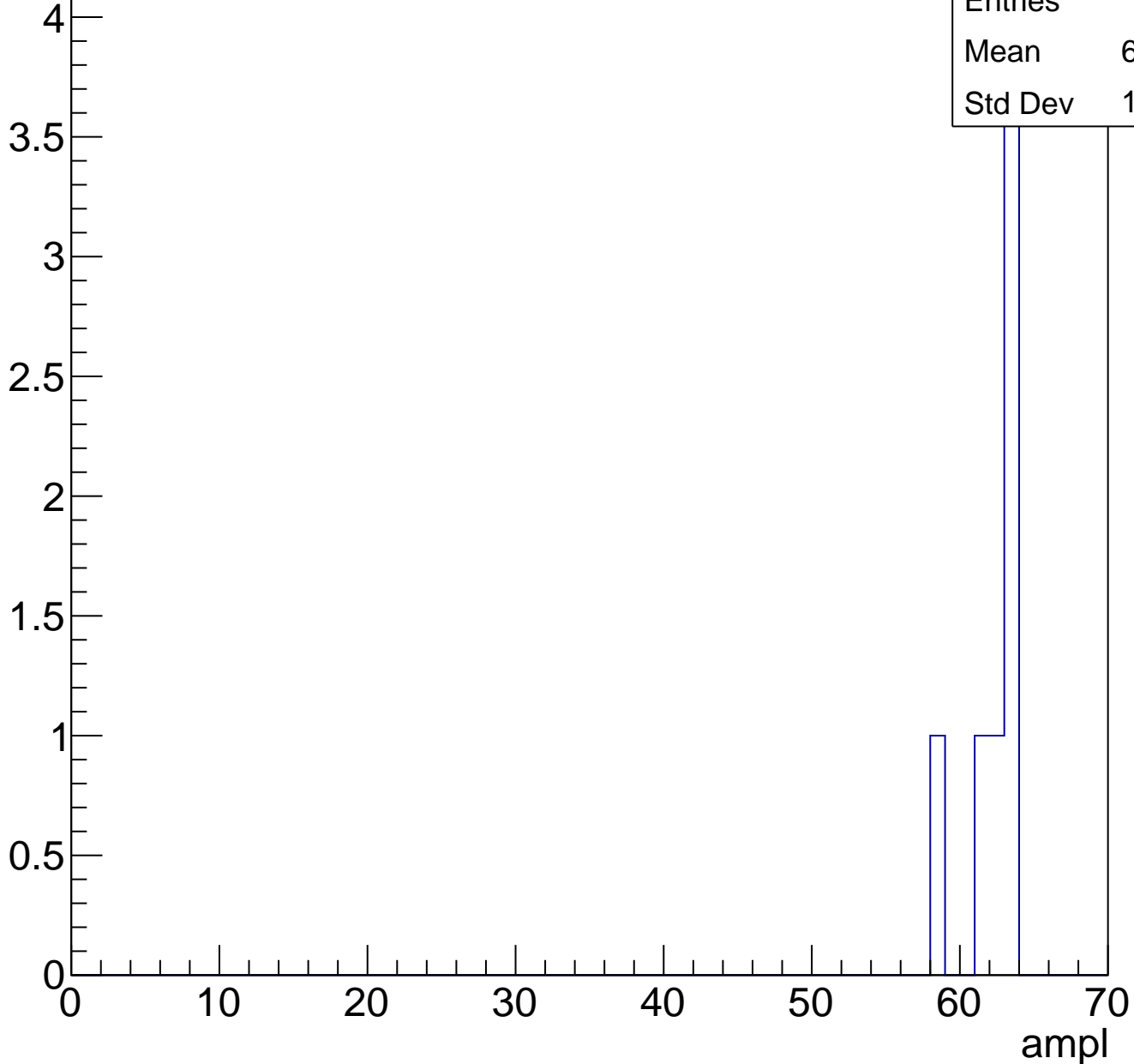
ampl



B1L102S, U22-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch12, adc0

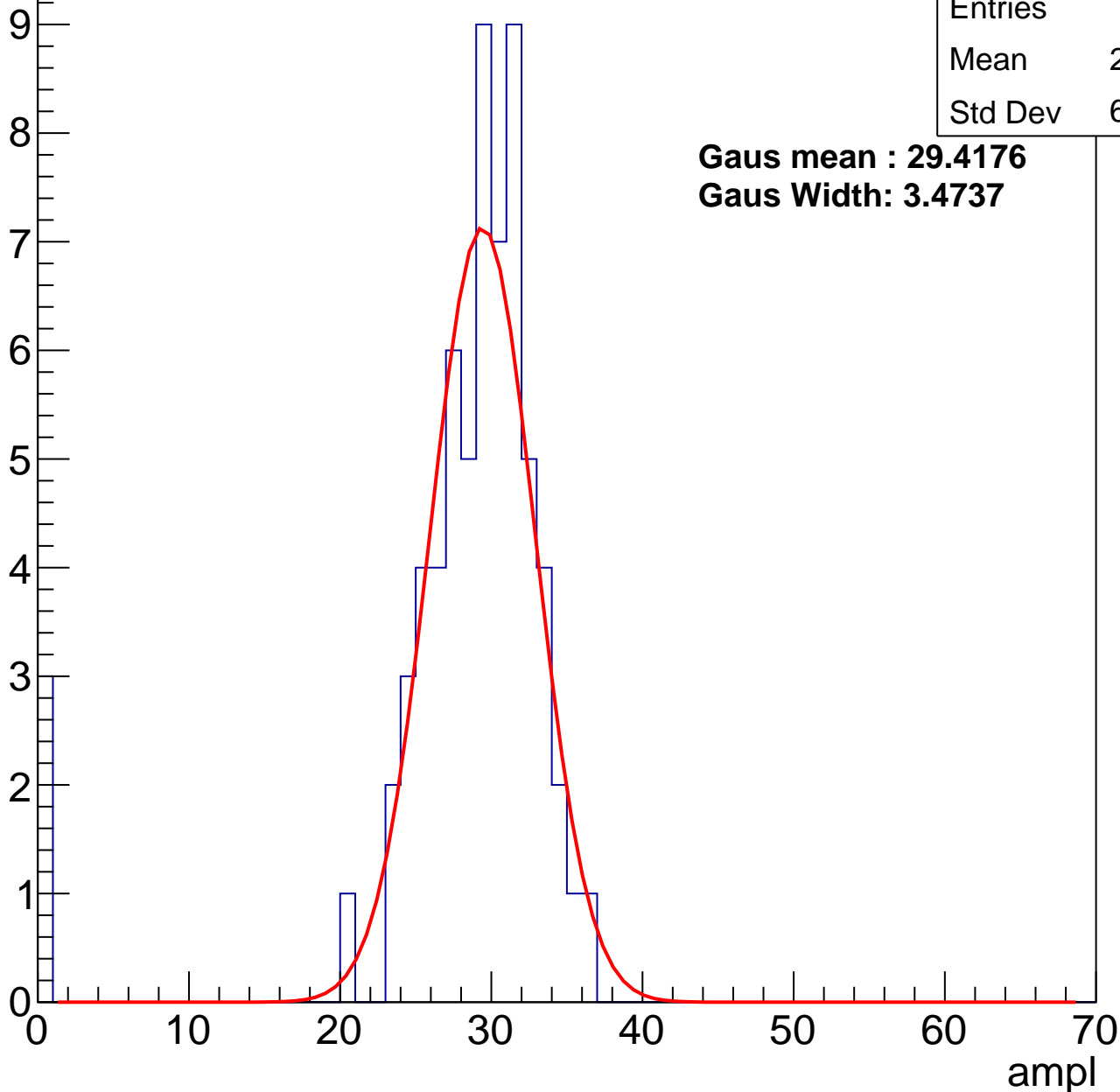
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.65
Std Dev	6.795

Gaus mean : 29.4176

Gaus Width: 3.4737



B1L102S, U22-ch12, adc1

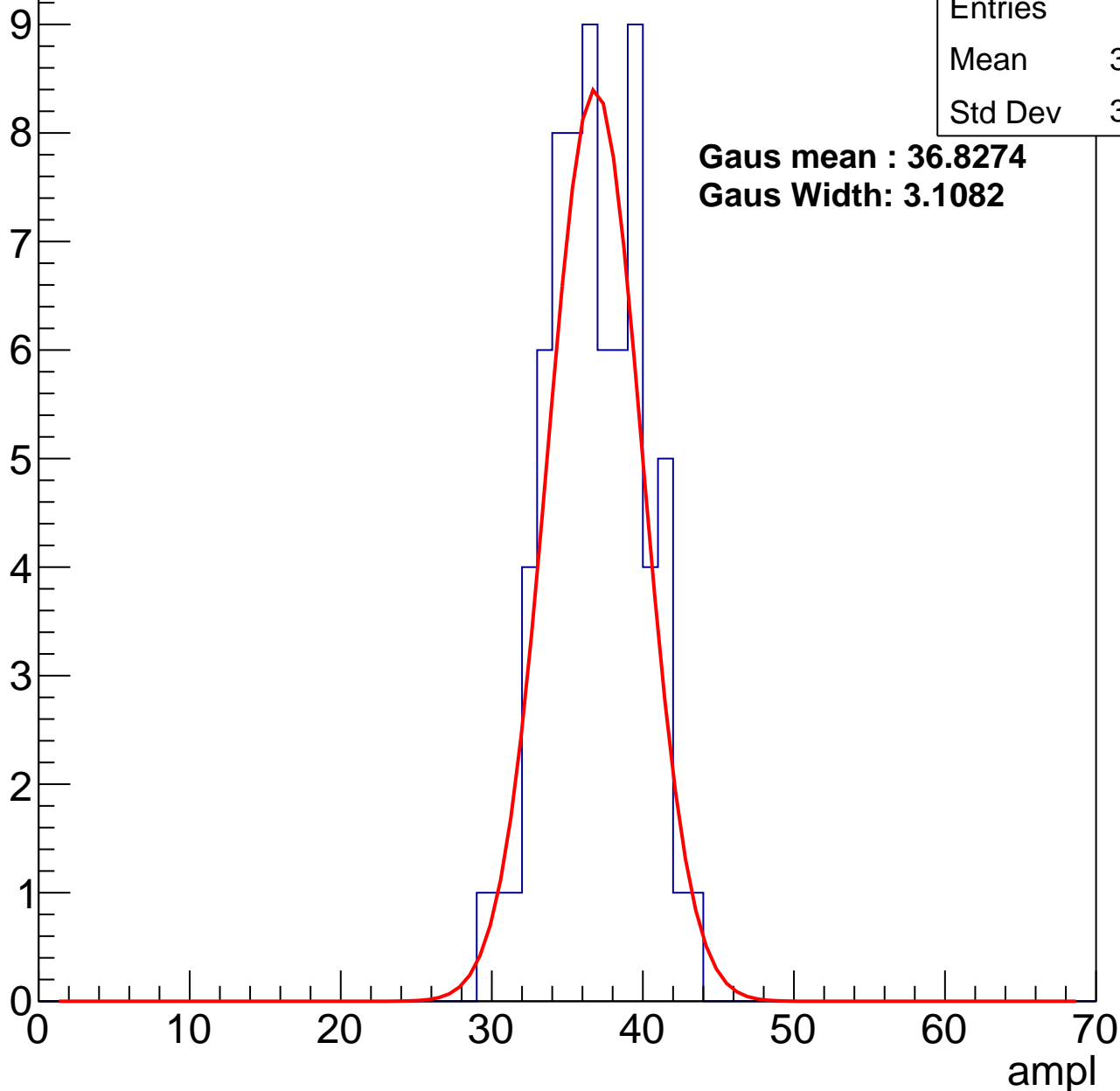
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.33
Std Dev	3.037

Gaus mean : 36.8274

Gaus Width: 3.1082



B1L102S, U22-ch12, adc2

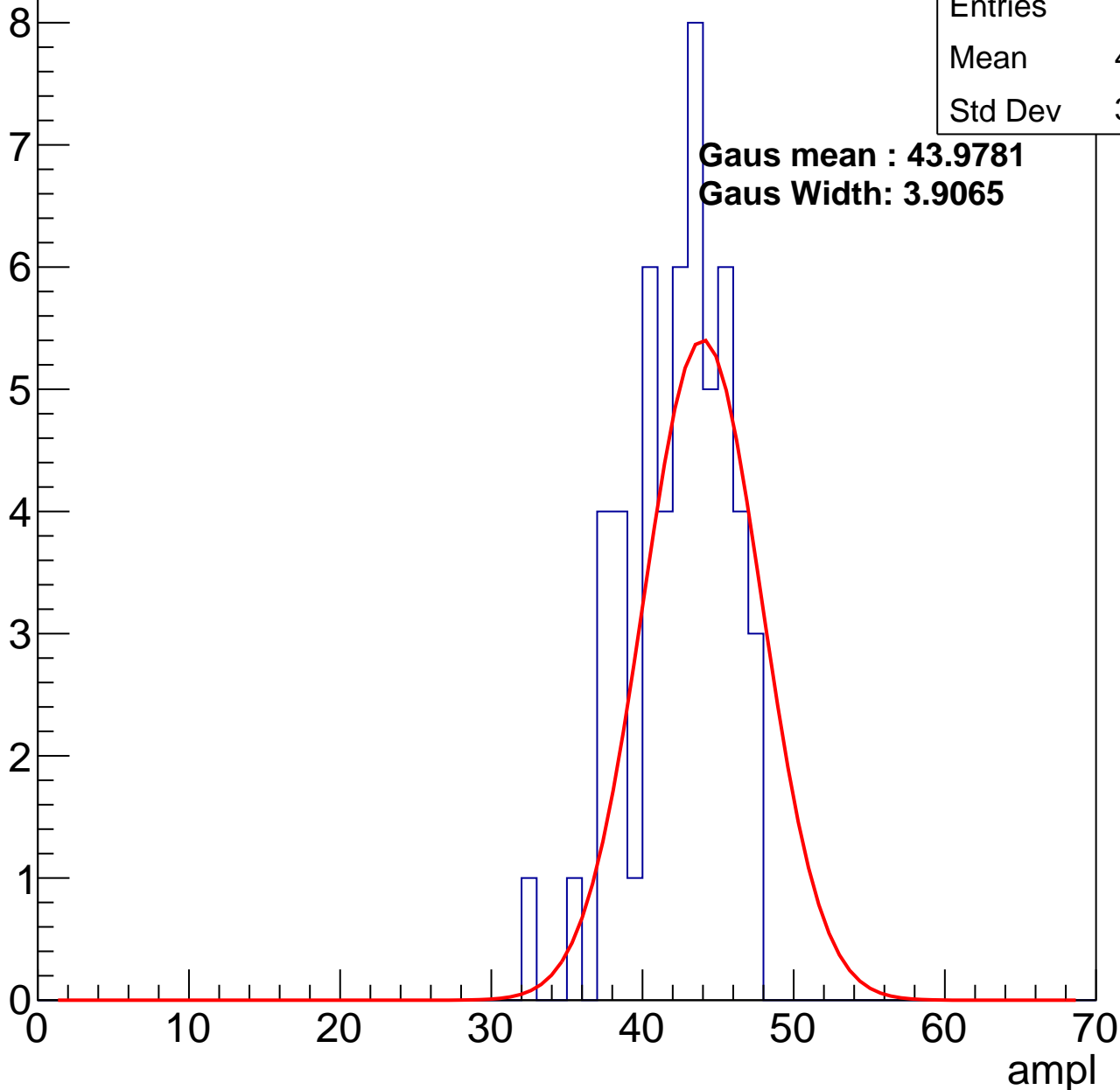
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.91
Std Dev	3.281

Gaus mean : 43.9781

Gaus Width: 3.9065

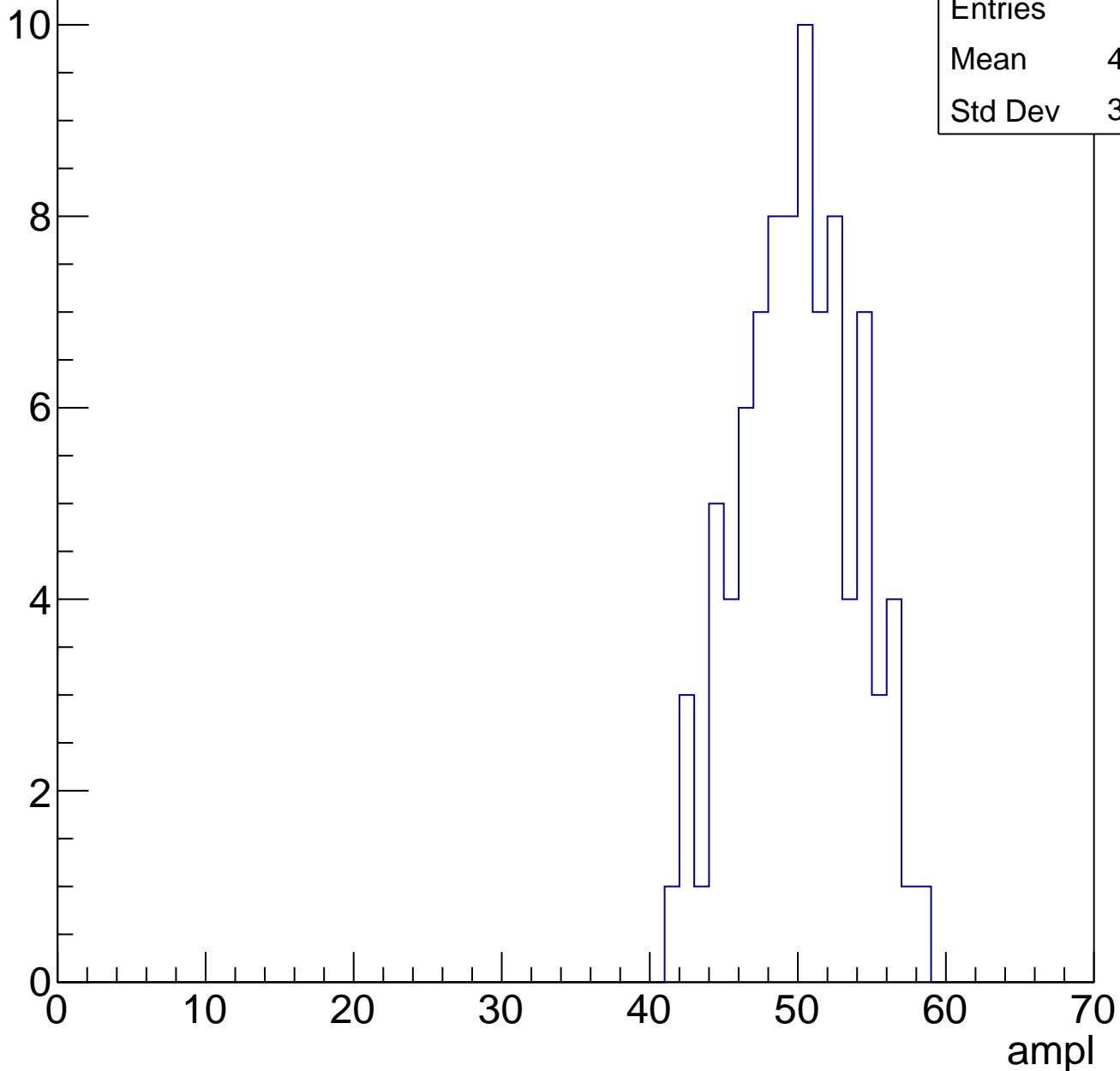


B1L102S, U22-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	49.52
Std Dev	3.843

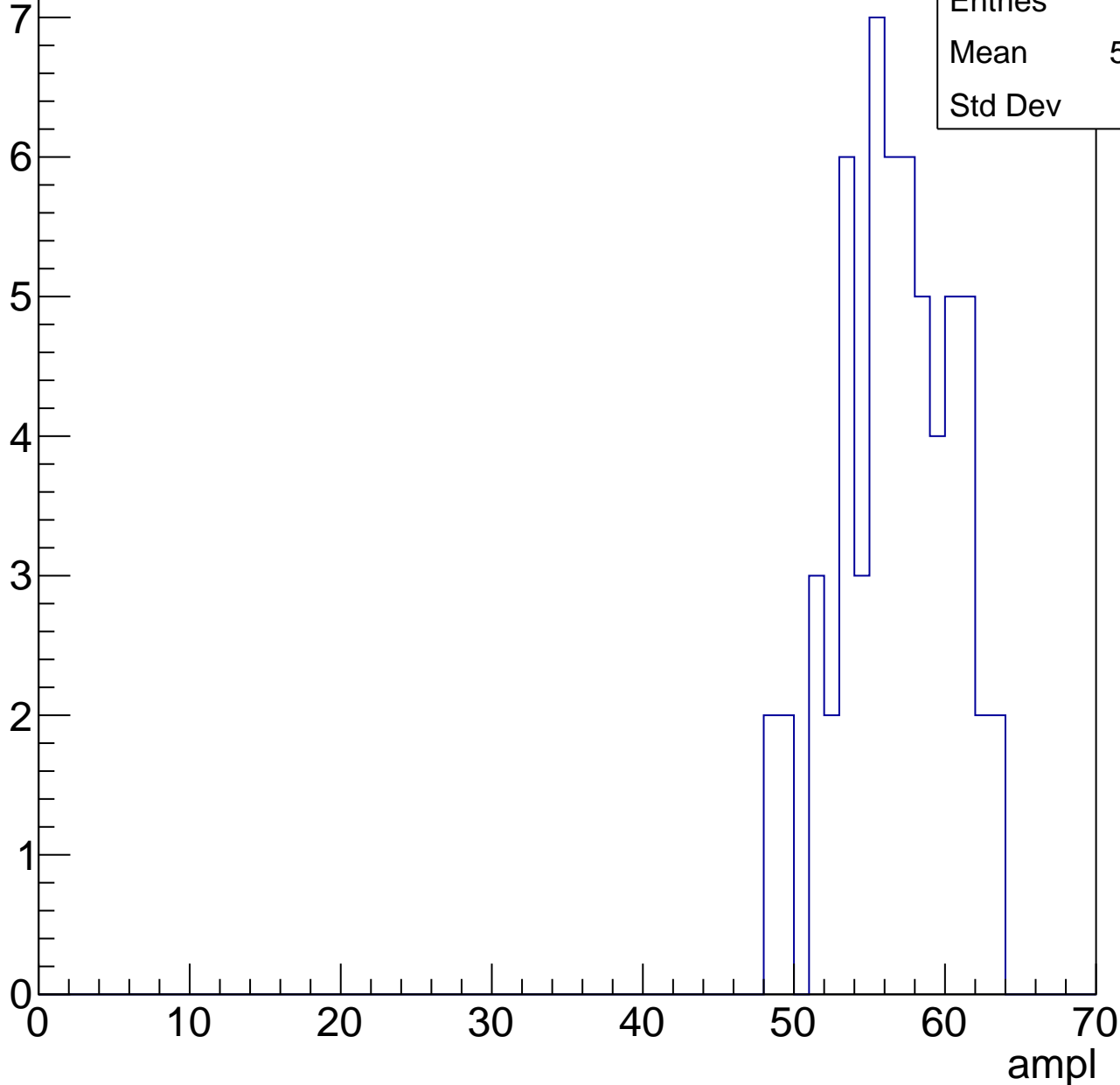


B1L102S, U22-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	56.25
Std Dev	3.74

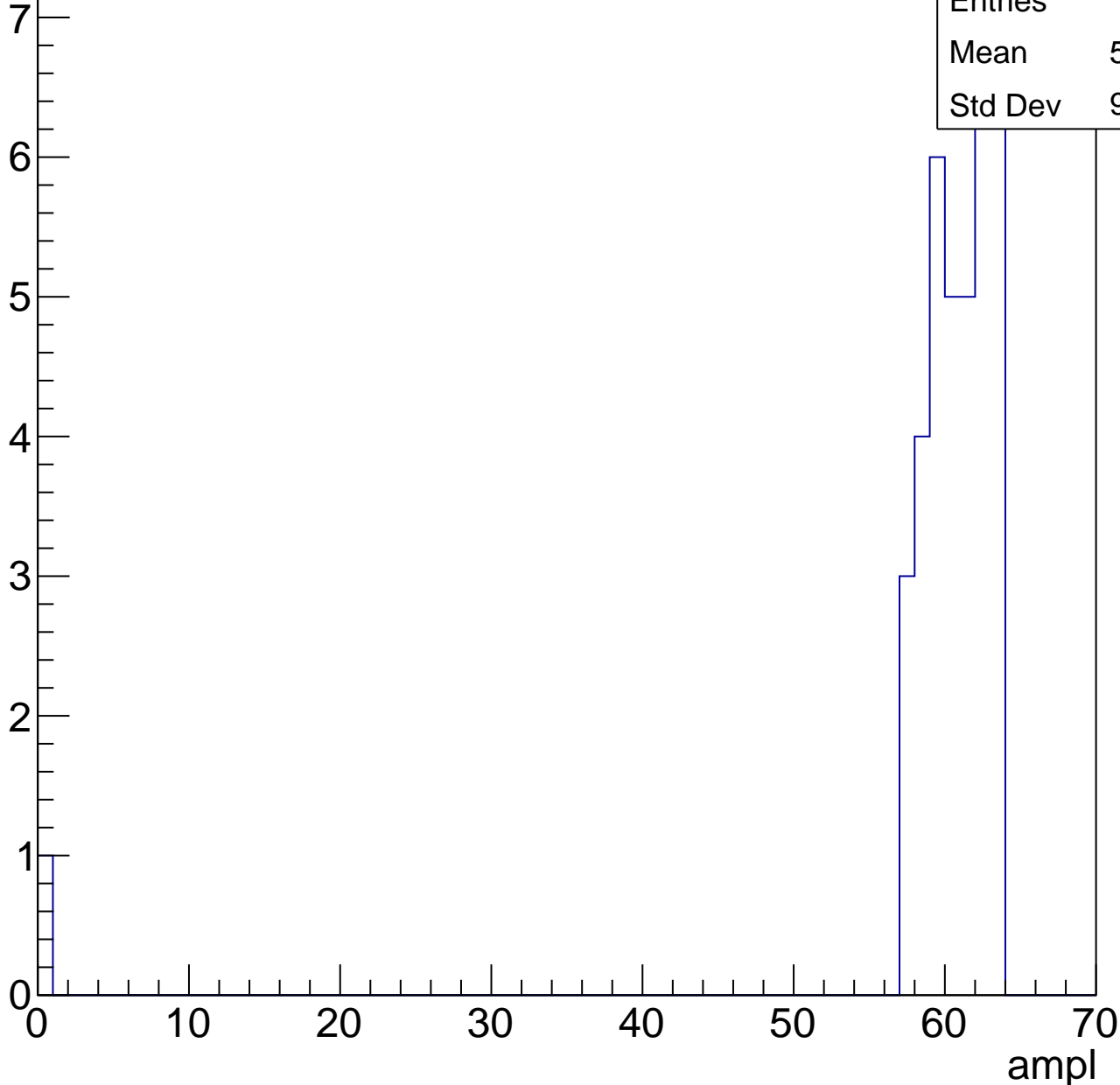


B1L102S, U22-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.87
Std Dev	9.863



B1L102S, U22-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch13, adc0

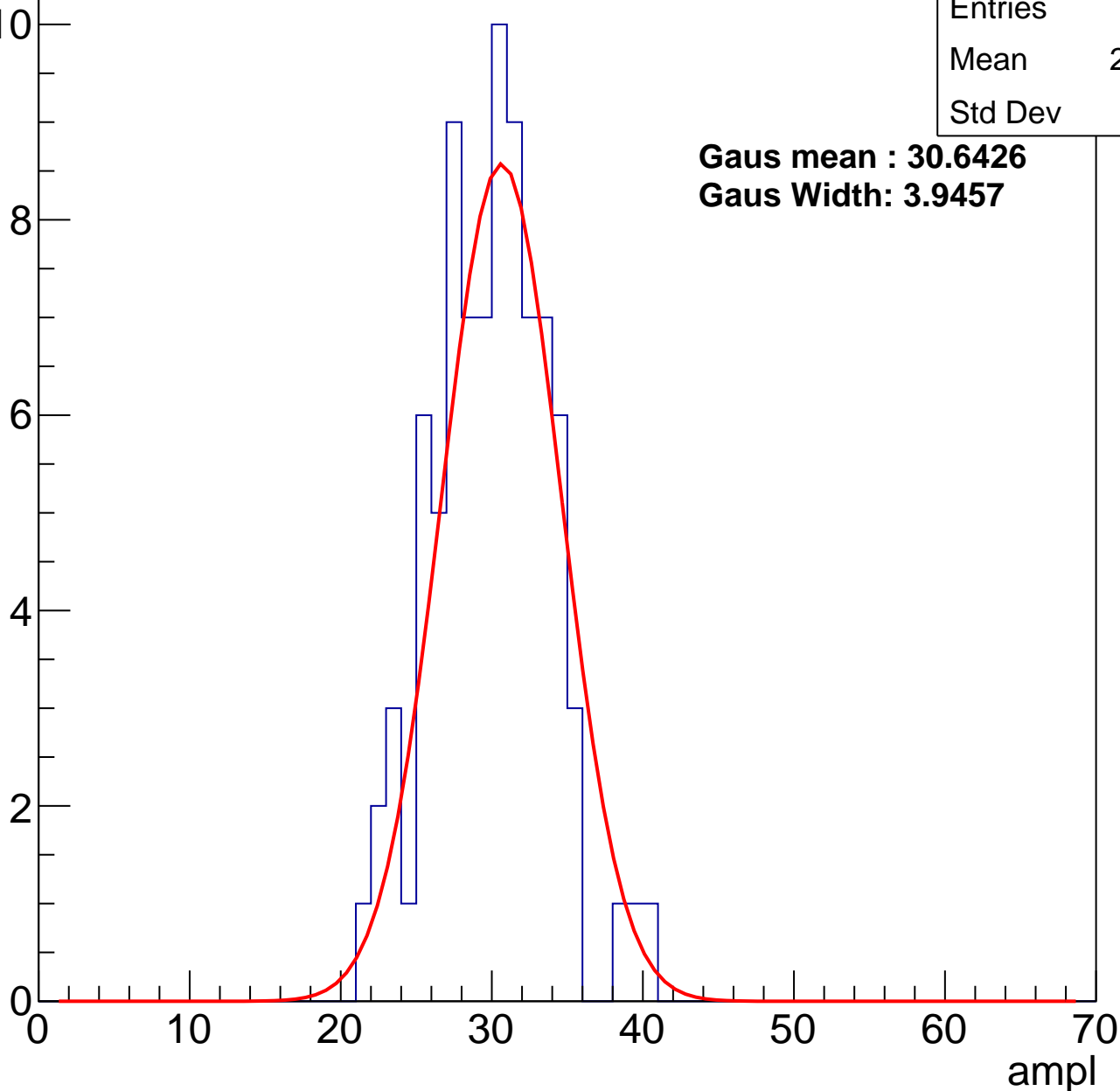
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	29.53
Std Dev	3.79

Gaus mean : 30.6426

Gaus Width: 3.9457



B1L102S, U22-ch13, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	37.22
Std Dev	3.273

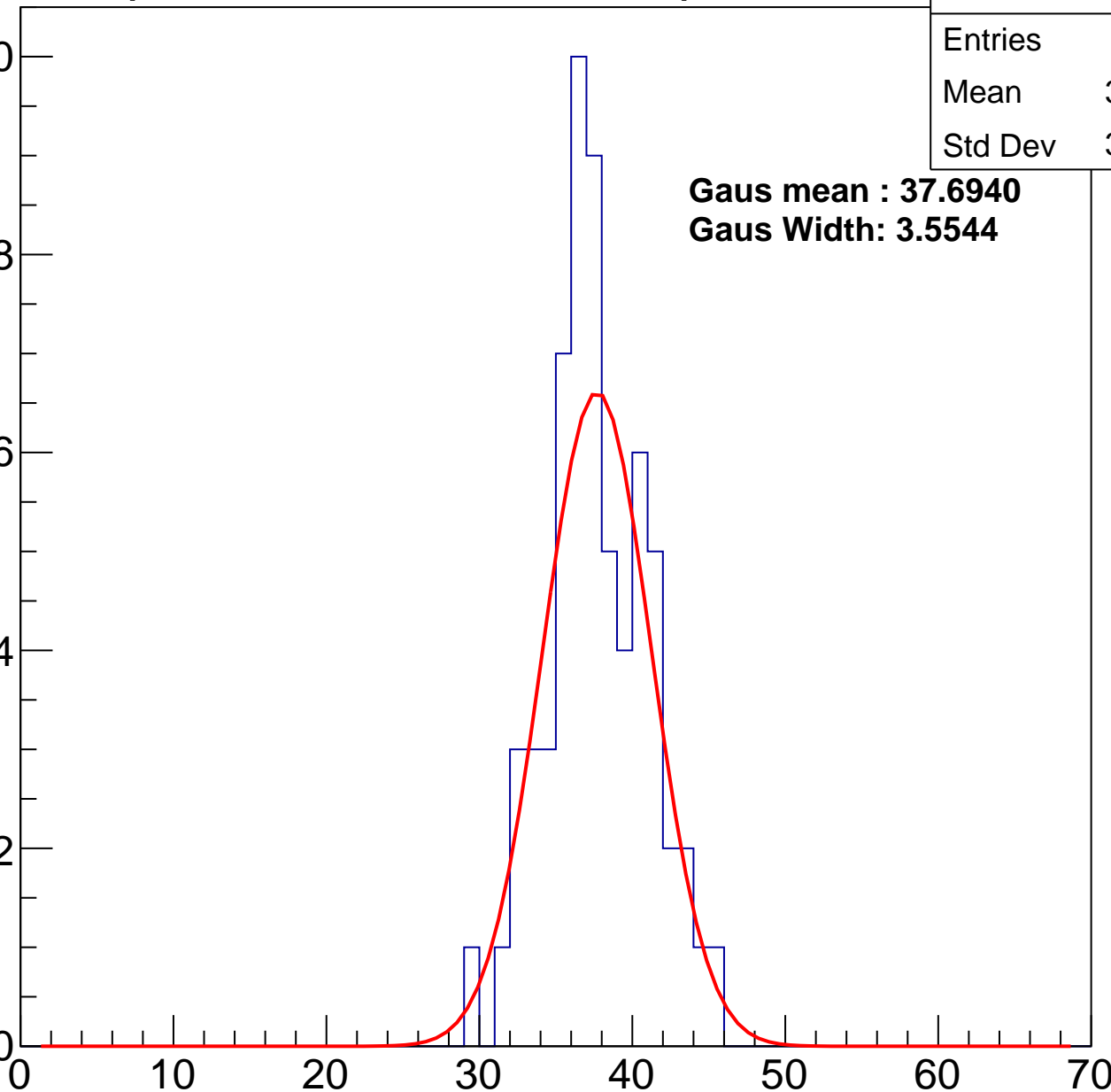
Gaus mean : 37.6940

Gaus Width: 3.5544

Entry

10
8
6
4
2
0

ampl



B1L102S, U22-ch13, adc2

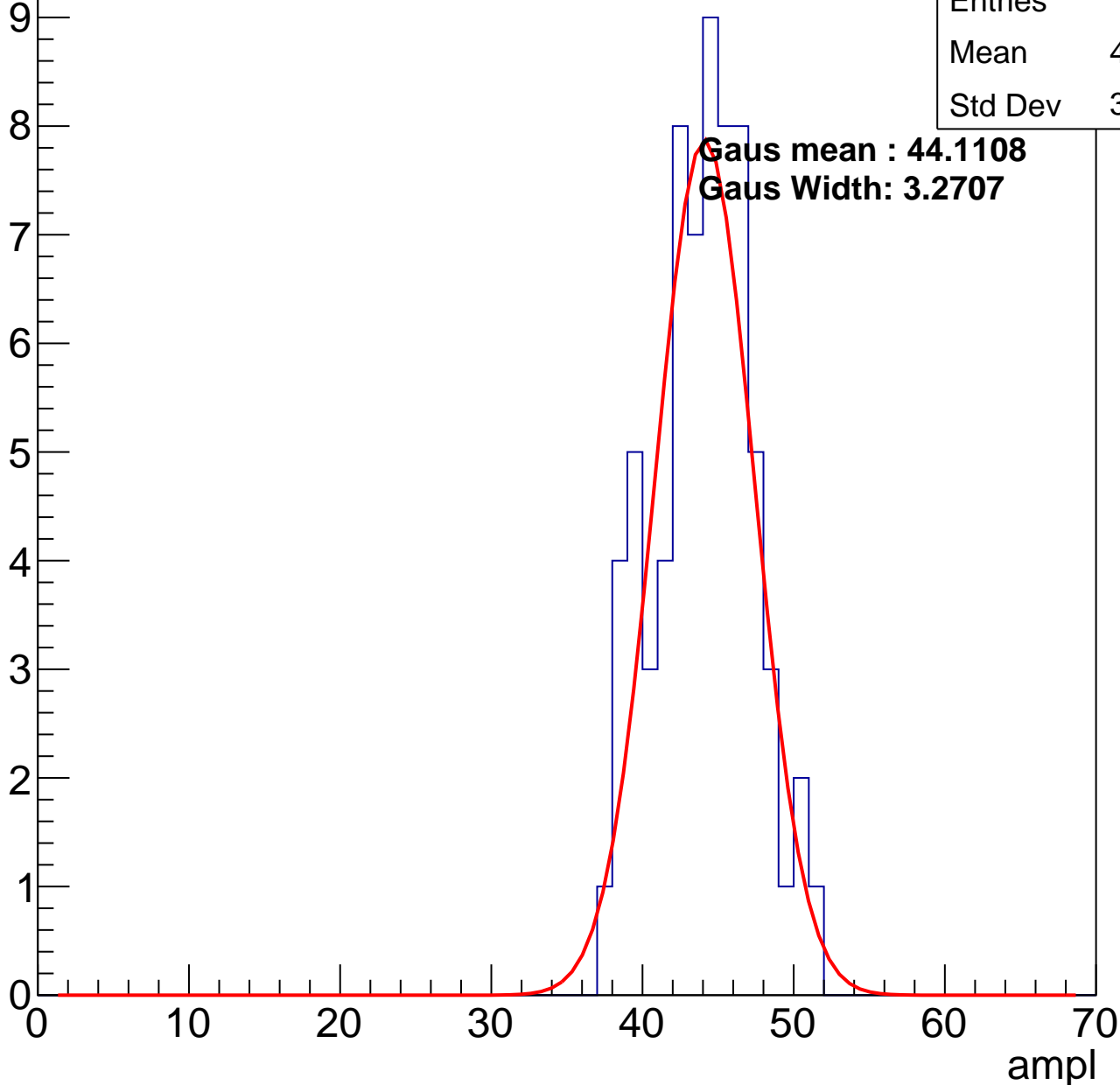
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.59
Std Dev	3.205

Gaus mean : 44.1108

Gaus Width: 3.2707

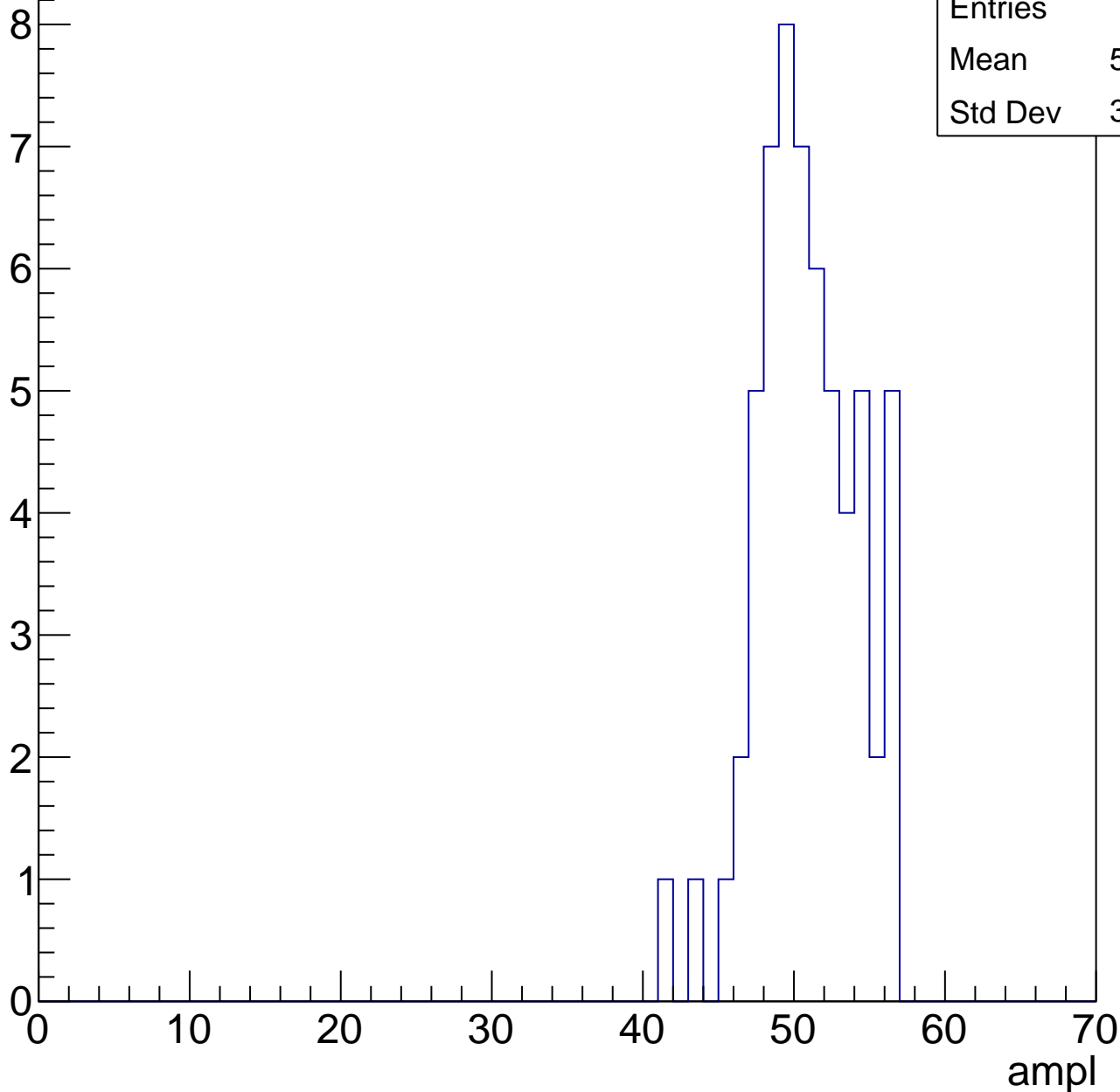


B1L102S, U22-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.37
Std Dev	3.272



B1L102S, U22-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	56.56
Std Dev	3.302

Entry

10

8

6

4

2

0

0

10

20

30

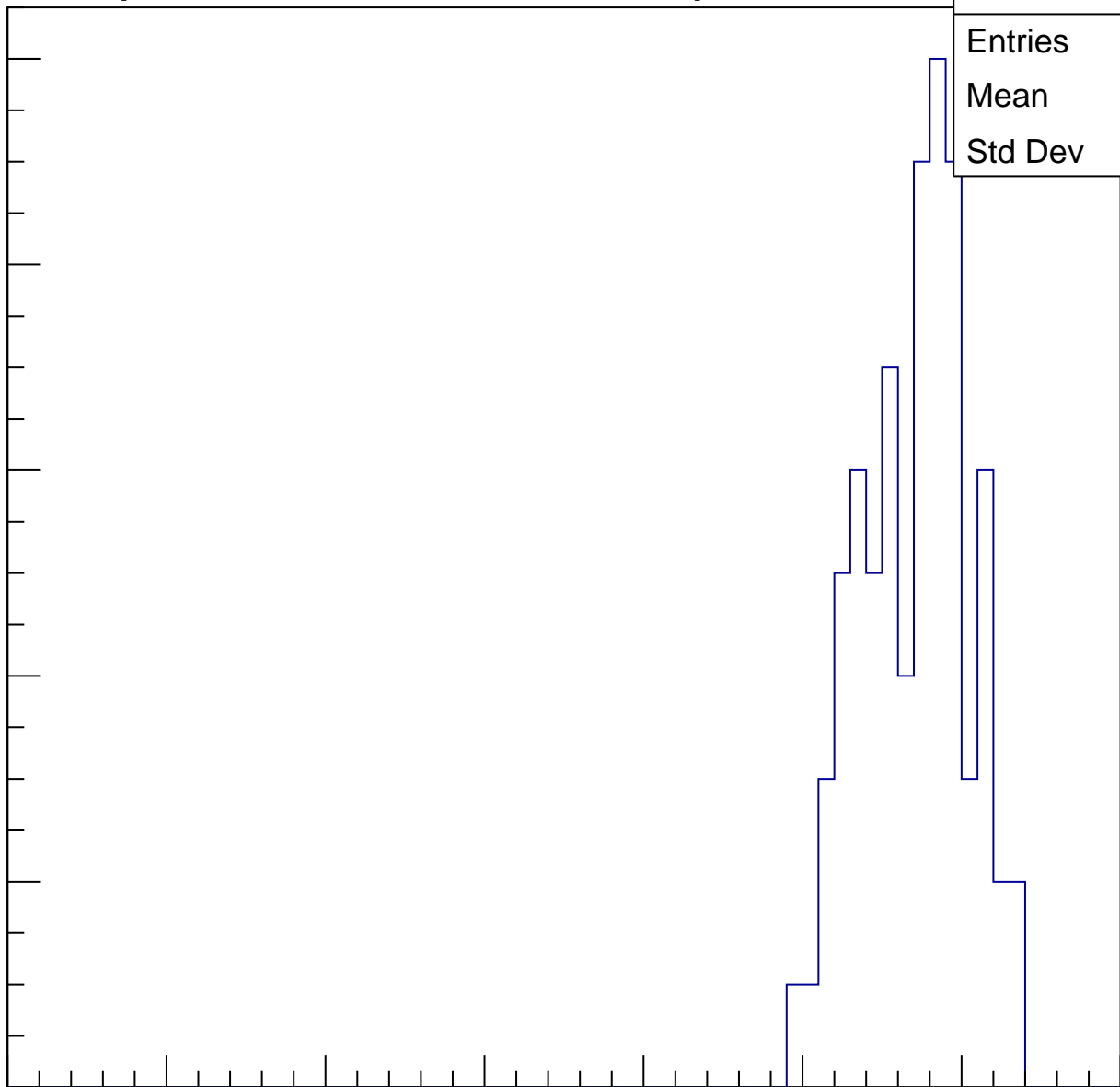
40

50

60

70

ampl

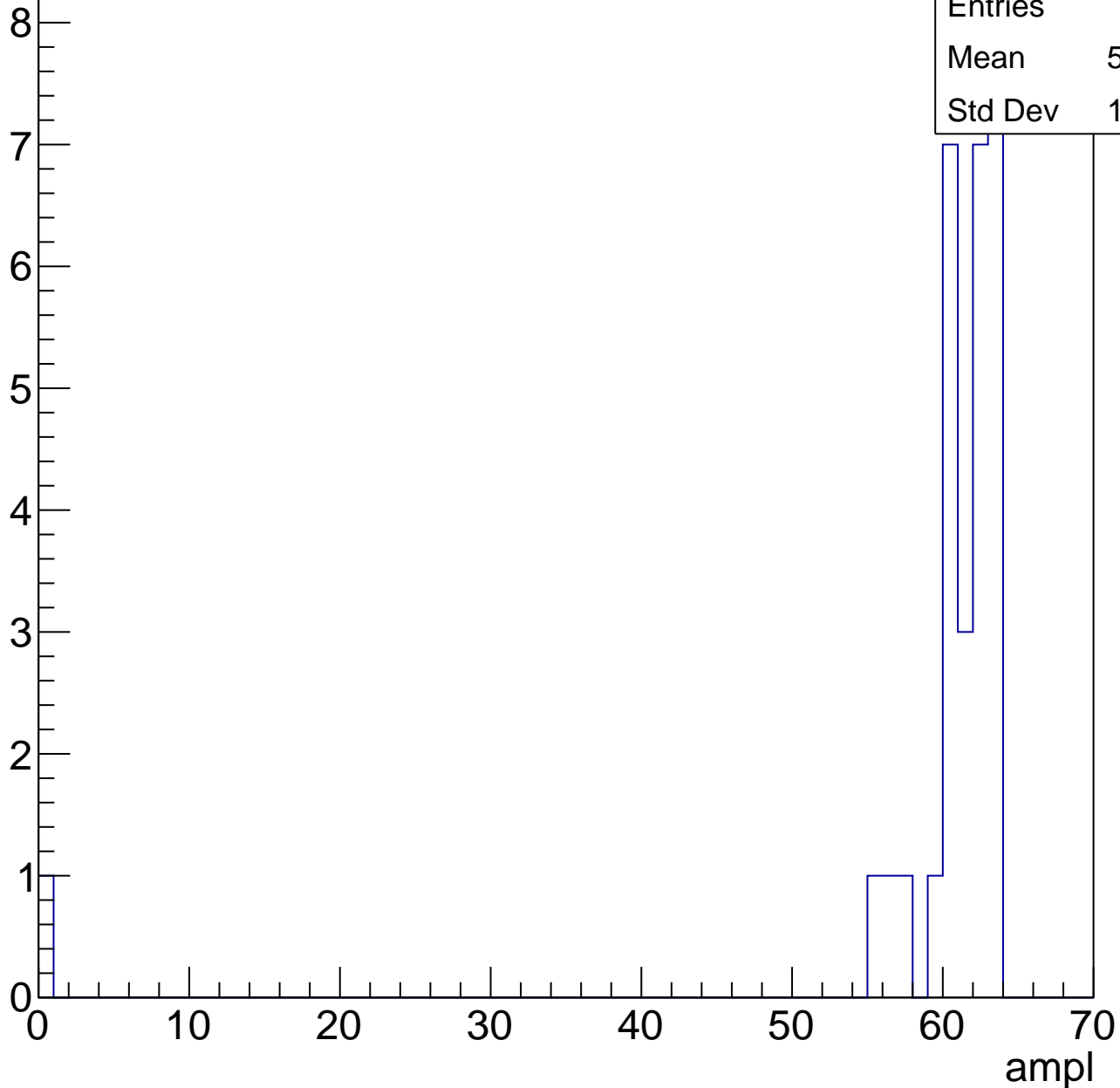


B1L102S, U22-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	58.93
Std Dev	11.14



B1L102S, U22-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch14, adc0

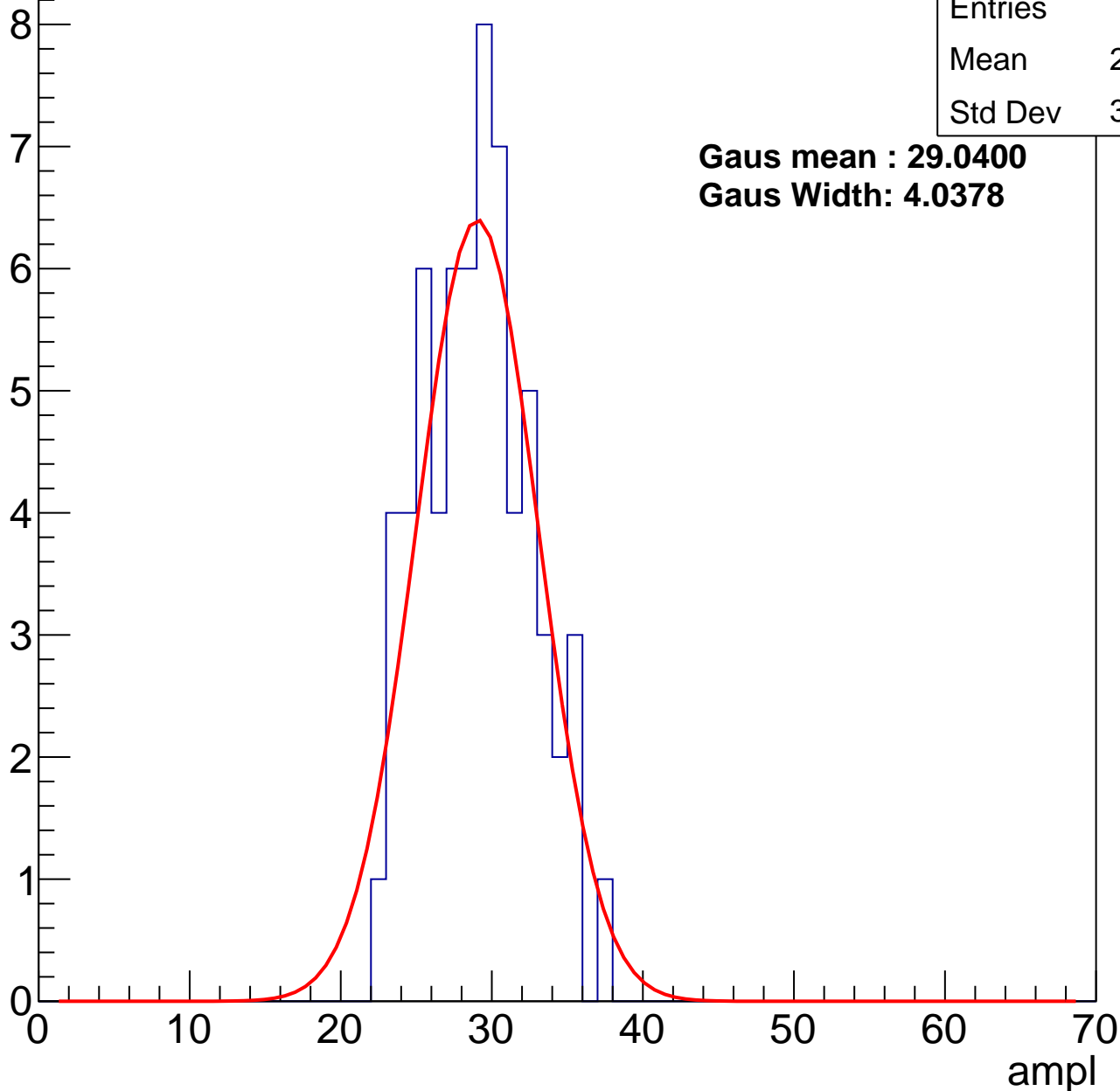
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.58
Std Dev	3.495

Gaus mean : 29.0400

Gaus Width: 4.0378



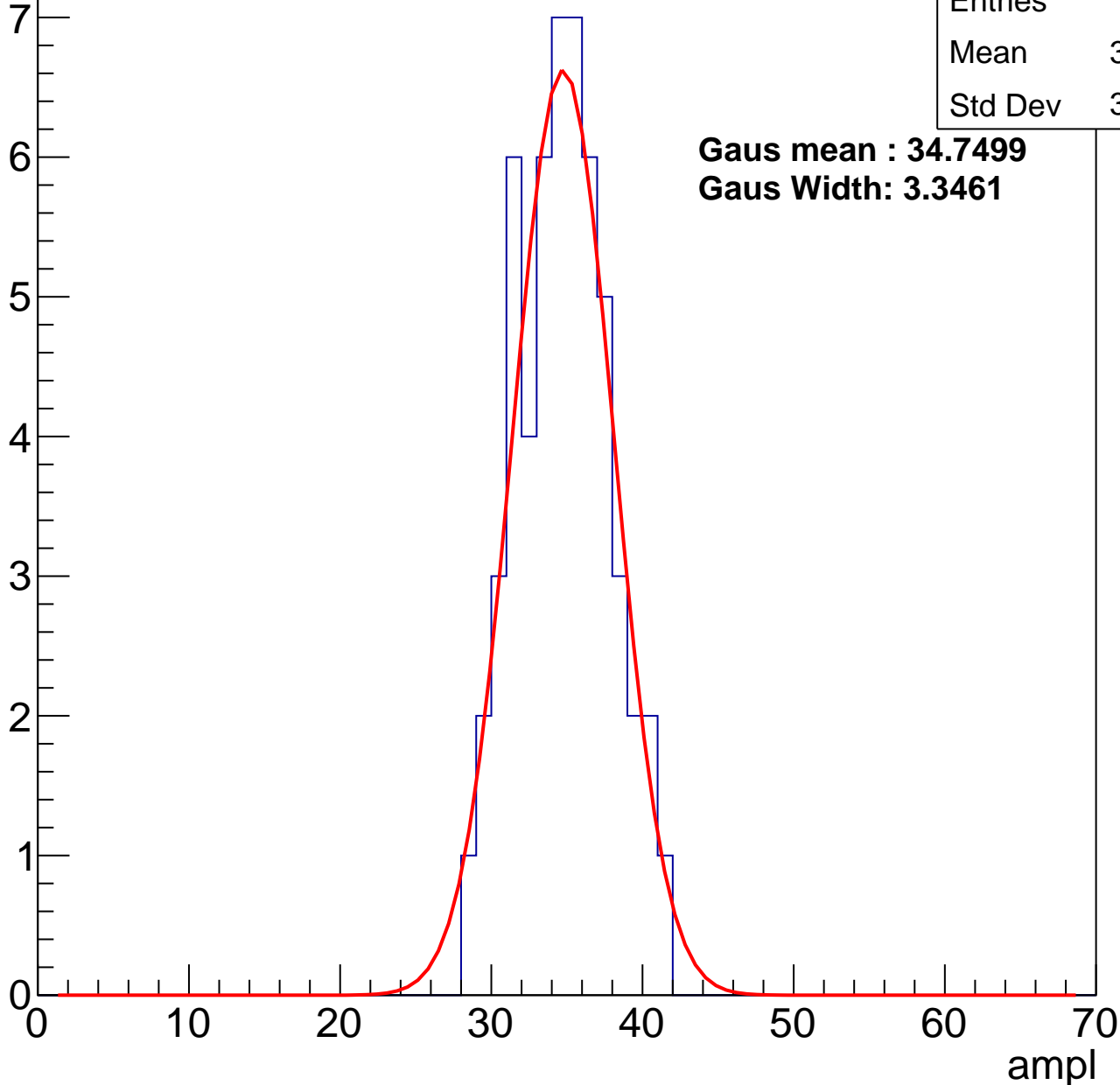
B1L102S, U22-ch14, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	34.27
Std Dev	3.018

Gaus mean : 34.7499
Gaus Width: 3.3461



B1L102S, U22-ch14, adc2

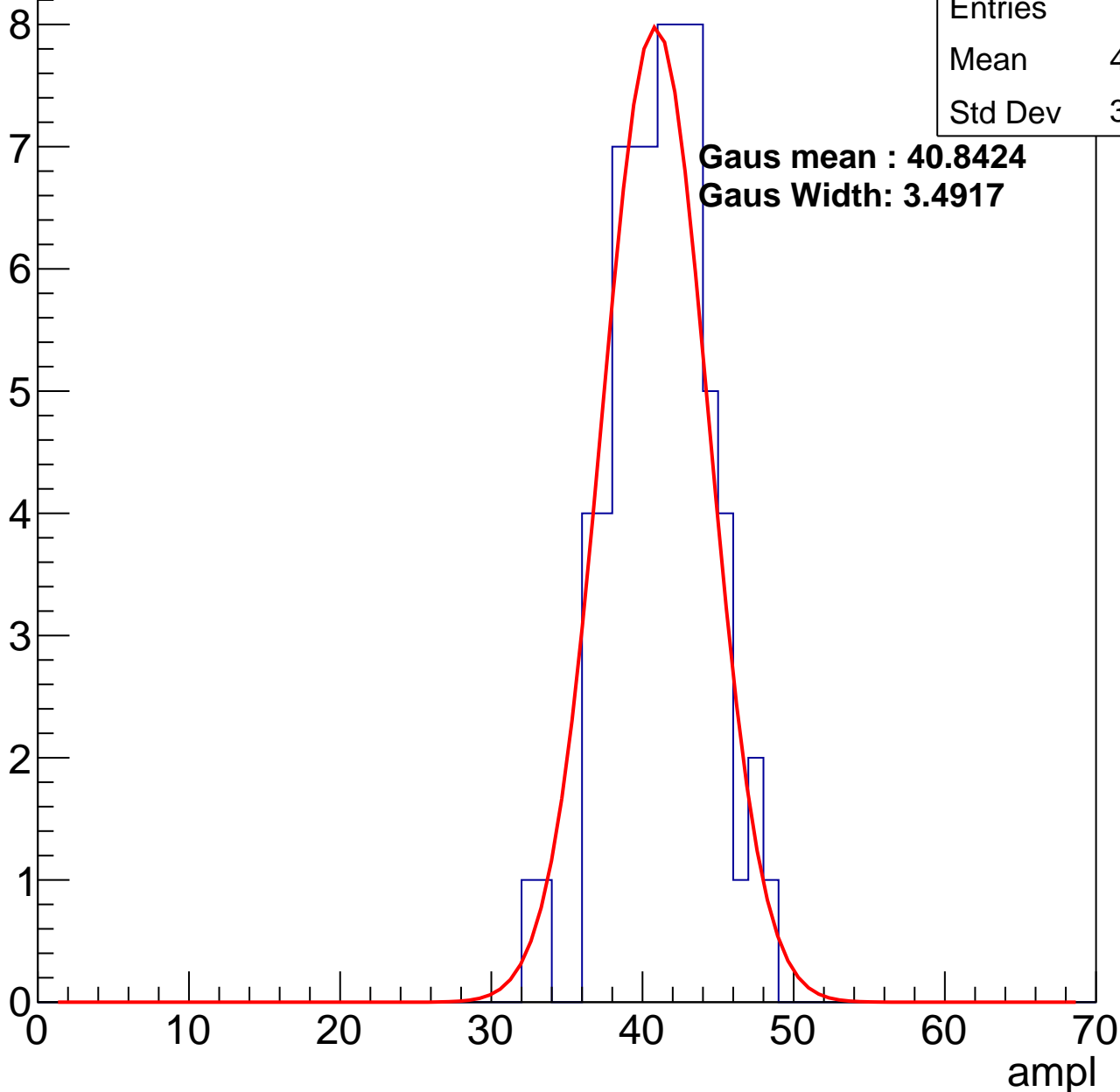
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	40.76
Std Dev	3.204

Gaus mean : 40.8424

Gaus Width: 3.4917

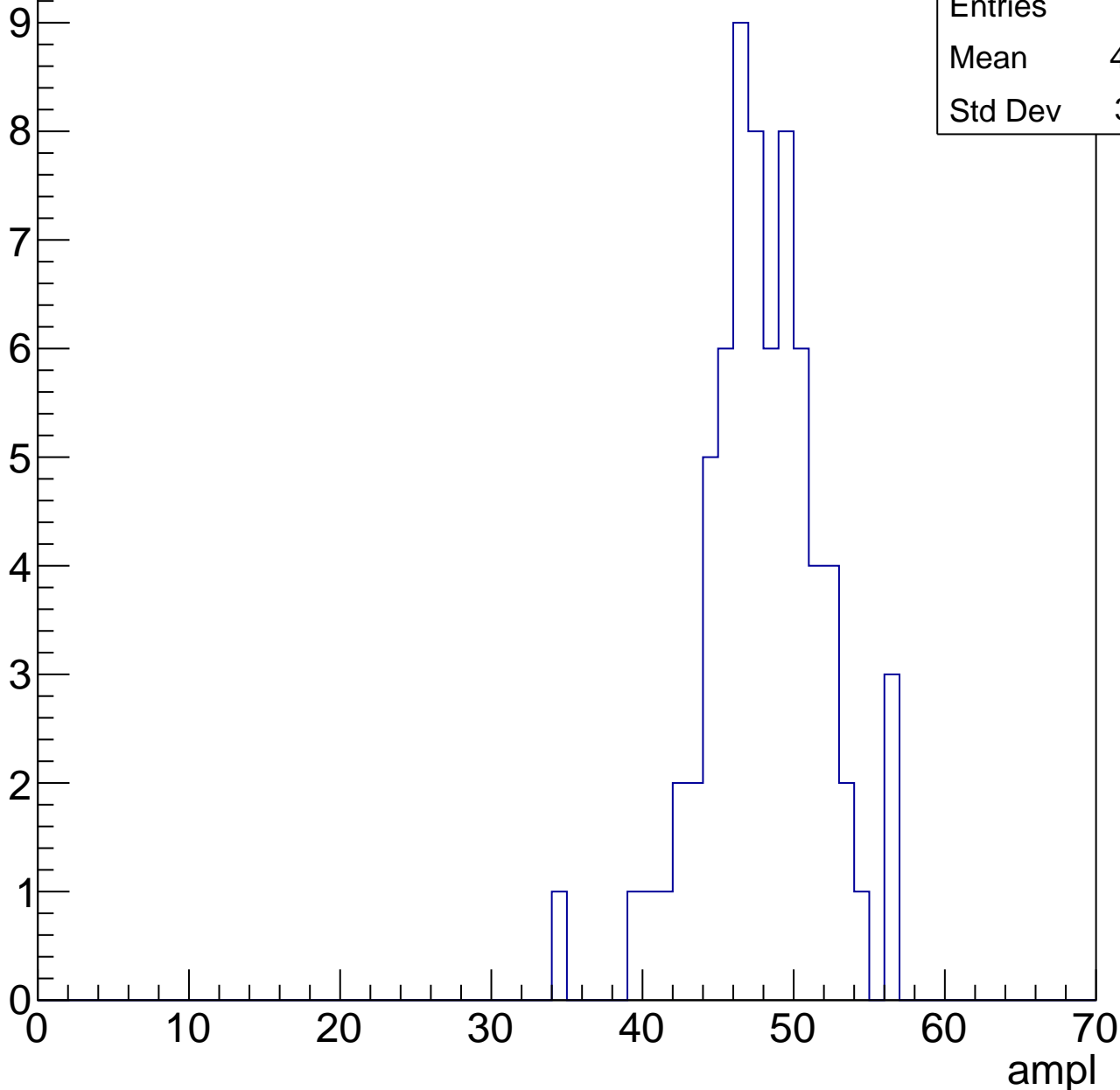


B1L102S, U22-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.49
Std Dev	3.931

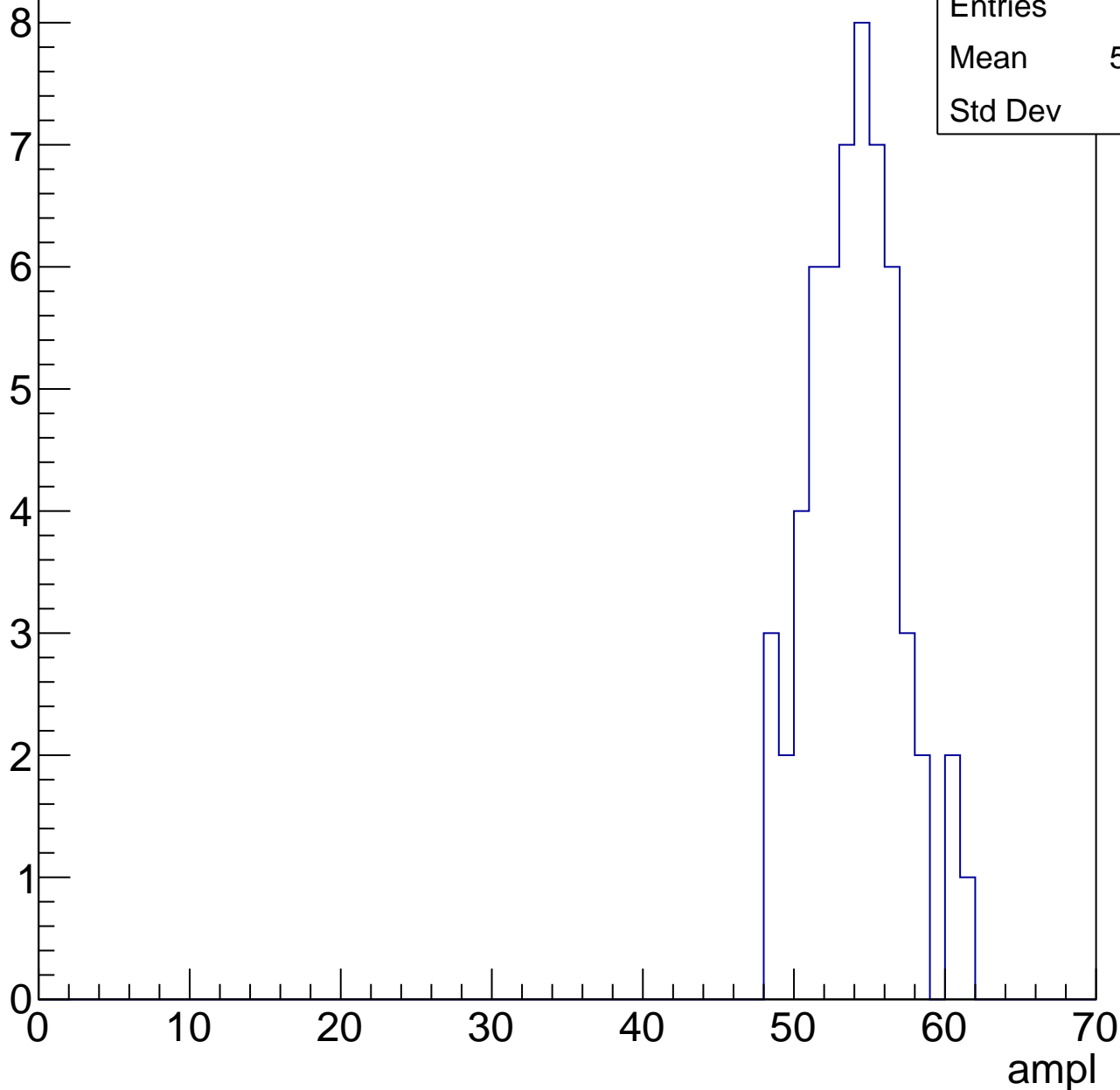


B1L102S, U22-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

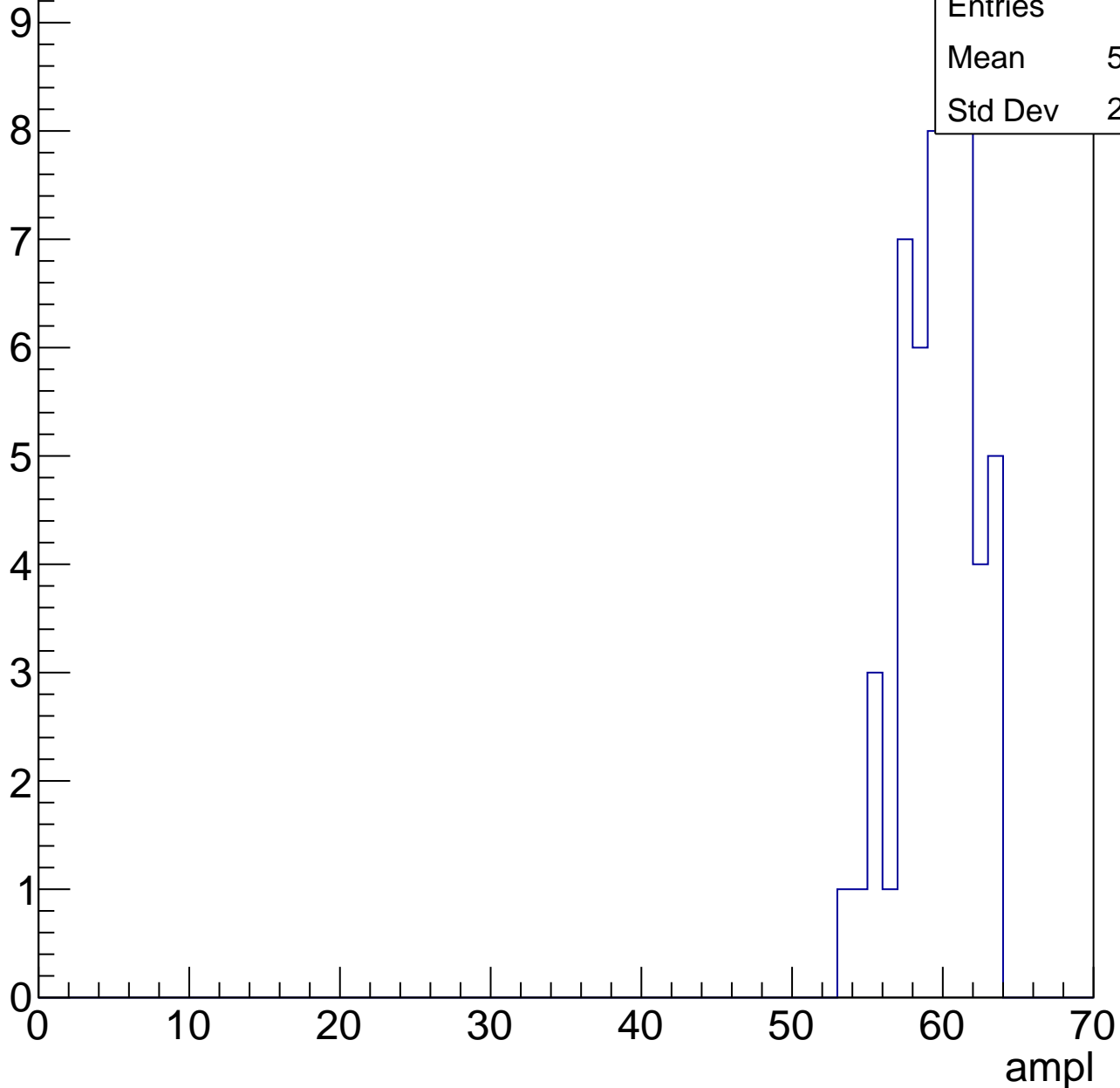
Entries	57
Mean	53.54
Std Dev	2.98



B1L102S, U22-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

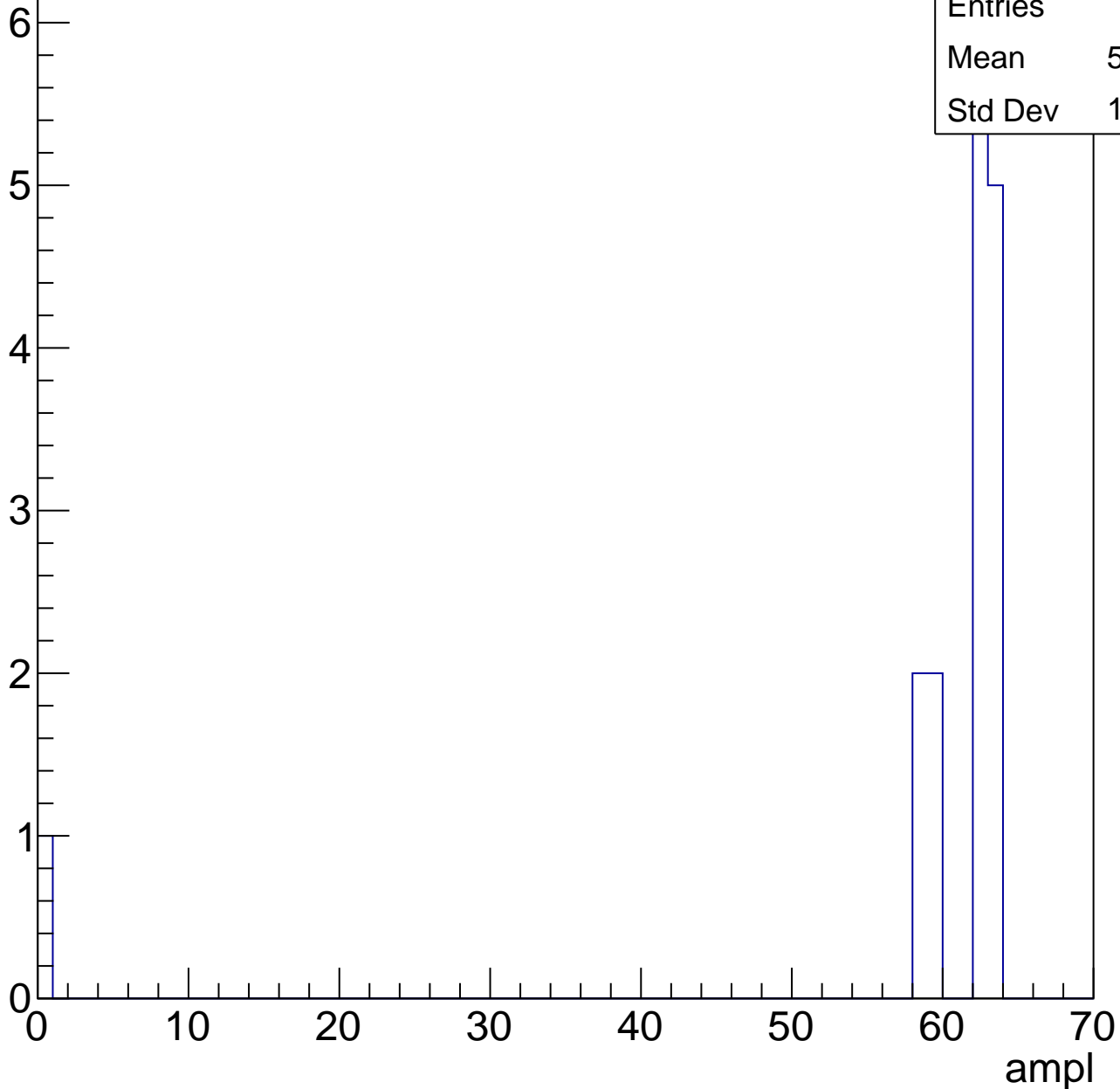


B1L102S, U22-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.56
Std Dev	14.97



B1L102S, U22-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch15, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	28.99
Std Dev	3.343

Gaus mean : 29.7438

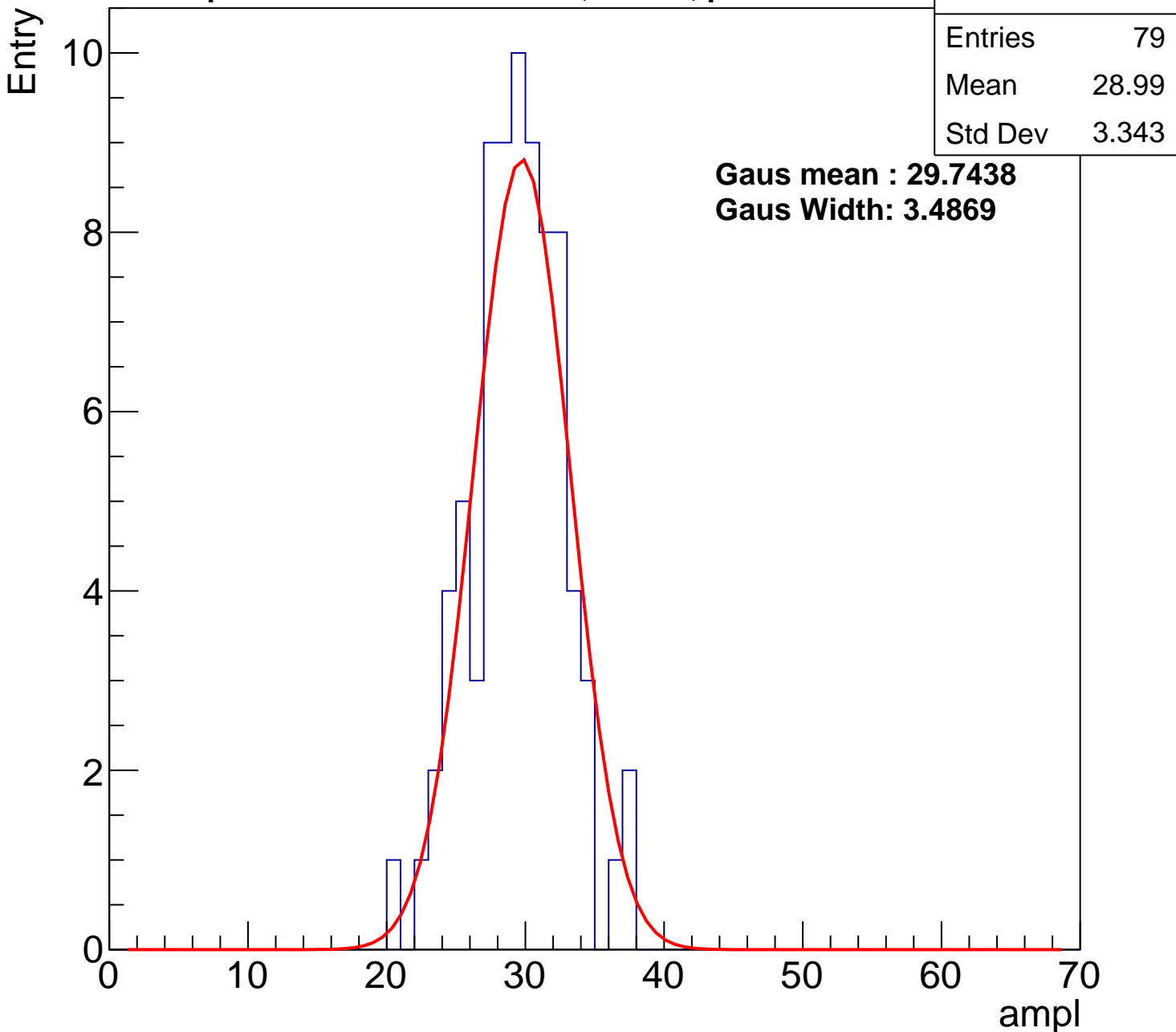
Gaus Width: 3.4869

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch15, adc1

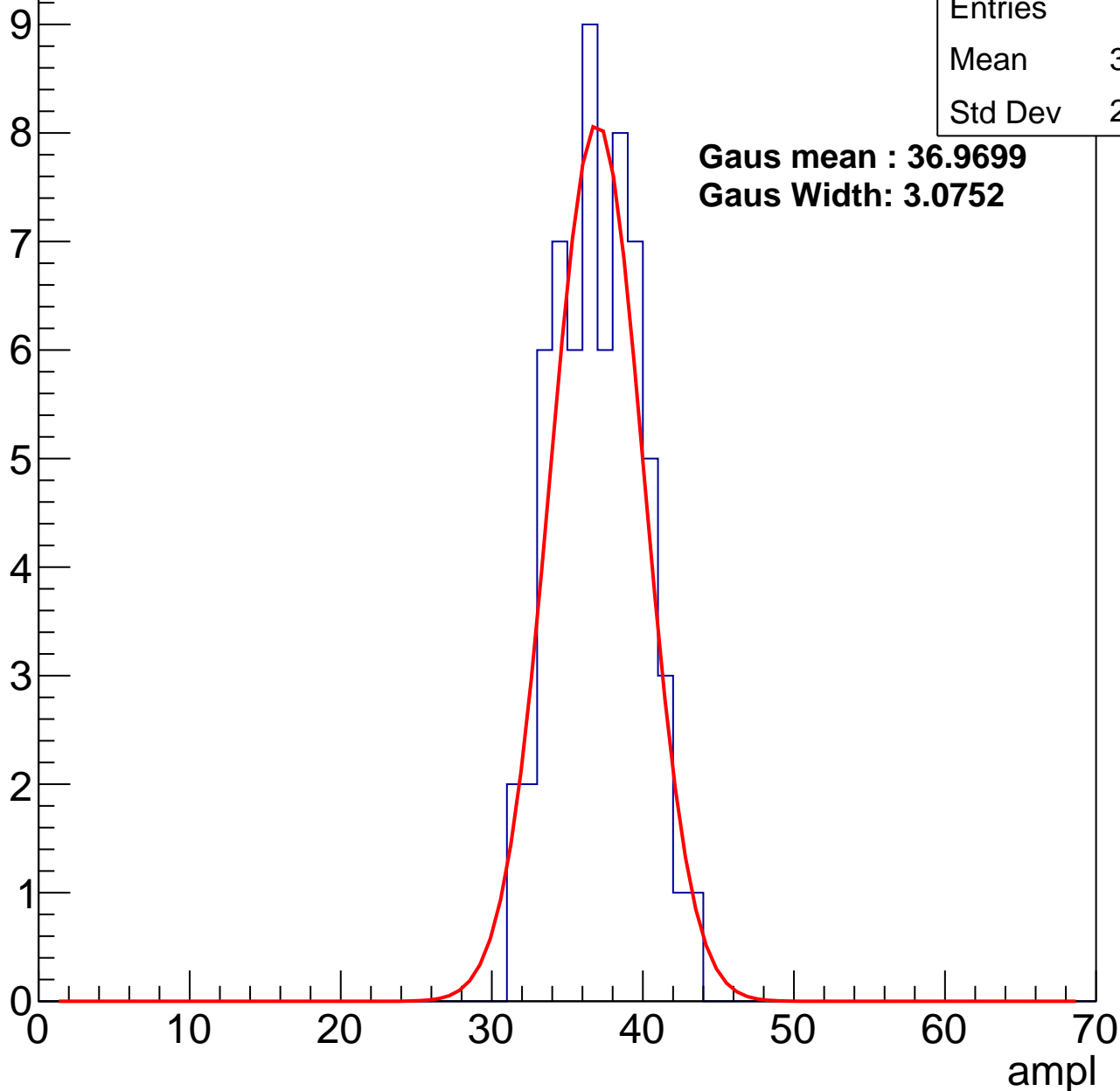
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.56
Std Dev	2.793

Gaus mean : 36.9699

Gaus Width: 3.0752

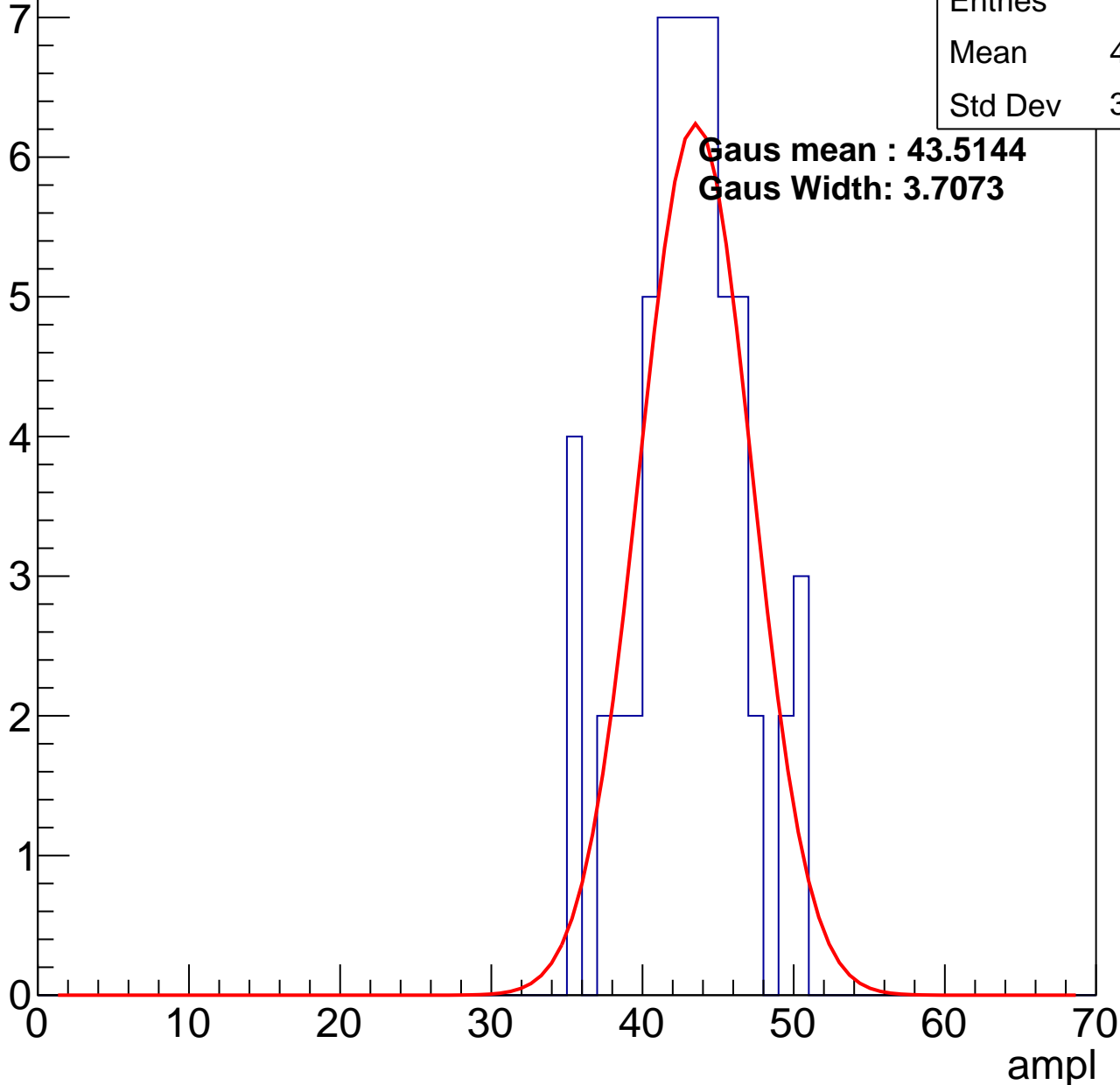


B1L102S, U22-ch15, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.58
Std Dev	3.657

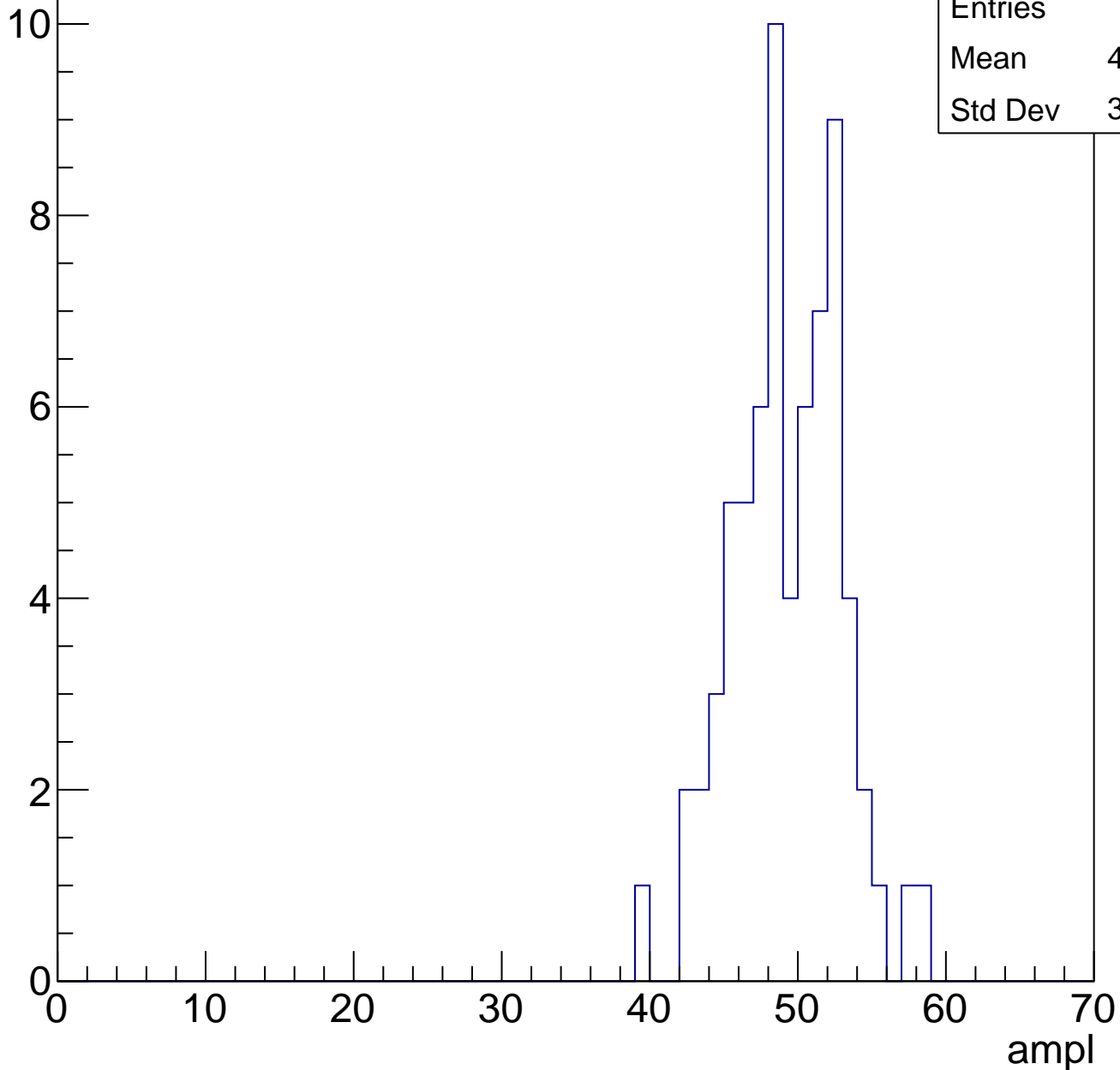


B1L102S, U22-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	48.83
Std Dev	3.635

Entry

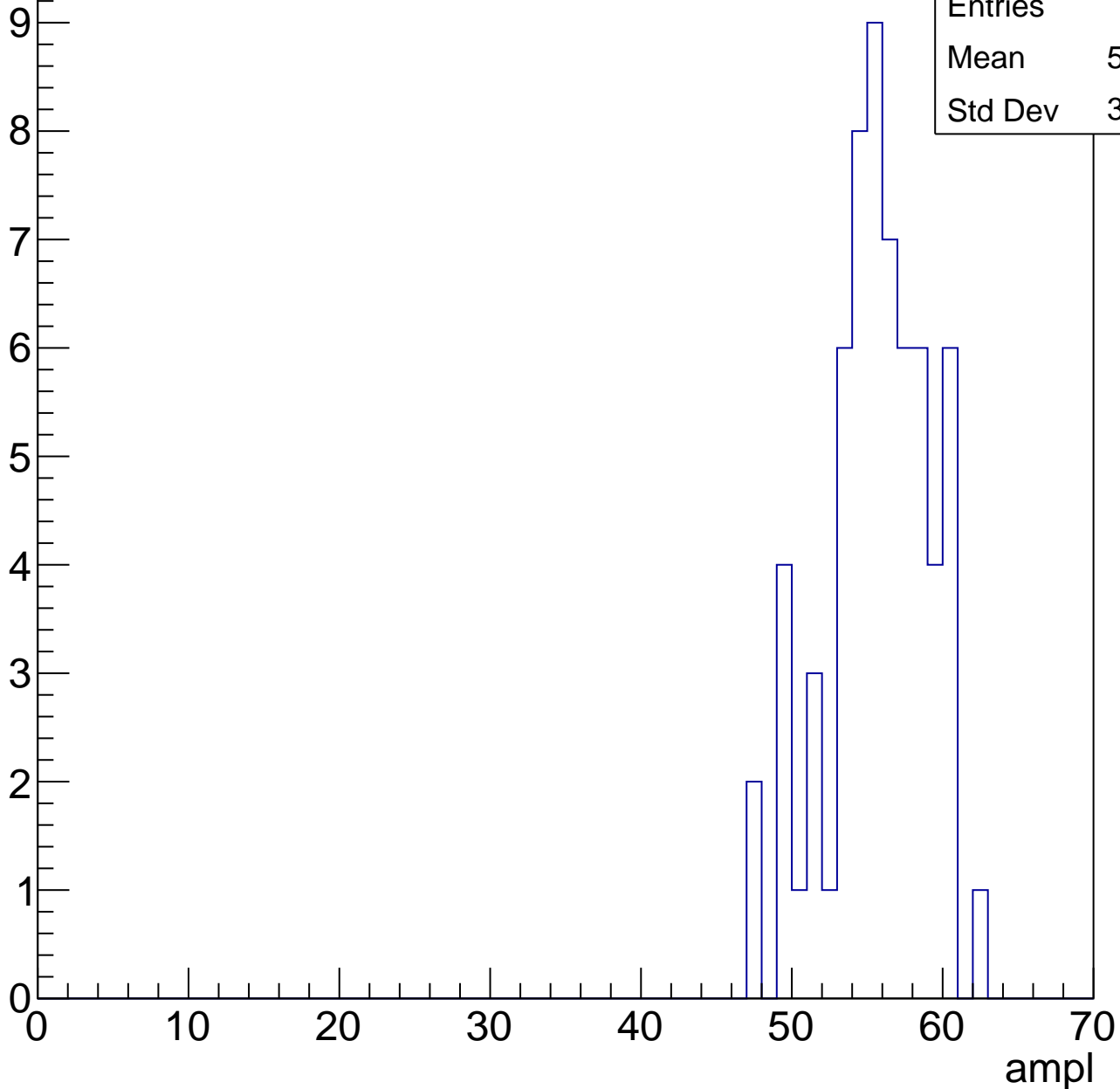


B1L102S, U22-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55.16
Std Dev	3.383

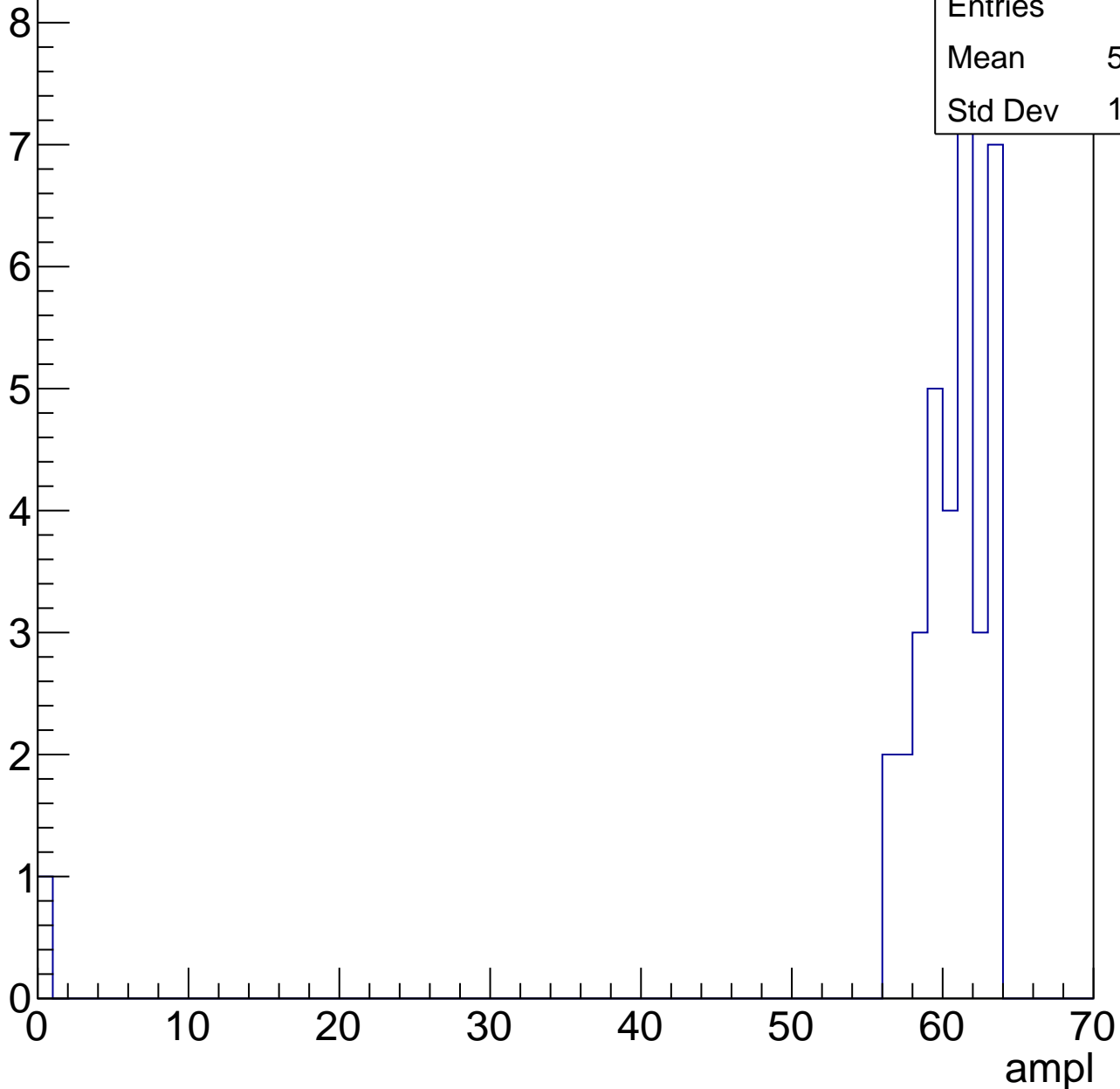


B1L102S, U22-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	58.57
Std Dev	10.25

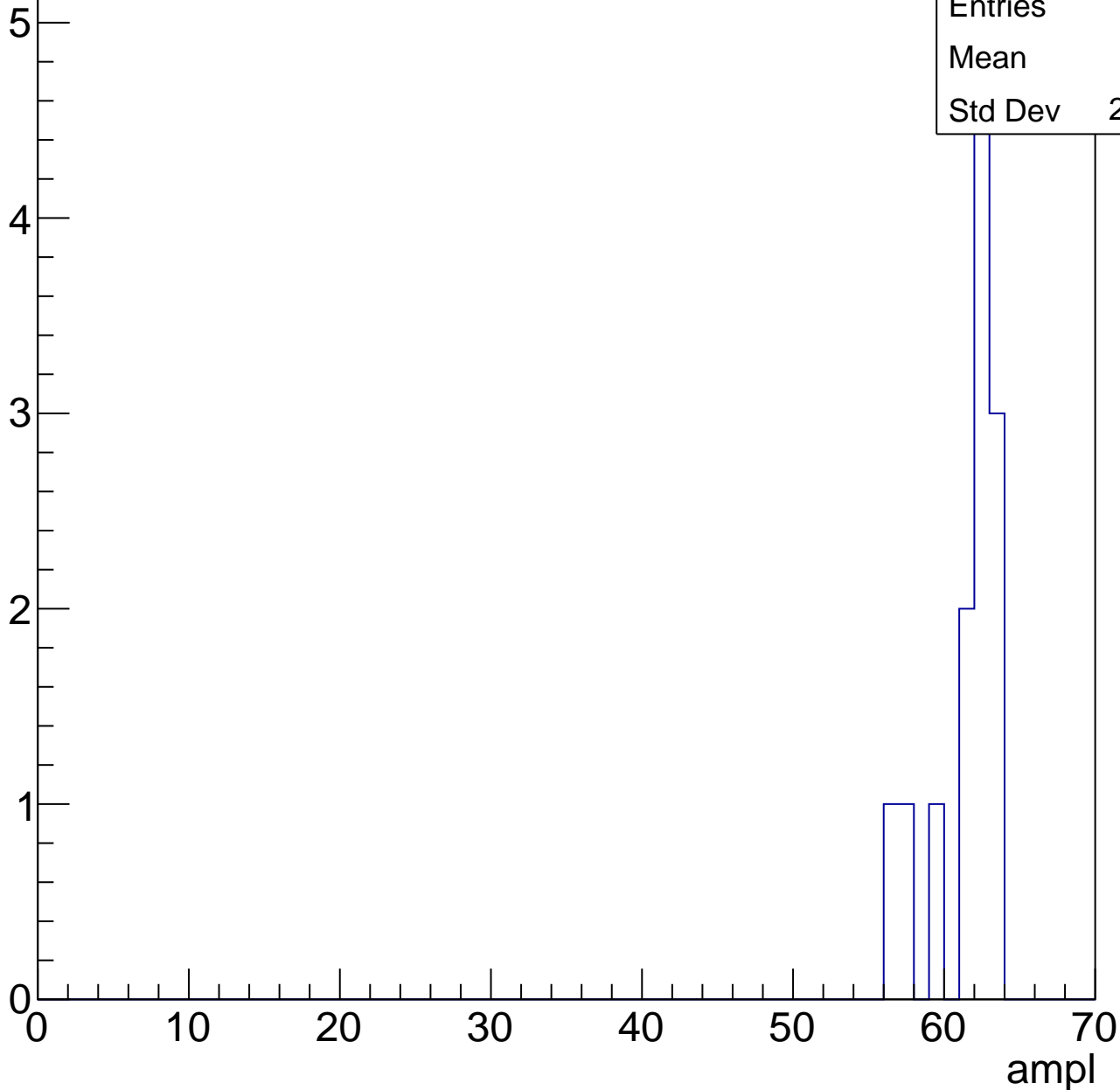


B1L102S, U22-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61
Std Dev	2.184



B1L102S, U22-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L102S, U22-ch16, adc0

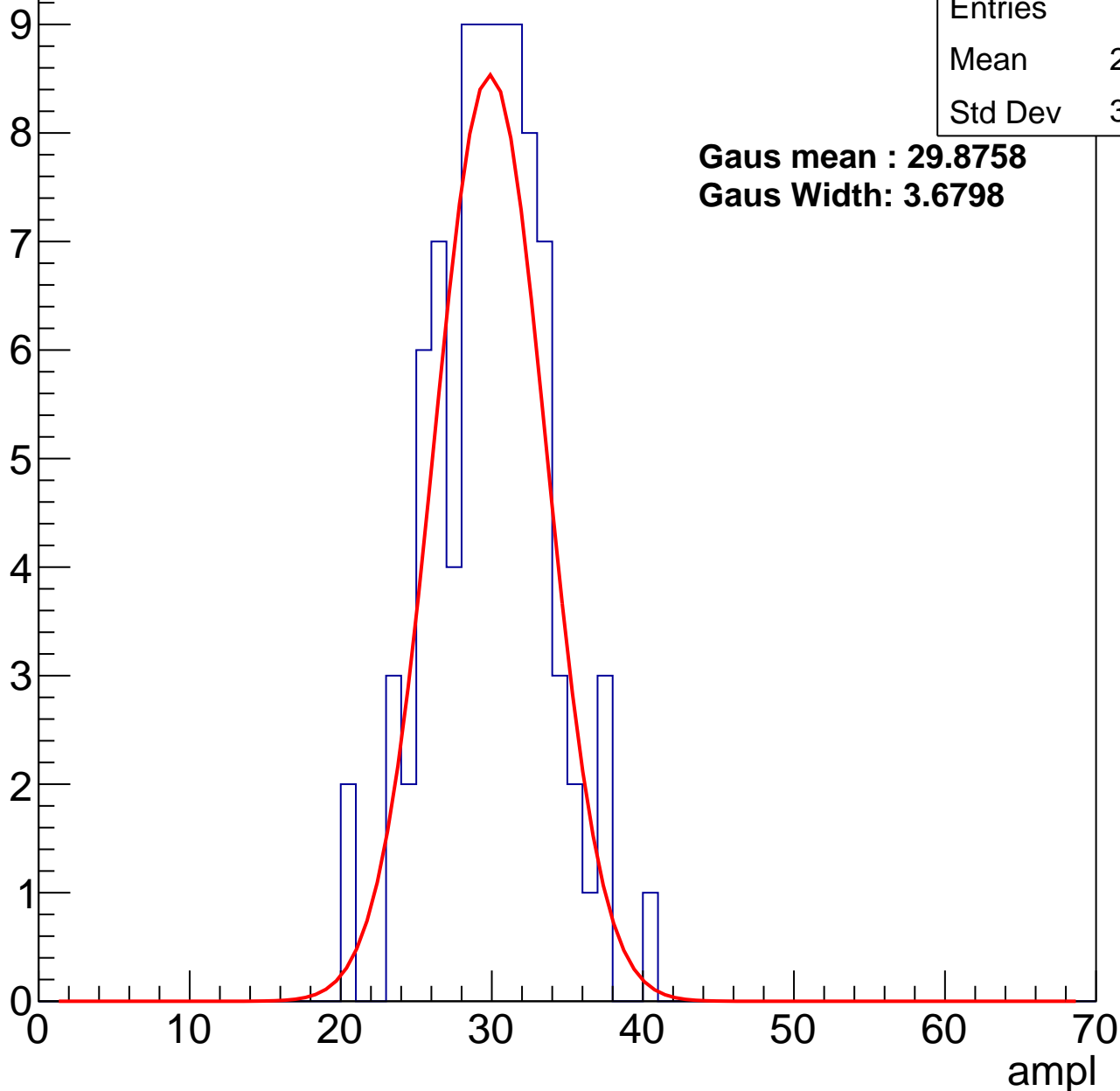
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	29.47
Std Dev	3.793

Gaus mean : 29.8758

Gaus Width: 3.6798



B1L102S, U22-ch16, adc1

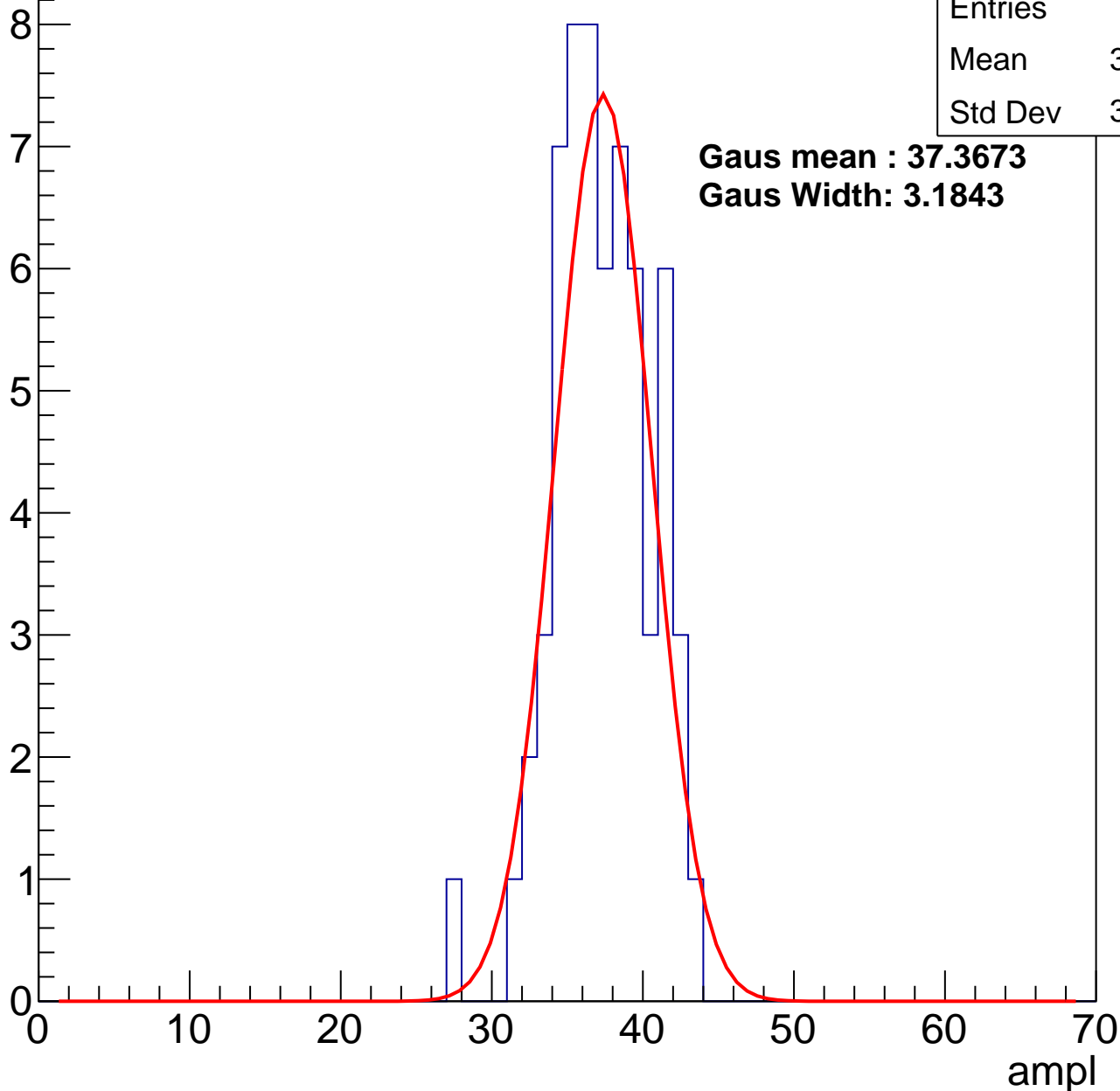
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.84
Std Dev	3.112

Gaus mean : 37.3673

Gaus Width: 3.1843



B1L102S, U22-ch16, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.43
Std Dev	3.11

Gaus mean : 44.0750

Gaus Width: 3.3308

10

8

6

4

2

0

0

2

4

6

8

ampl

0

10

20

30

40

50

60

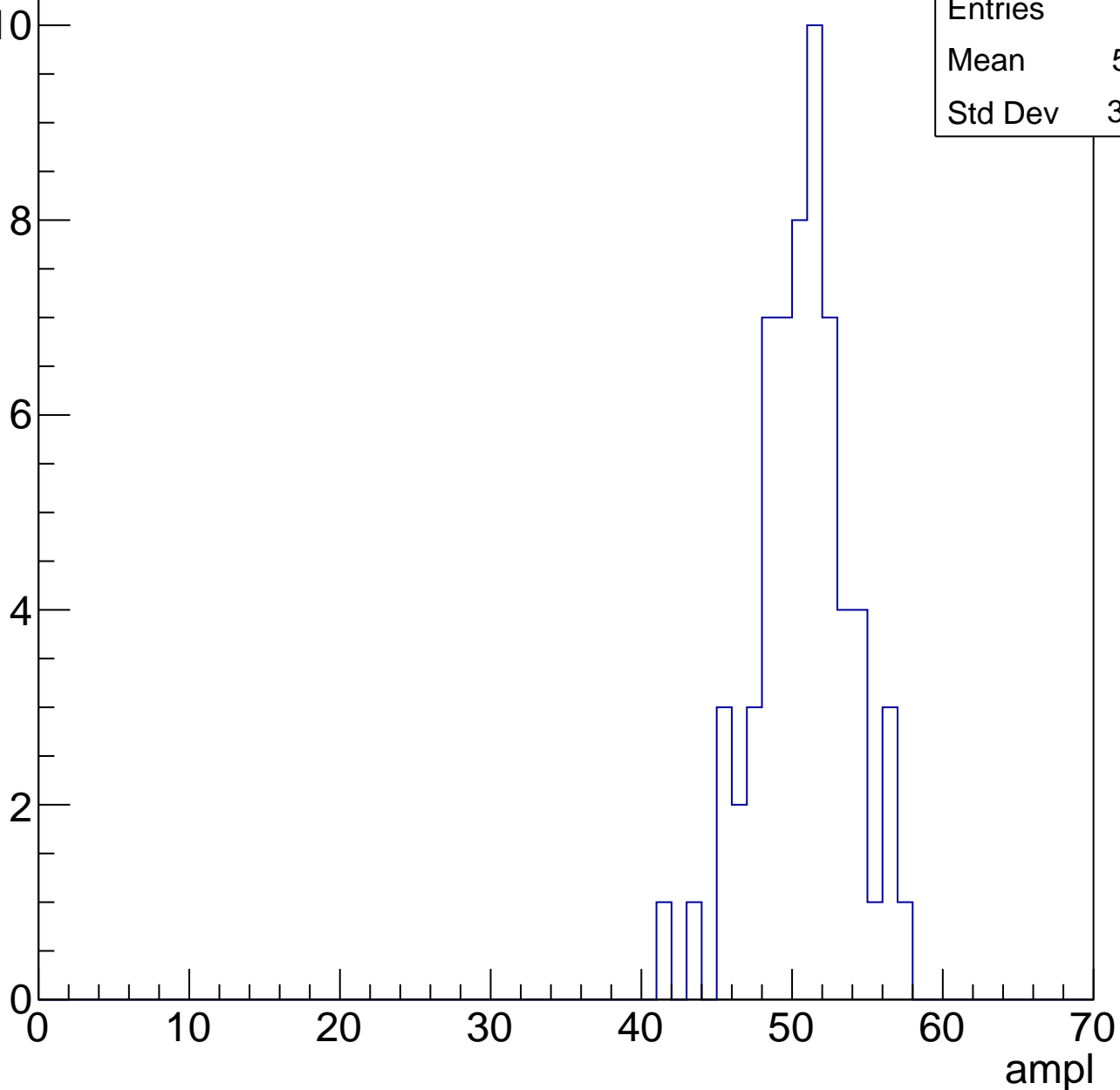
70

B1L102S, U22-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	50.21
Std Dev	3.153

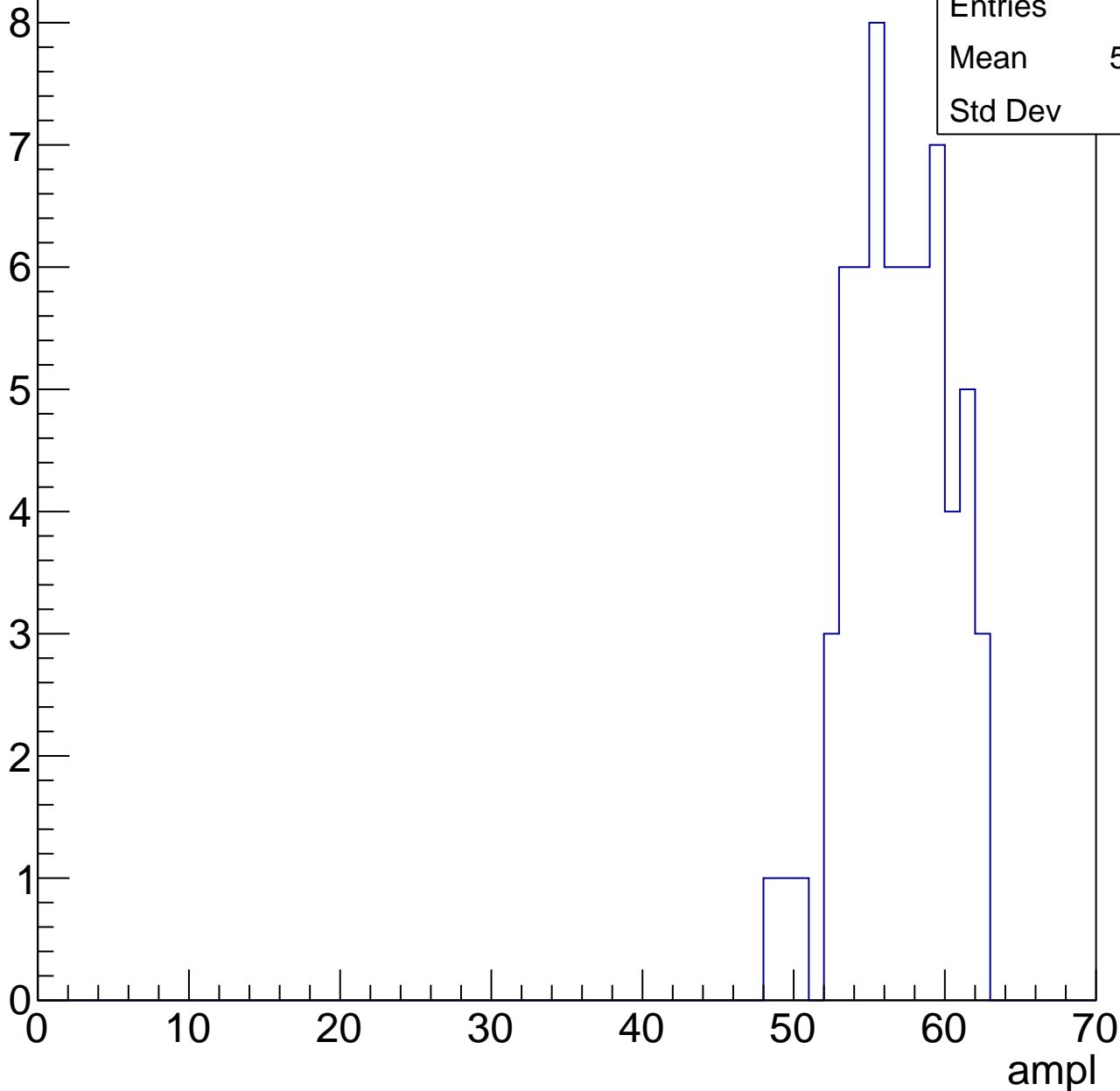


B1L102S, U22-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	56.43
Std Dev	3.24

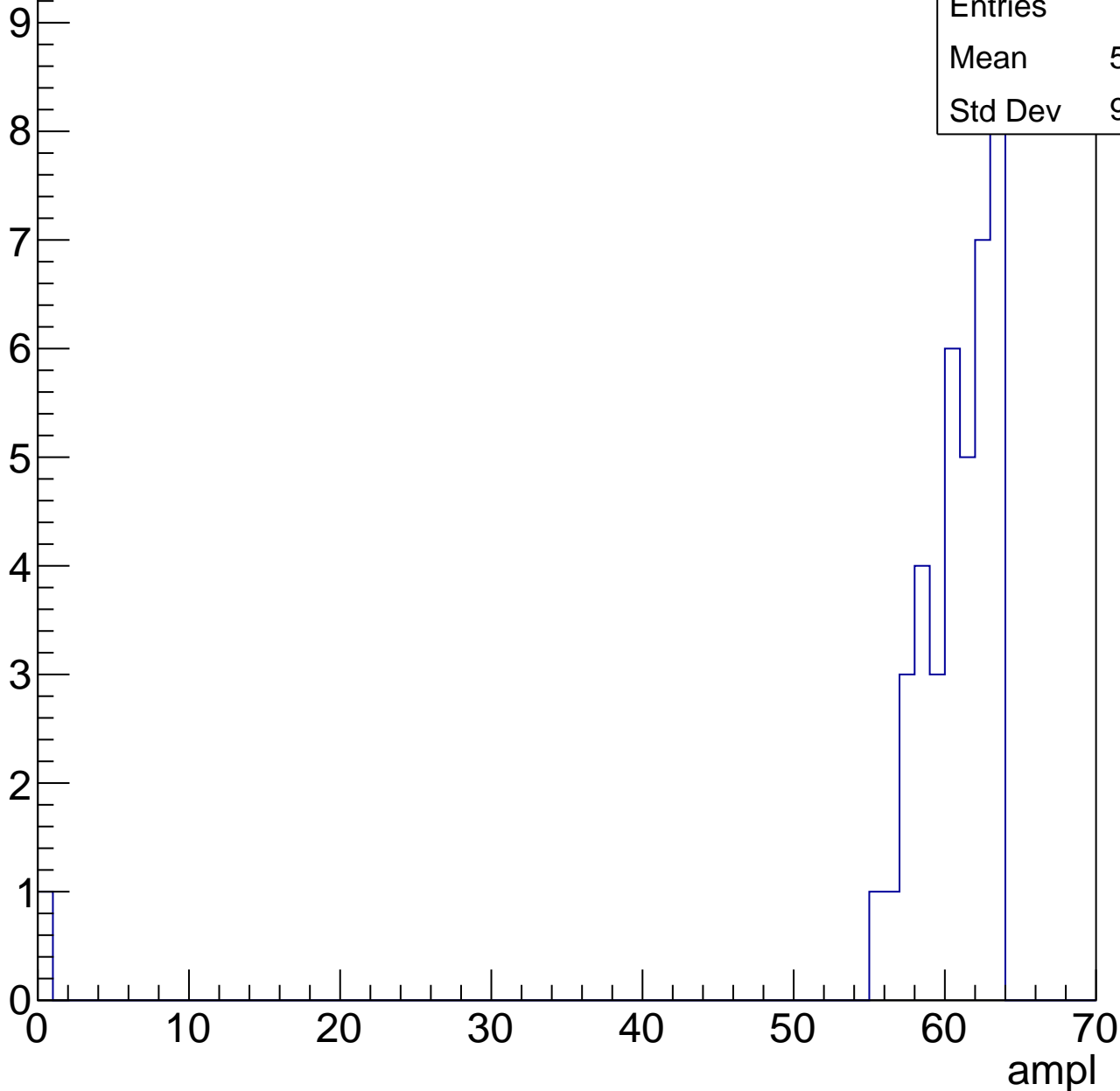


B1L102S, U22-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.92
Std Dev	9.689



B1L102S, U22-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U22-ch17, adc0

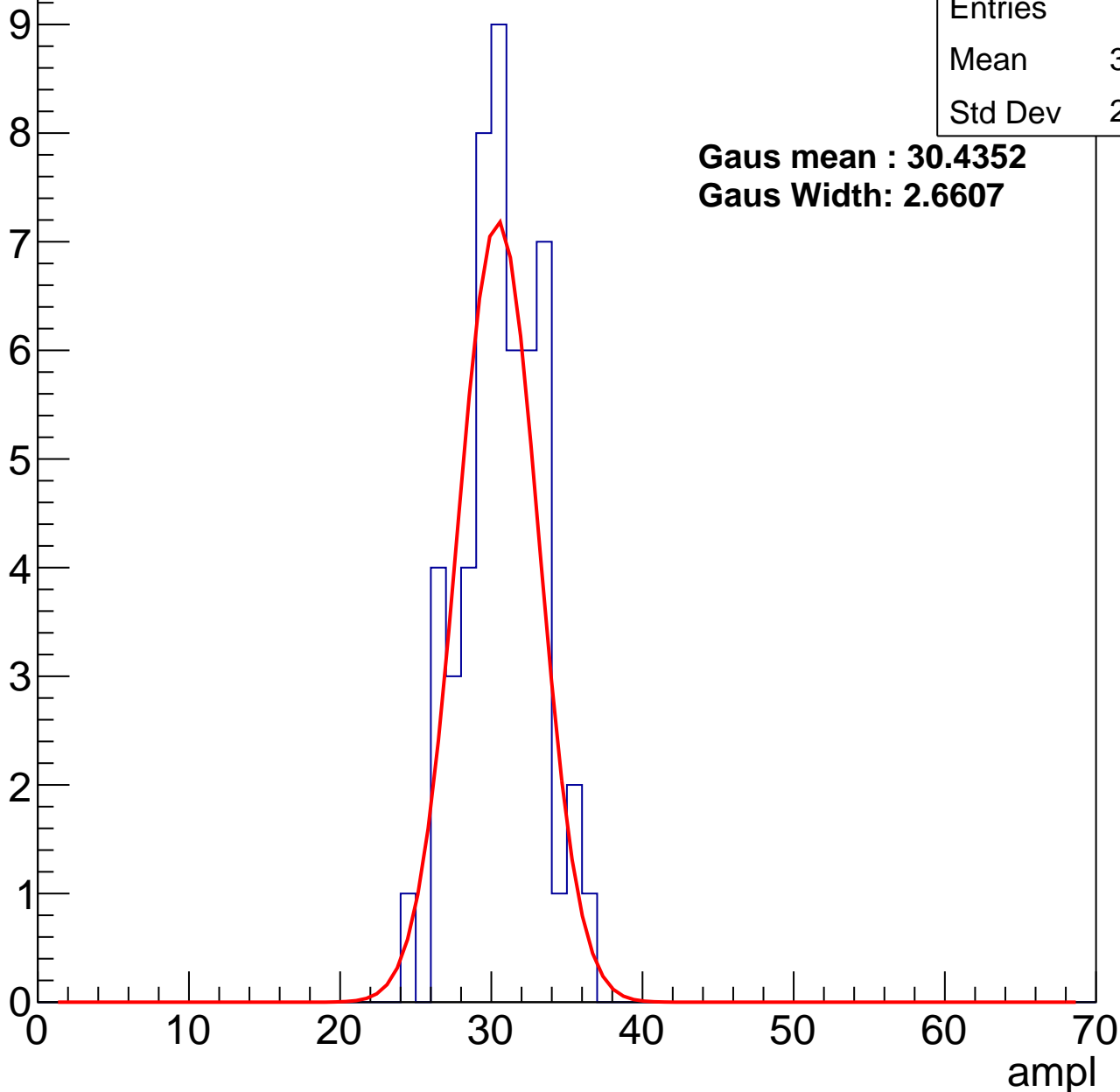
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	30.23
Std Dev	2.569

Gaus mean : 30.4352

Gaus Width: 2.6607



B1L102S, U22-ch17, adc1

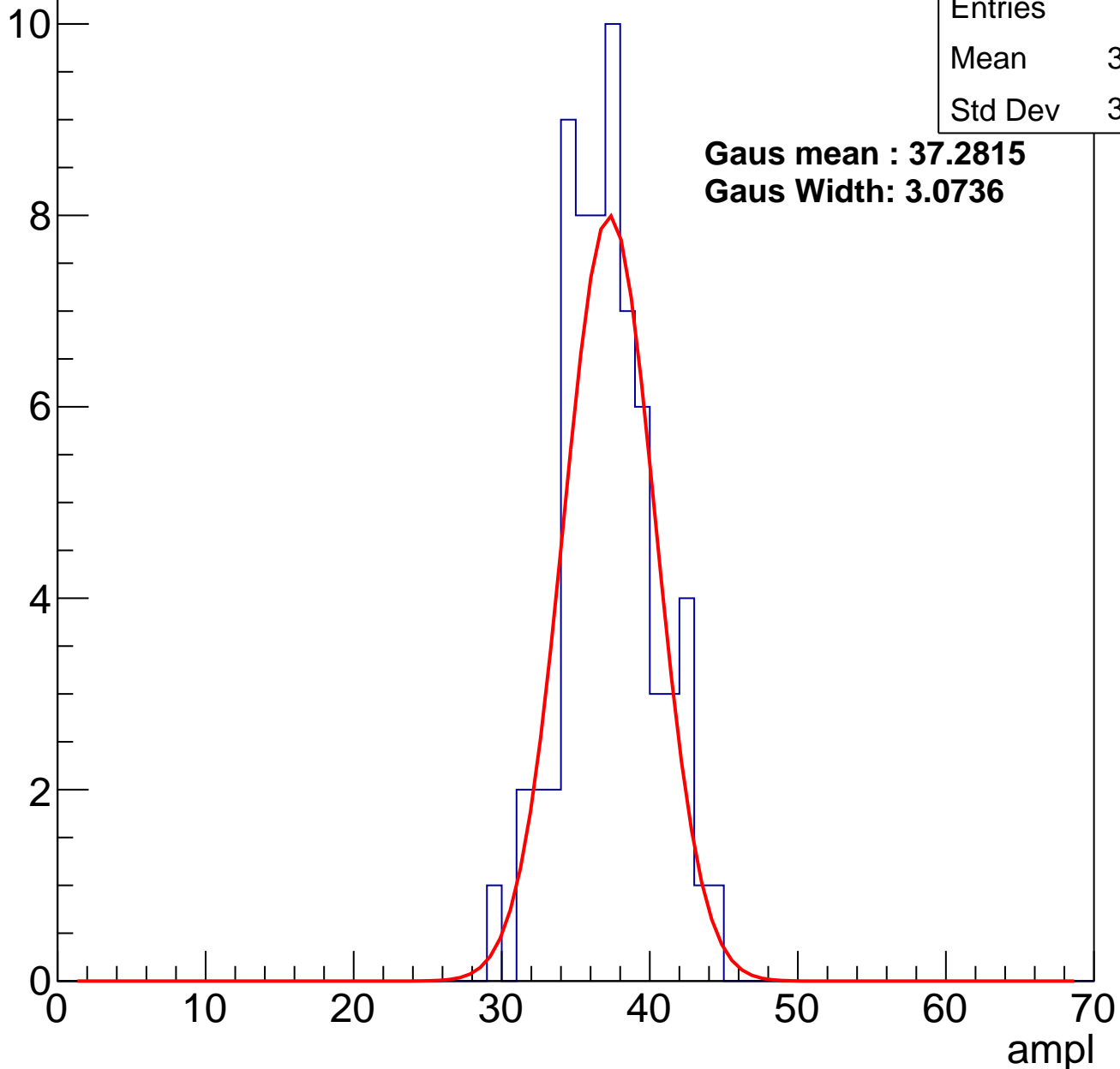
calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	36.76
Std Dev	3.057

Gaus mean : 37.2815

Gaus Width: 3.0736

Entry



B1L102S, U22-ch17, adc2

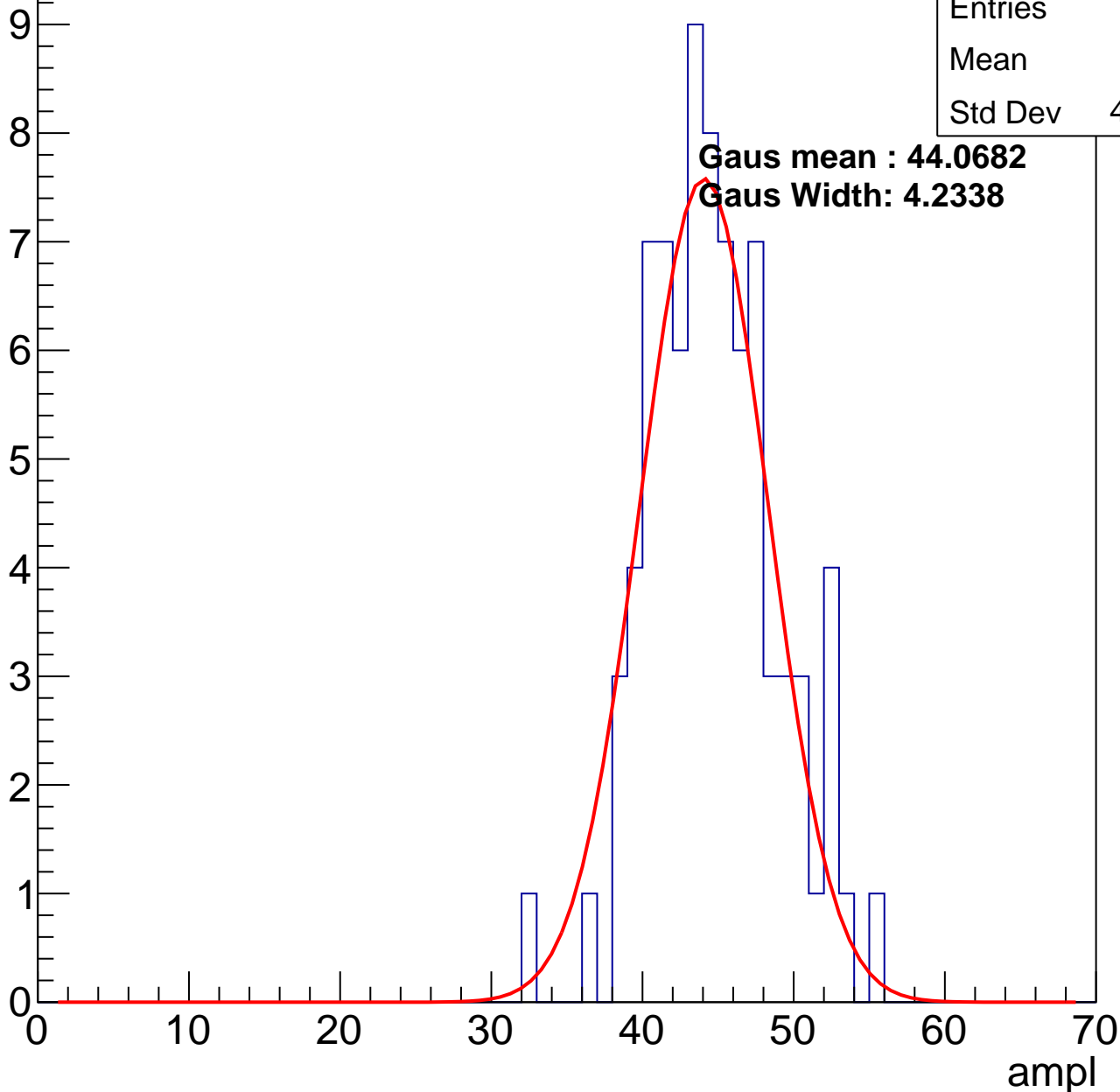
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	44.2
Std Dev	4.209

Gaus mean : 44.0682

Gaus Width: 4.2338

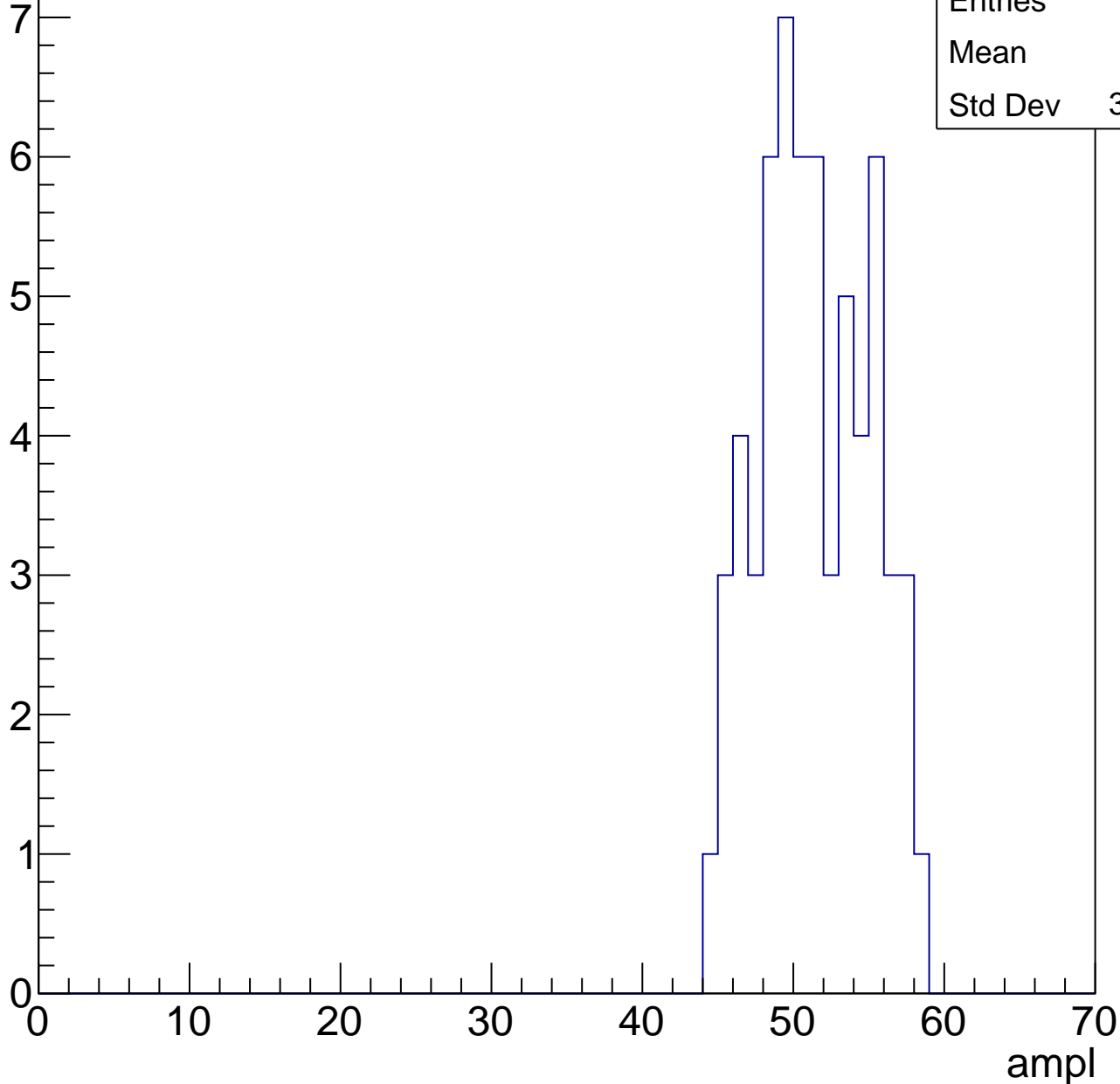


B1L102S, U22-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.9
Std Dev	3.575

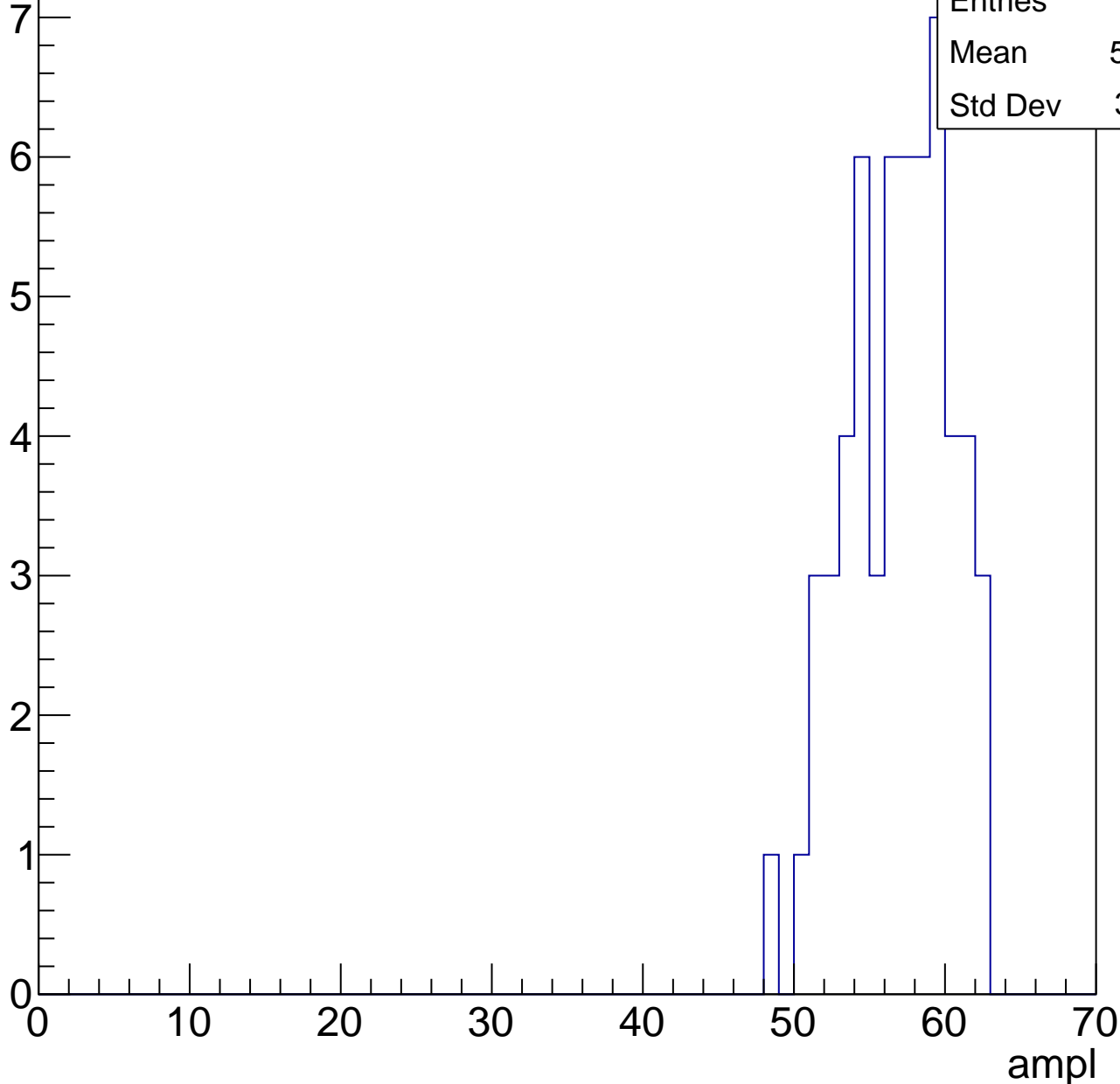


B1L102S, U22-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	56.44
Std Dev	3.351

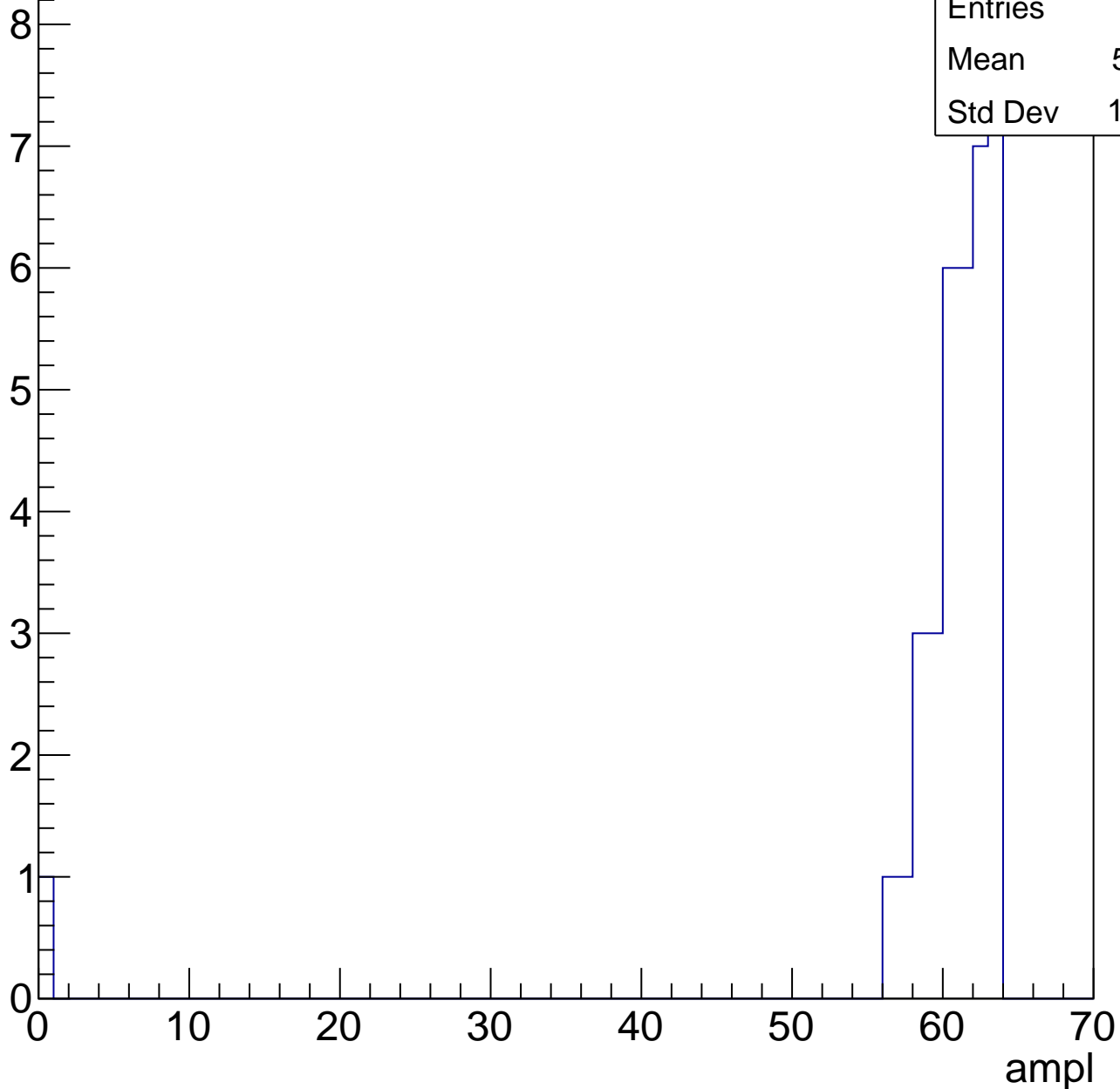


B1L102S, U22-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	59.11
Std Dev	10.16



B1L102S, U22-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	63
Std Dev	0

B1L102S, U22-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch18, adc0

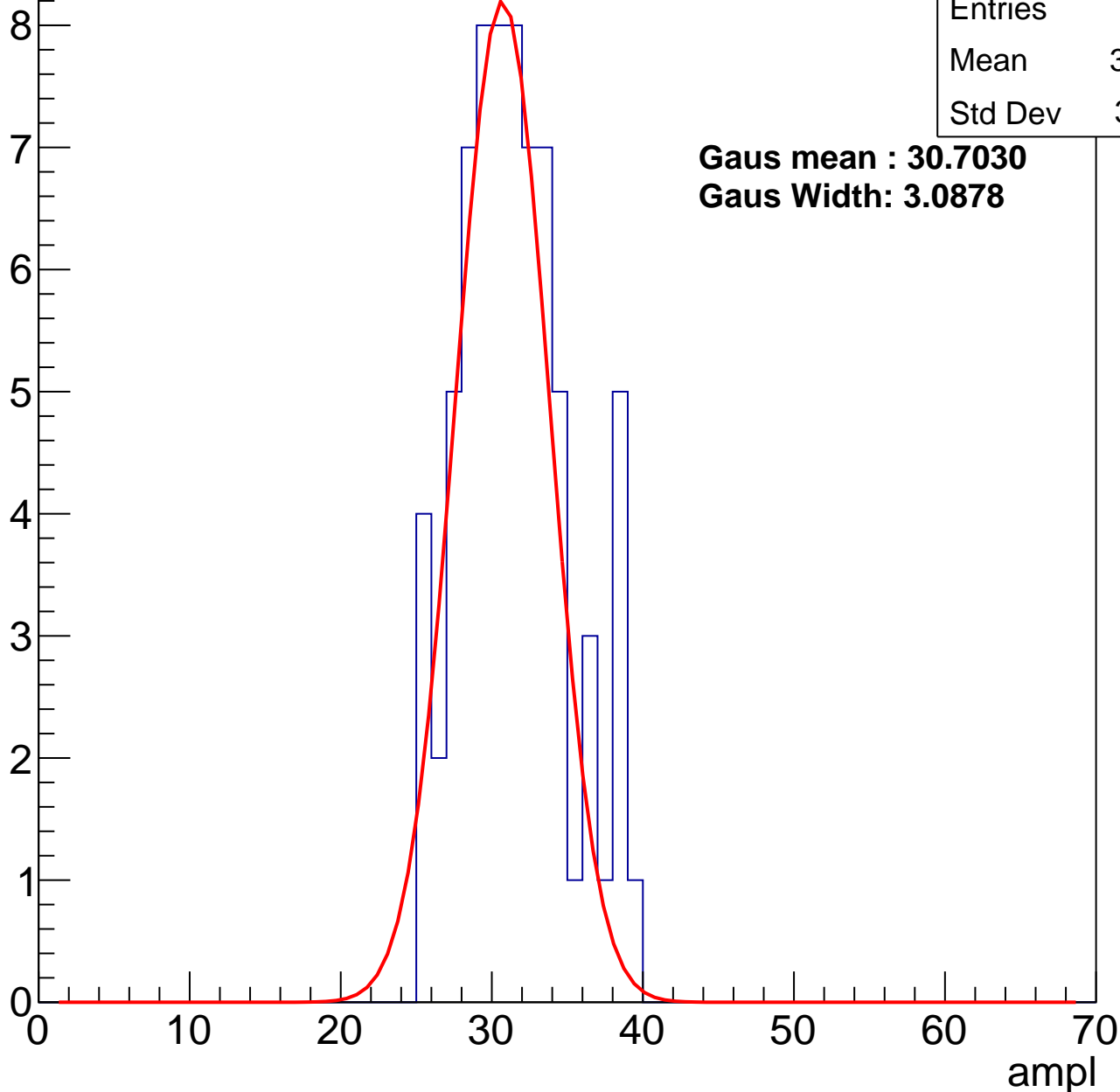
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	31.07
Std Dev	3.521

Gaus mean : 30.7030

Gaus Width: 3.0878



B1L102S, U22-ch18, adc1

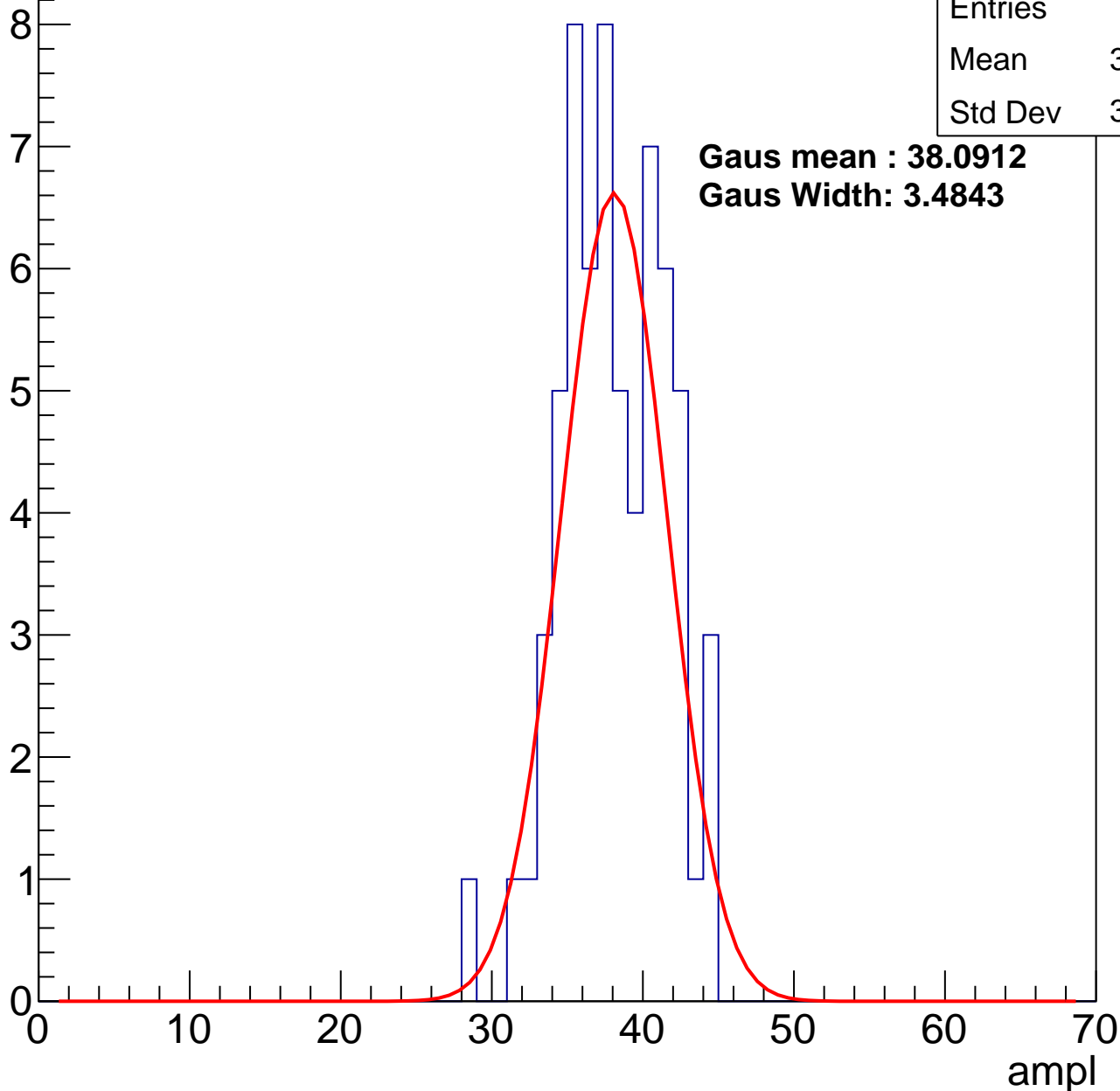
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.64
Std Dev	3.388

Gaus mean : 38.0912

Gaus Width: 3.4843



B1L102S, U22-ch18, adc2

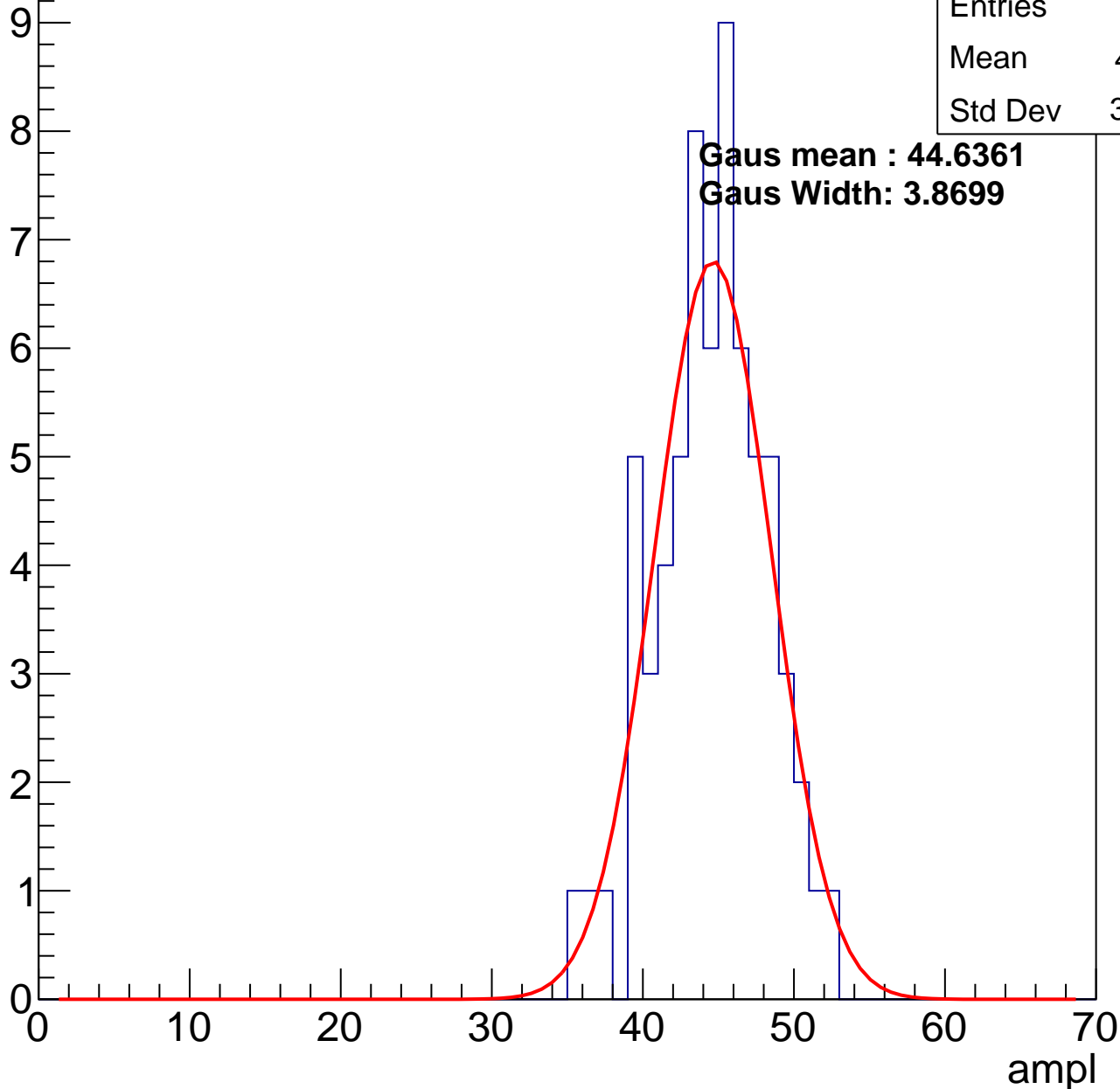
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	44.11
Std Dev	3.585

Gaus mean : 44.6361

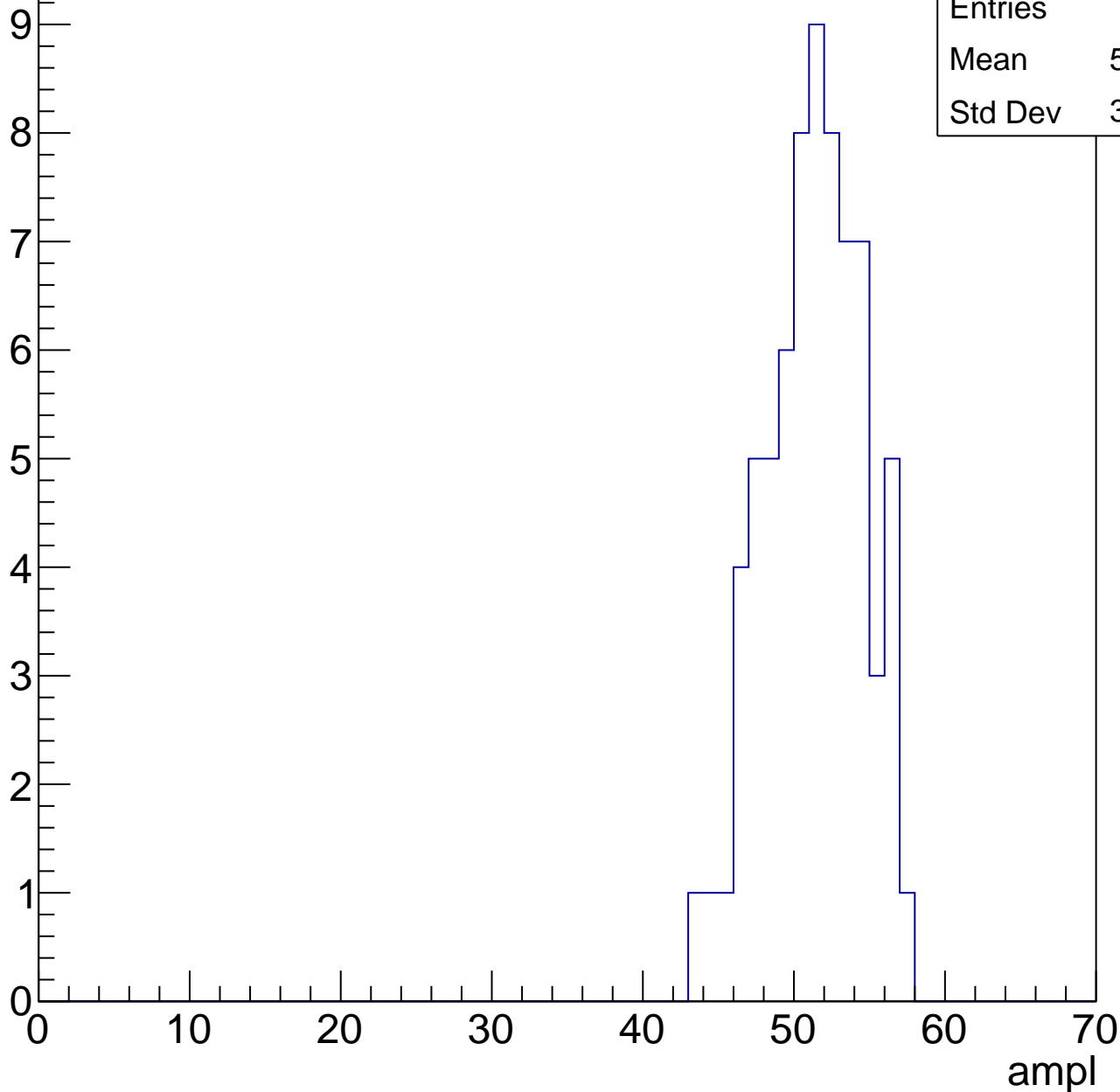
Gaus Width: 3.8699



B1L102S, U22-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

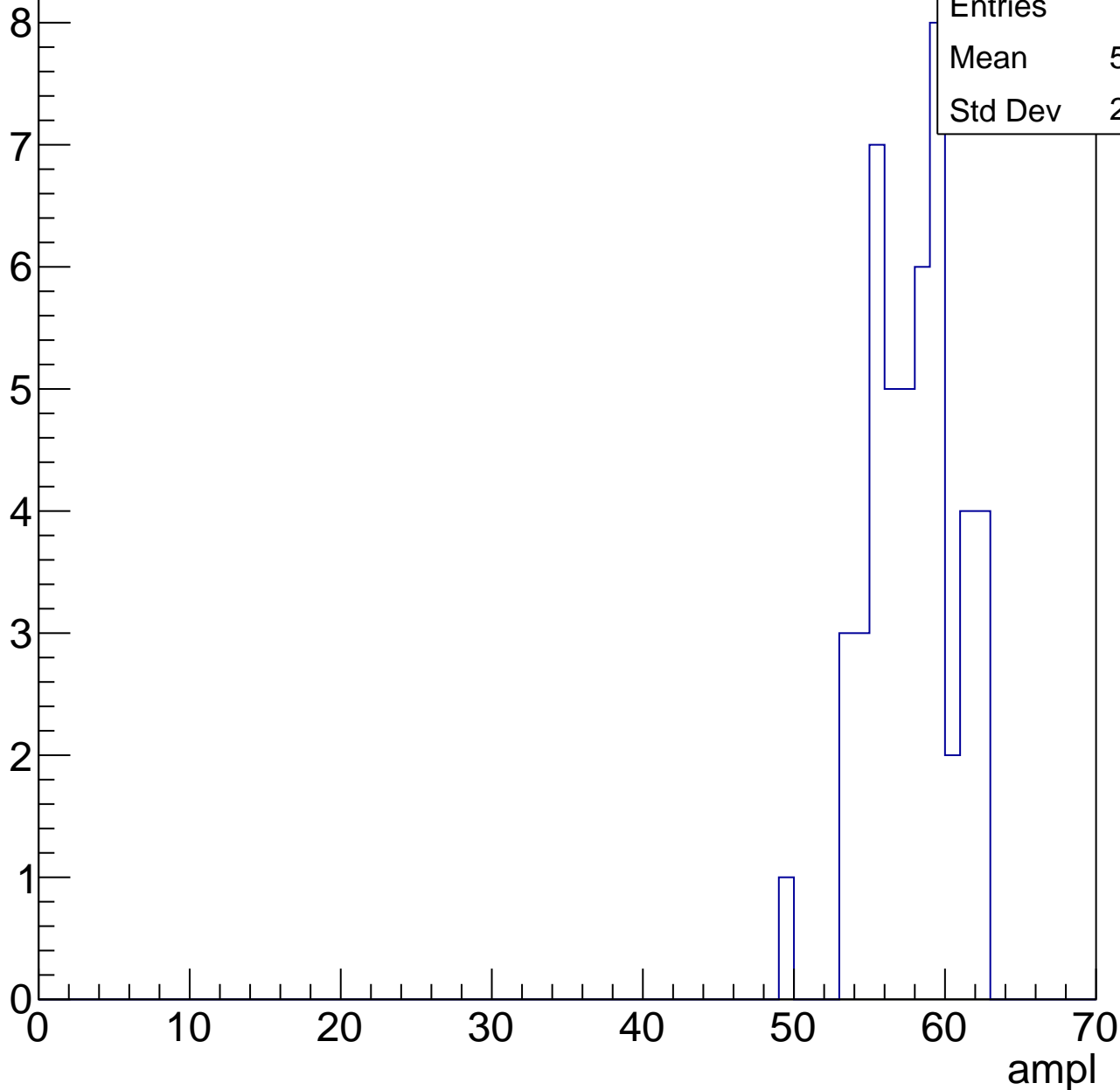


B1L102S, U22-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	57.33
Std Dev	2.838

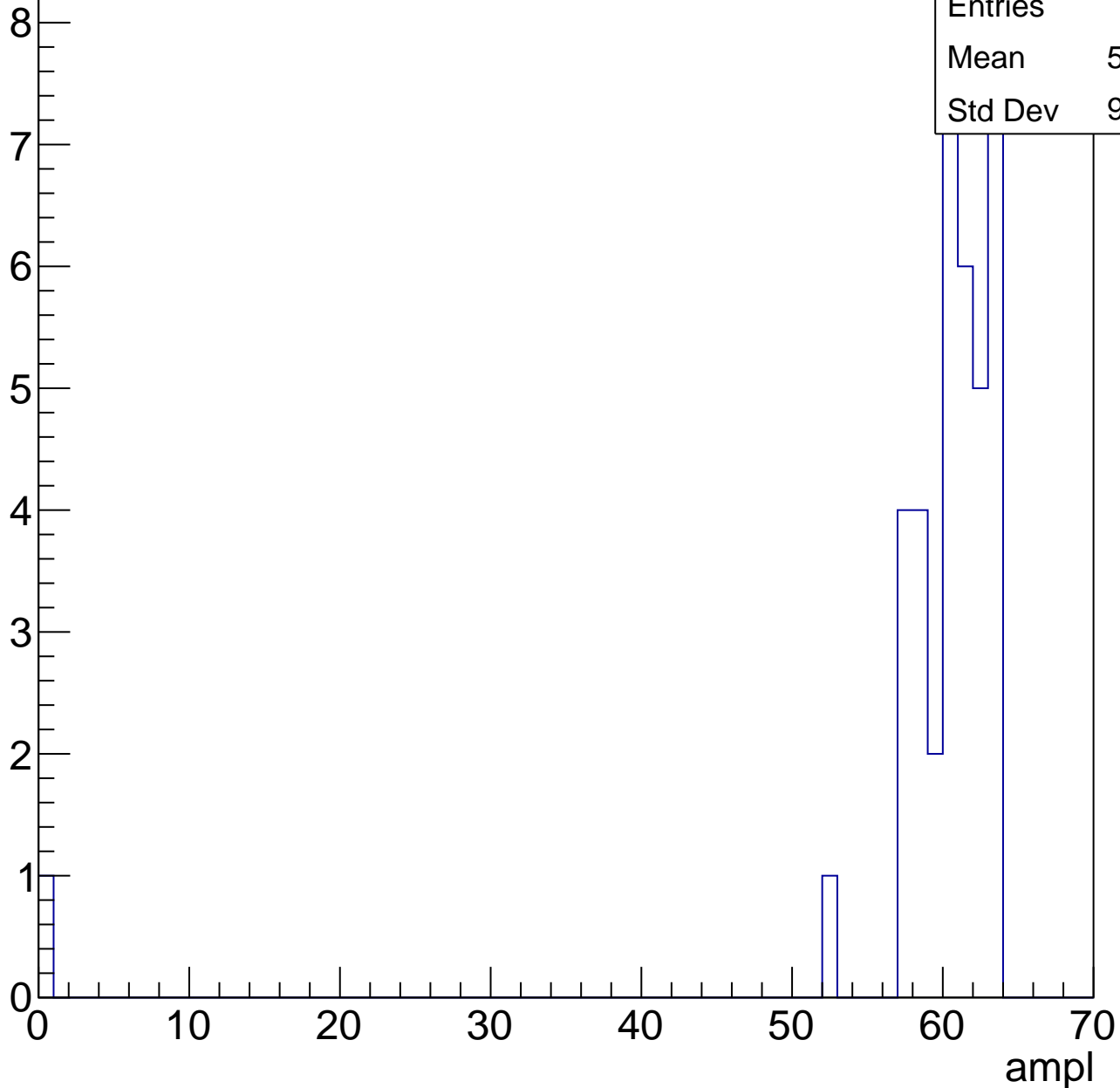


B1L102S, U22-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.72
Std Dev	9.808



B1L102S, U22-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	12
Std Dev	12

B1L102S, U22-ch19, adc0

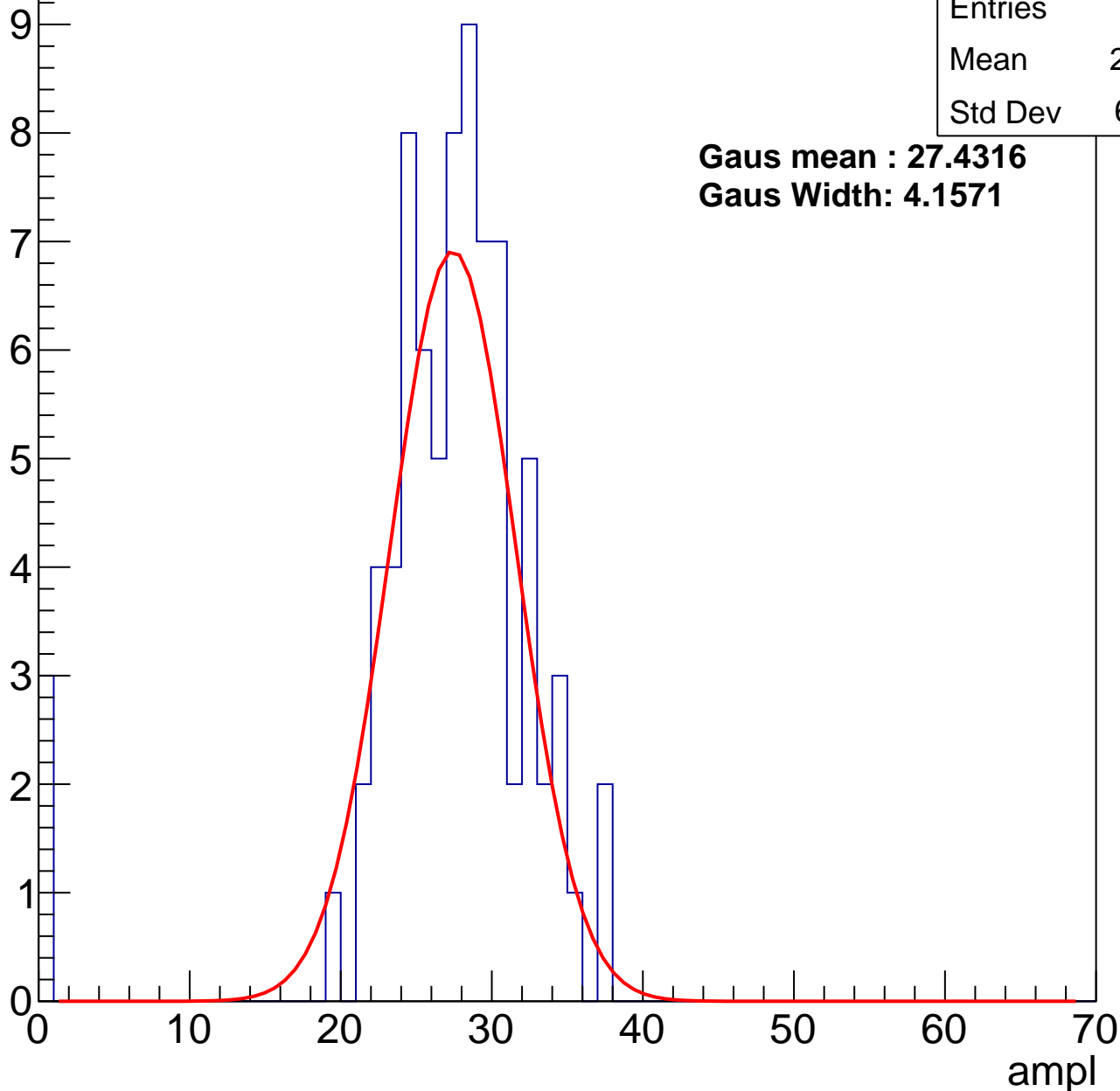
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	26.49
Std Dev	6.471

Gaus mean : 27.4316

Gaus Width: 4.1571



B1L102S, U22-ch19, adc1

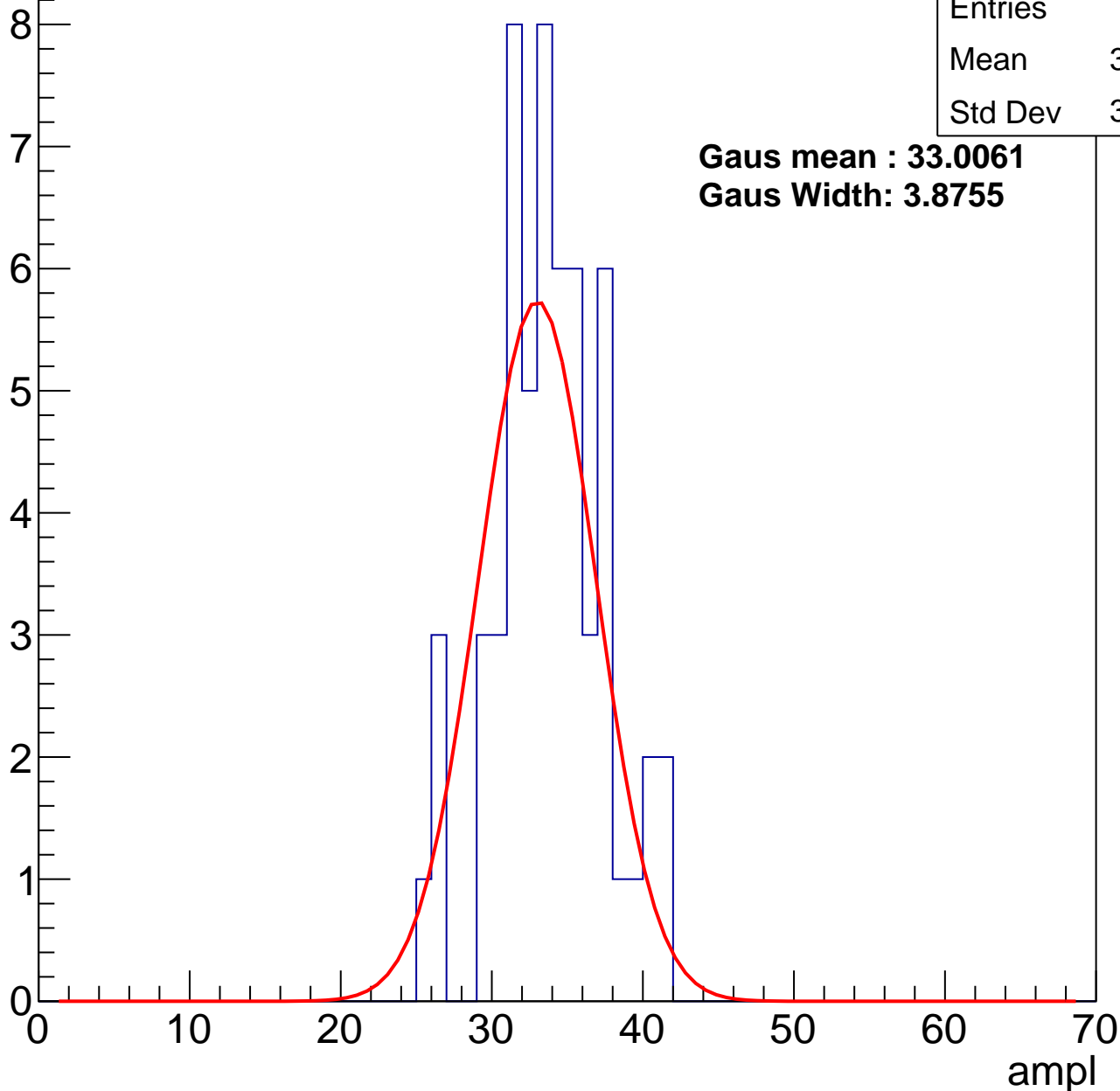
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	33.36
Std Dev	3.609

Gaus mean : 33.0061

Gaus Width: 3.8755



B1L102S, U22-ch19, adc2

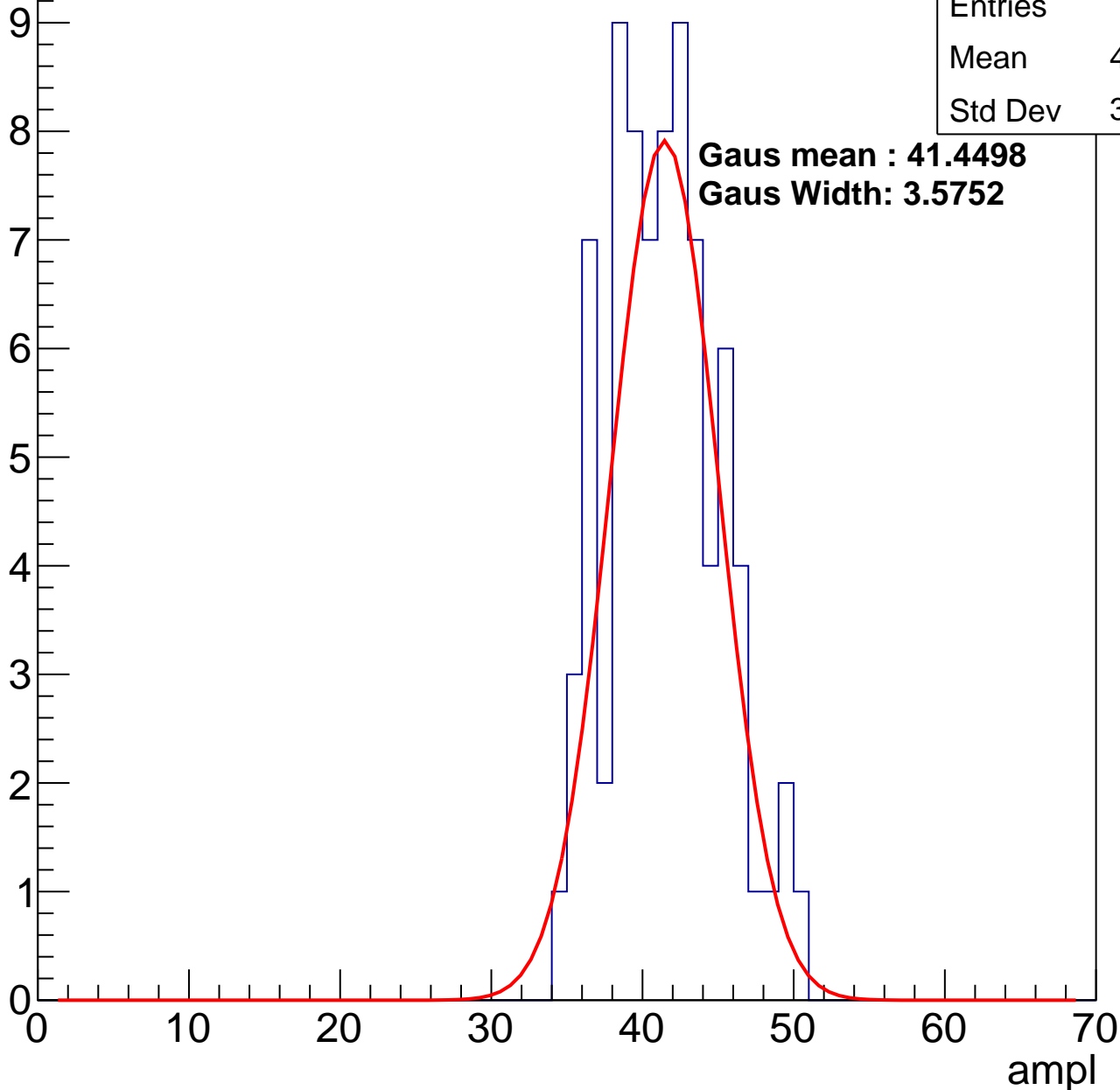
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	40.99
Std Dev	3.618

Gaus mean : 41.4498

Gaus Width: 3.5752

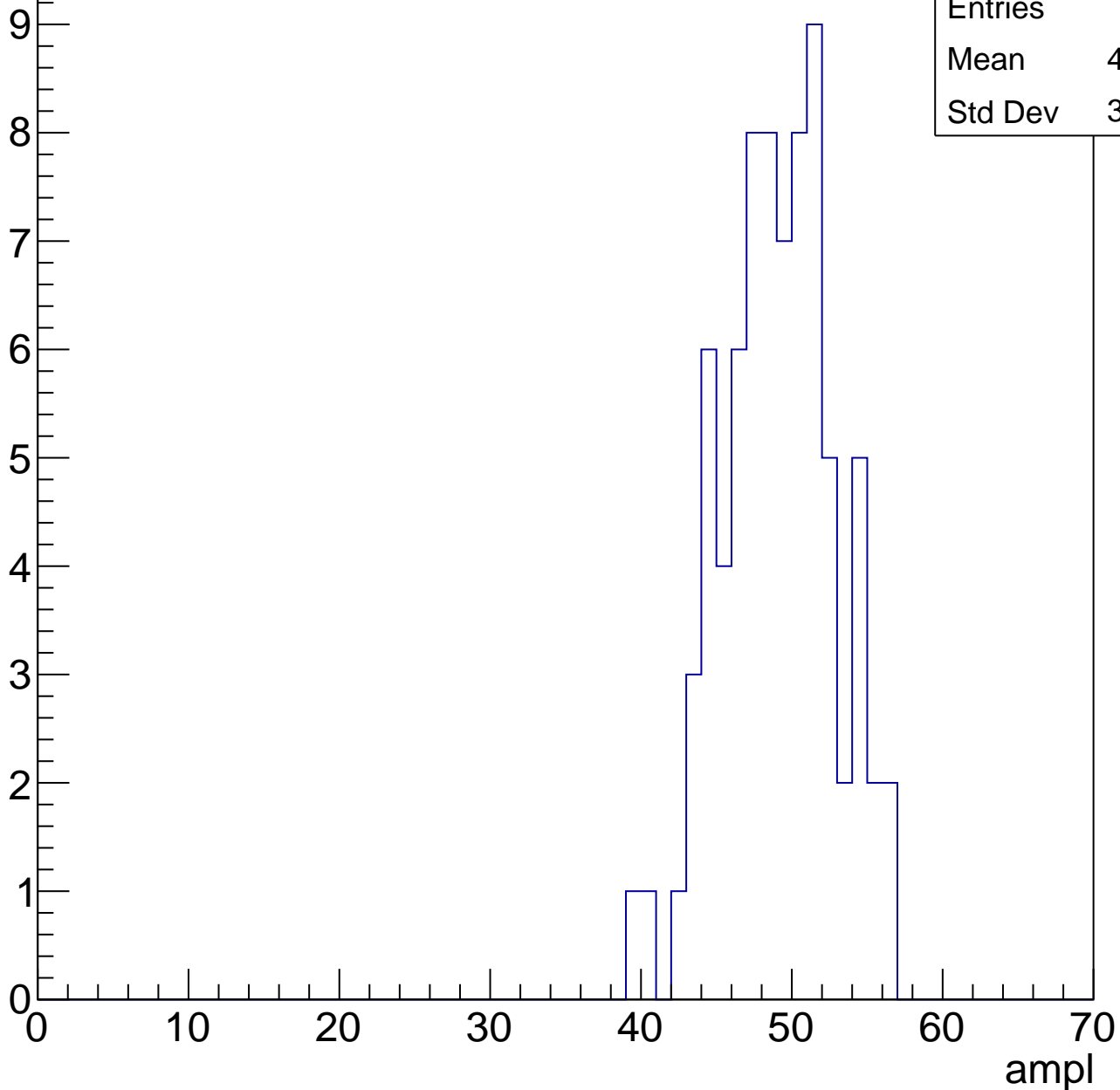


B1L102S, U22-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	48.59
Std Dev	3.667

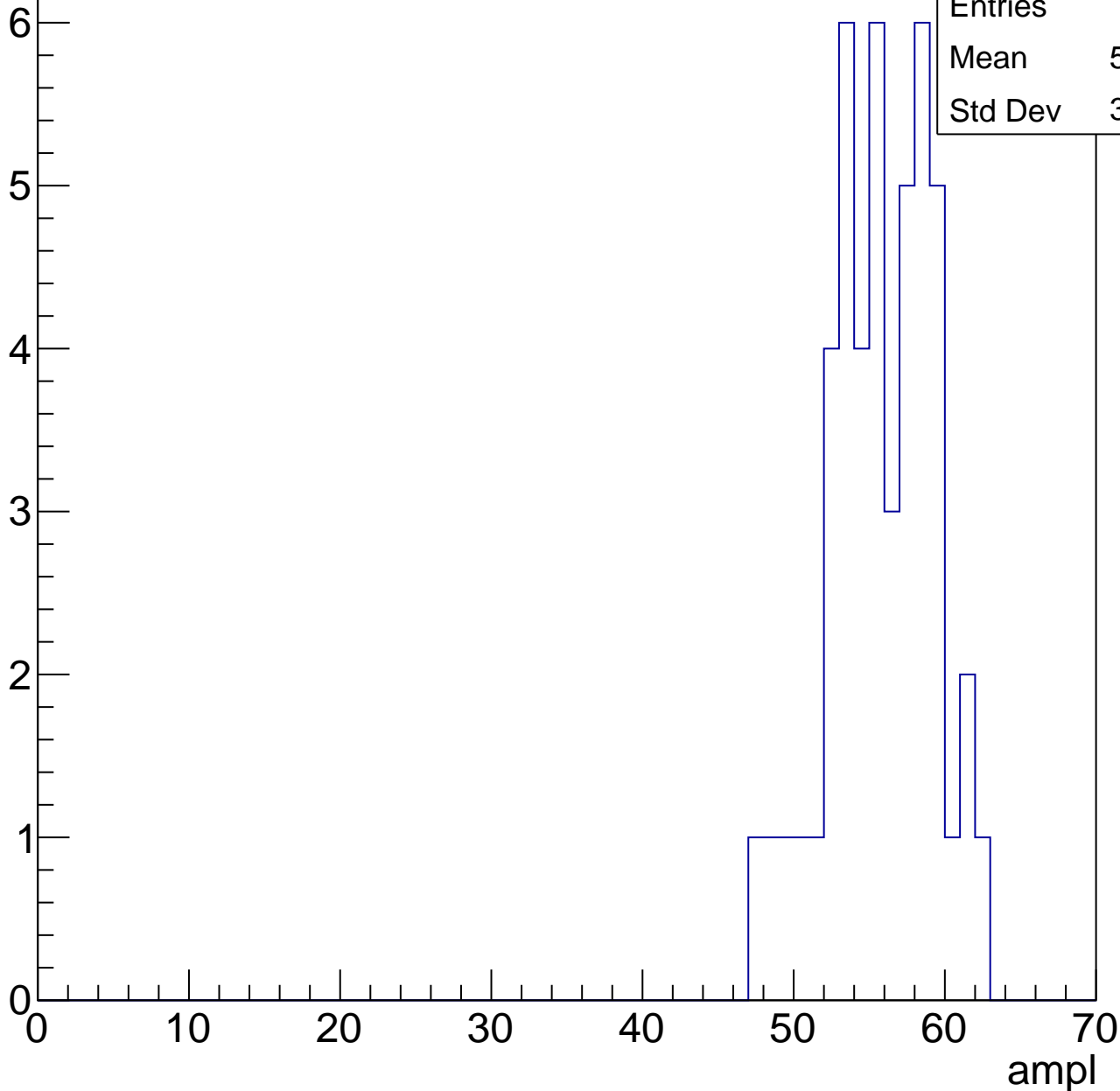


B1L102S, U22-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.35
Std Dev	3.388

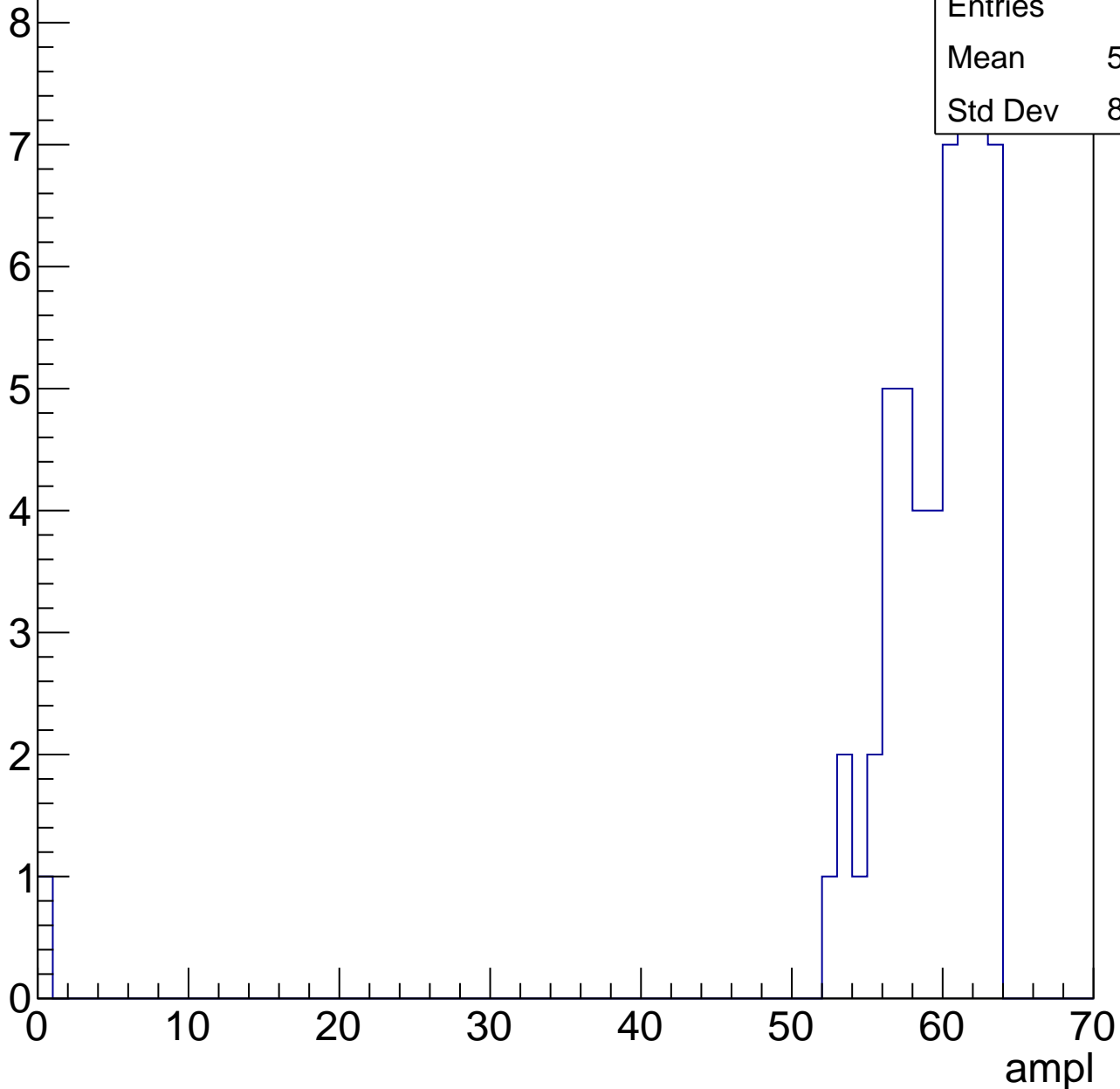


B1L102S, U22-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

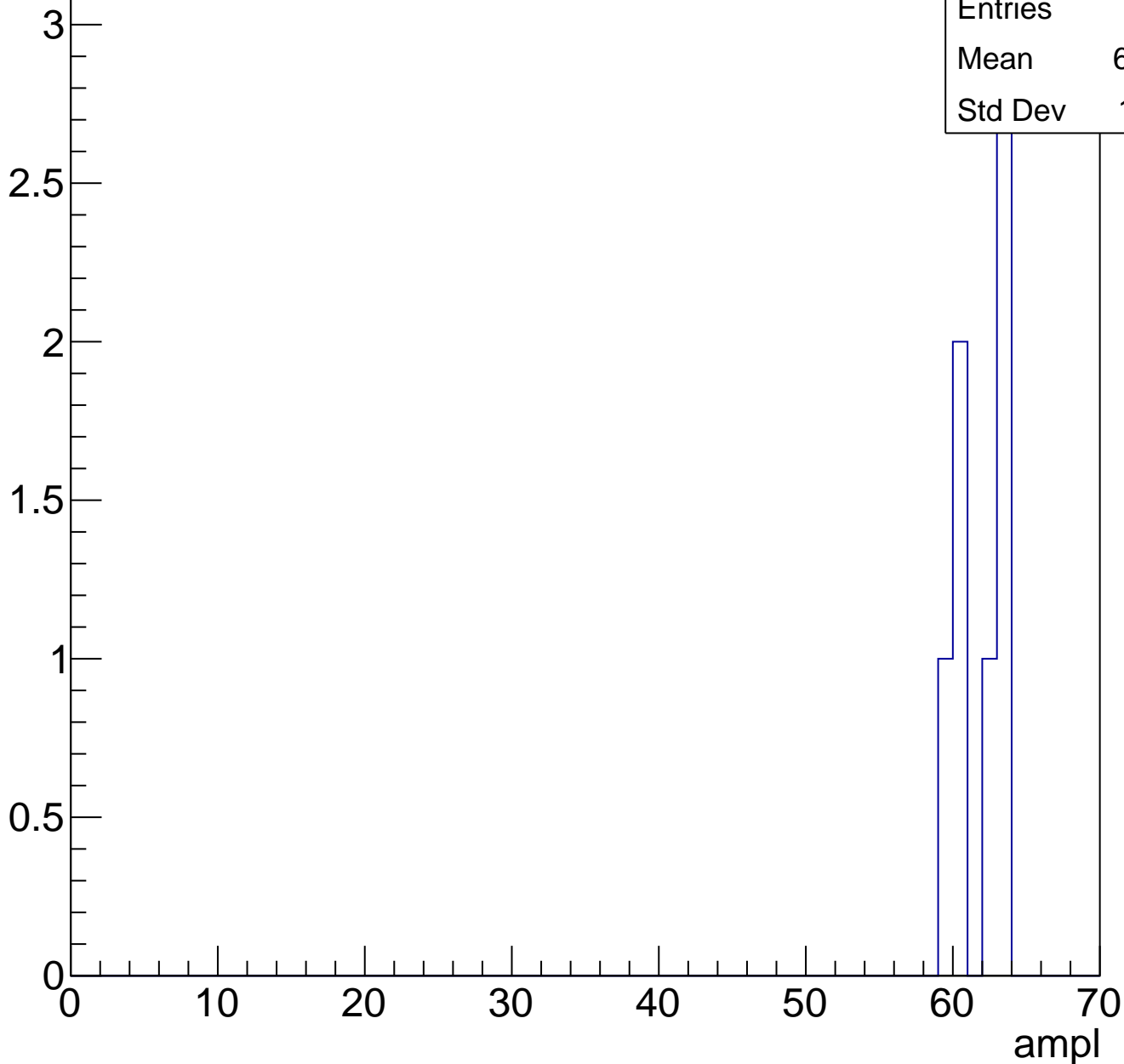
Entries	55
Mean	58.18
Std Dev	8.436



B1L102S, U22-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch20, adc0

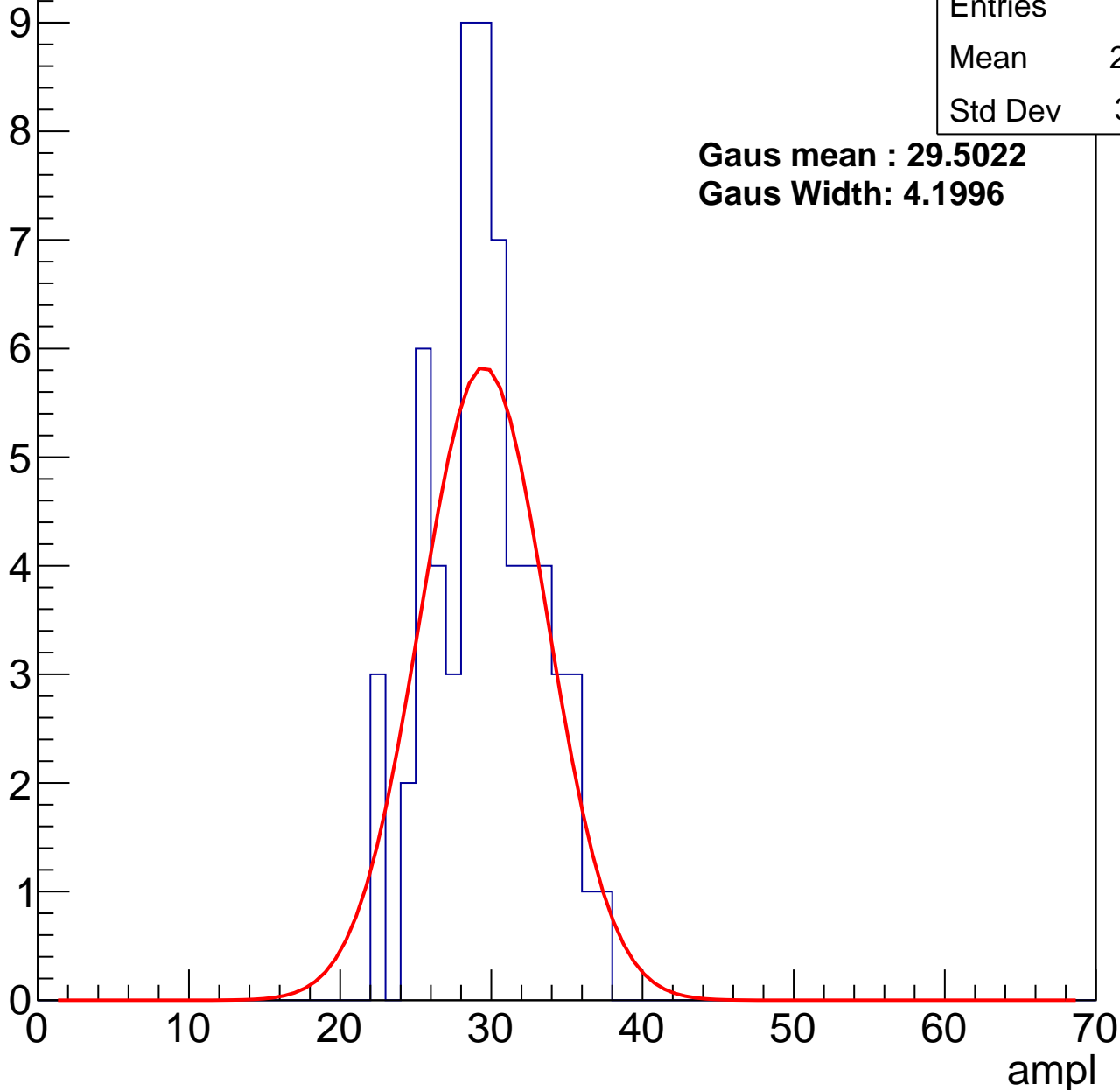
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.14
Std Dev	3.491

Gaus mean : 29.5022

Gaus Width: 4.1996



B1L102S, U22-ch20, adc1

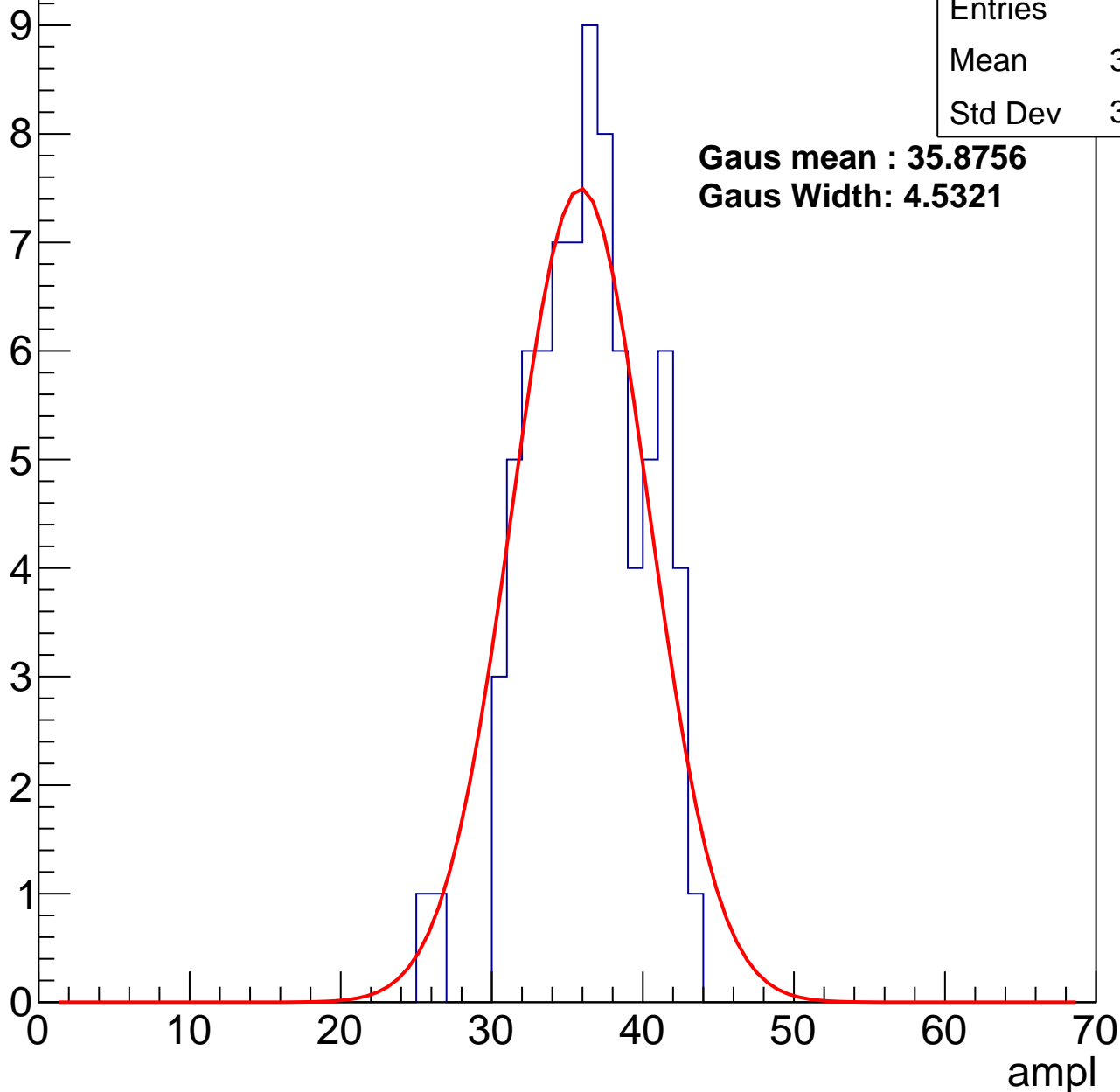
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.82
Std Dev	3.778

Gaus mean : 35.8756

Gaus Width: 4.5321



B1L102S, U22-ch20, adc2

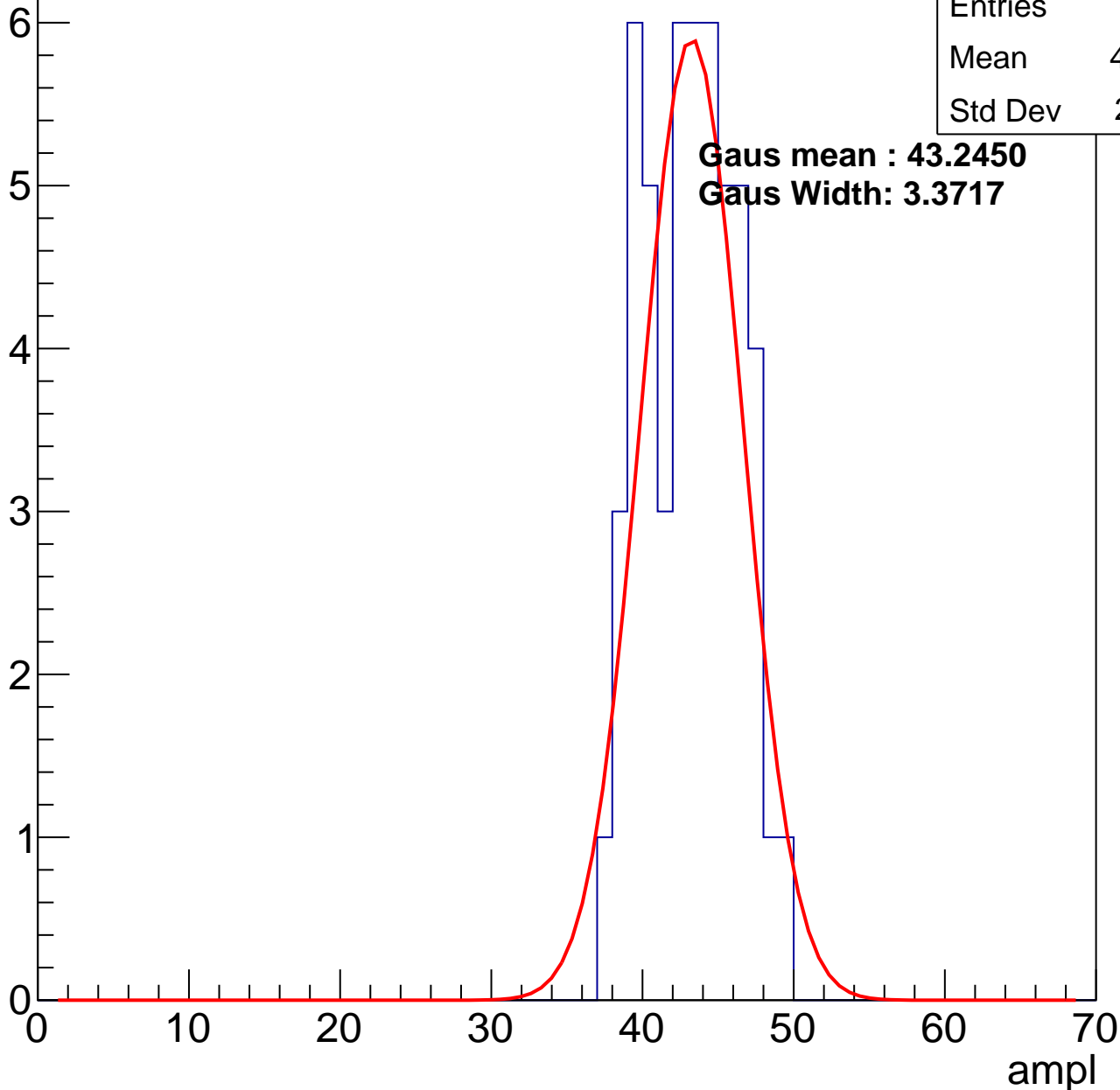
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.73
Std Dev	2.981

Gaus mean : 43.2450

Gaus Width: 3.3717

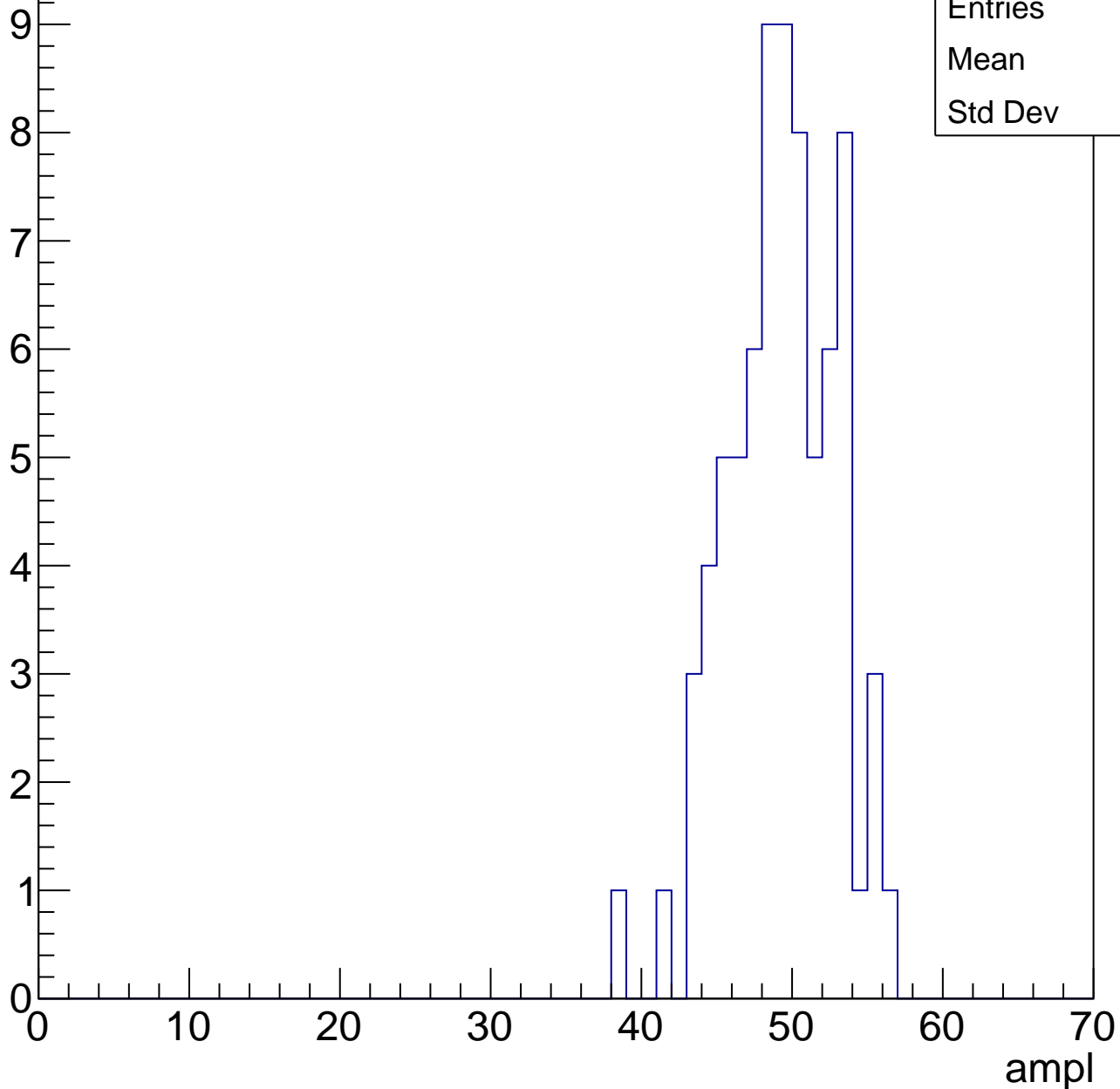


B1L102S, U22-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	48.8
Std Dev	3.54

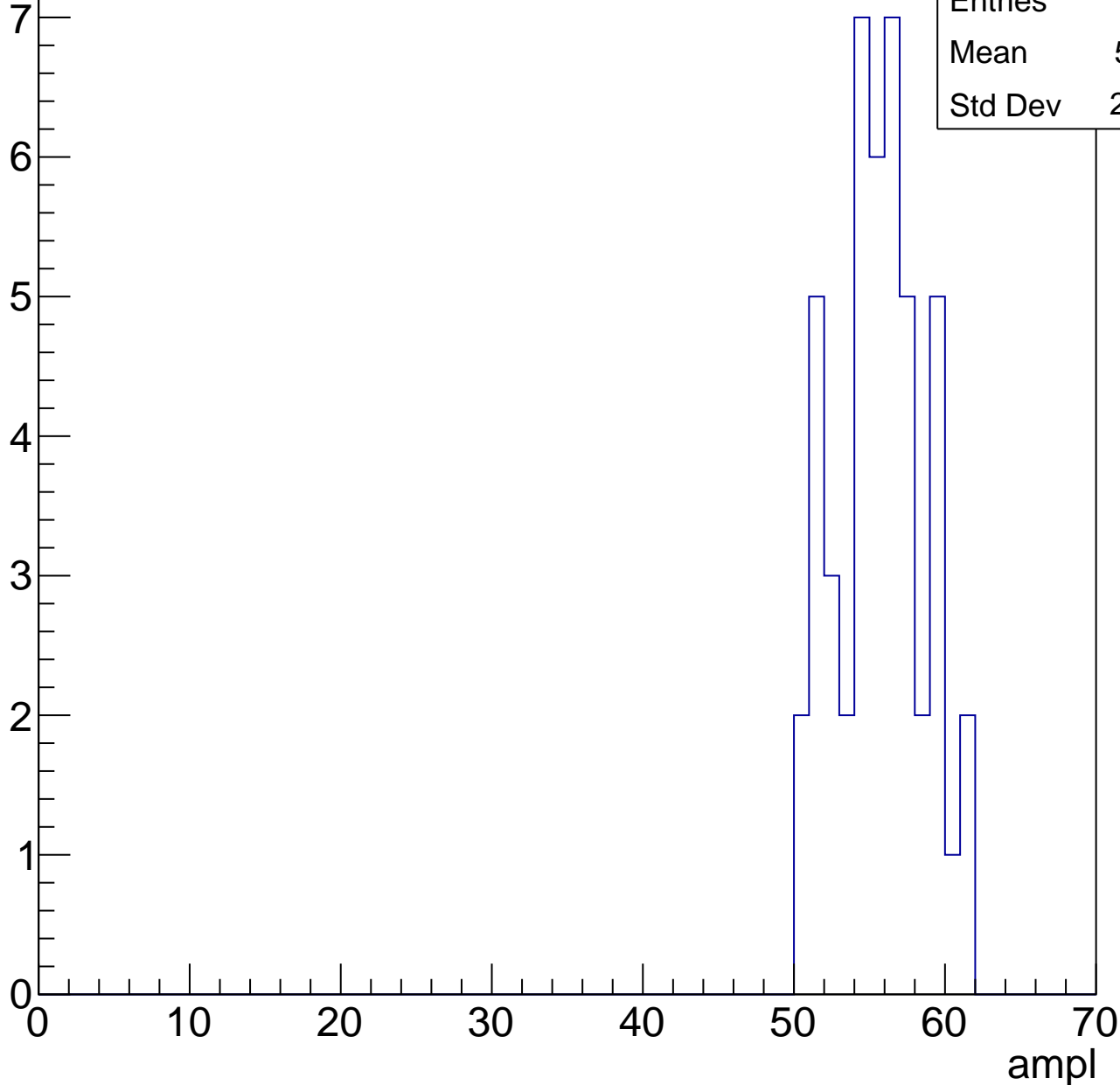


B1L102S, U22-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

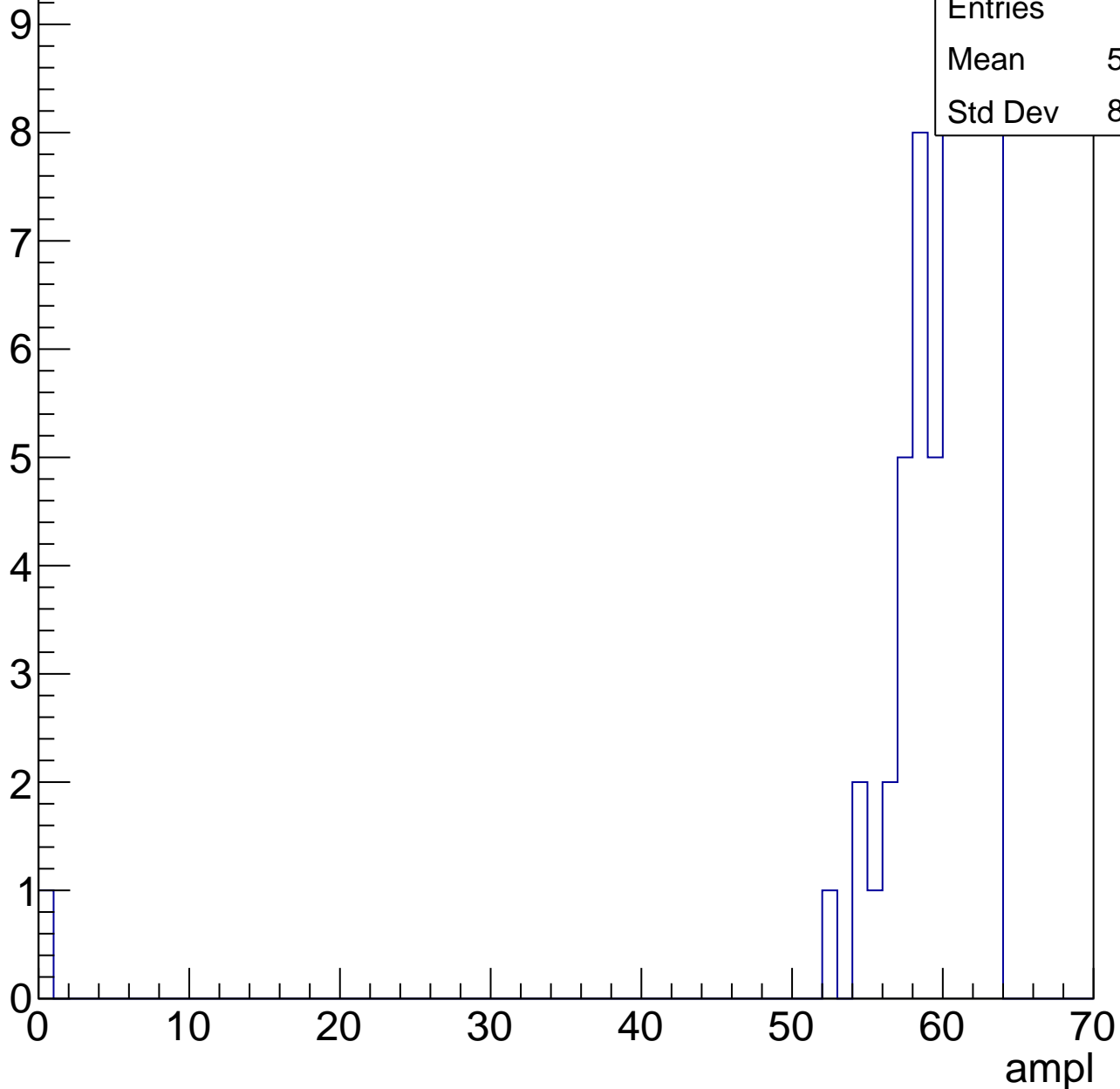
Entries	47
Mean	55.21
Std Dev	2.888



B1L102S, U22-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U22-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch21, adc0

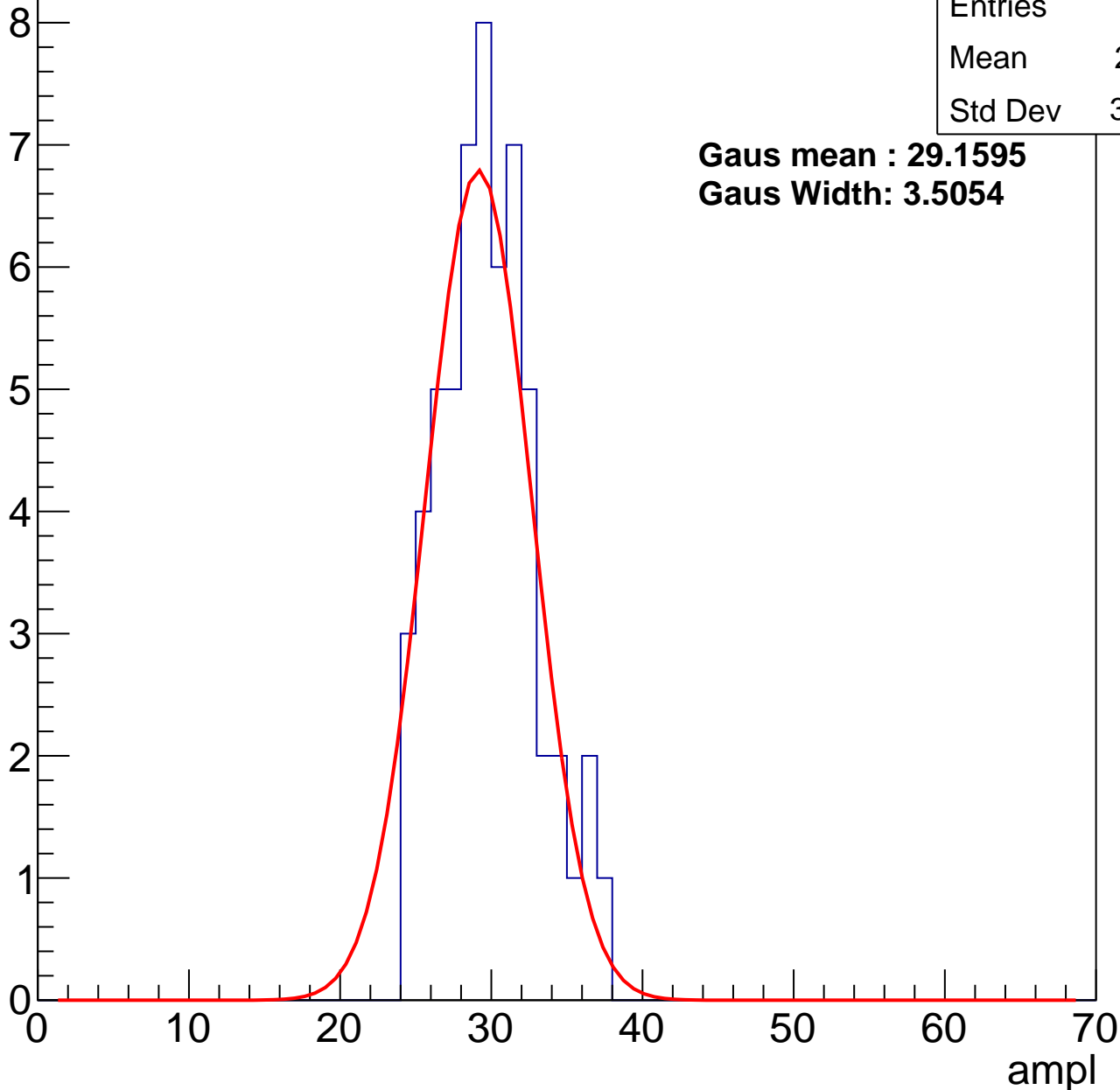
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	29.31
Std Dev	3.119

Gaus mean : 29.1595

Gaus Width: 3.5054



B1L102S, U22-ch21, adc1

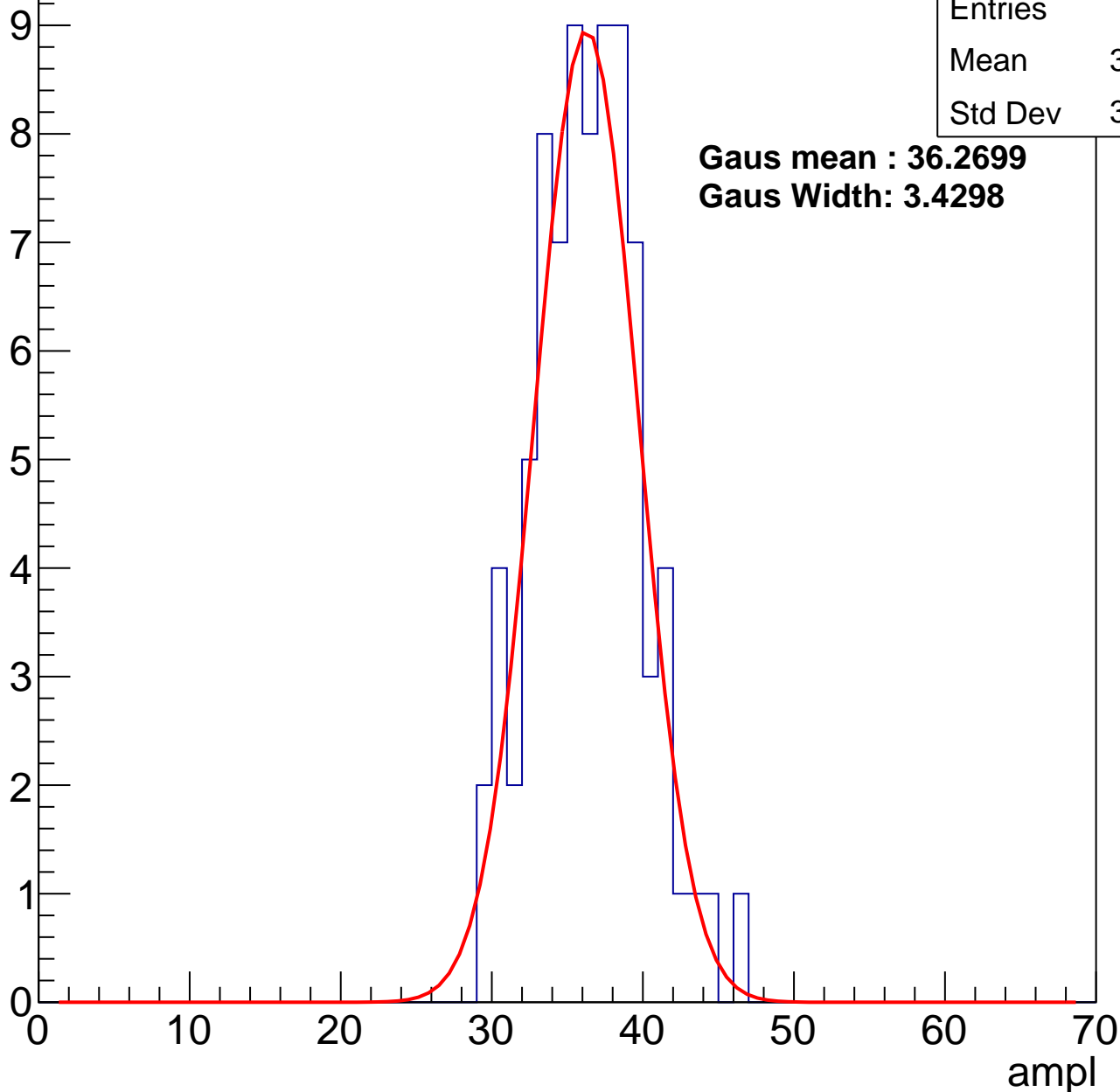
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.95
Std Dev	3.489

Gaus mean : 36.2699

Gaus Width: 3.4298



B1L102S, U22-ch21, adc2

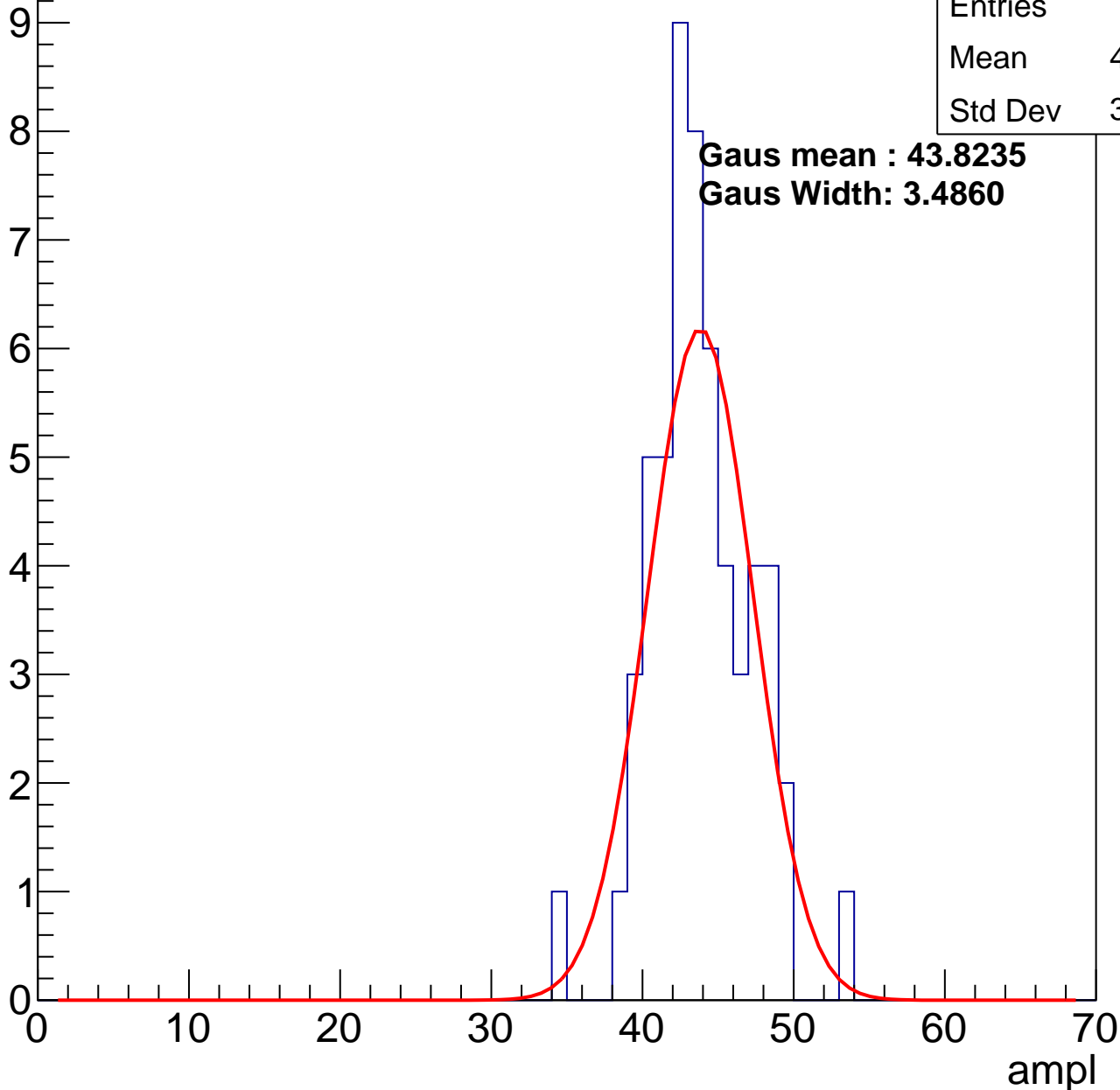
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.38
Std Dev	3.287

Gaus mean : 43.8235

Gaus Width: 3.4860

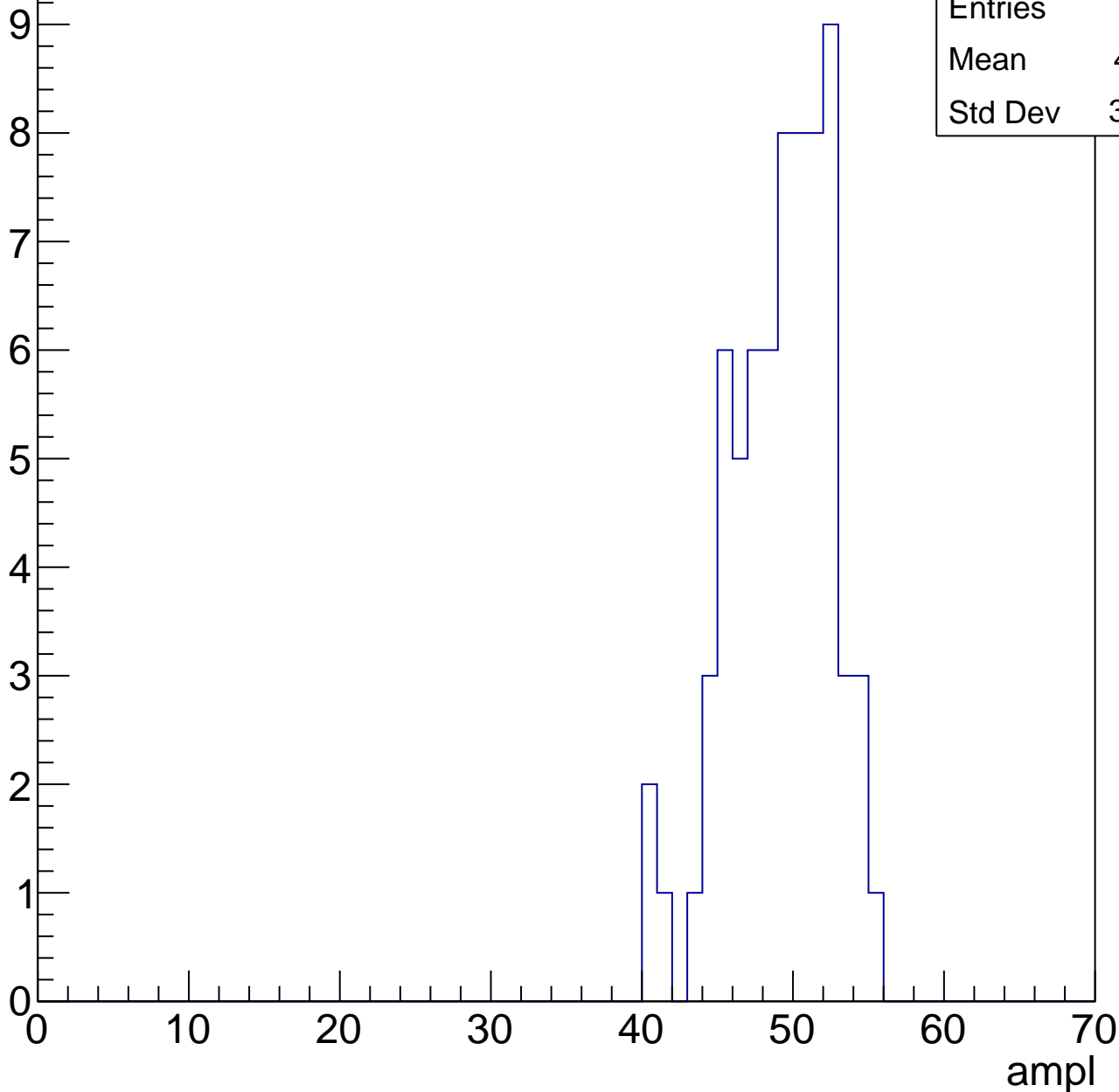


B1L102S, U22-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.71
Std Dev	3.343

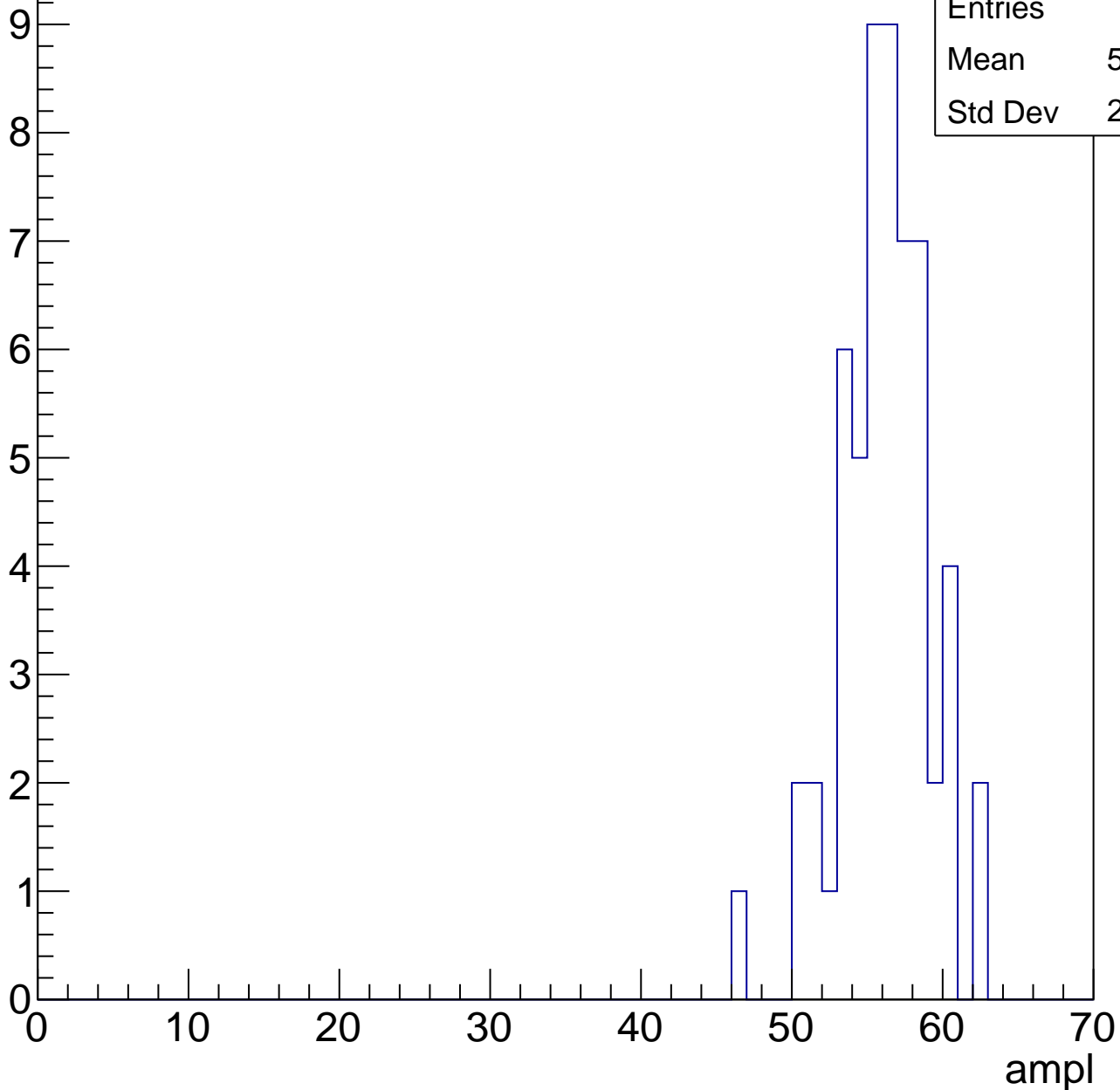


B1L102S, U22-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.68
Std Dev	2.974

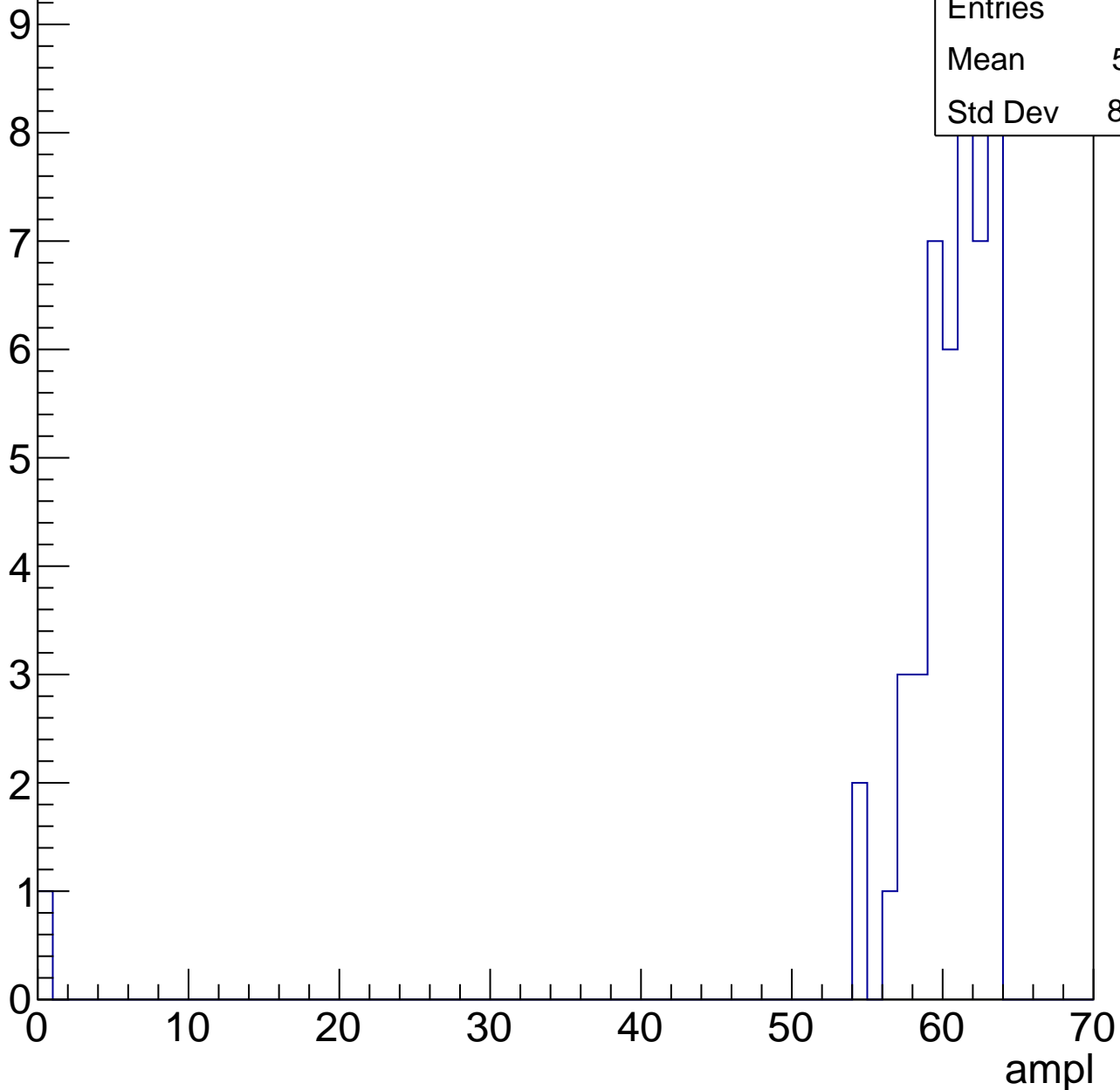


B1L102S, U22-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

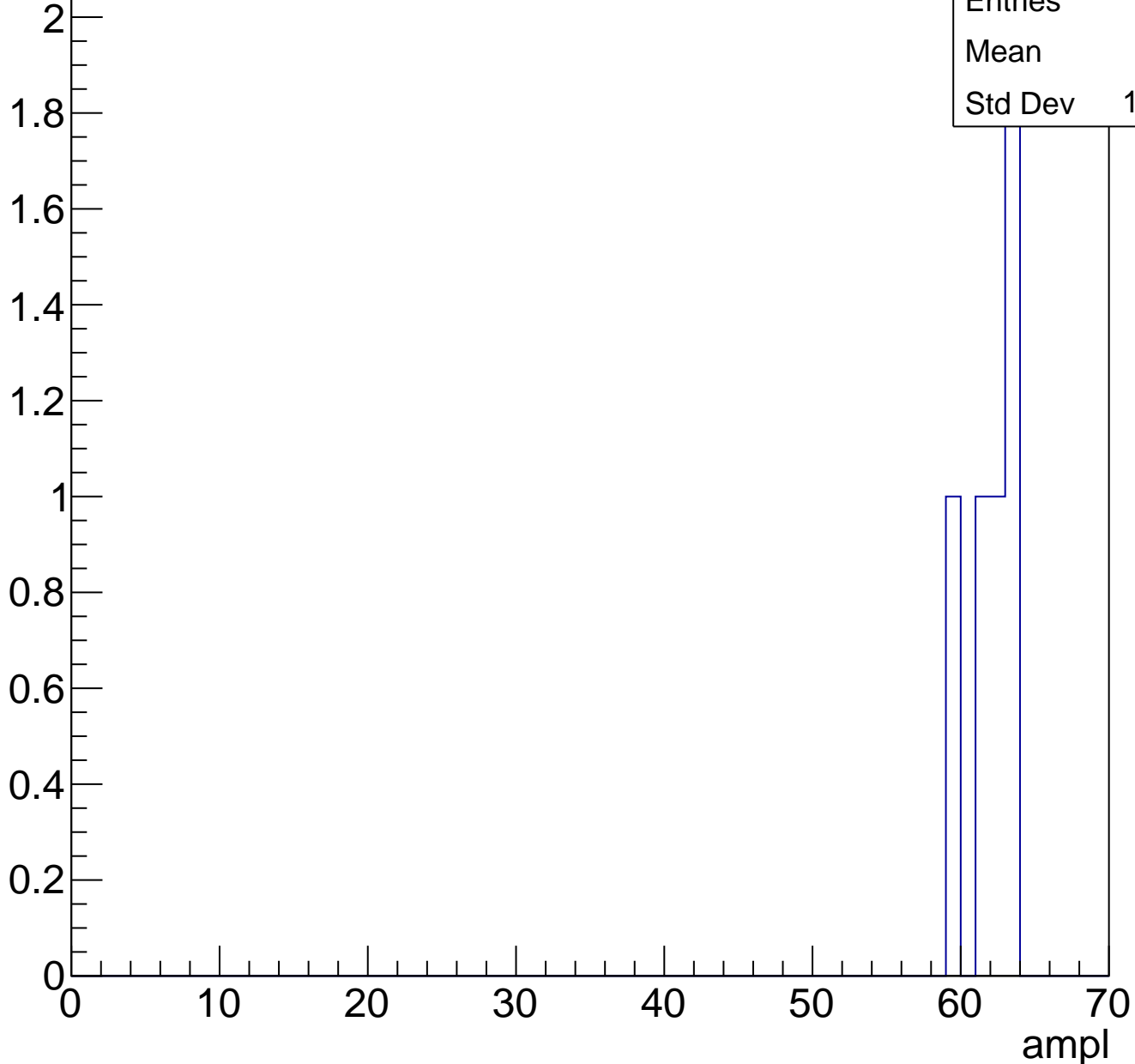
Entries	47
Mean	58.91
Std Dev	8.977



B1L102S, U22-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

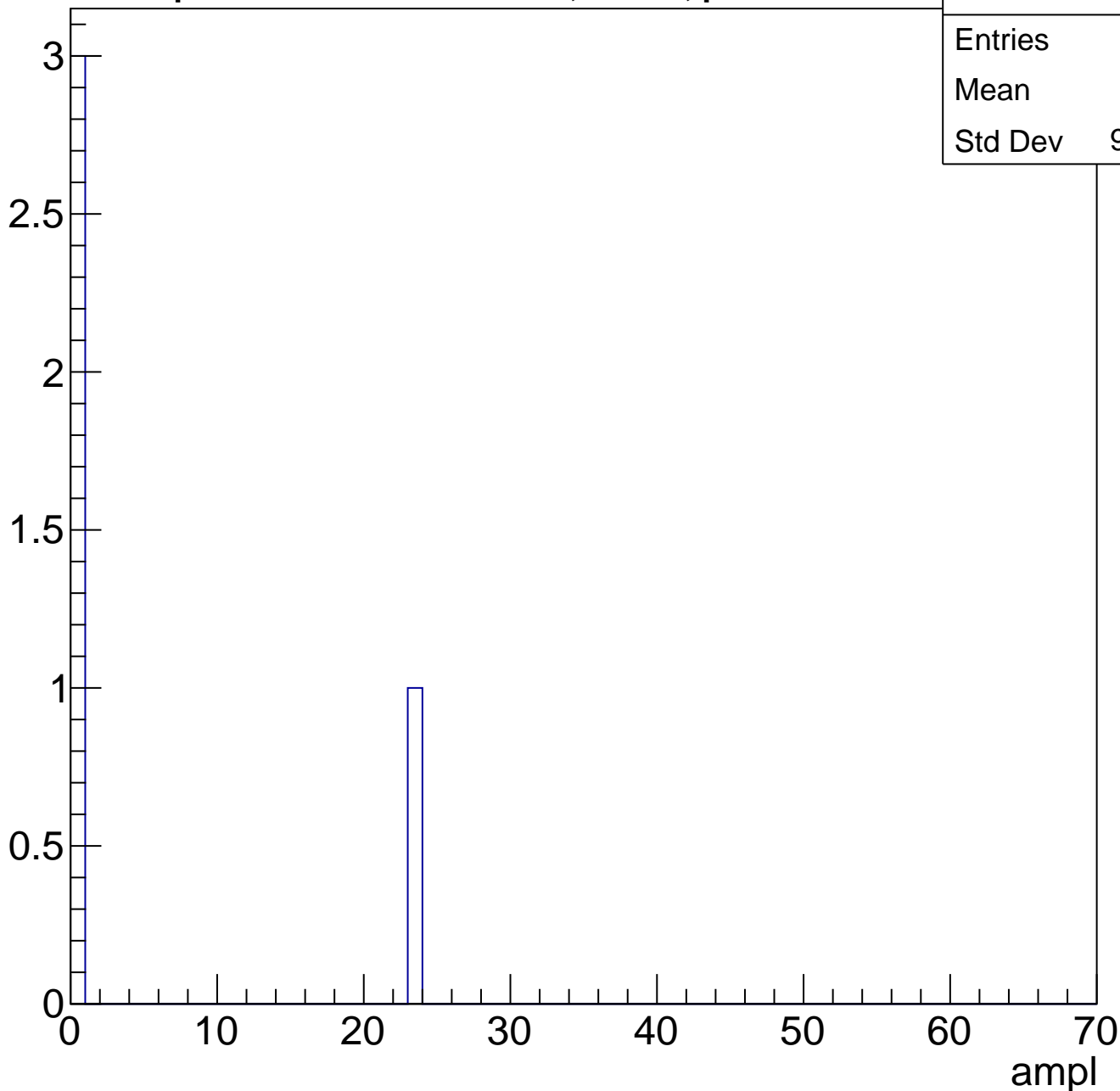


Entries	5
Mean	61.6
Std Dev	1.497

B1L102S, U22-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	5.75
Std Dev	9.959

B1L102S, U22-ch22, adc0

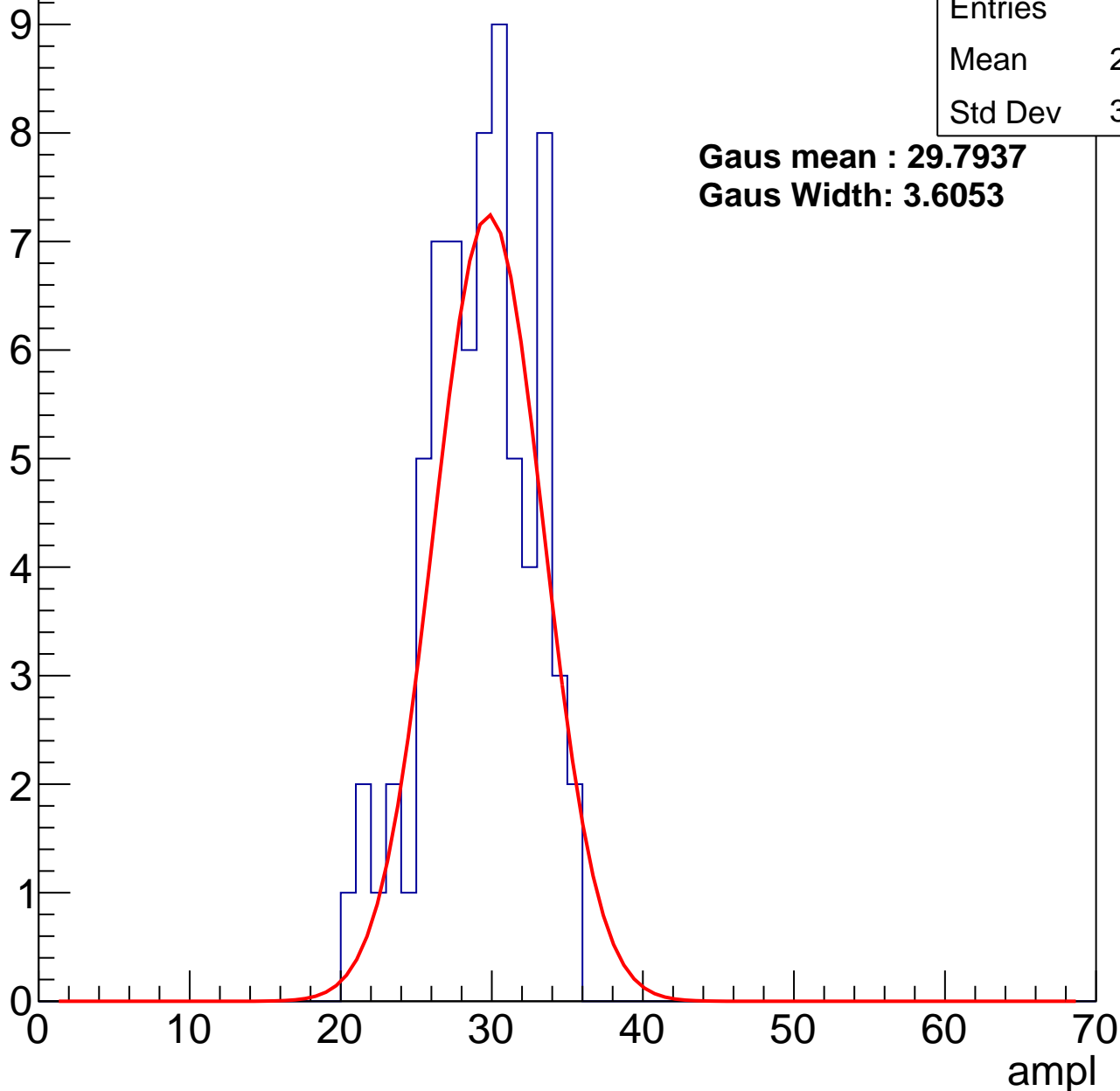
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.72
Std Dev	3.493

Gaus mean : 29.7937

Gaus Width: 3.6053



B1L102S, U22-ch22, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	36.43
Std Dev	3.585

Gaus mean : 37.3403

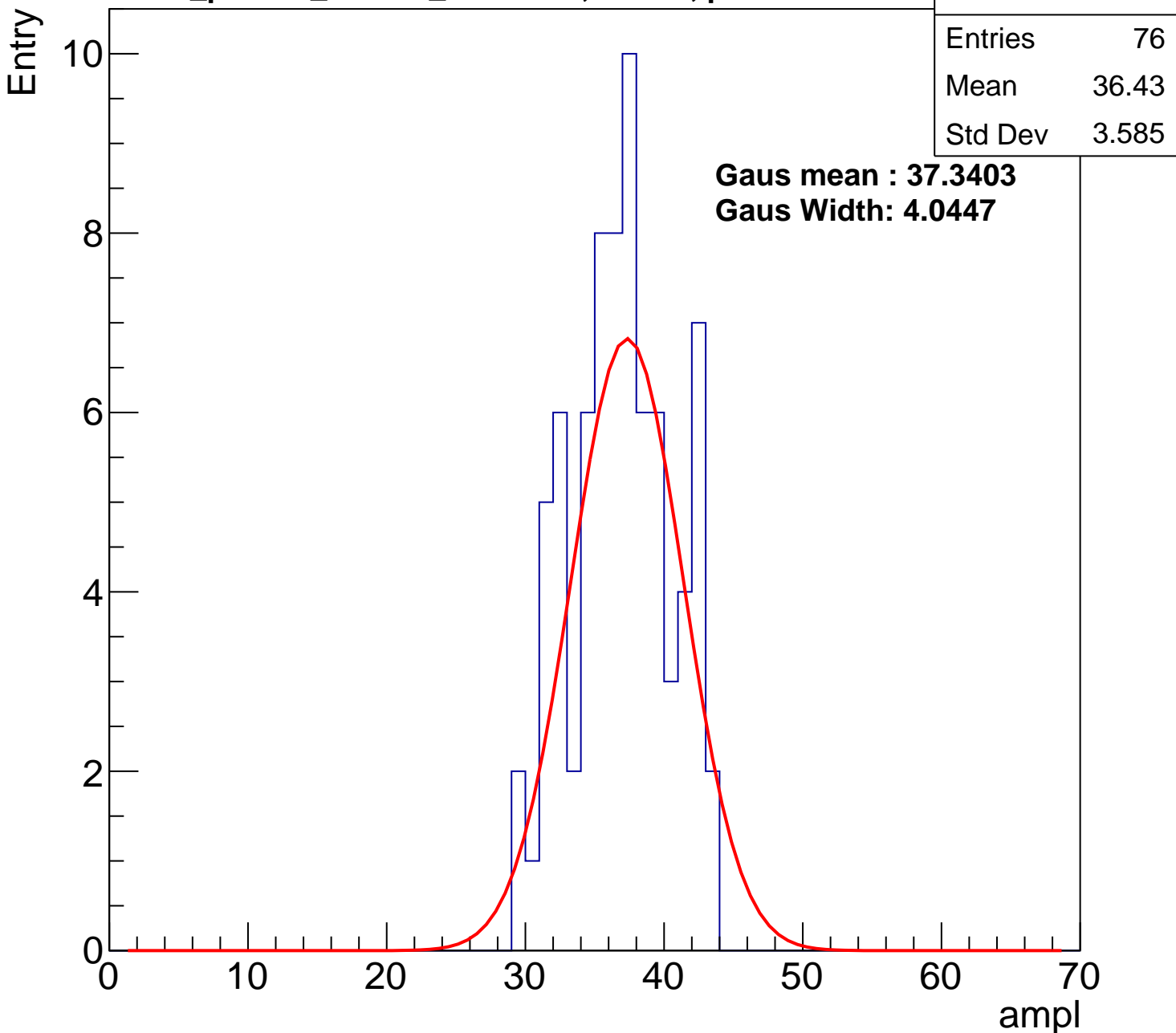
Gaus Width: 4.0447

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch22, adc2

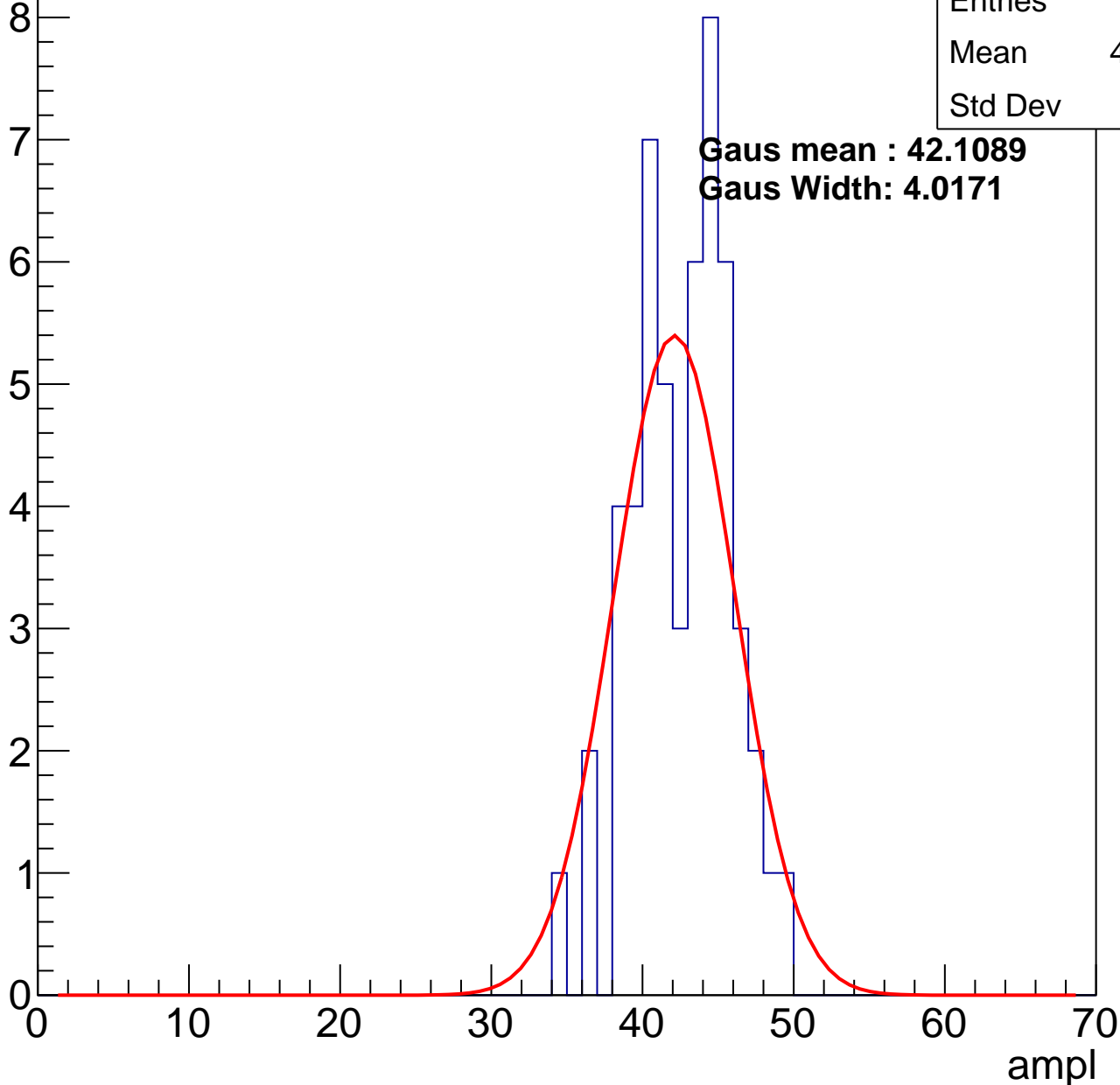
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.15
Std Dev	3.2

Gaus mean : 42.1089

Gaus Width: 4.0171

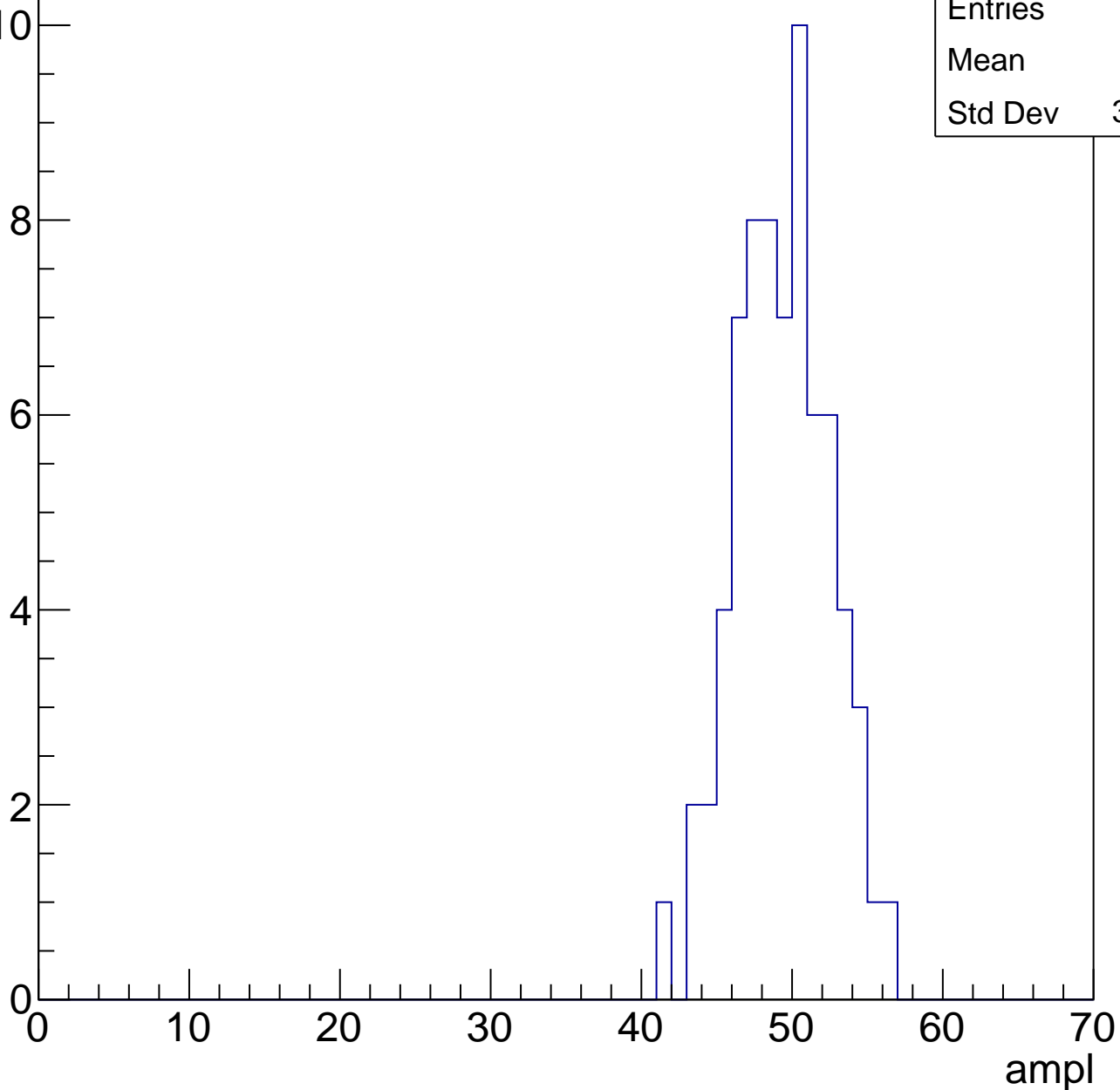


B1L102S, U22-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.9
Std Dev	3.081

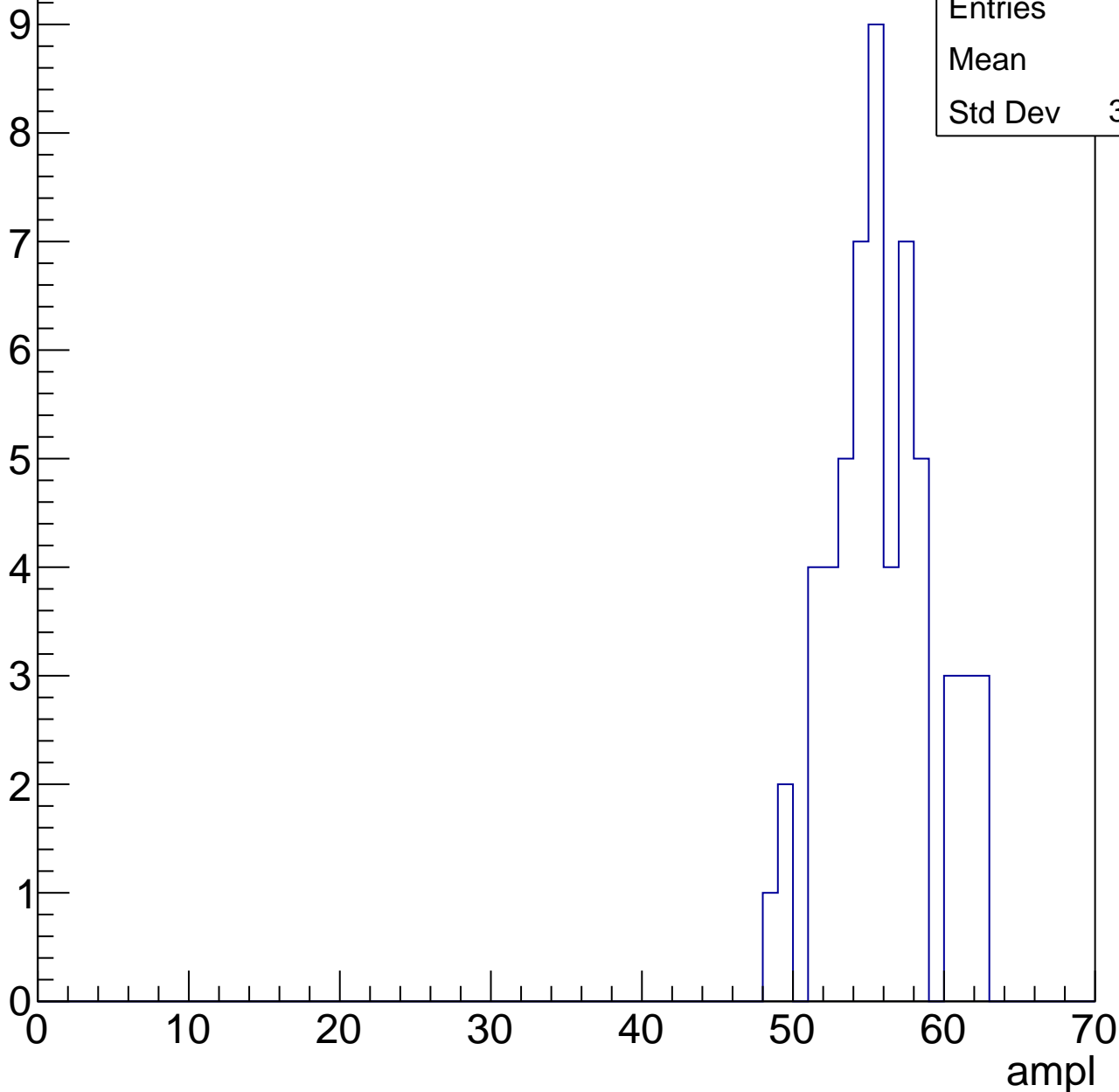


B1L102S, U22-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.4
Std Dev	3.366



B1L102S, U22-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	50
Mean	58.36
Std Dev	8.634

0

2

4

6

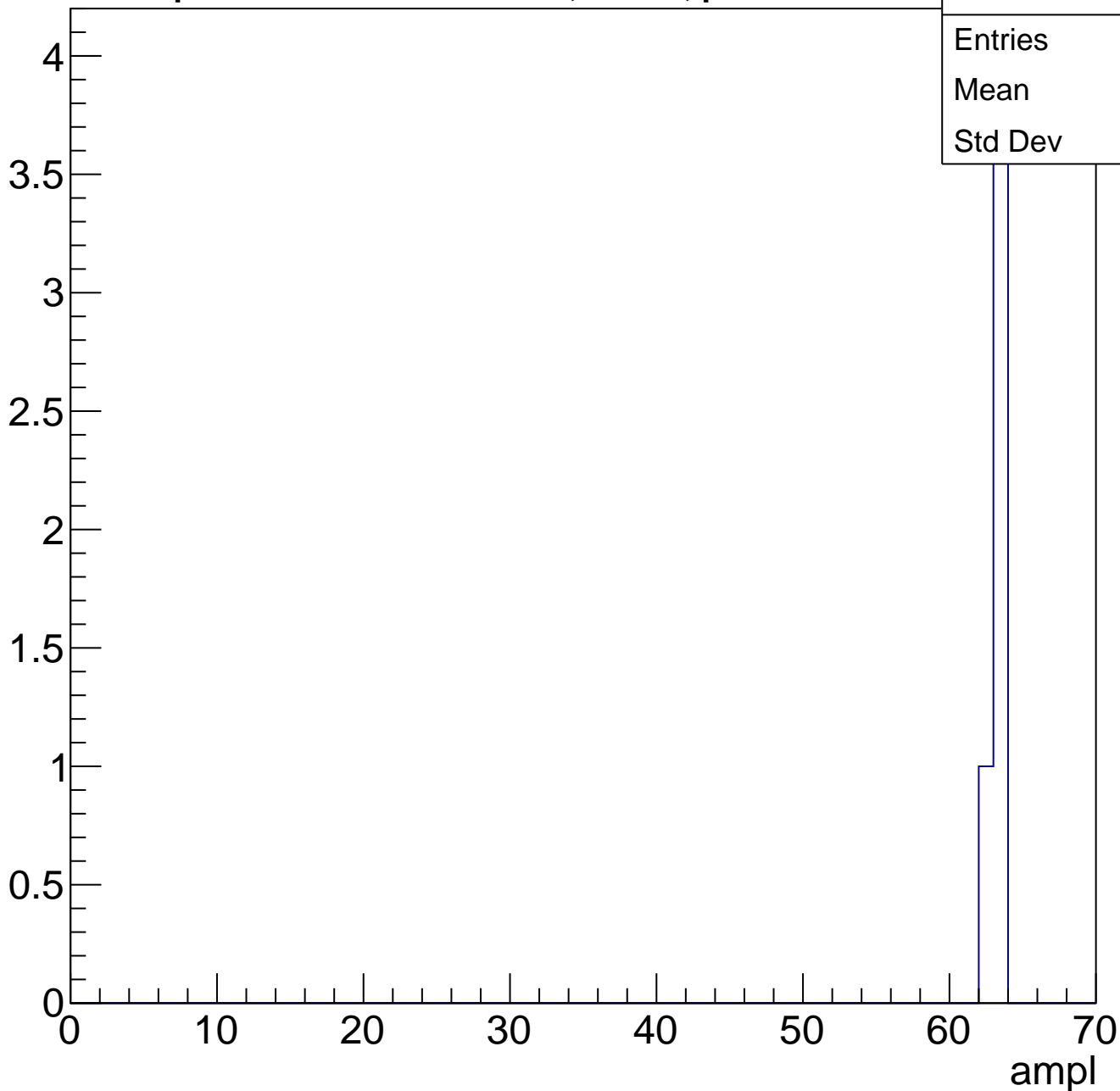
8

10

B1L102S, U22-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch23, adc0

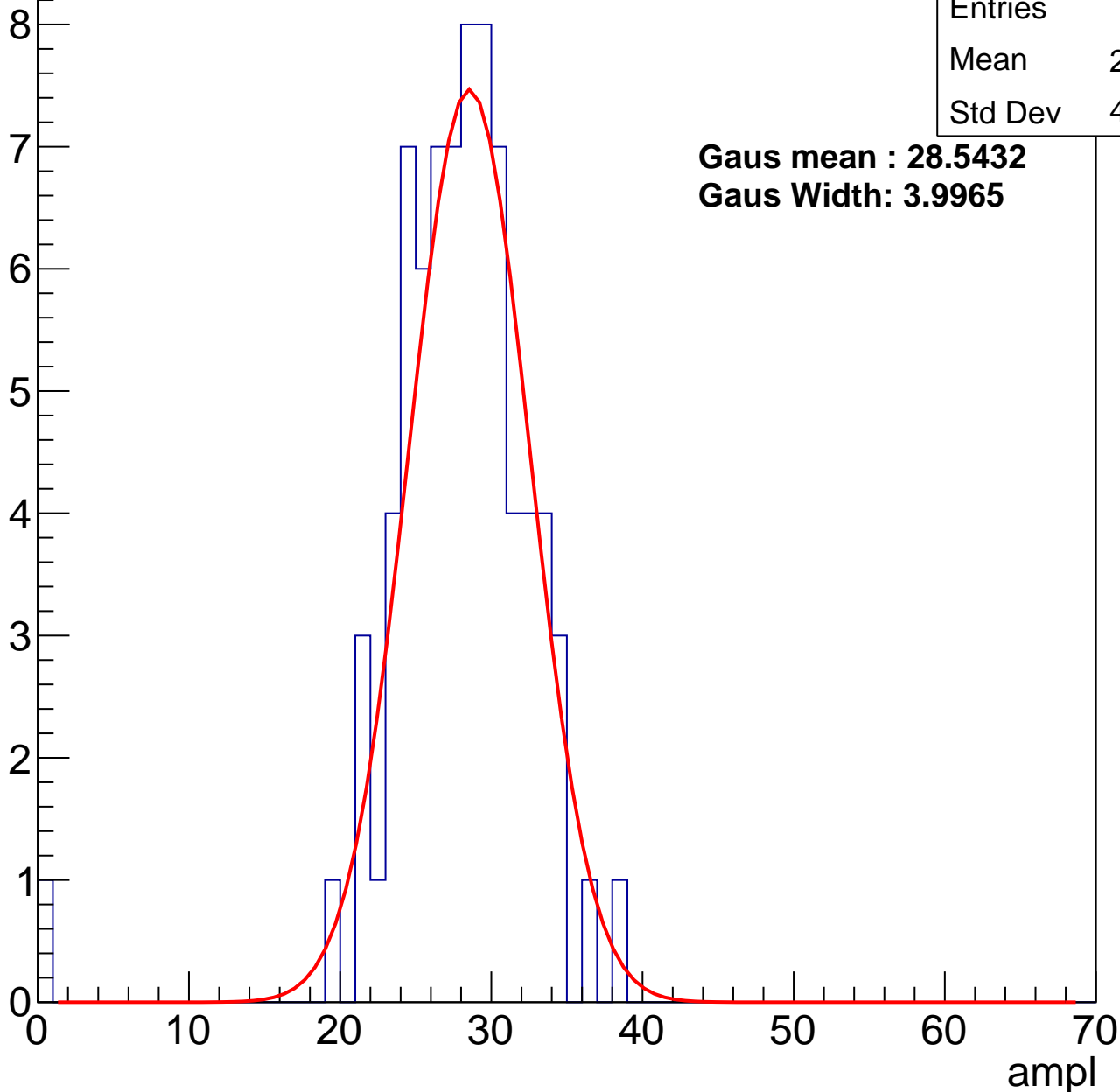
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	27.42
Std Dev	4.876

Gaus mean : 28.5432

Gaus Width: 3.9965



B1L102S, U22-ch23, adc1

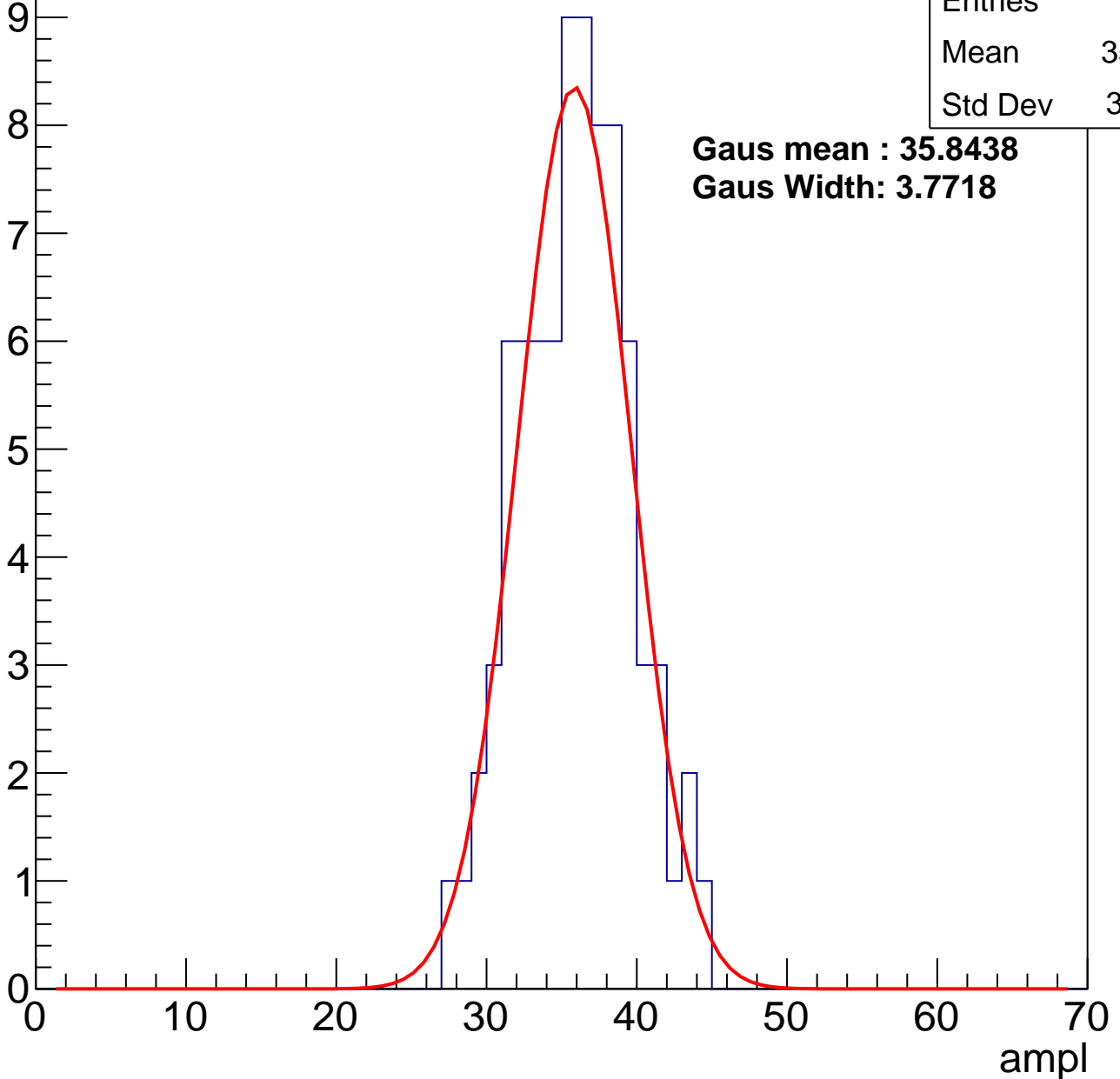
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.44
Std Dev	3.641

Gaus mean : 35.8438

Gaus Width: 3.7718



B1L102S, U22-ch23, adc2

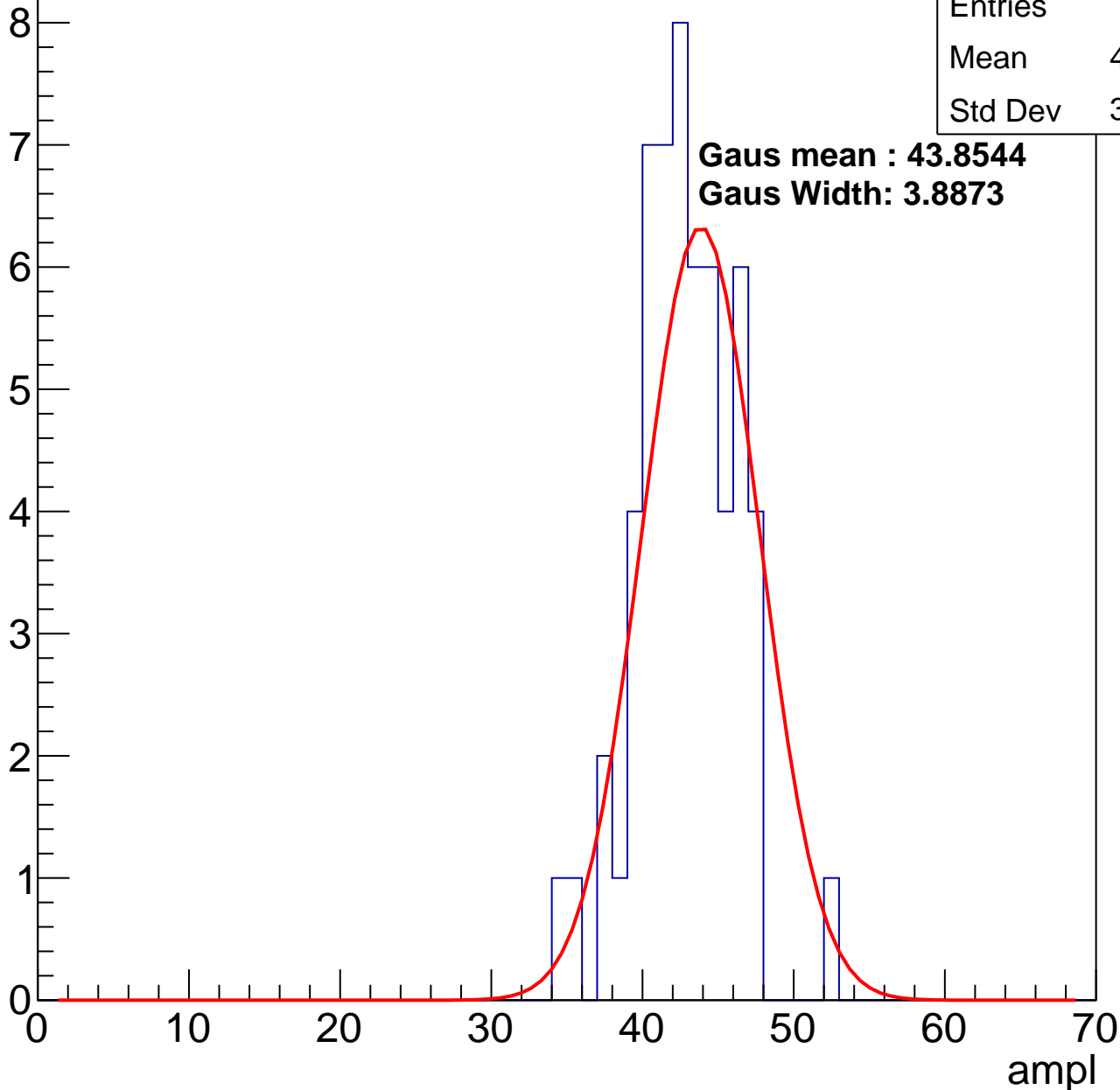
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.38
Std Dev	3.226

Gaus mean : 43.8544

Gaus Width: 3.8873

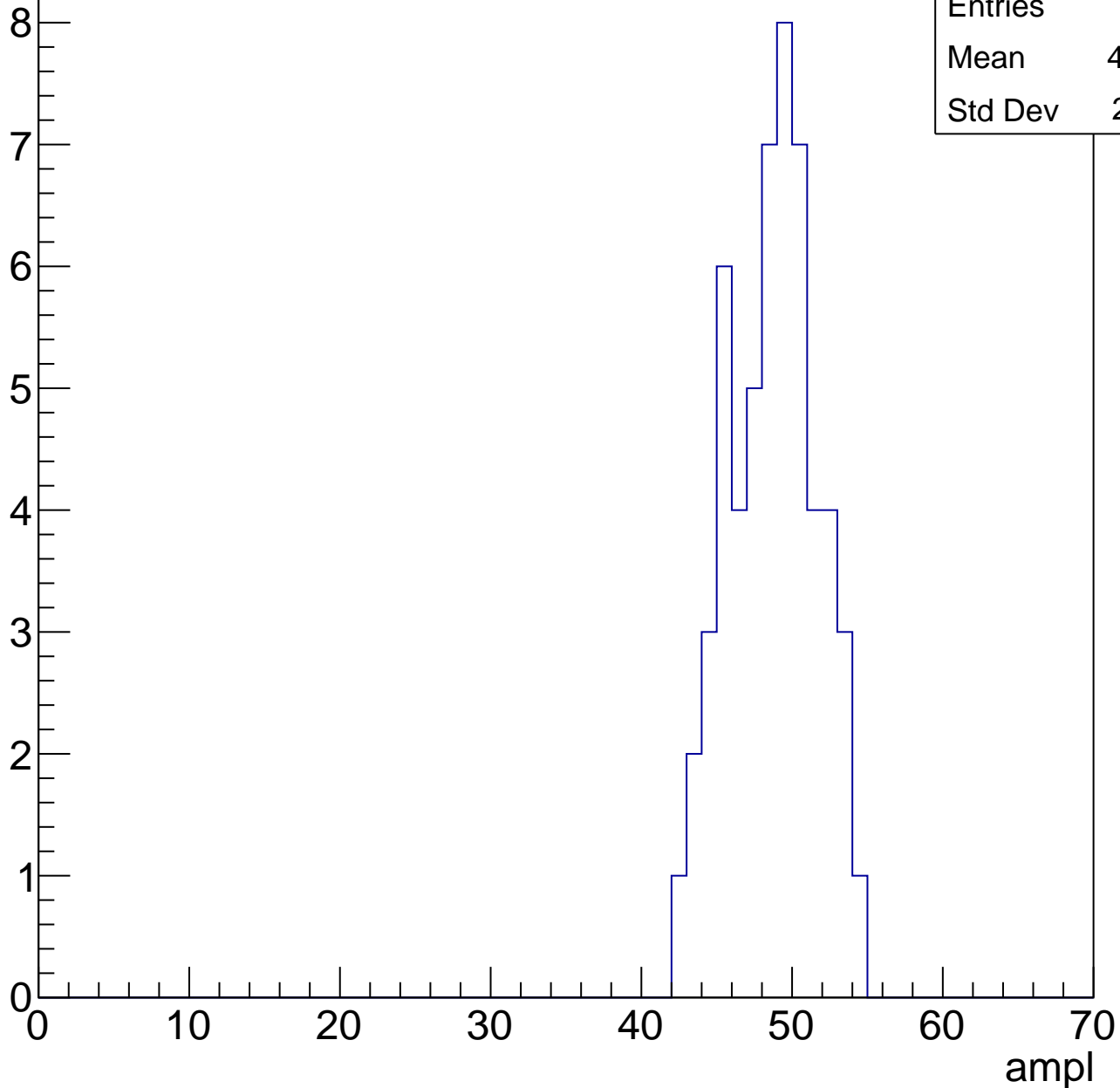


B1L102S, U22-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.22
Std Dev	2.871

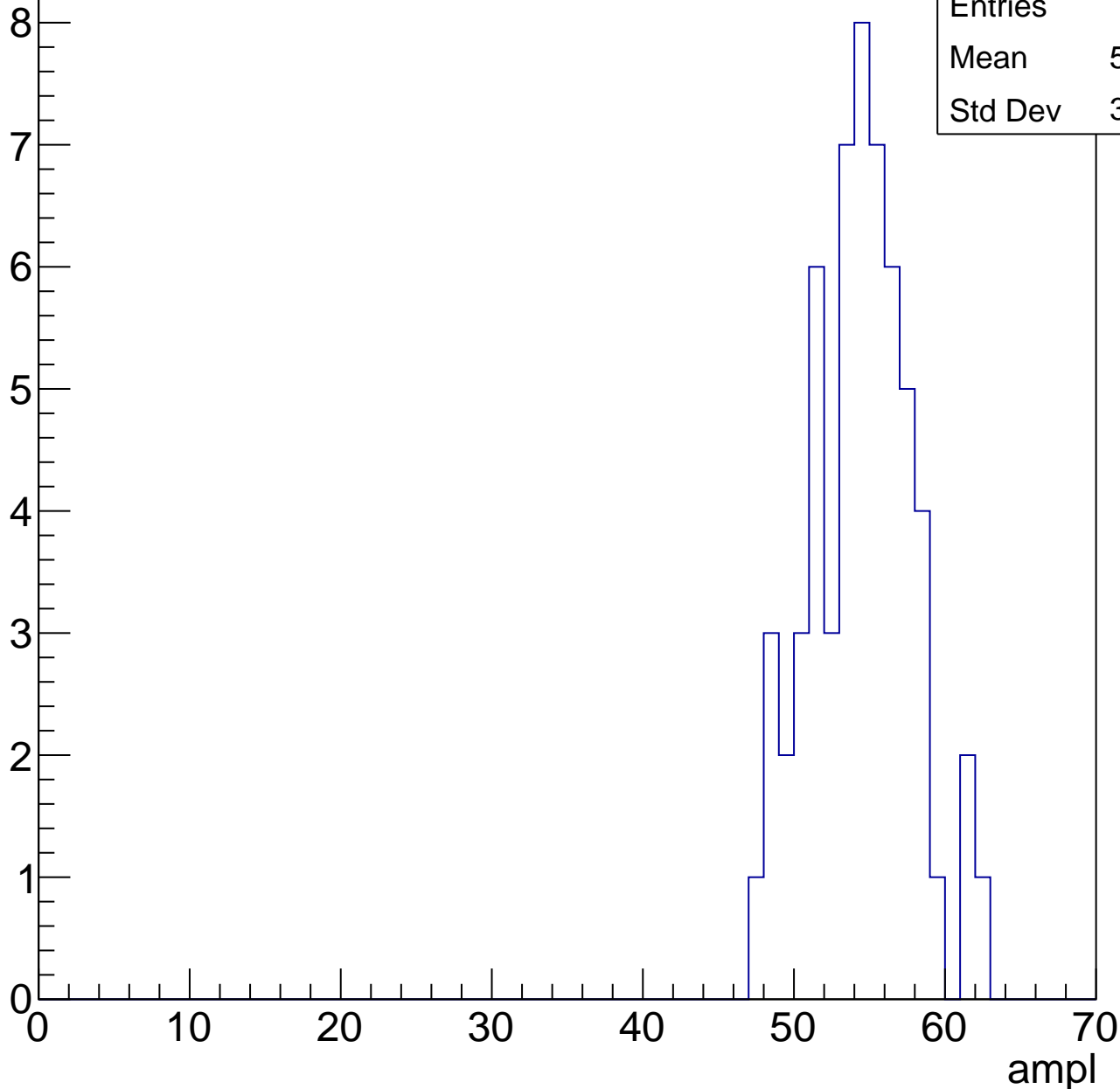


B1L102S, U22-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	53.98
Std Dev	3.332

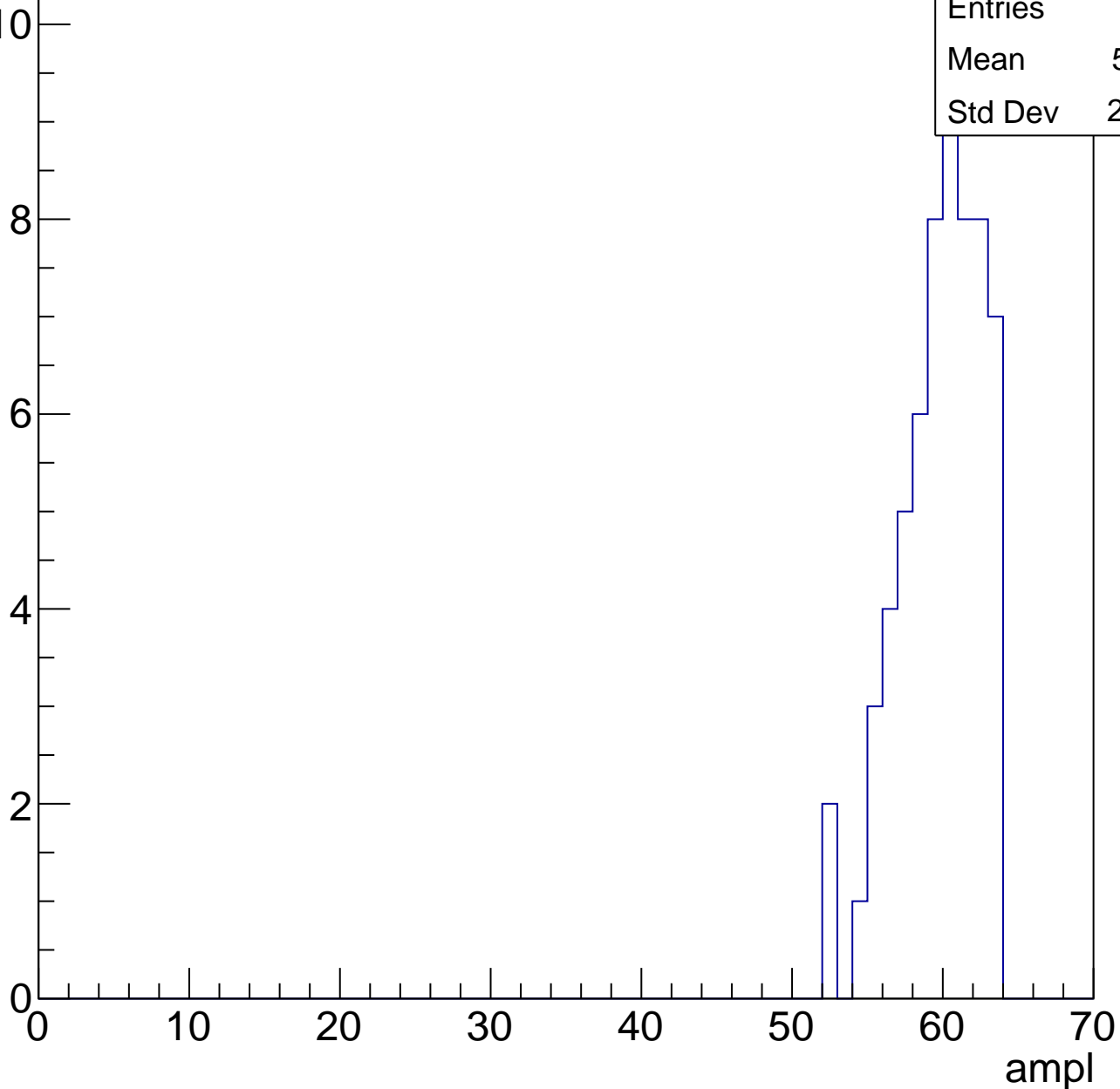


B1L102S, U22-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

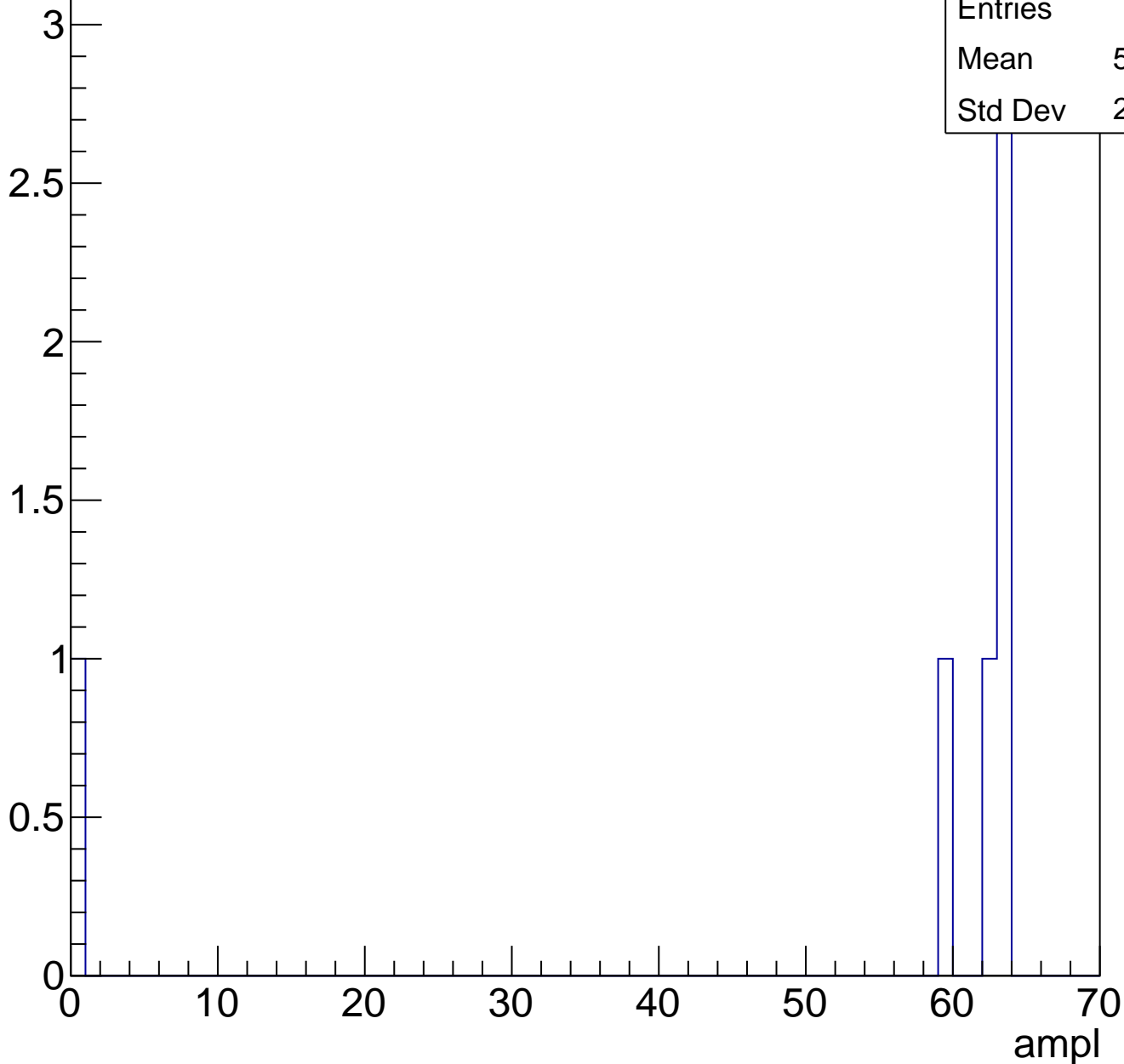
Entries	62
Mean	59.31
Std Dev	2.704



B1L102S, U22-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch24, adc0

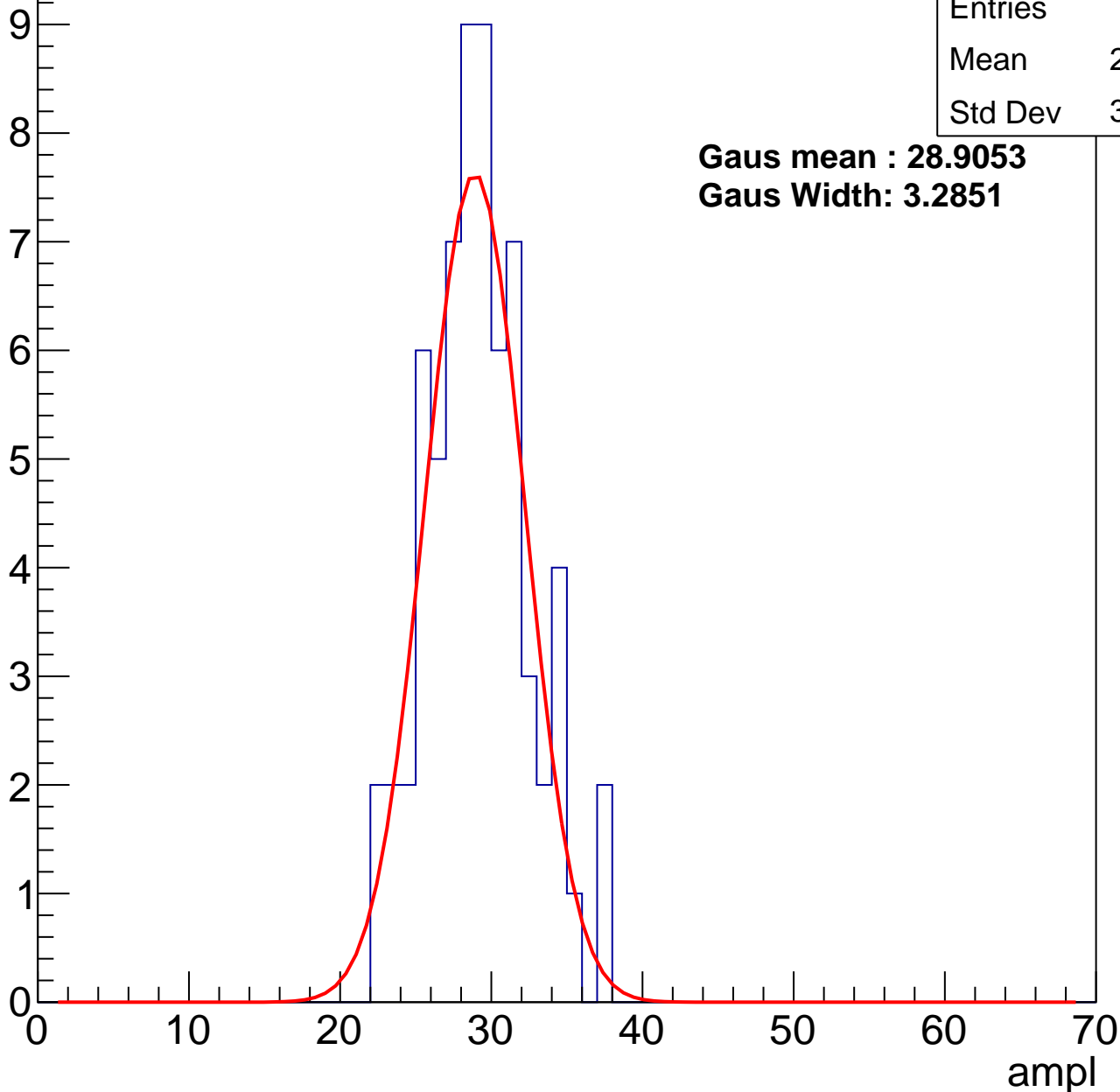
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.72
Std Dev	3.345

Gaus mean : 28.9053

Gaus Width: 3.2851



B1L102S, U22-ch24, adc1

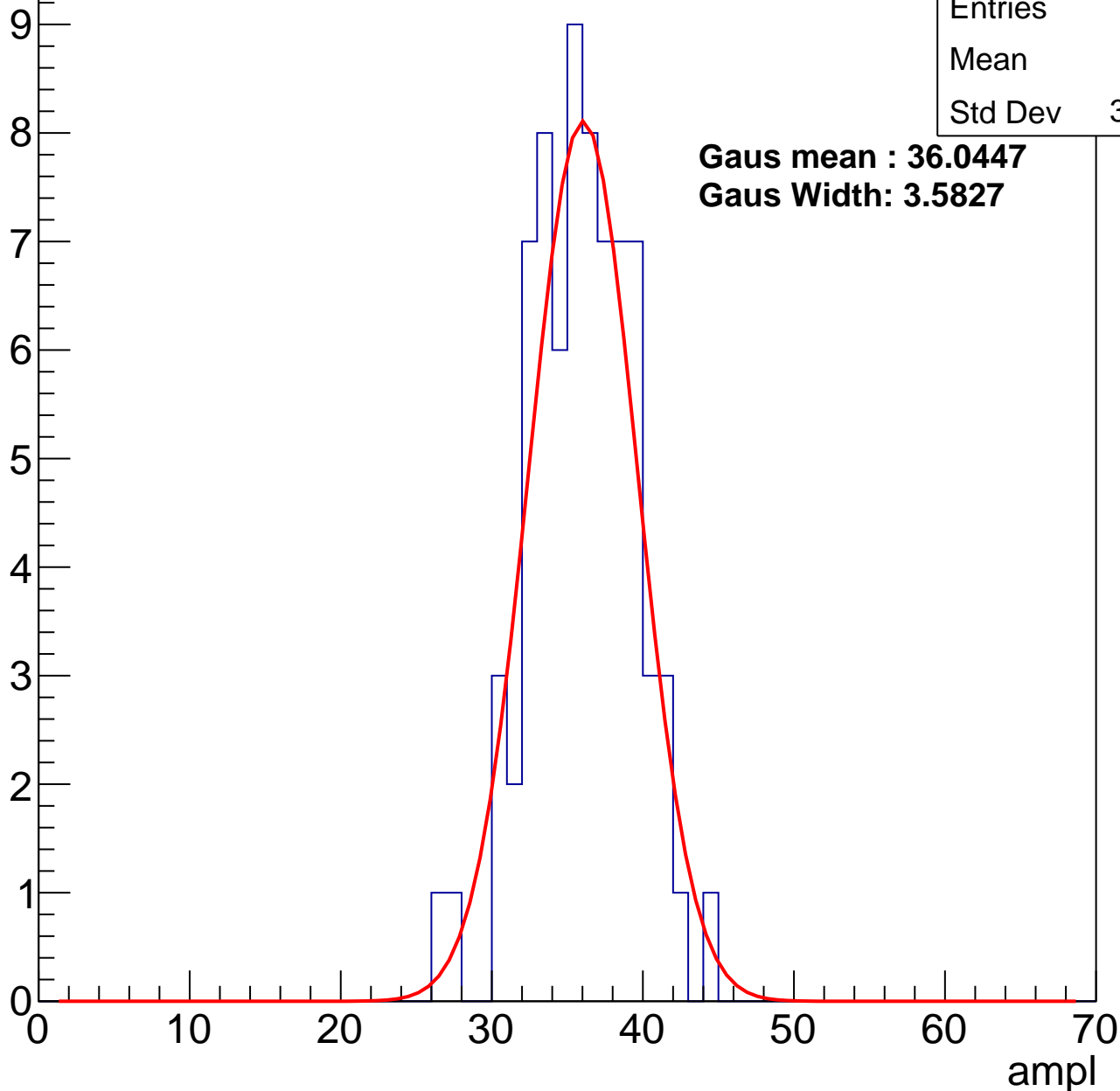
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.5
Std Dev	3.406

Gaus mean : 36.0447

Gaus Width: 3.5827



B1L102S, U22-ch24, adc2

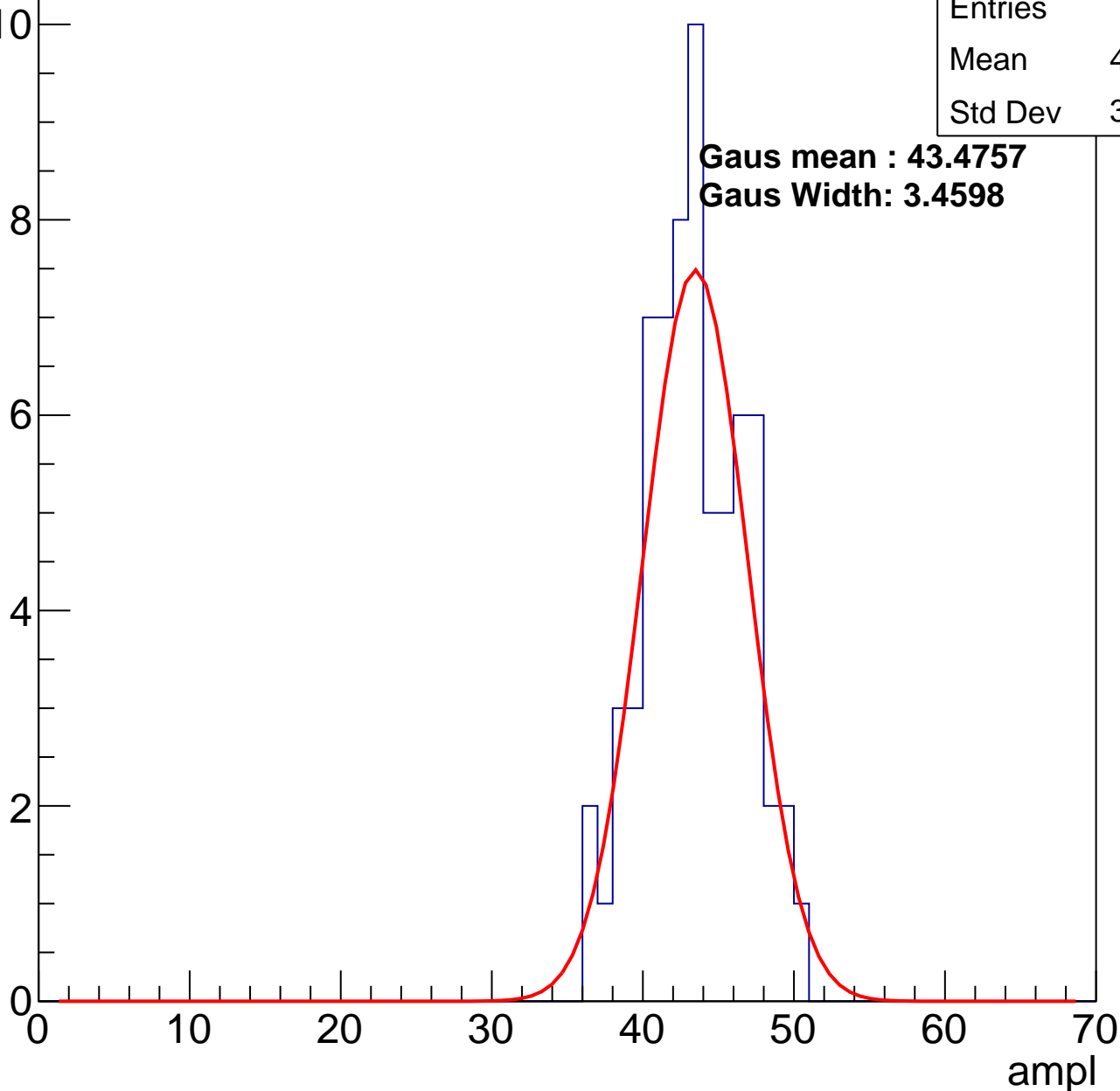
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.94
Std Dev	3.212

Gaus mean : 43.4757

Gaus Width: 3.4598

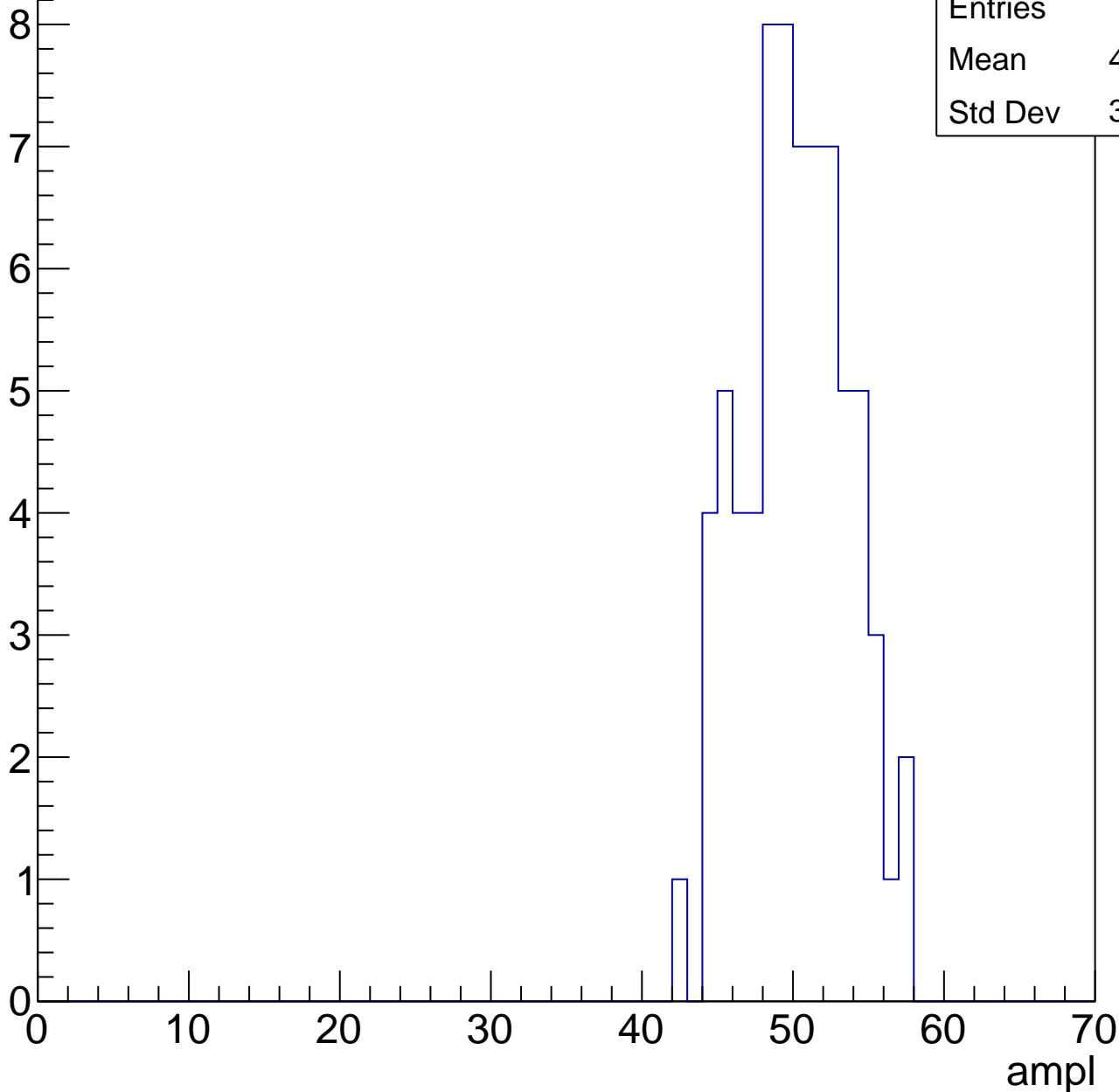


B1L102S, U22-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.75
Std Dev	3.434

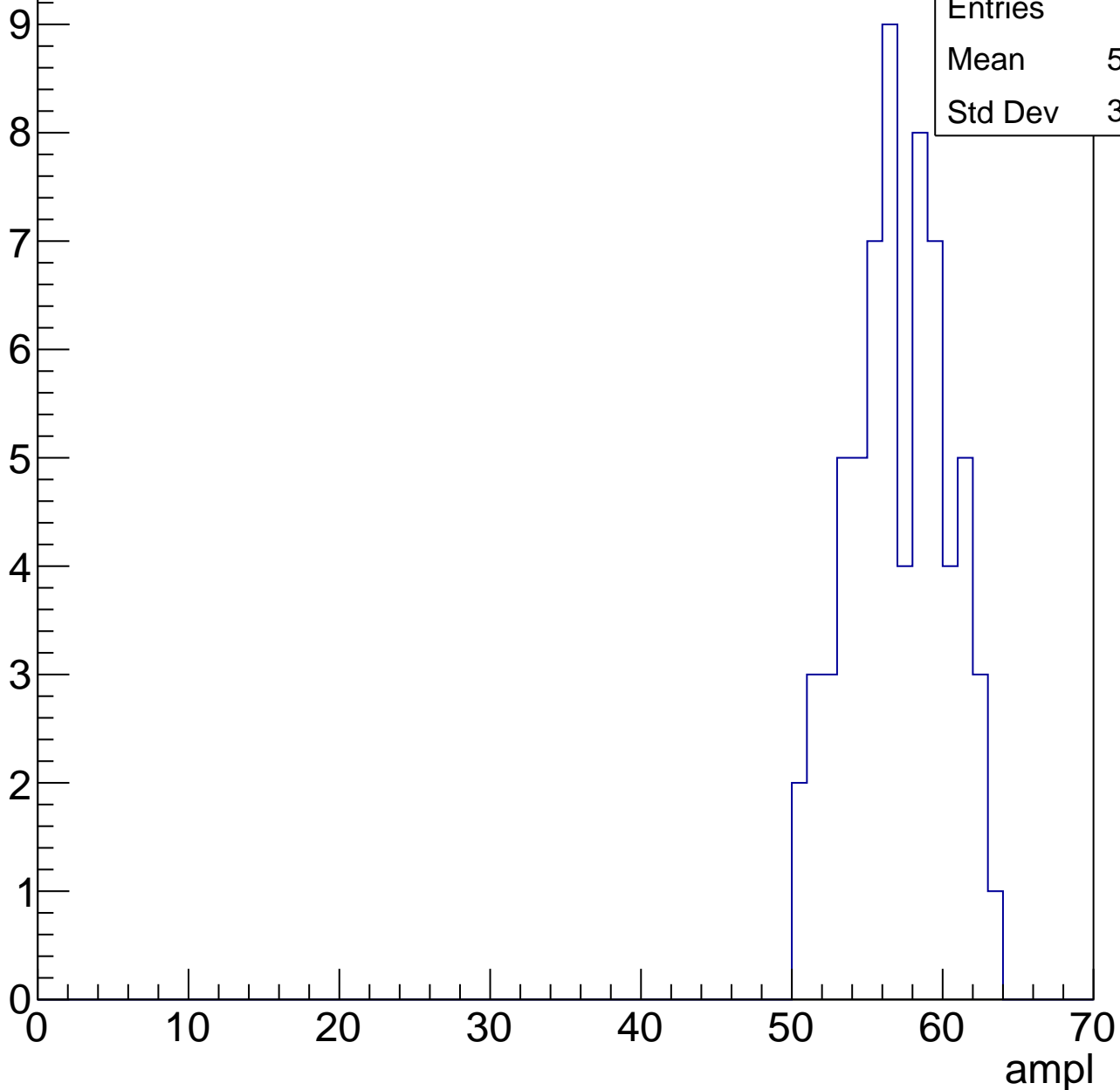


B1L102S, U22-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	56.55
Std Dev	3.239

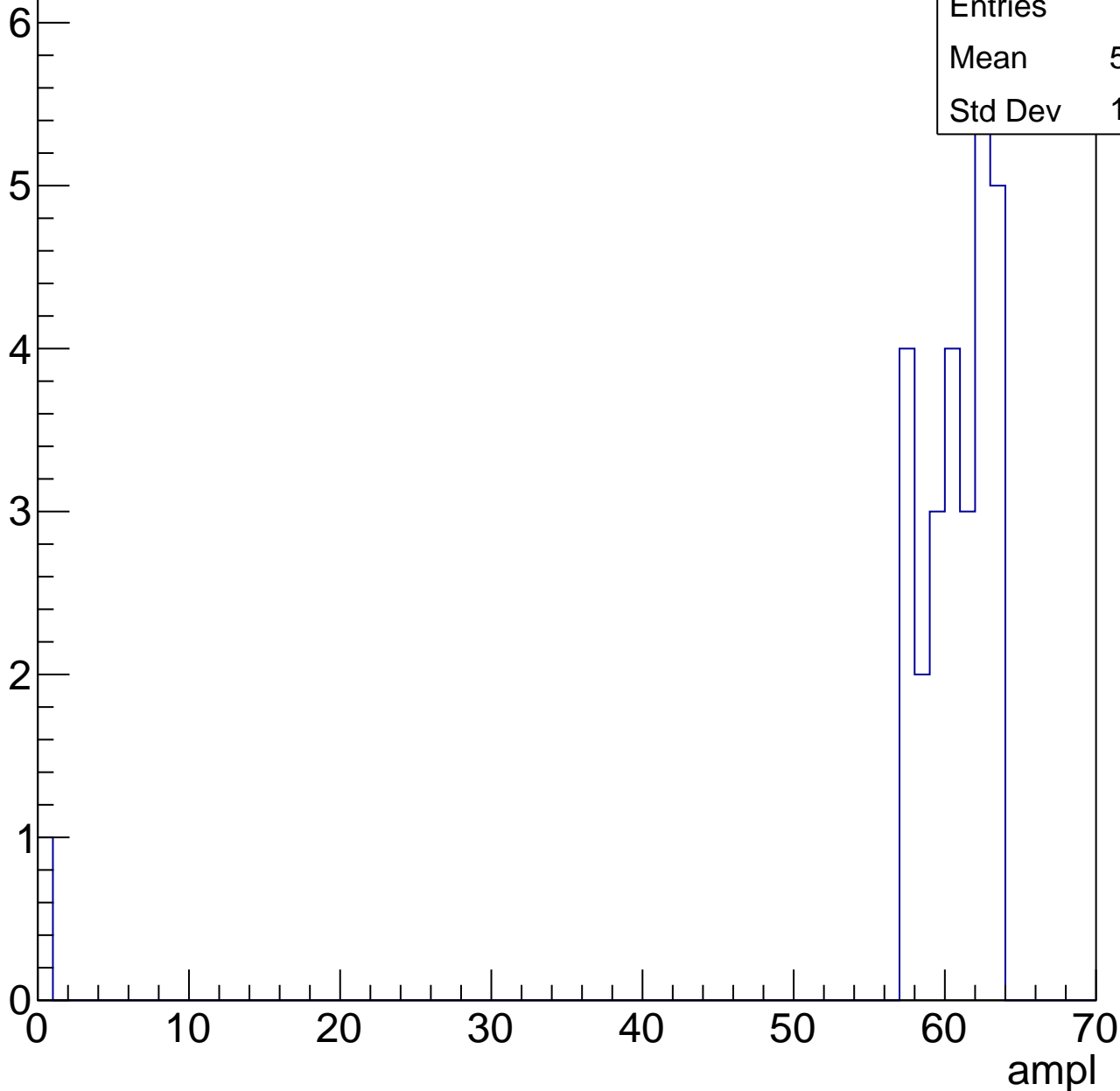


B1L102S, U22-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

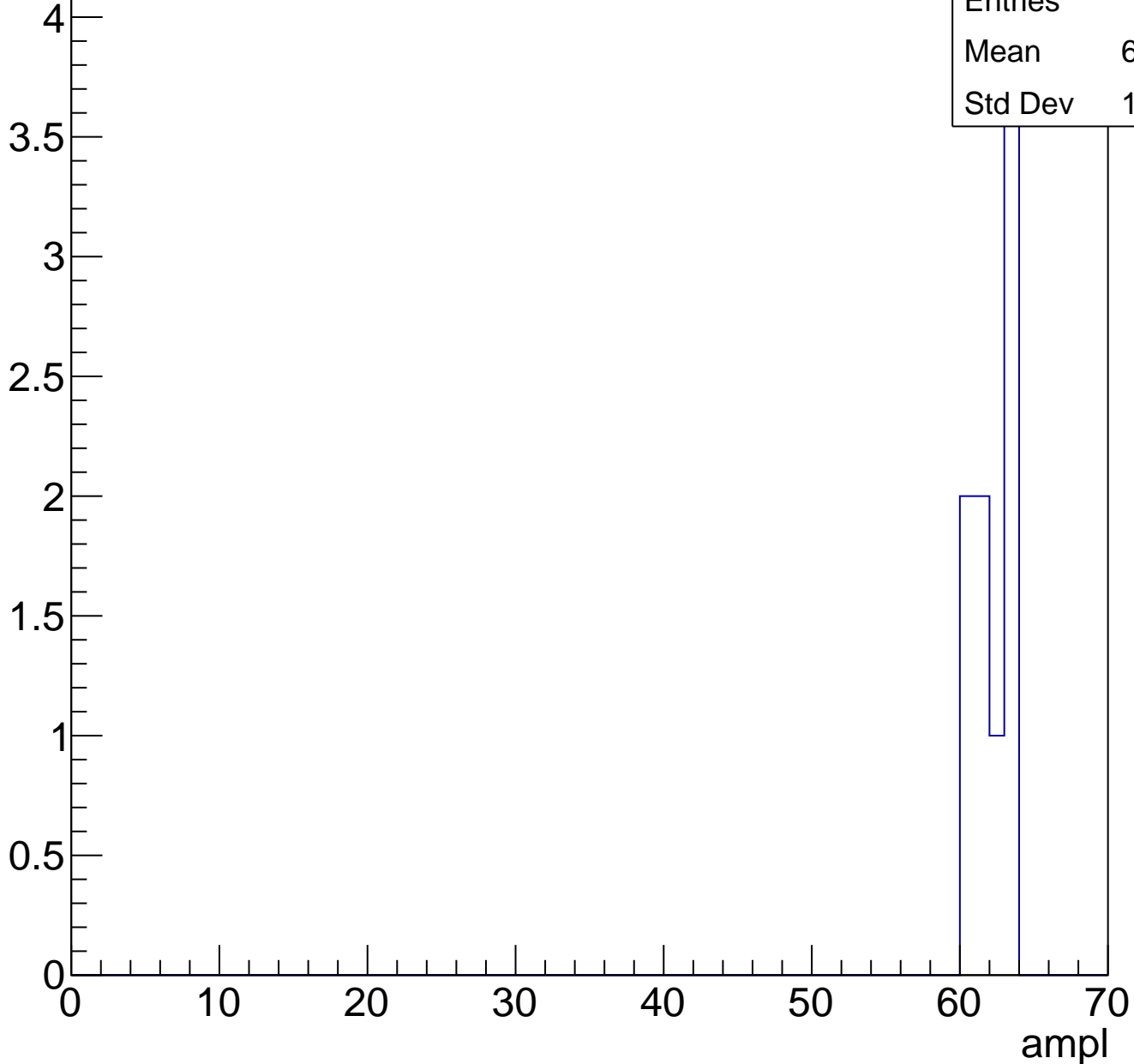
Entries	28
Mean	58.25
Std Dev	11.39



B1L102S, U22-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch25, adc0

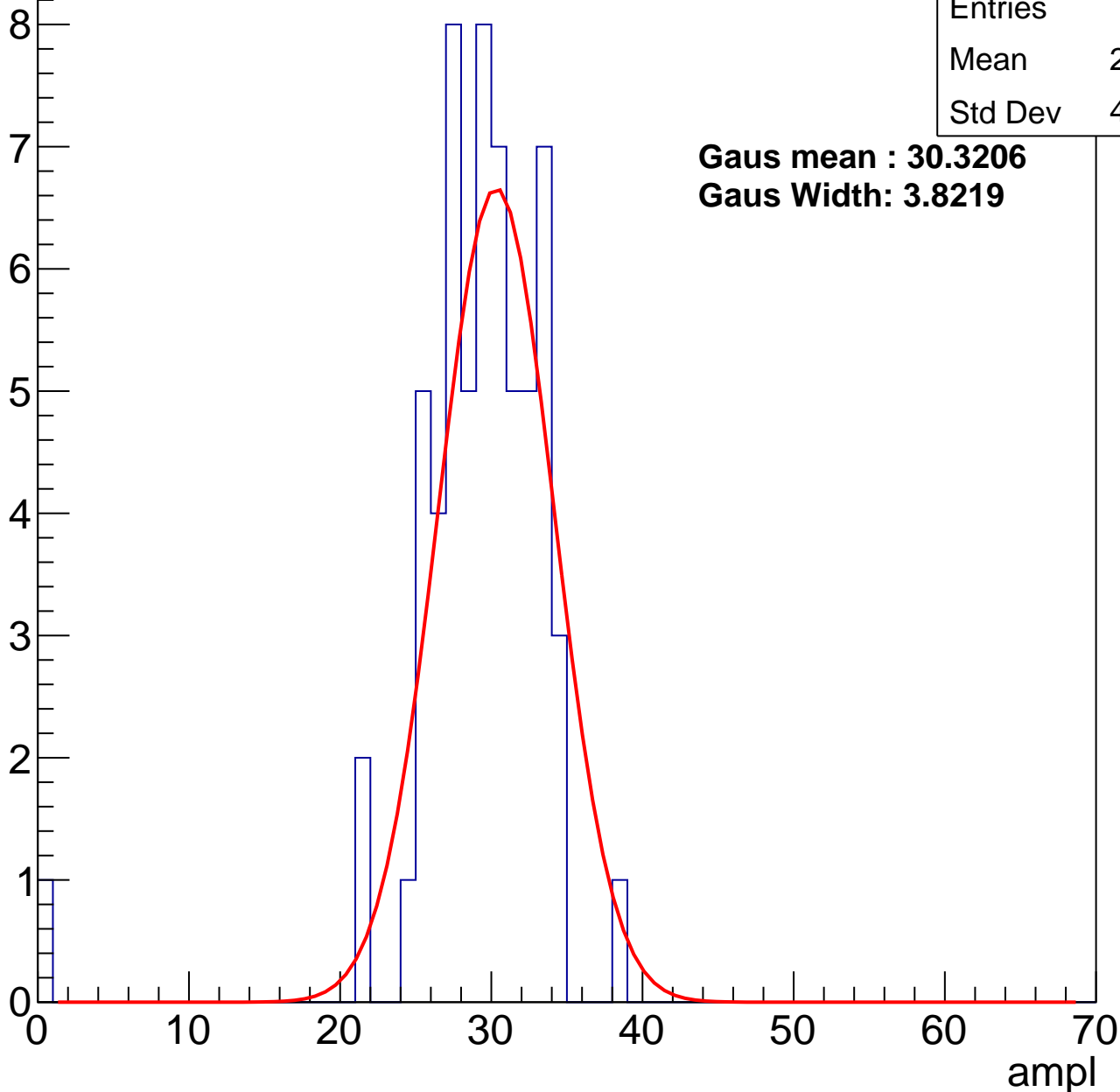
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.69
Std Dev	4.888

Gaus mean : 30.3206

Gaus Width: 3.8219



B1L102S, U22-ch25, adc1

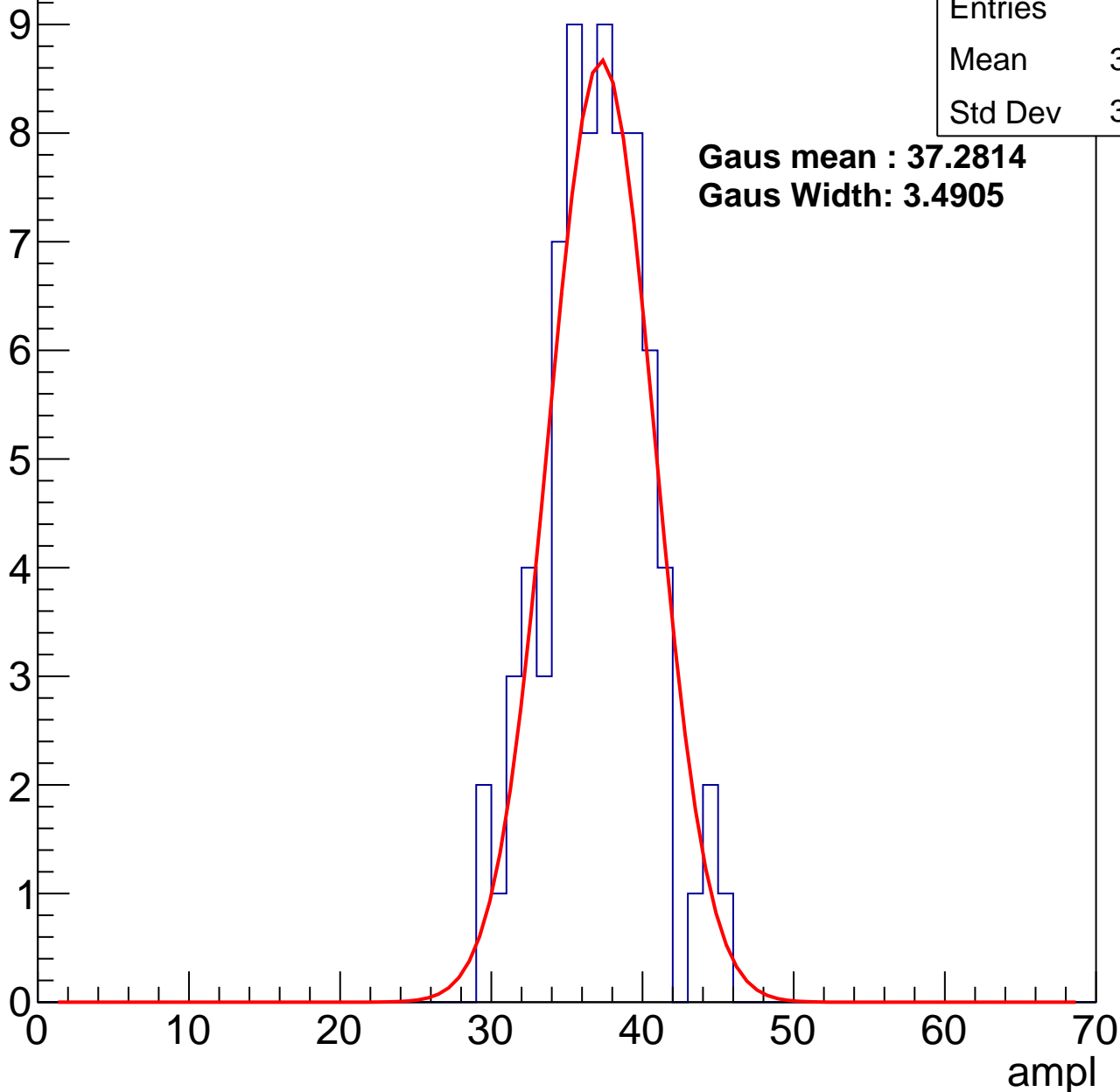
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.55
Std Dev	3.404

Gaus mean : 37.2814

Gaus Width: 3.4905



B1L102S, U22-ch25, adc2

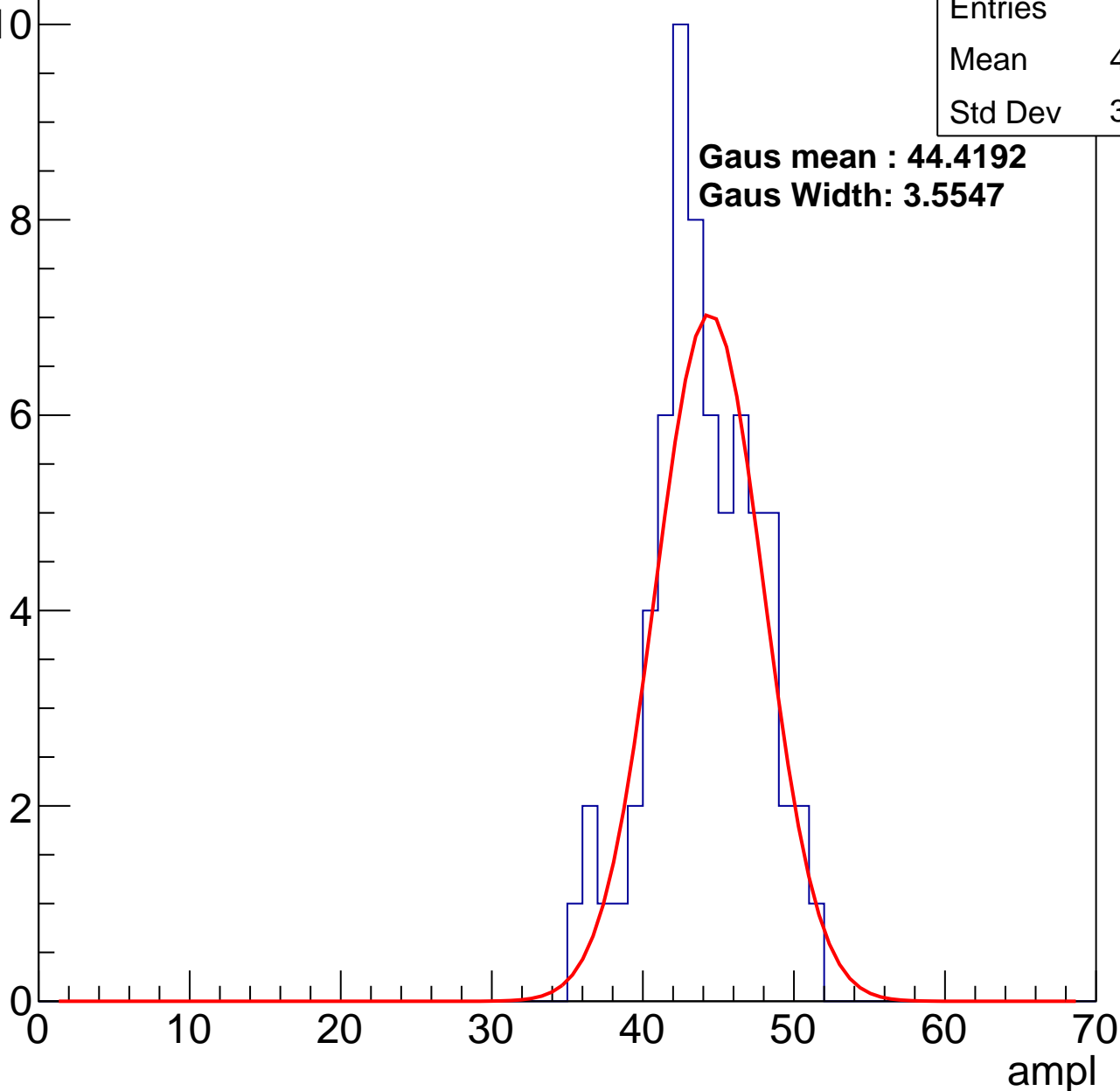
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.57
Std Dev	3.504

Gaus mean : 44.4192

Gaus Width: 3.5547

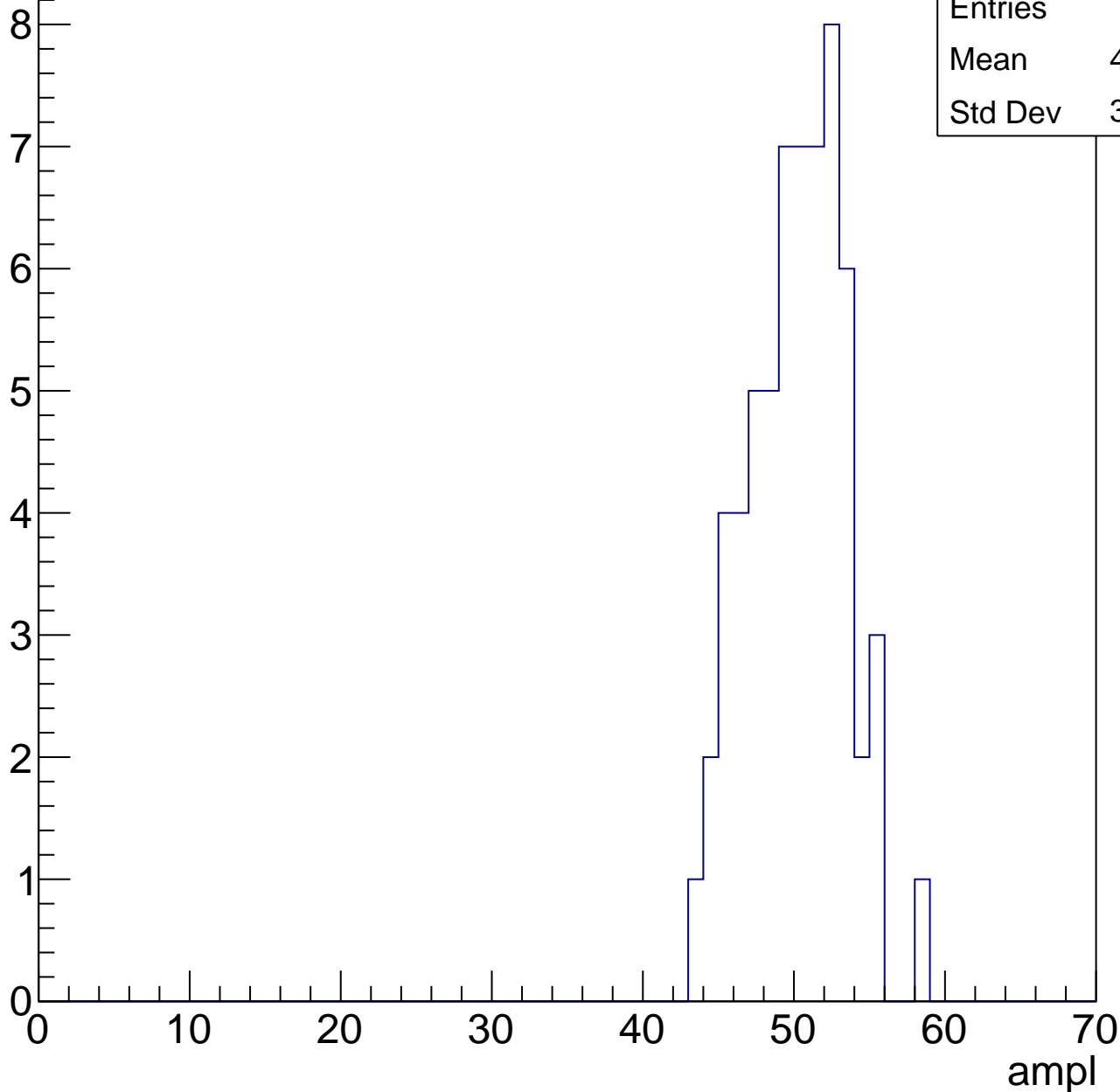


B1L102S, U22-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.76
Std Dev	3.156

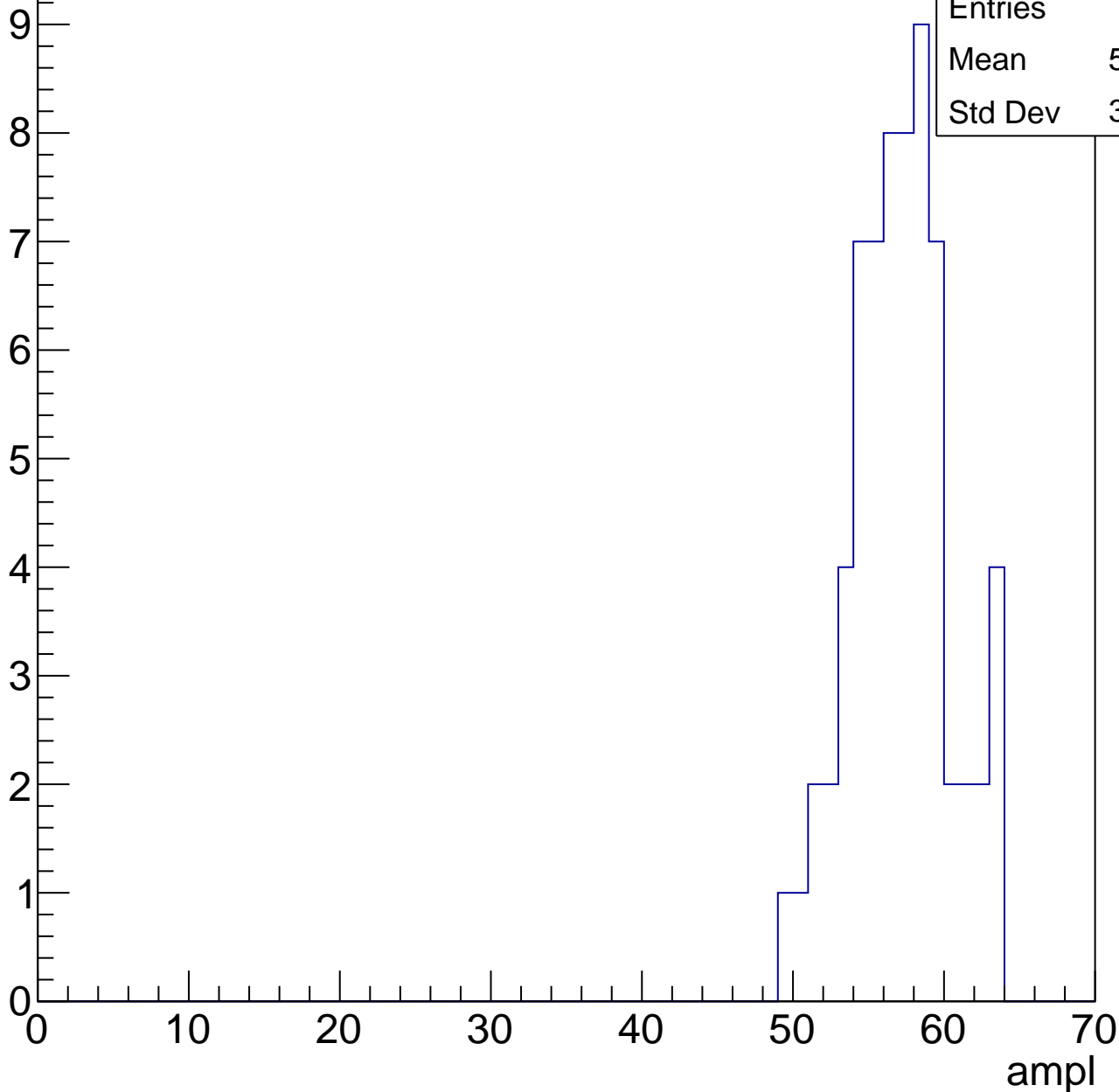


B1L102S, U22-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	56.62
Std Dev	3.185

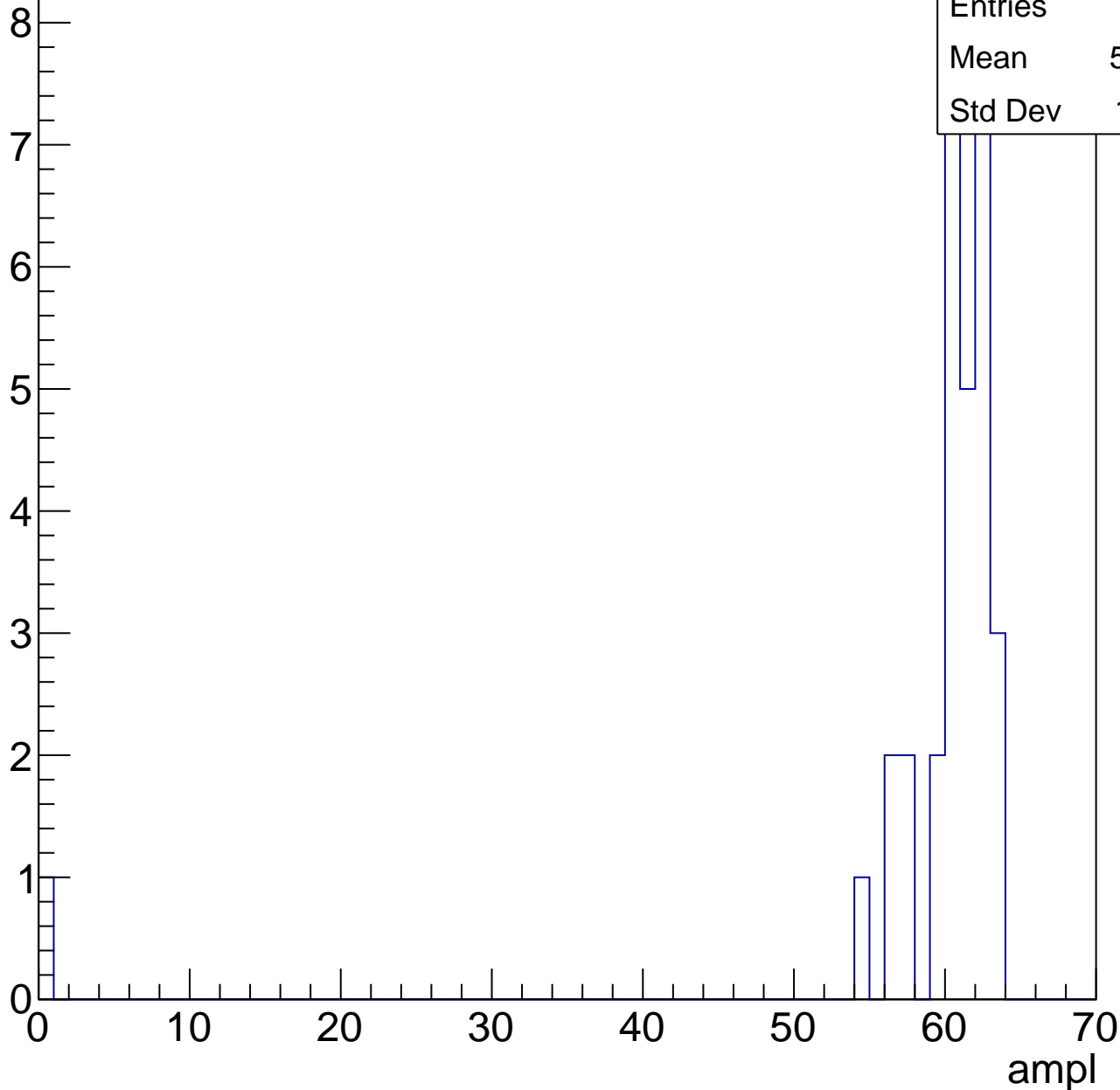


B1L102S, U22-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

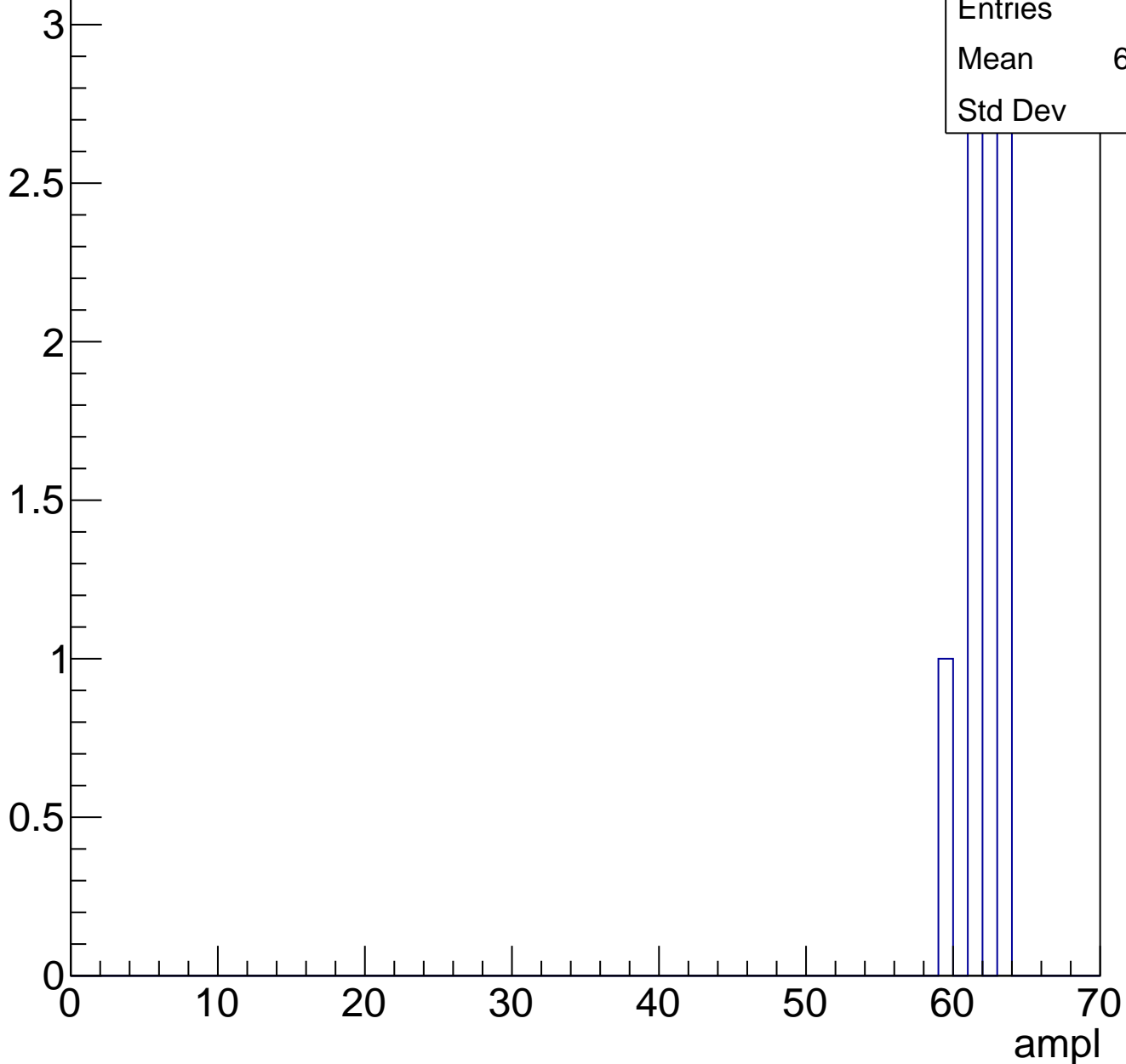
Entries	32
Mean	58.38
Std Dev	10.71



B1L102S, U22-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch26, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	28.6
Std Dev	3.324

Gaus mean : 29.8640

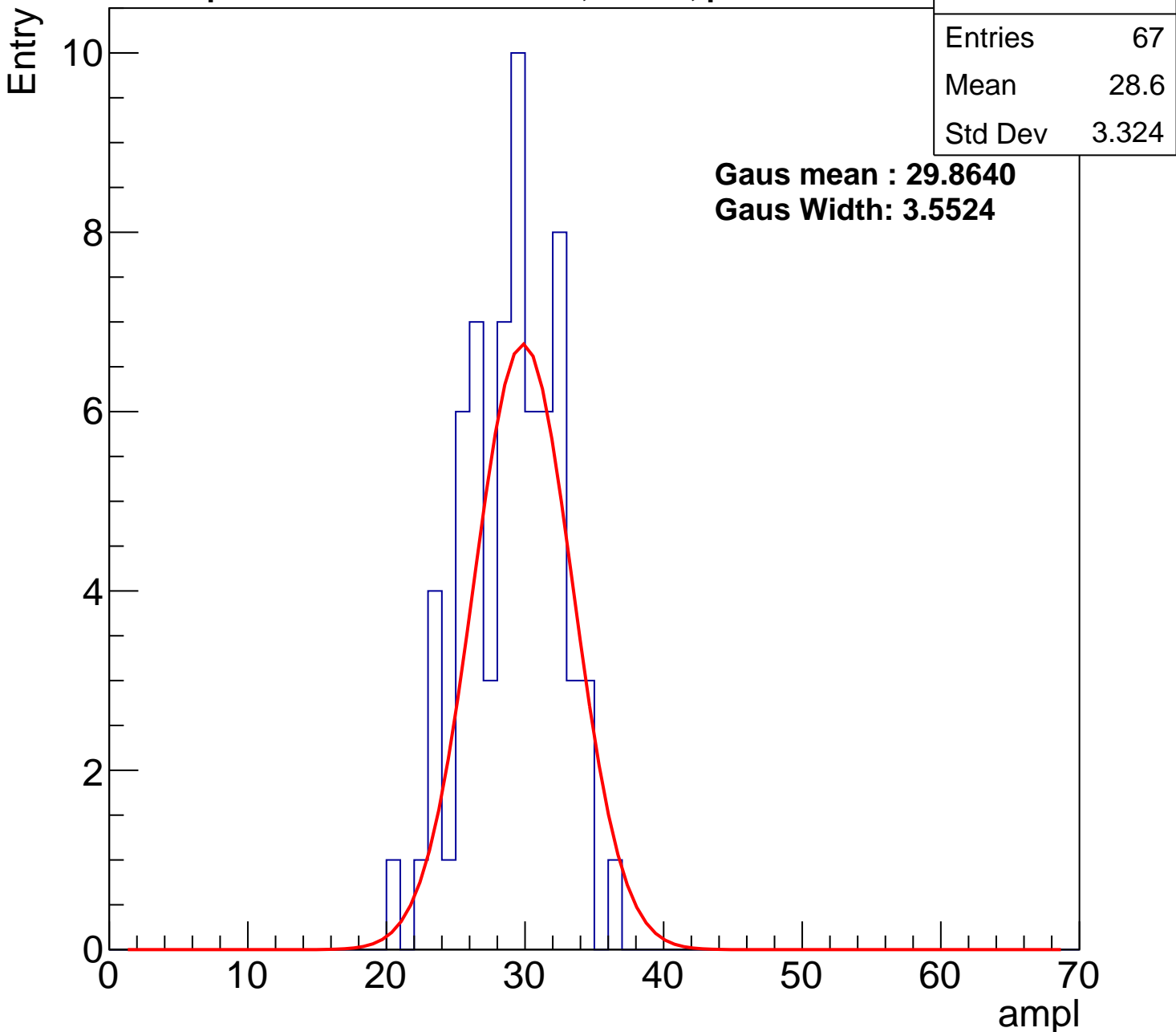
Gaus Width: 3.5524

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch26, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	36.44
Std Dev	3.434

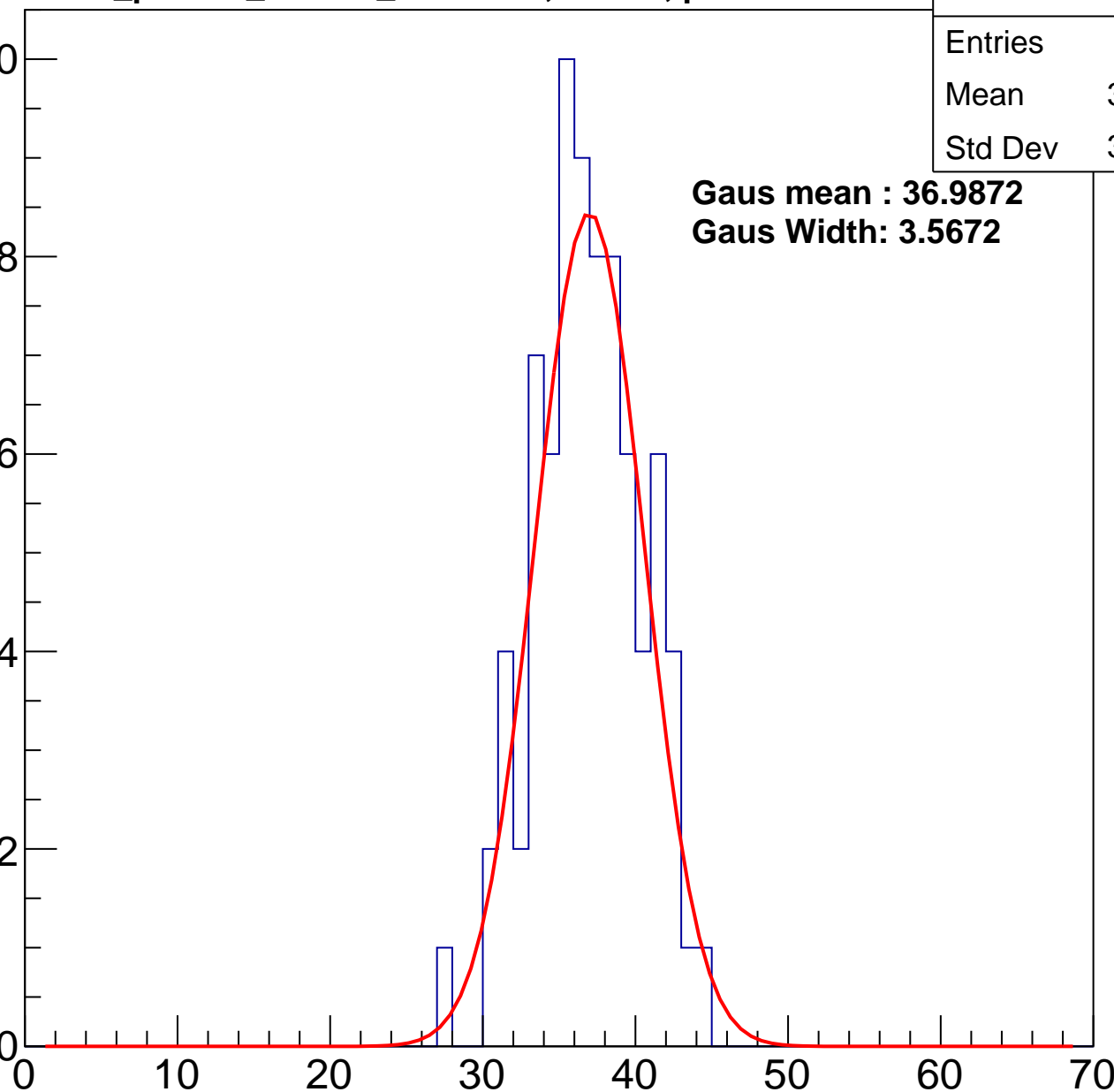
Gaus mean : 36.9872

Gaus Width: 3.5672

Entry

10
8
6
4
2
0

ampl



B1L102S, U22-ch26, adc2

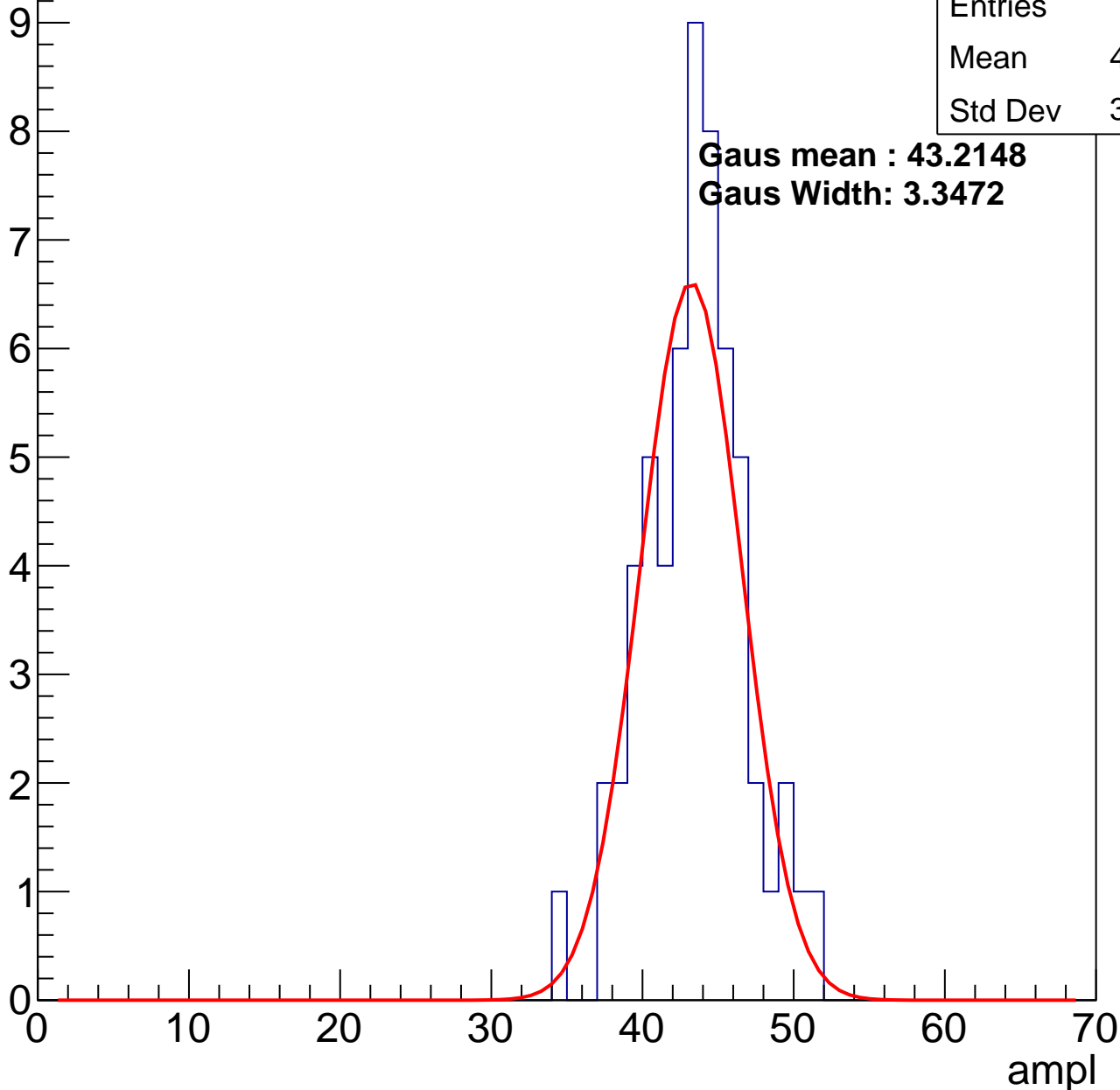
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.98
Std Dev	3.327

Gaus mean : 43.2148

Gaus Width: 3.3472

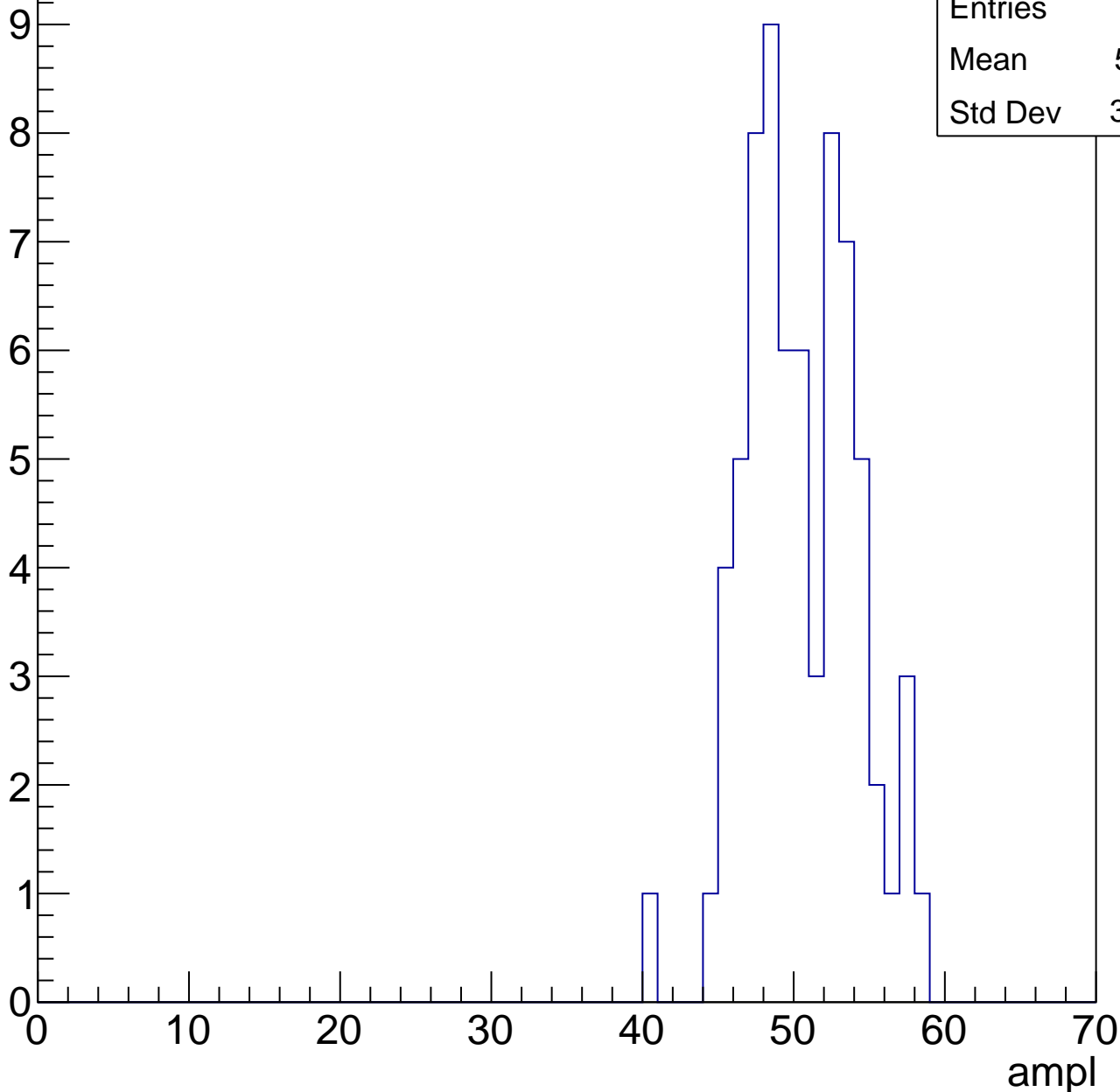


B1L102S, U22-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	50.01
Std Dev	3.596

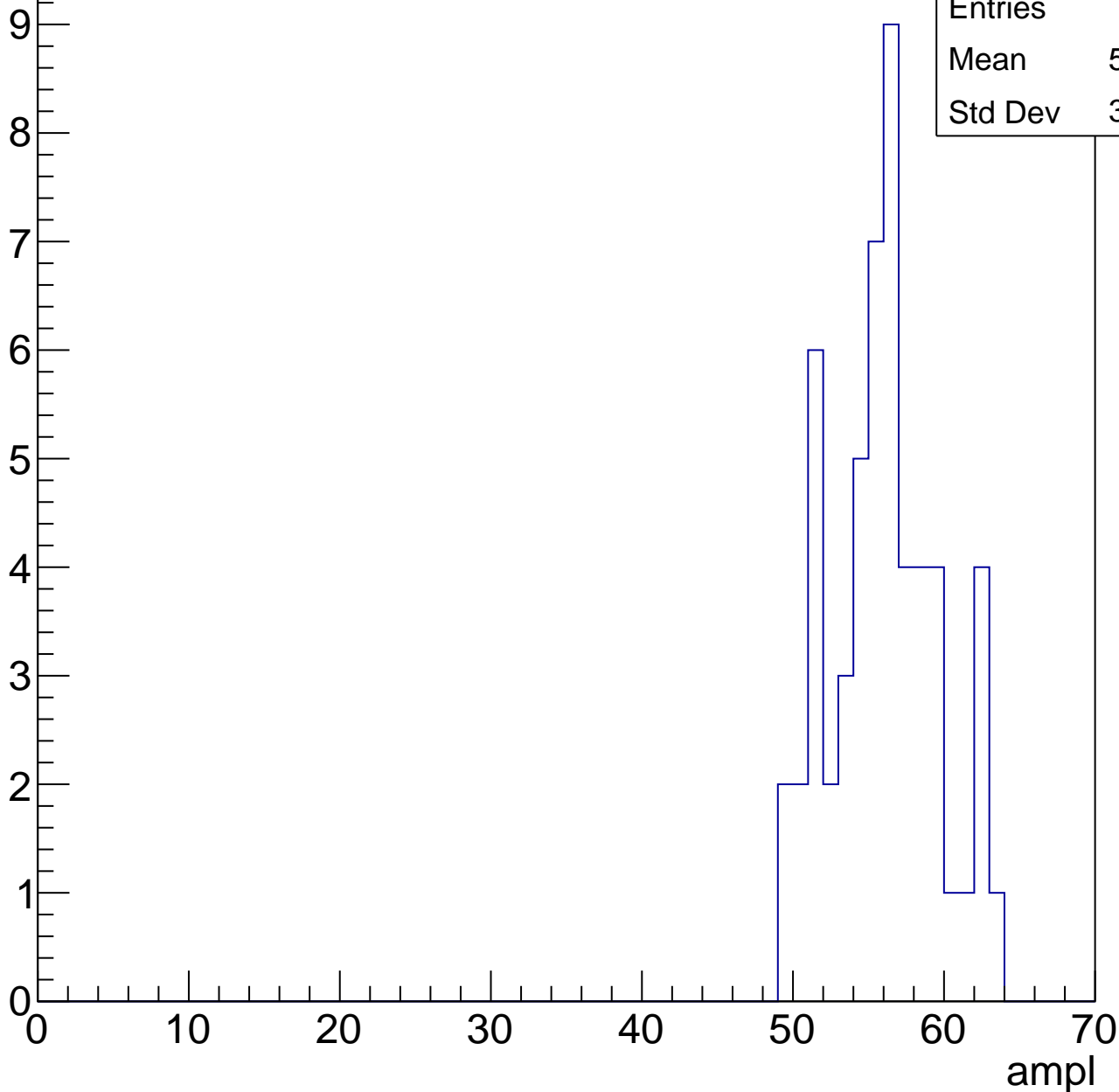


B1L102S, U22-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.53
Std Dev	3.526

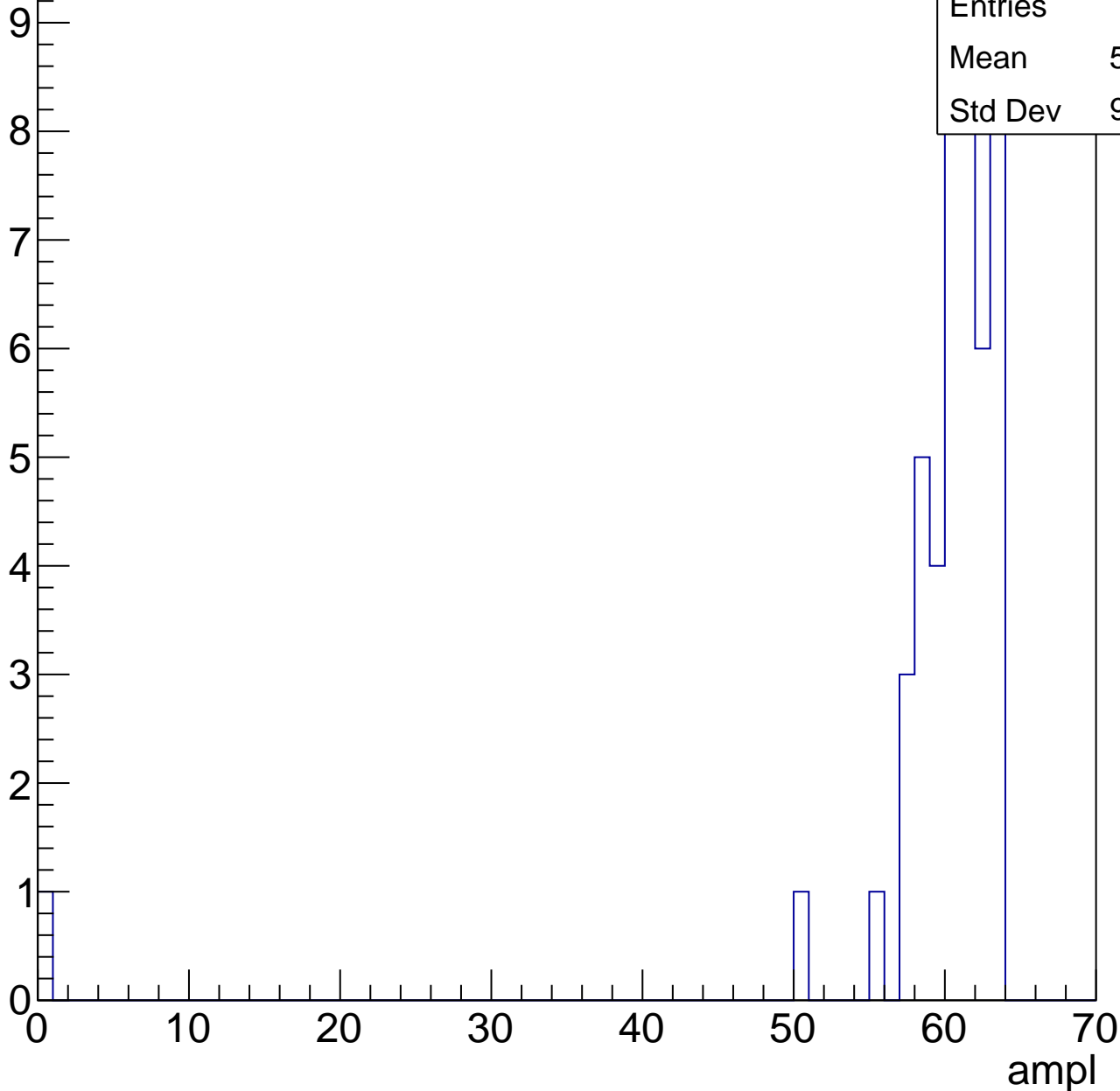


B1L102S, U22-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.87
Std Dev	9.017



B1L102S, U22-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

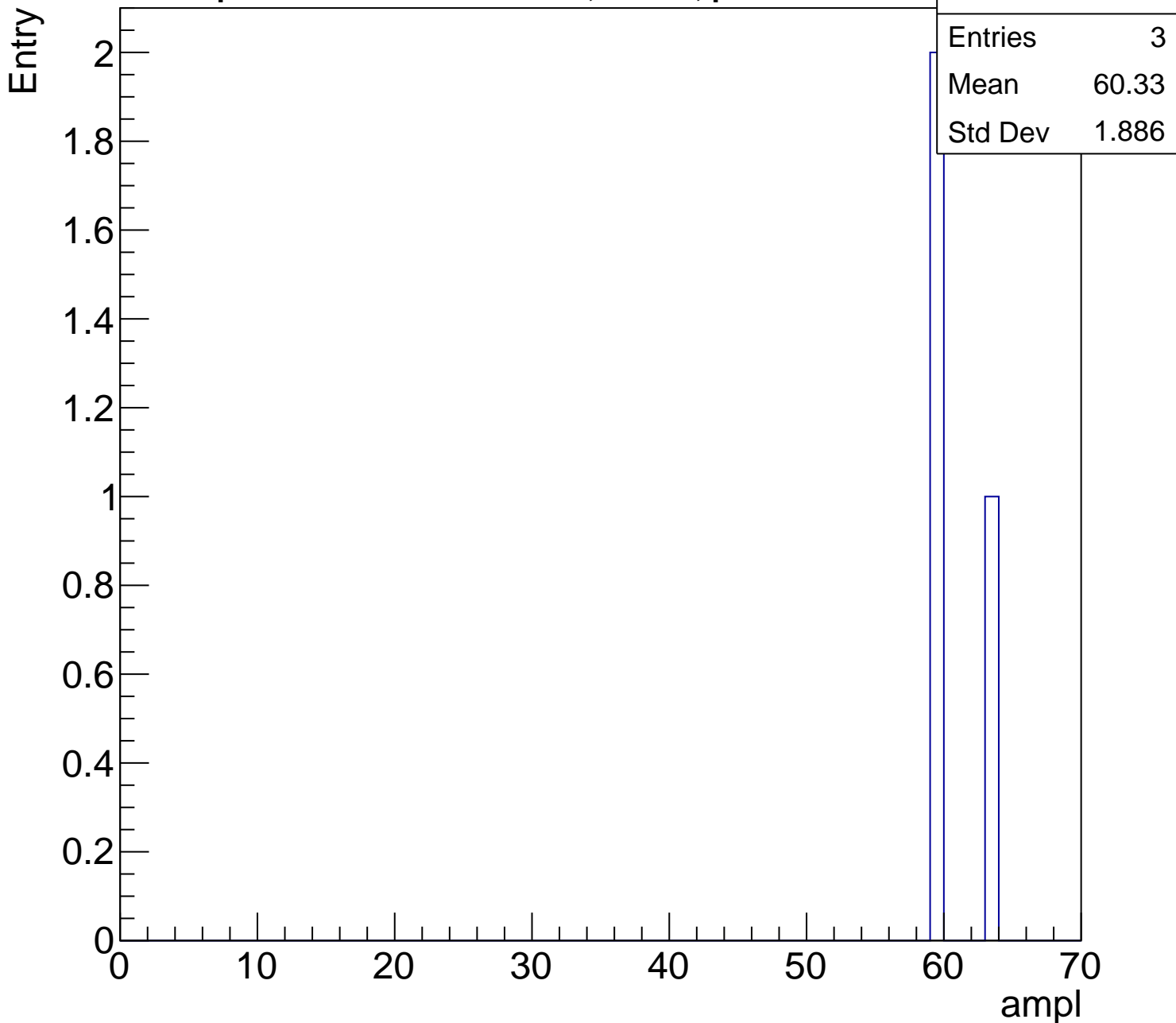
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	60.33
Std Dev	1.886

0 10 20 30 40 50 60 70

ampl



B1L102S, U22-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

B1L102S, U22-ch27, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	56
Mean	27.64
Std Dev	2.949

Gaus mean : 29.1856

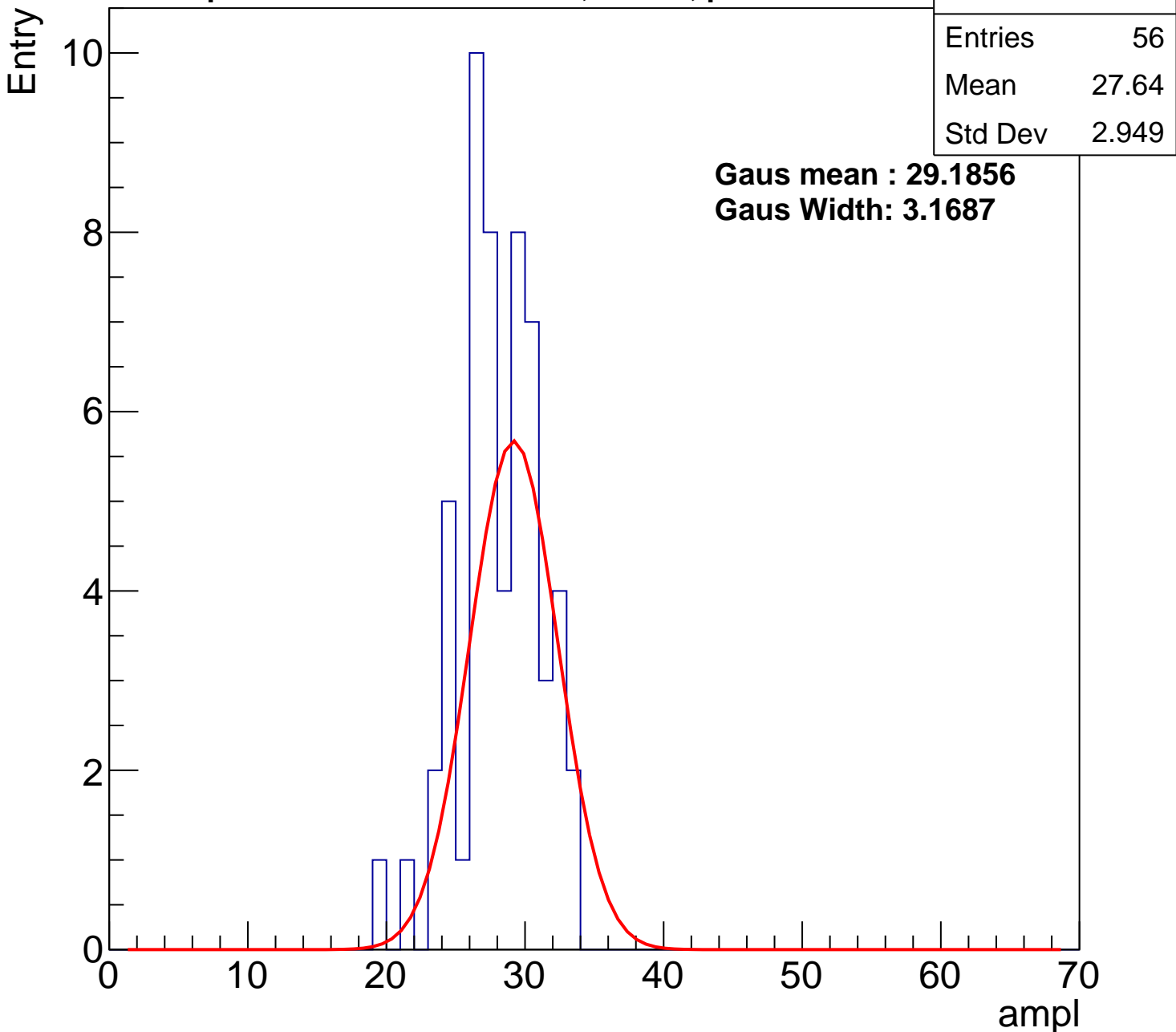
Gaus Width: 3.1687

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch27, adc1

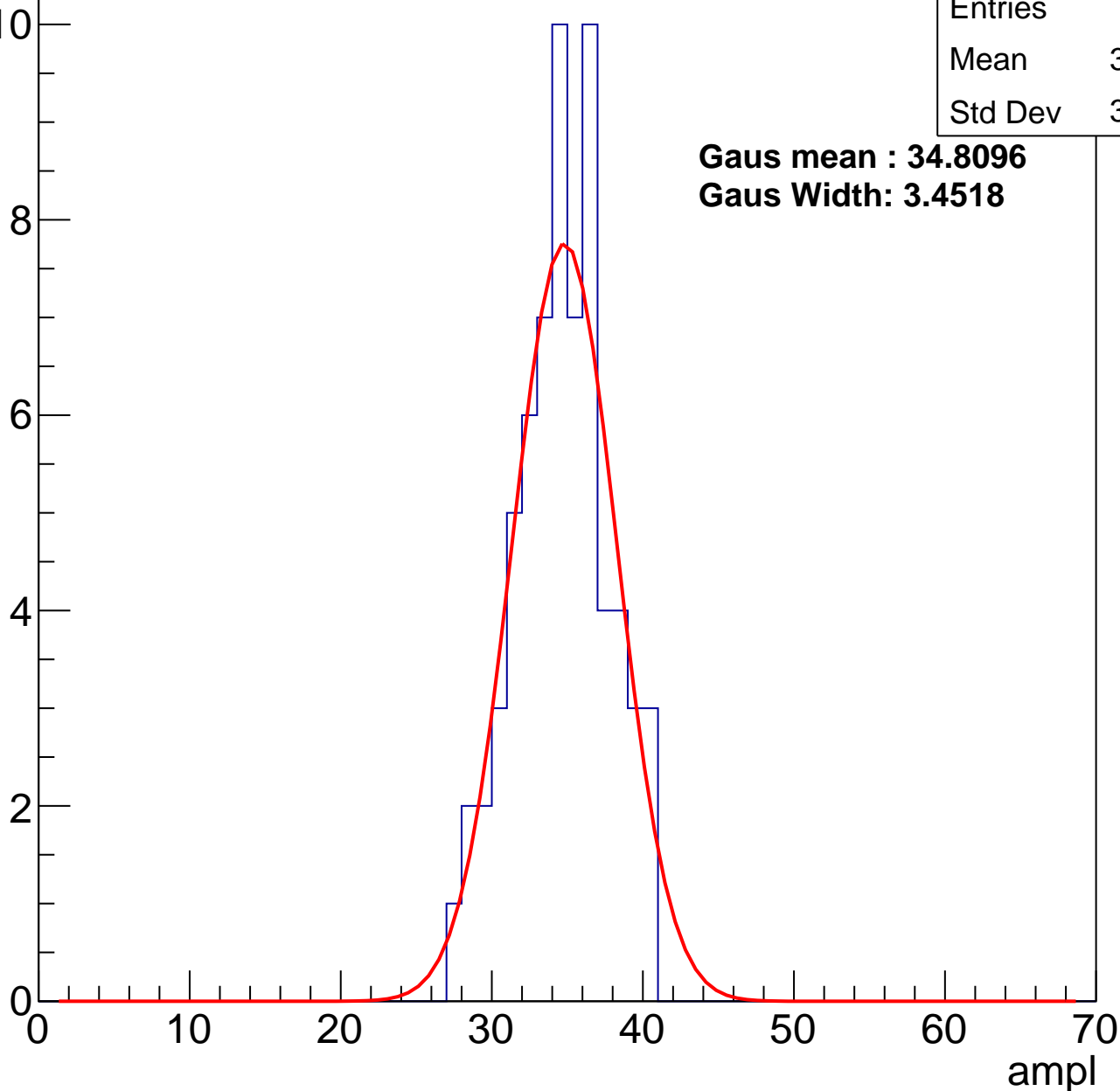
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.19
Std Dev	3.048

Gaus mean : 34.8096

Gaus Width: 3.4518



B1L102S, U22-ch27, adc2

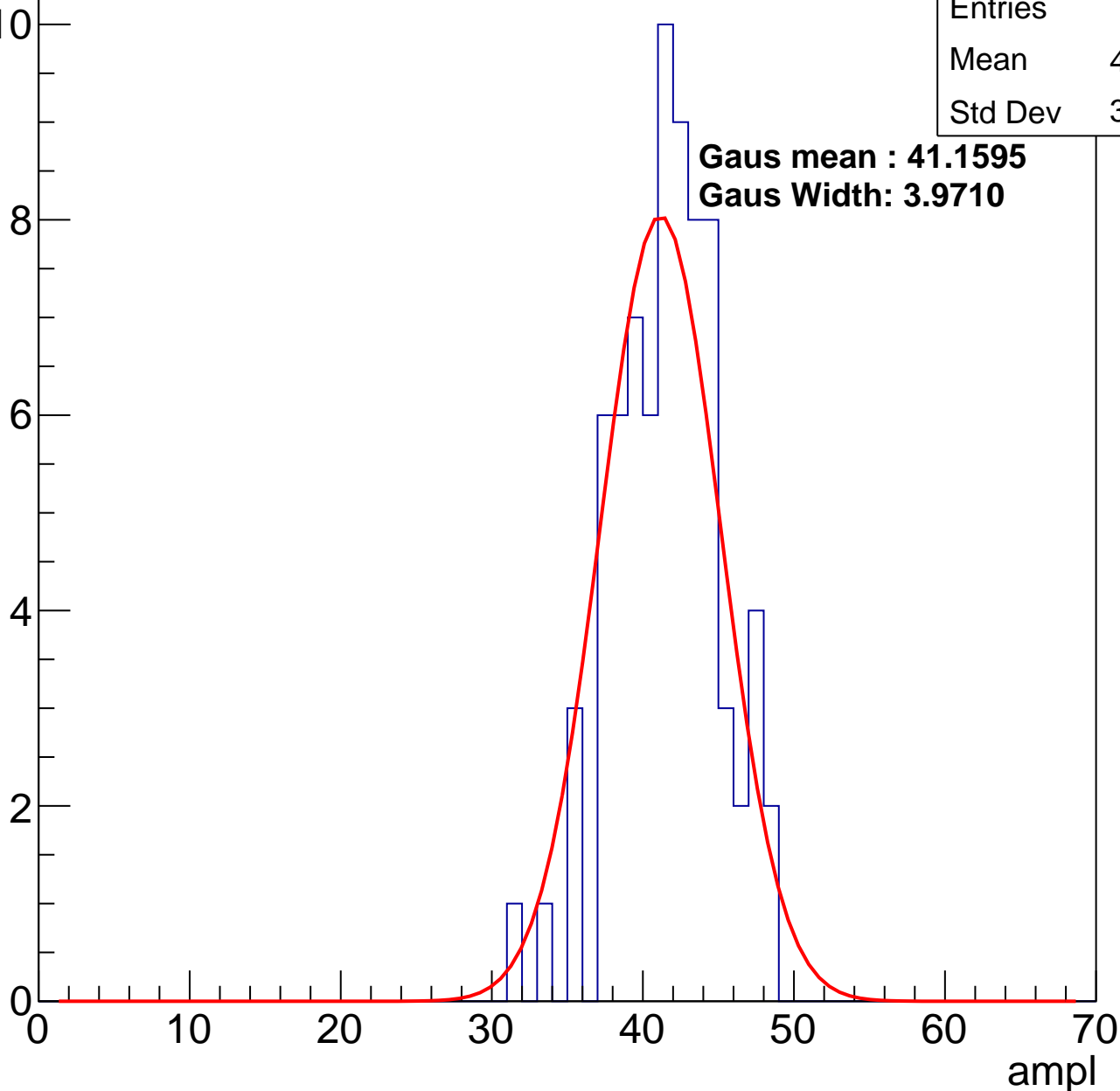
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	41.14
Std Dev	3.455

Gaus mean : 41.1595

Gaus Width: 3.9710

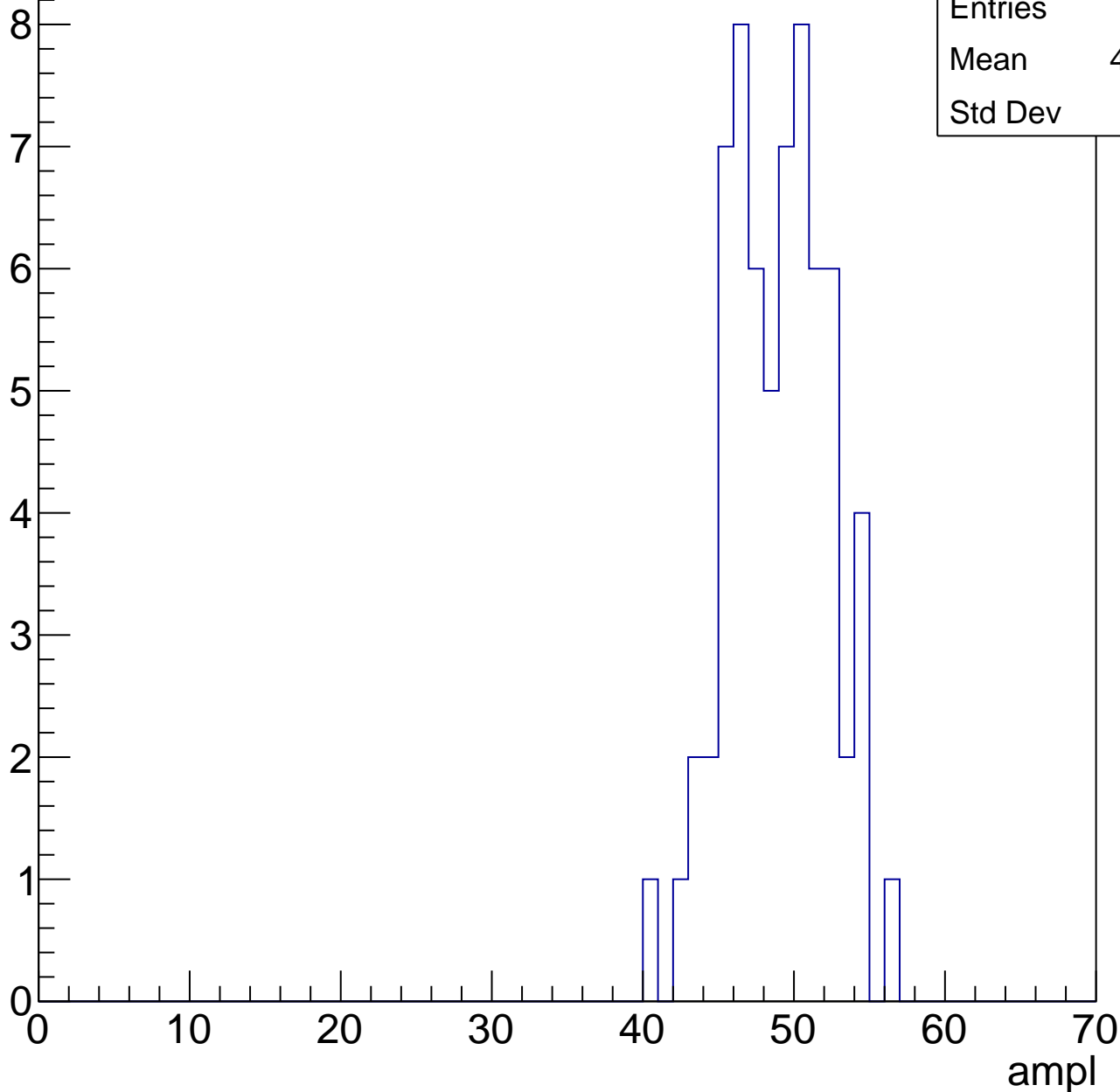


B1L102S, U22-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.48
Std Dev	3.29

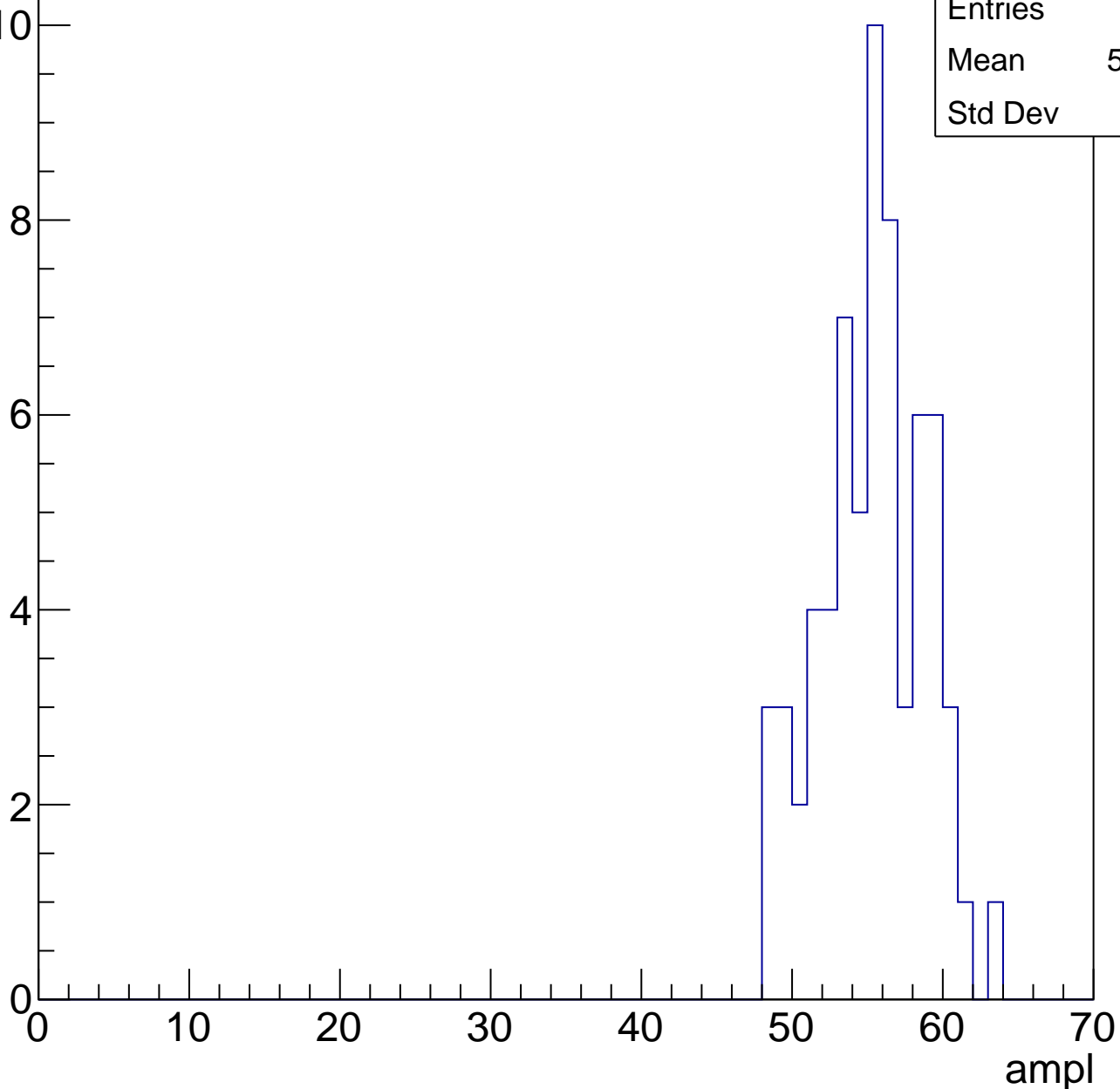


B1L102S, U22-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	54.83
Std Dev	3.44

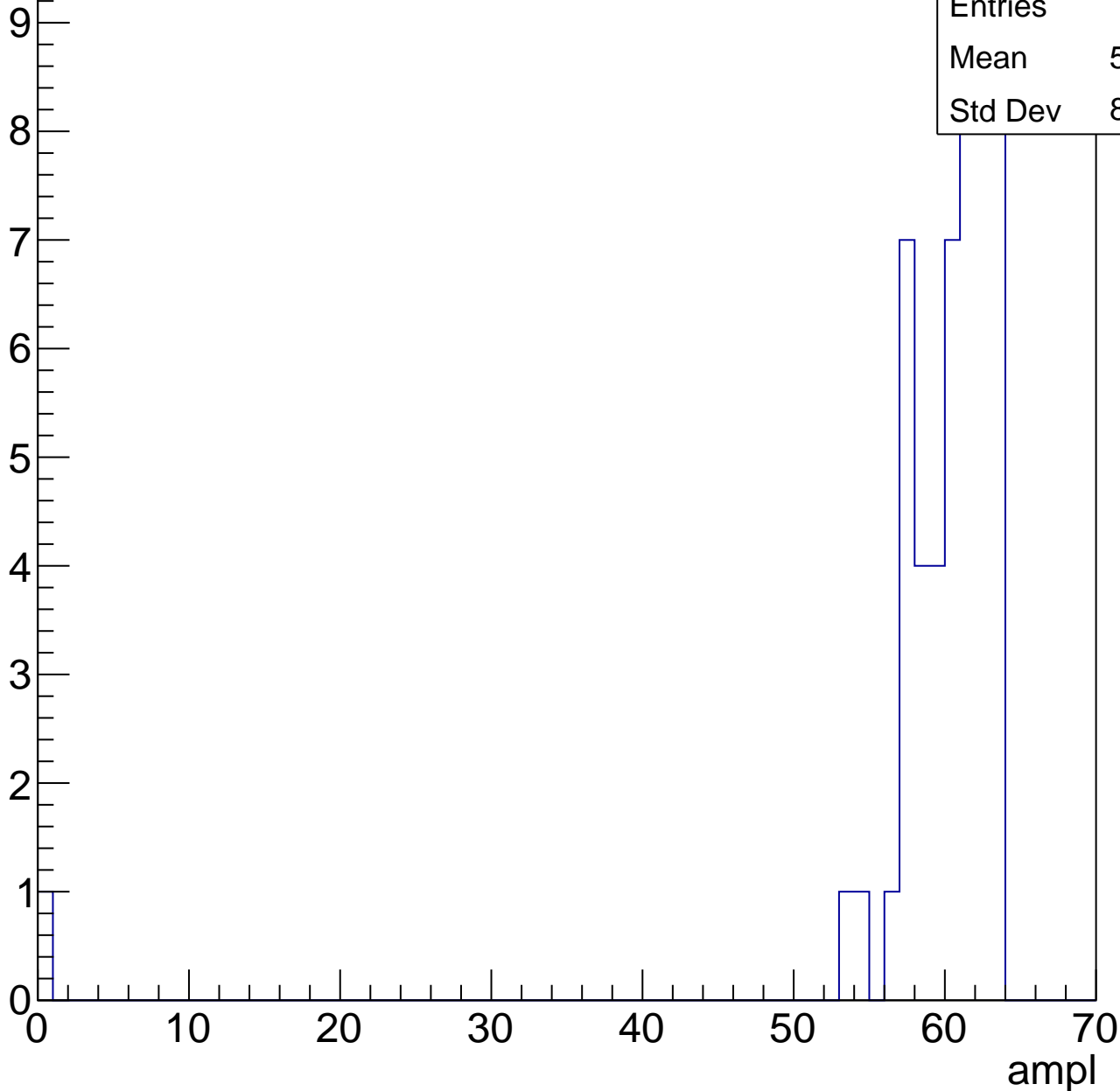


B1L102S, U22-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.87
Std Dev	8.587



B1L102S, U22-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch28, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	29.65
Std Dev	3.454

Gaus mean : 30.1574

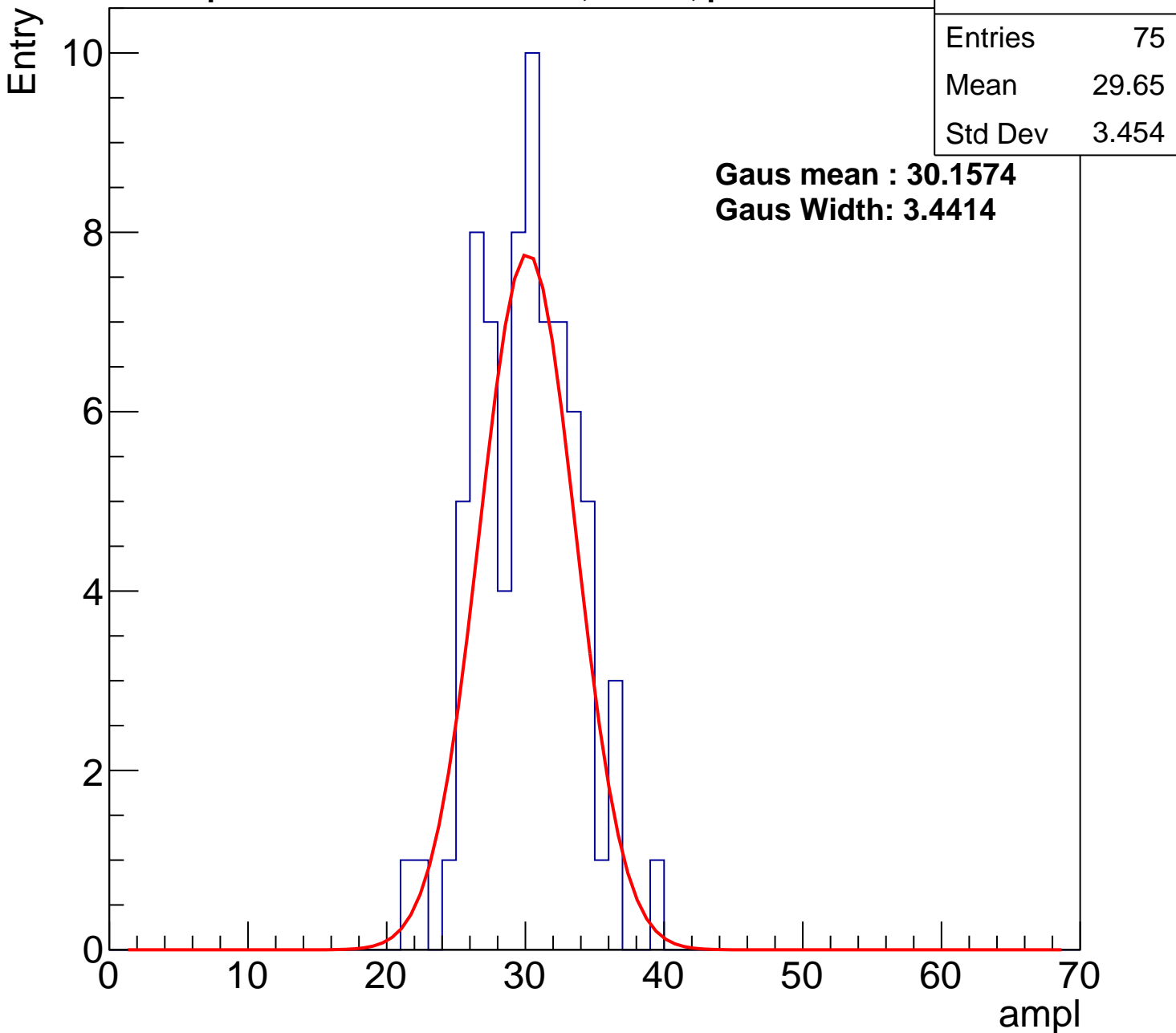
Gaus Width: 3.4414

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch28, adc1

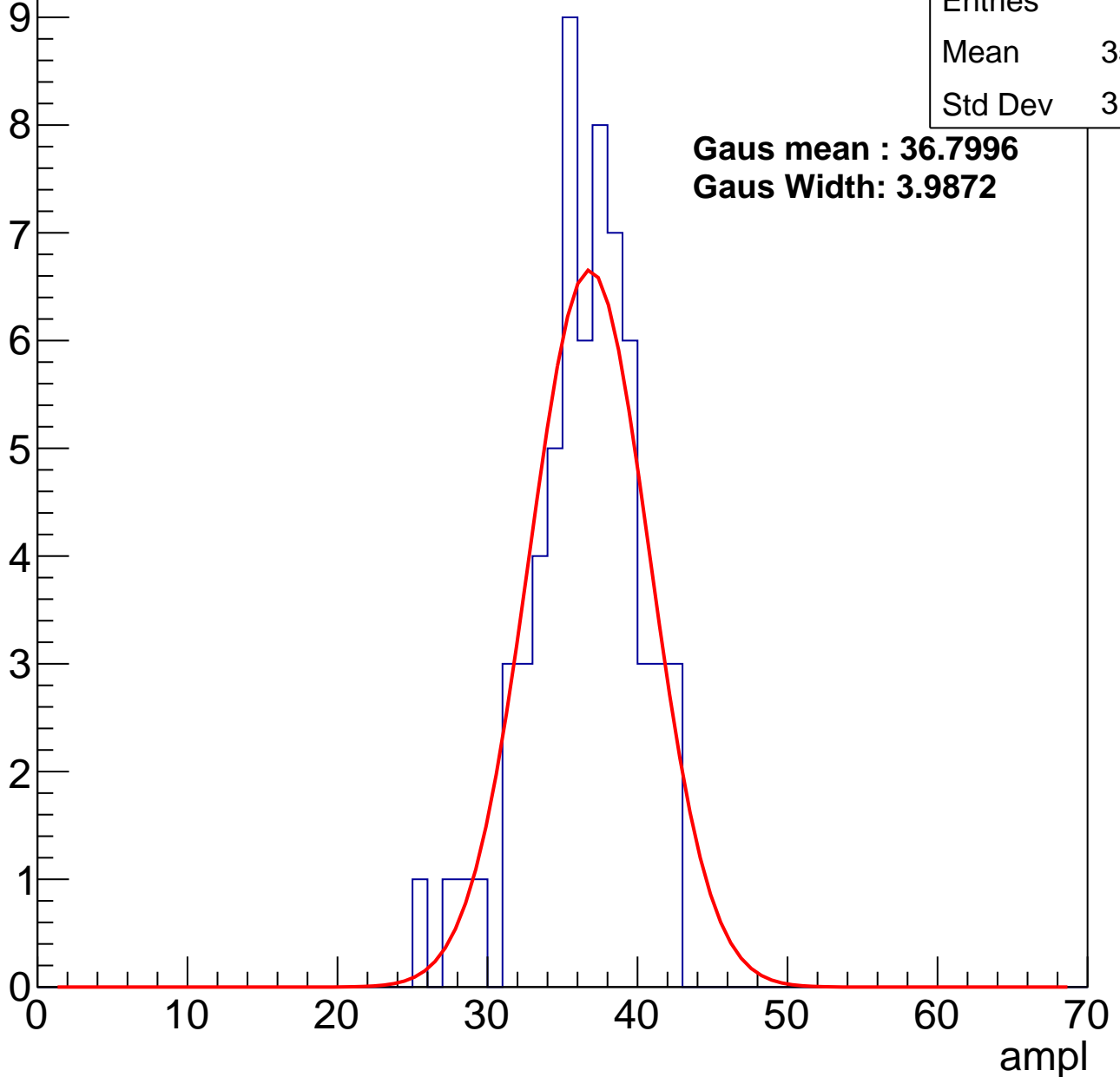
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.88
Std Dev	3.586

Gaus mean : 36.7996

Gaus Width: 3.9872



B1L102S, U22-ch28, adc2

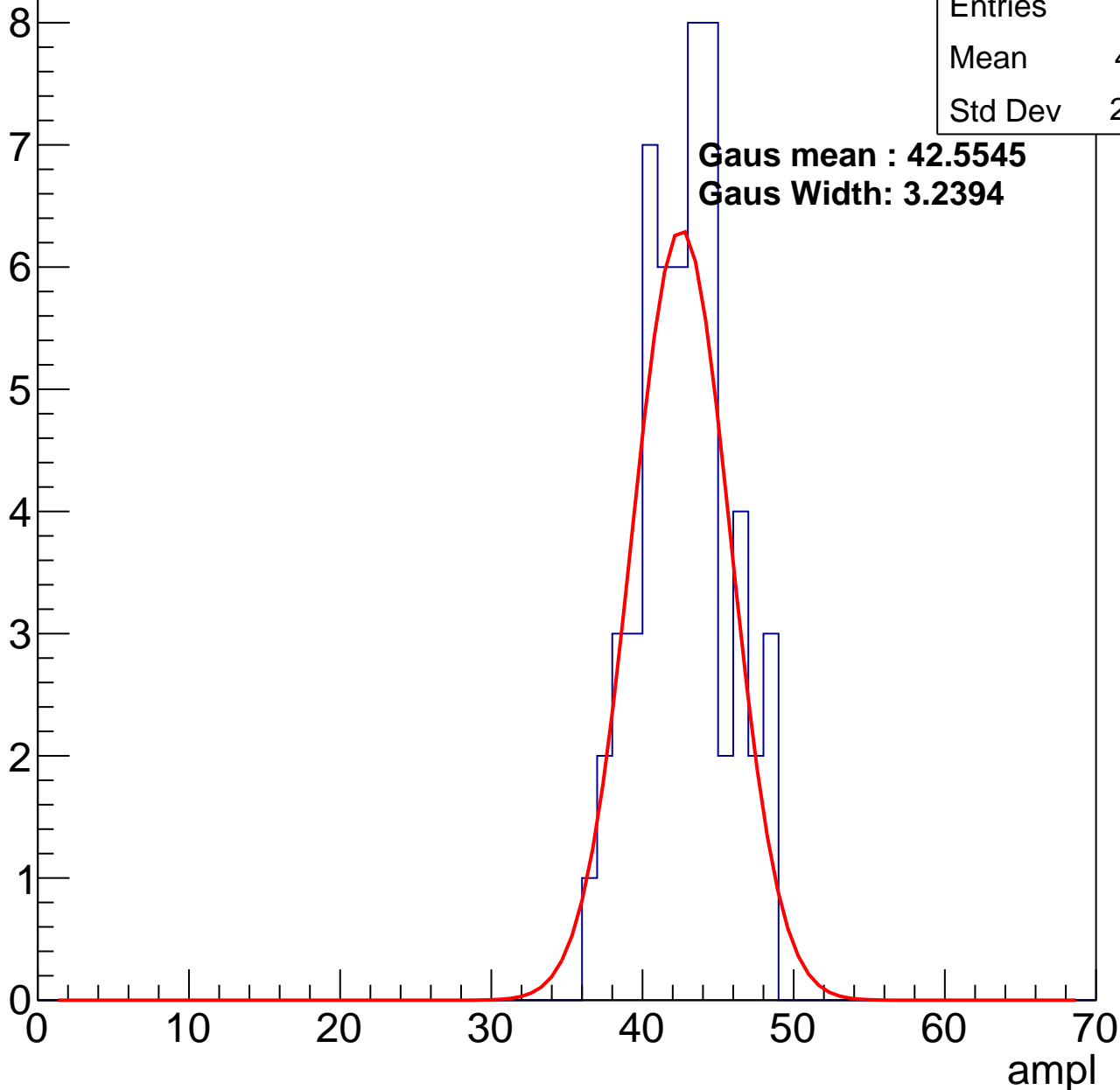
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.31
Std Dev	2.922

Gaus mean : 42.5545

Gaus Width: 3.2394



B1L102S, U22-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

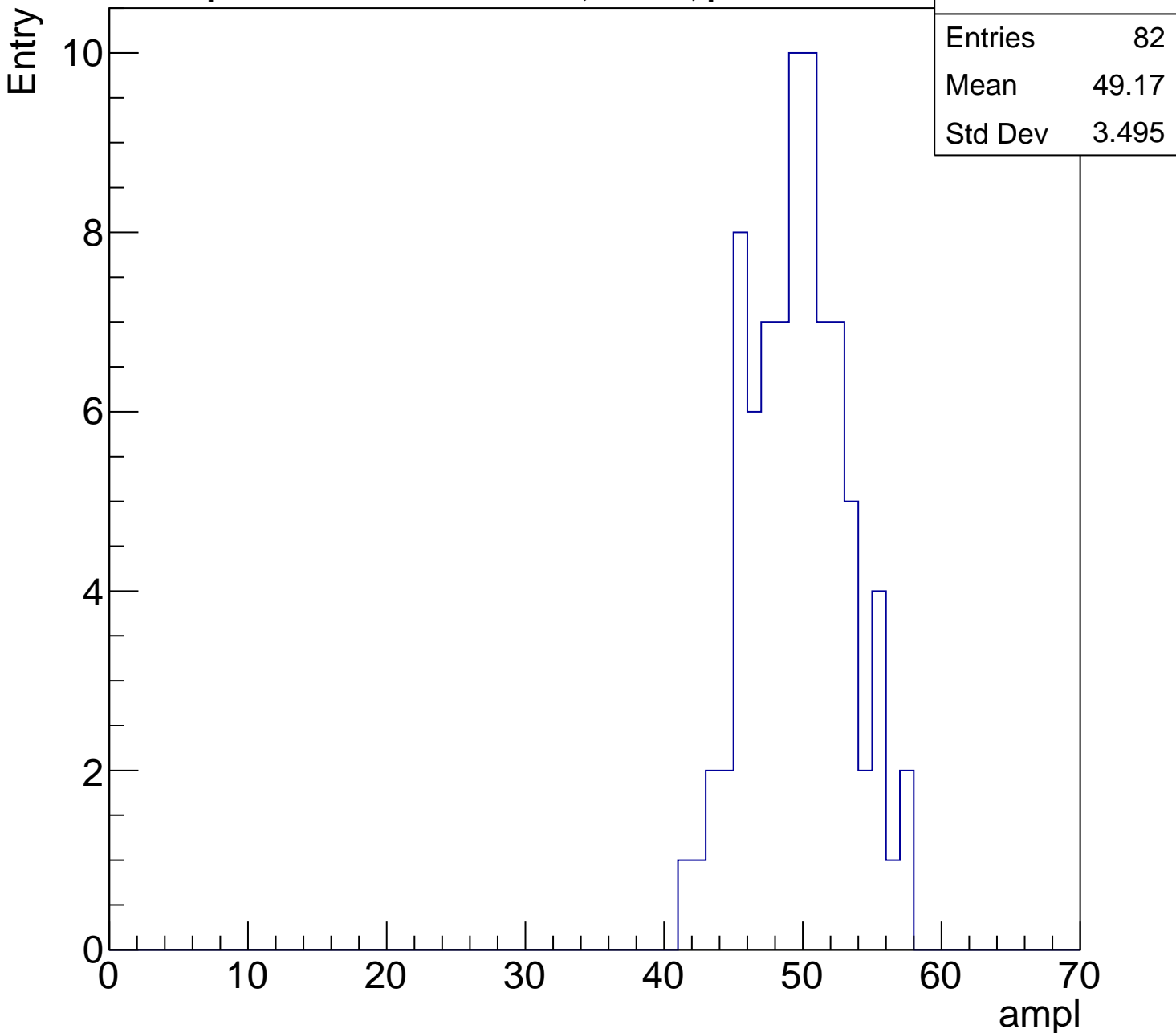
Entries	82
Mean	49.17
Std Dev	3.495

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

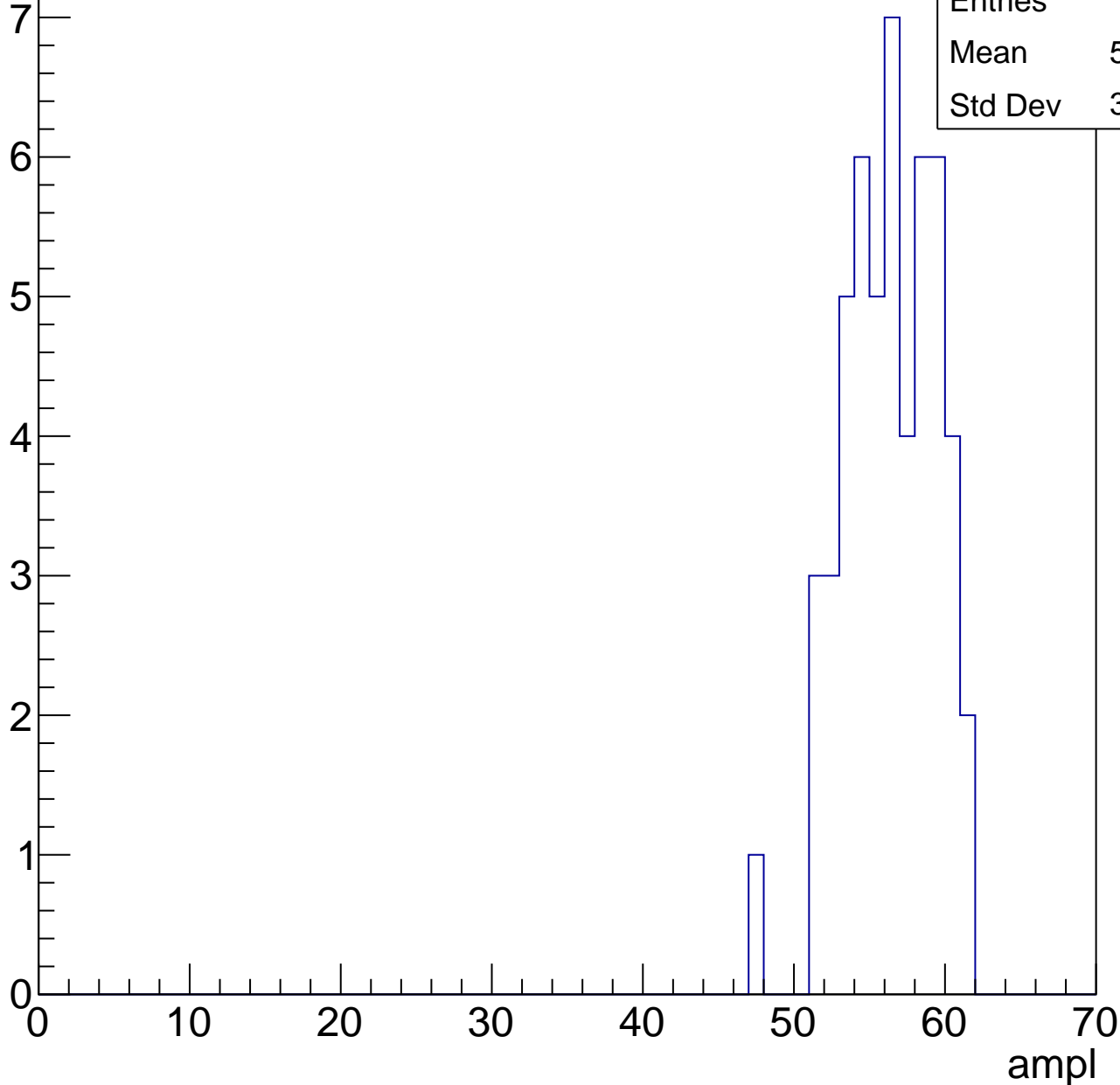


B1L102S, U22-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.85
Std Dev	3.015

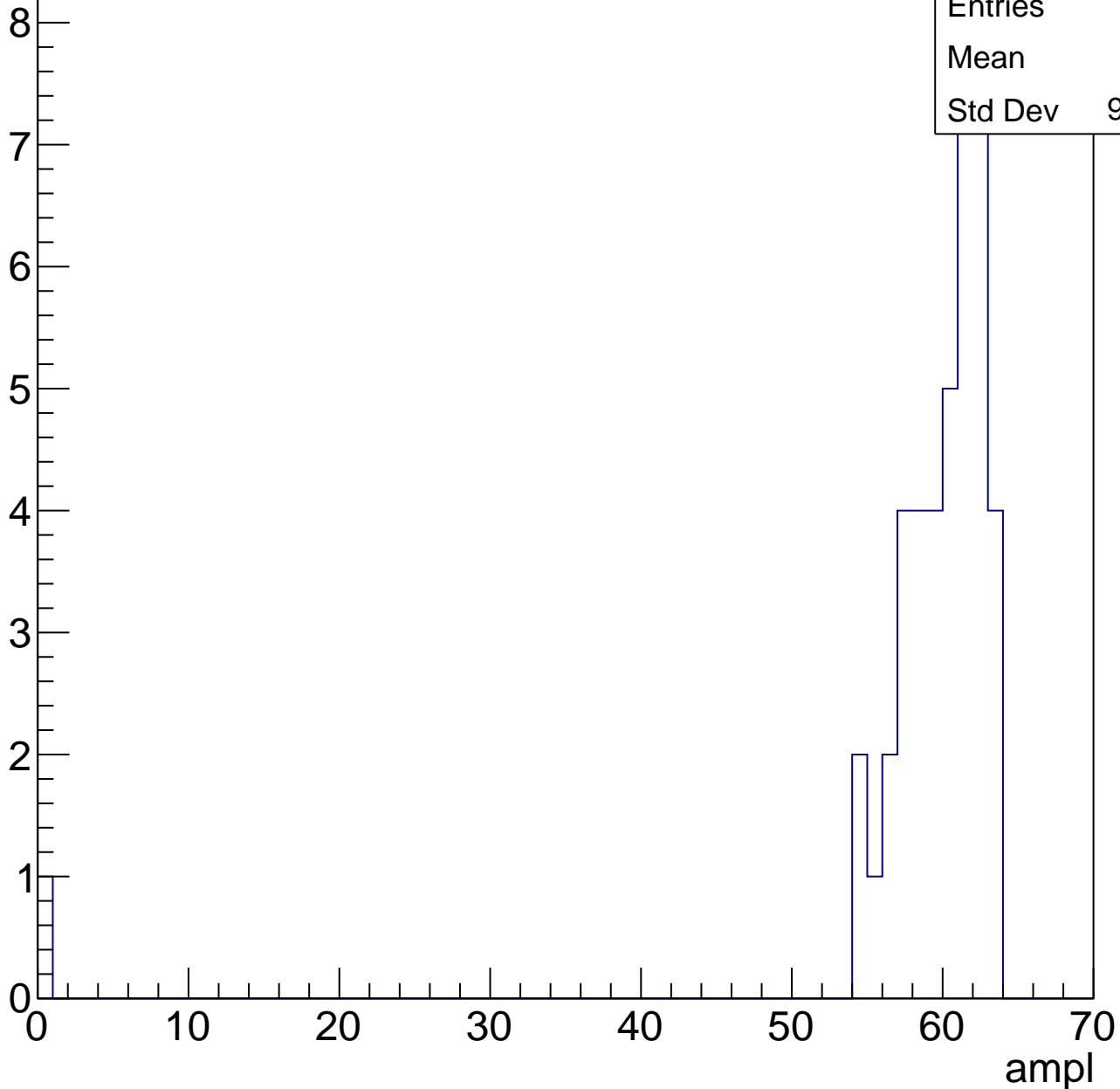


B1L102S, U22-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.3
Std Dev	9.322

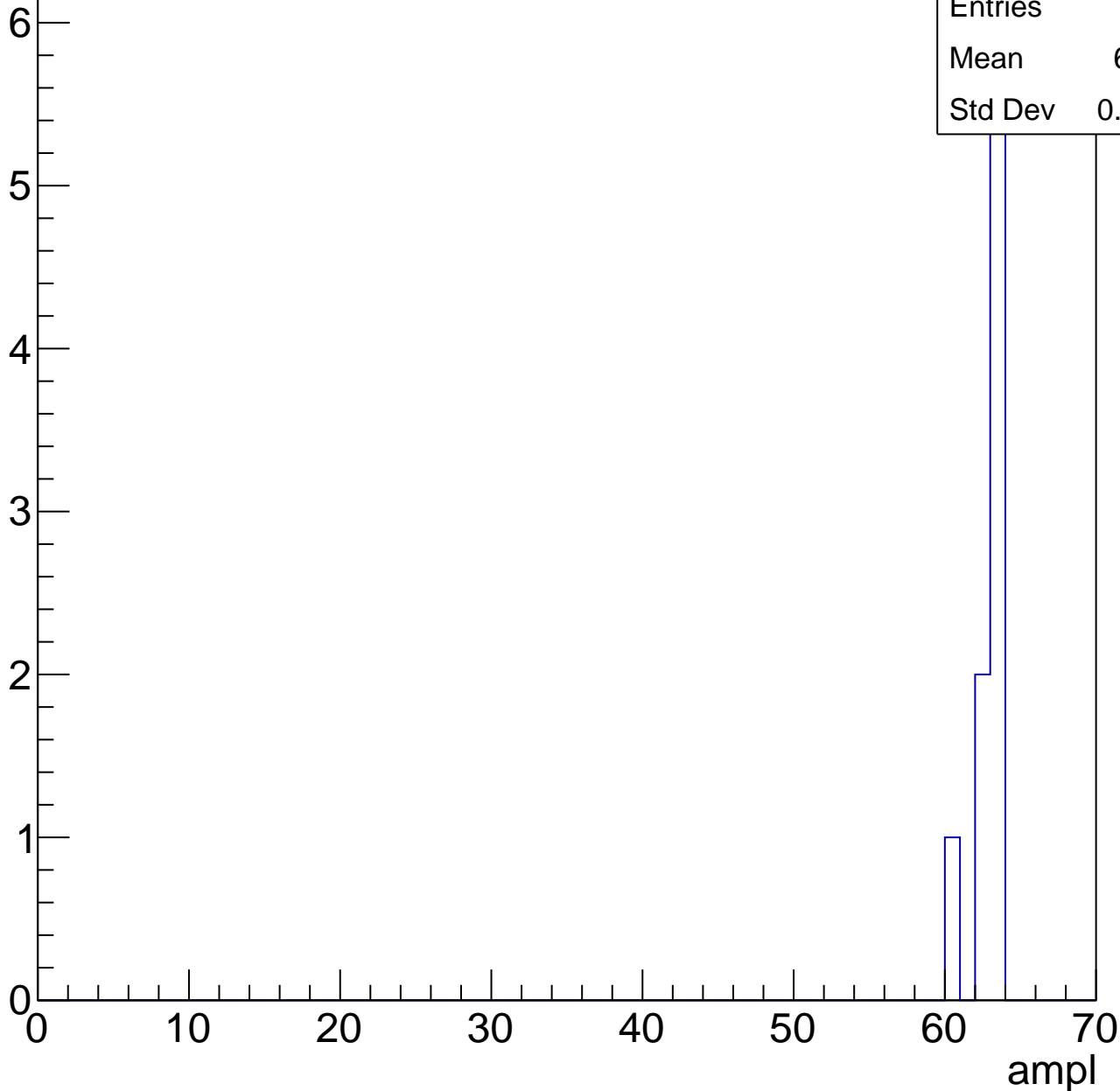


B1L102S, U22-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62.44
Std Dev	0.9558



B1L102S, U22-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch29, adc0

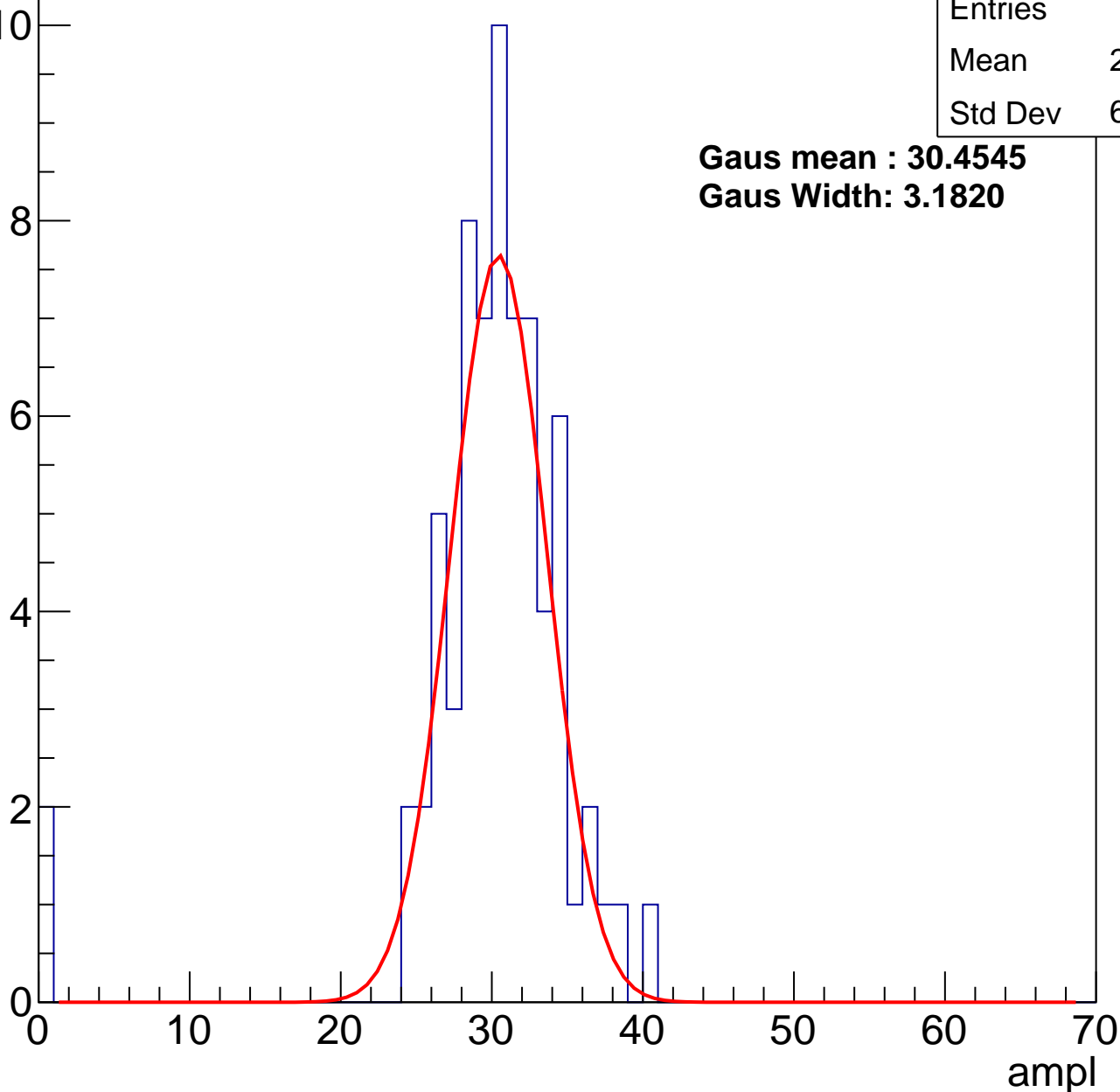
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.49
Std Dev	6.052

Gaus mean : 30.4545

Gaus Width: 3.1820



B1L102S, U22-ch29, adc1

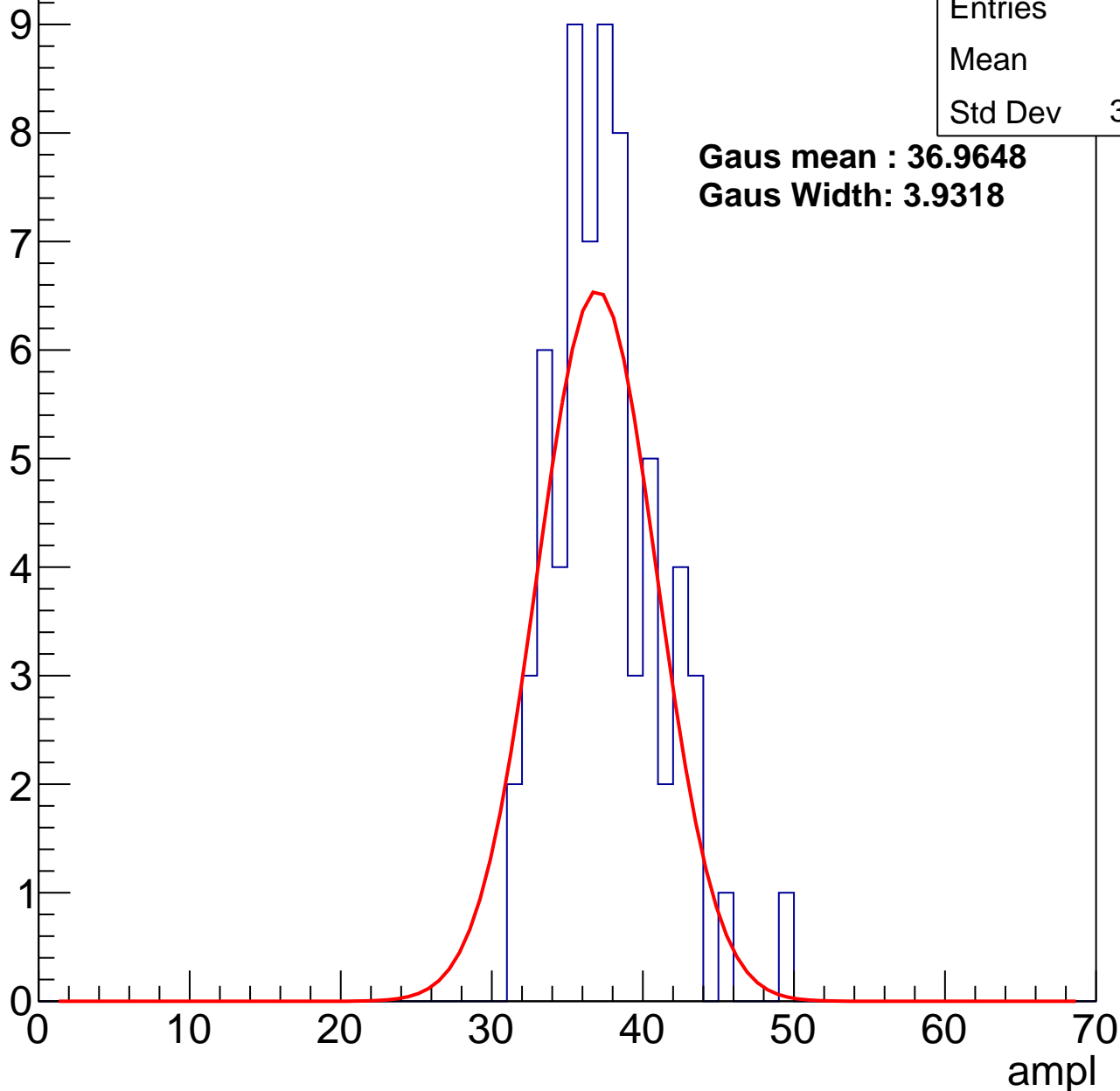
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	37.1
Std Dev	3.529

Gaus mean : 36.9648

Gaus Width: 3.9318



B1L102S, U22-ch29, adc2

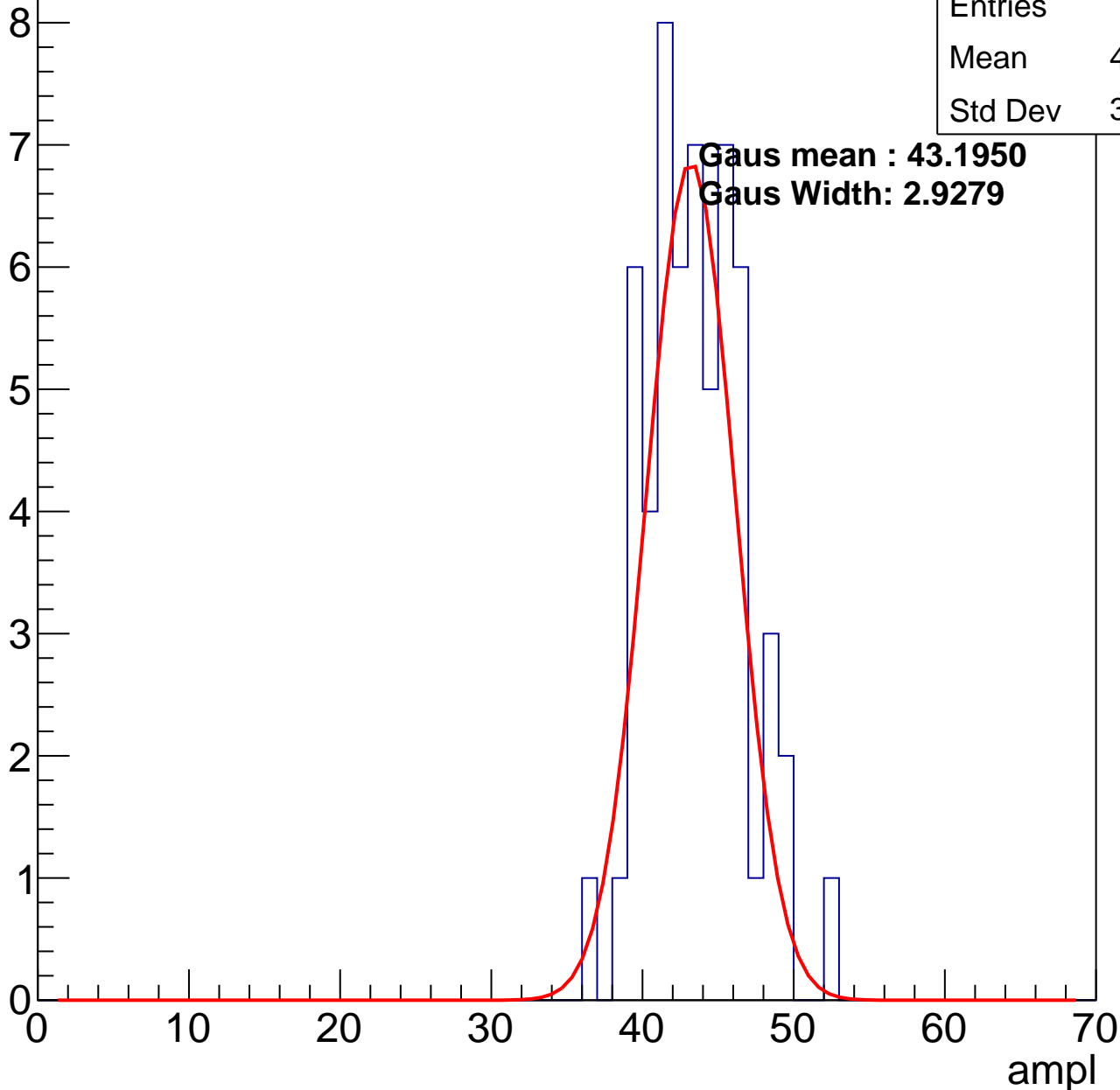
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.12
Std Dev	3.146

Gaus mean : 43.1950

Gaus Width: 2.9279

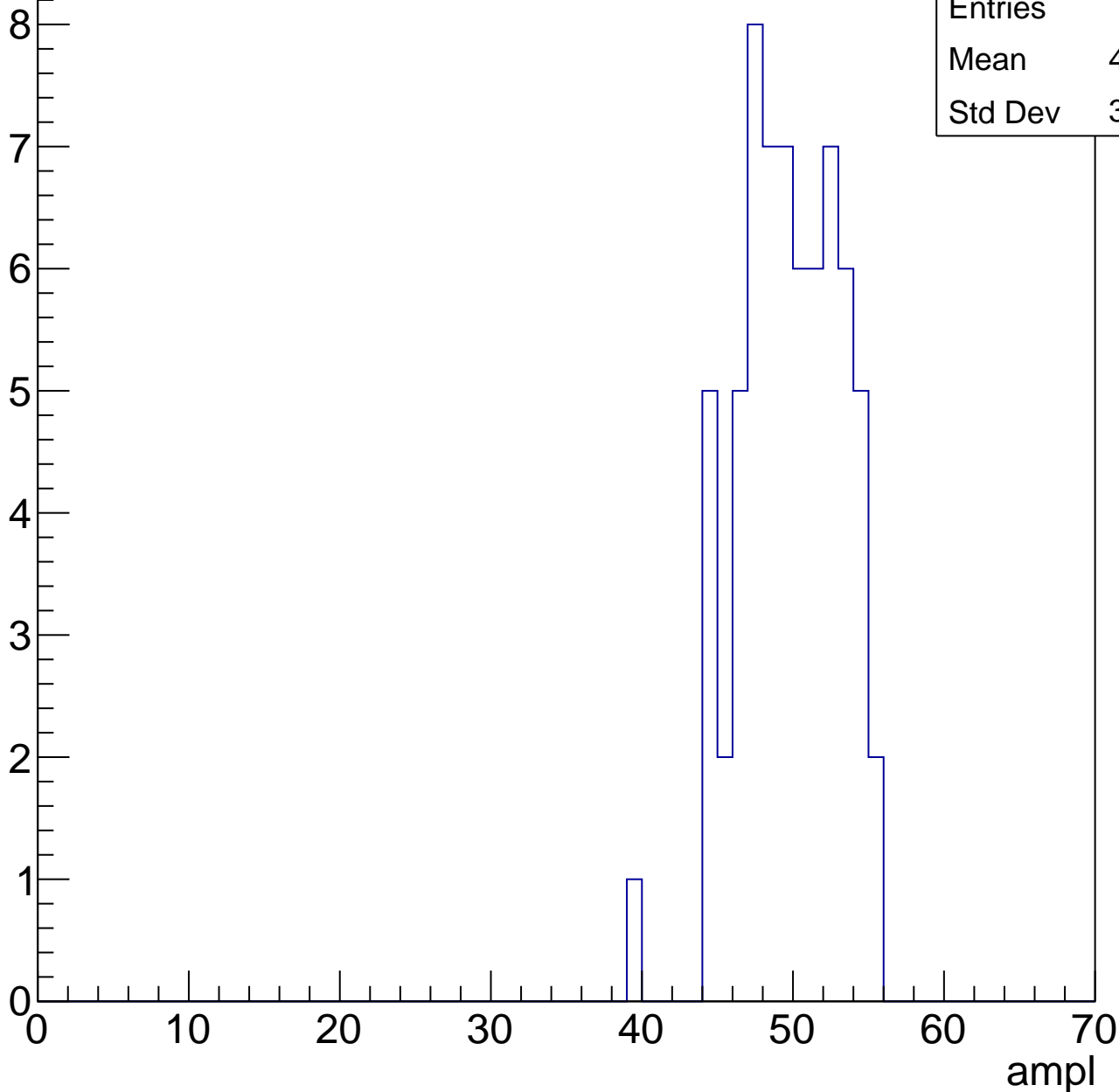


B1L102S, U22-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.28
Std Dev	3.282

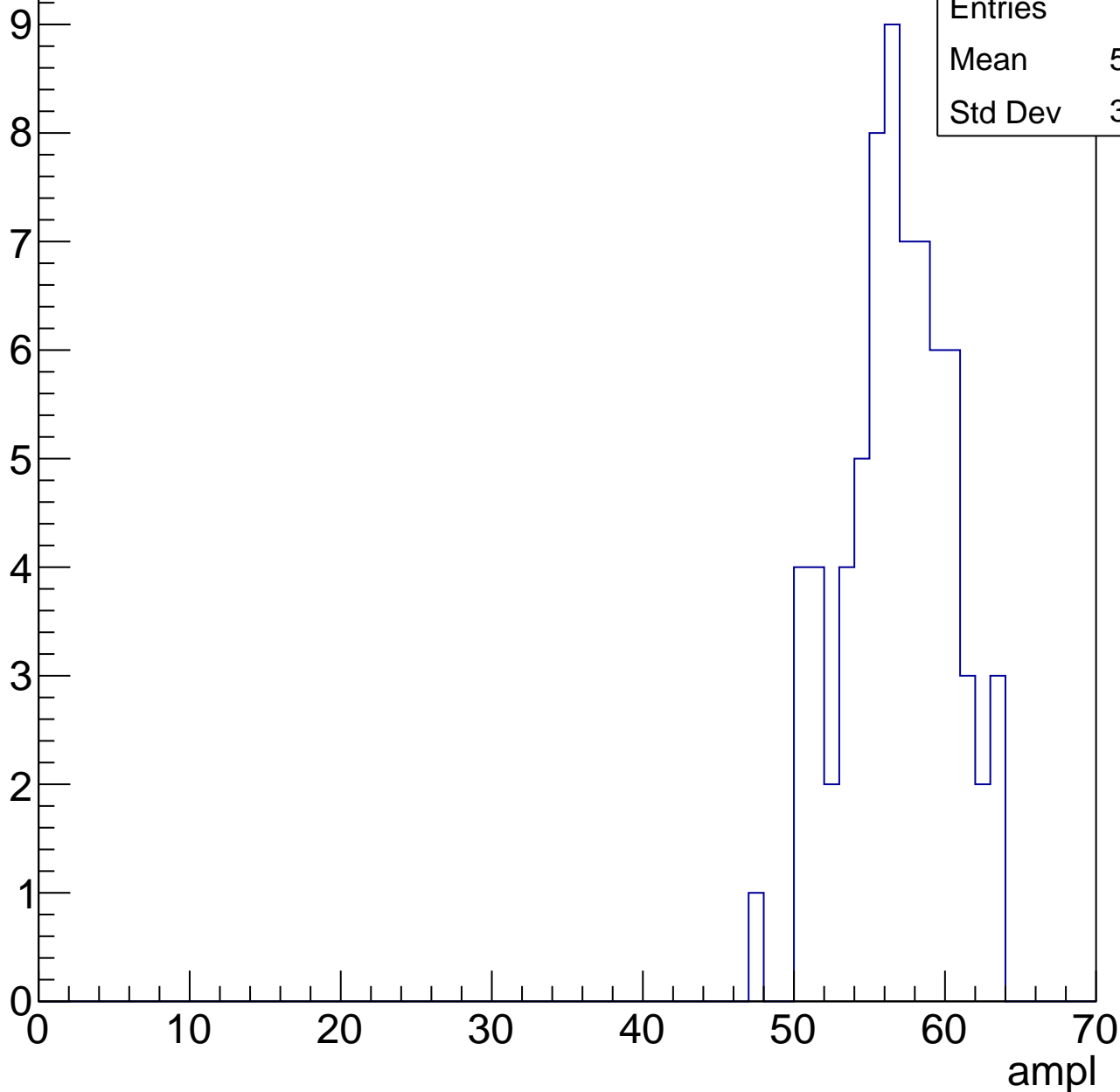


B1L102S, U22-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	56.28
Std Dev	3.549

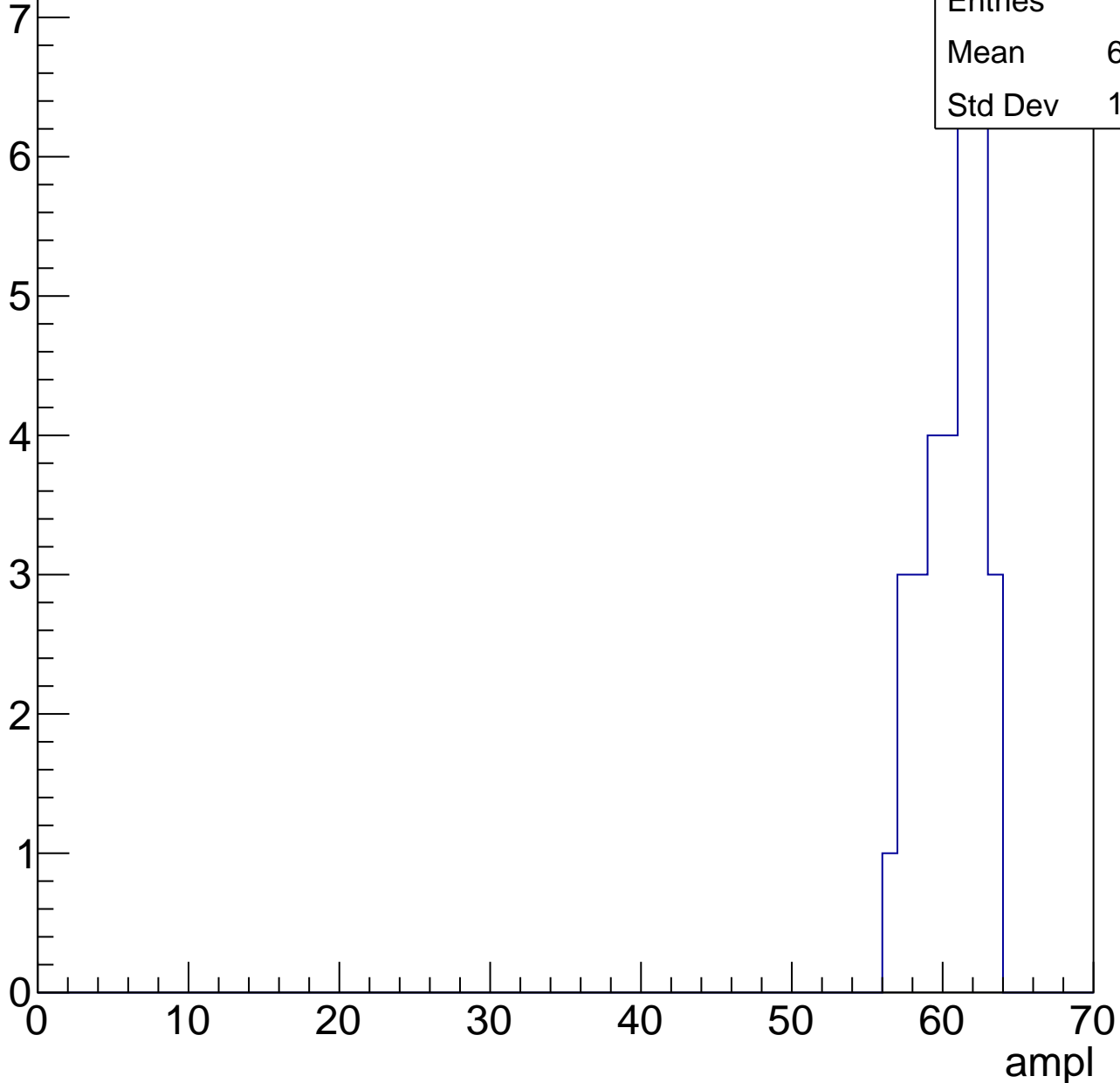


B1L102S, U22-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

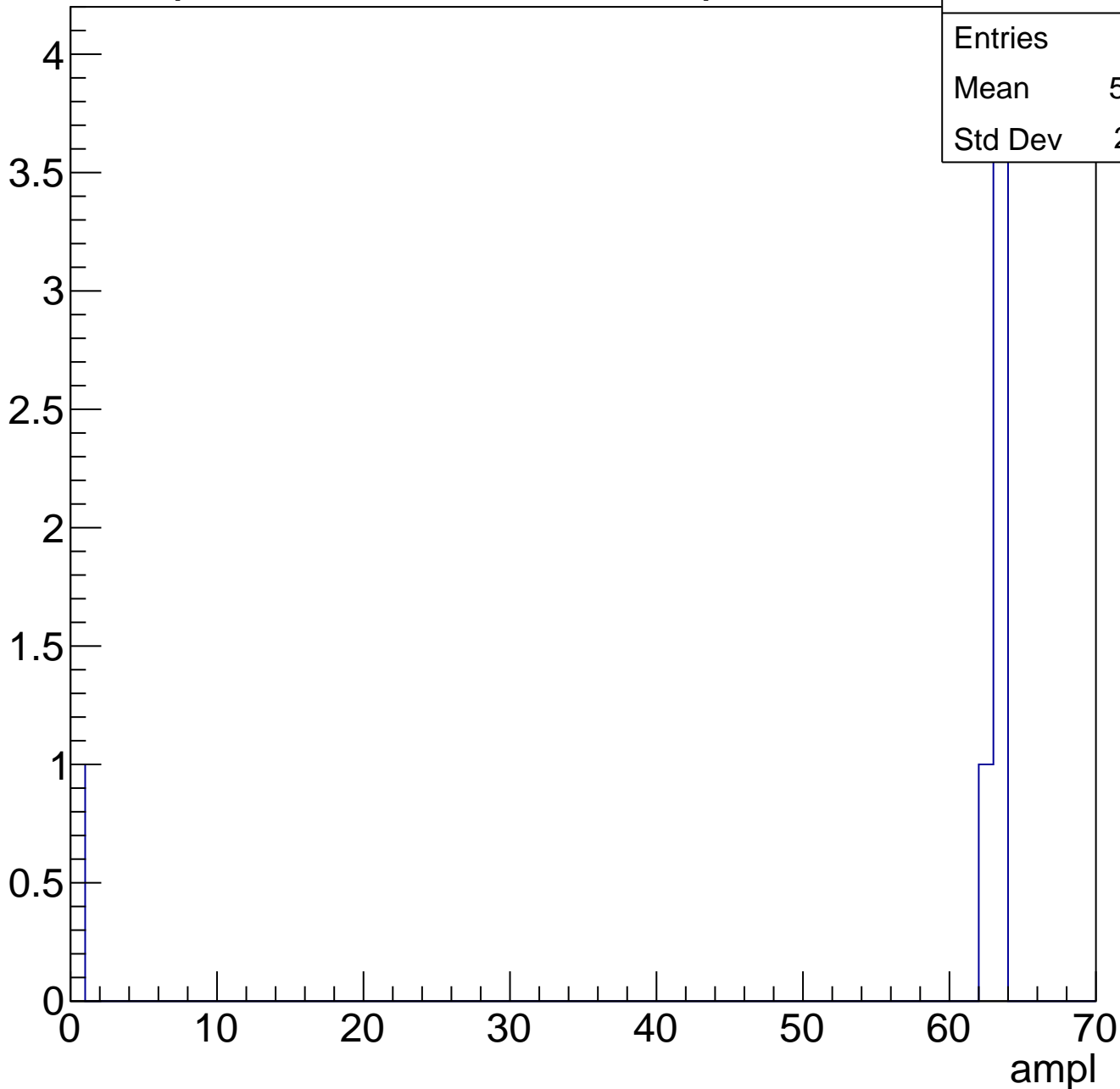
Entries	32
Mean	60.22
Std Dev	1.932



B1L102S, U22-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch30, adc0

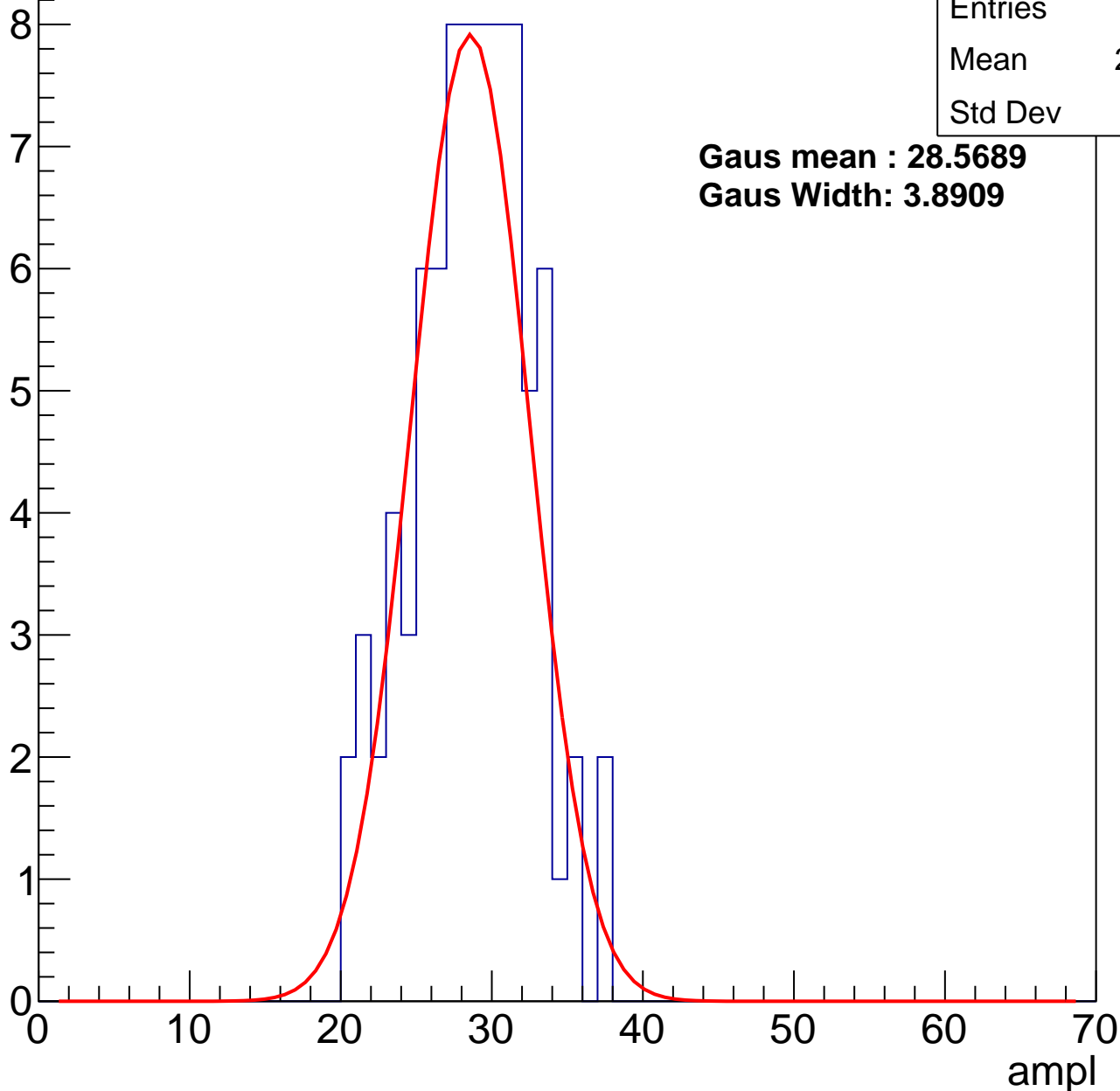
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.21
Std Dev	3.85

Gaus mean : 28.5689

Gaus Width: 3.8909



B1L102S, U22-ch30, adc1

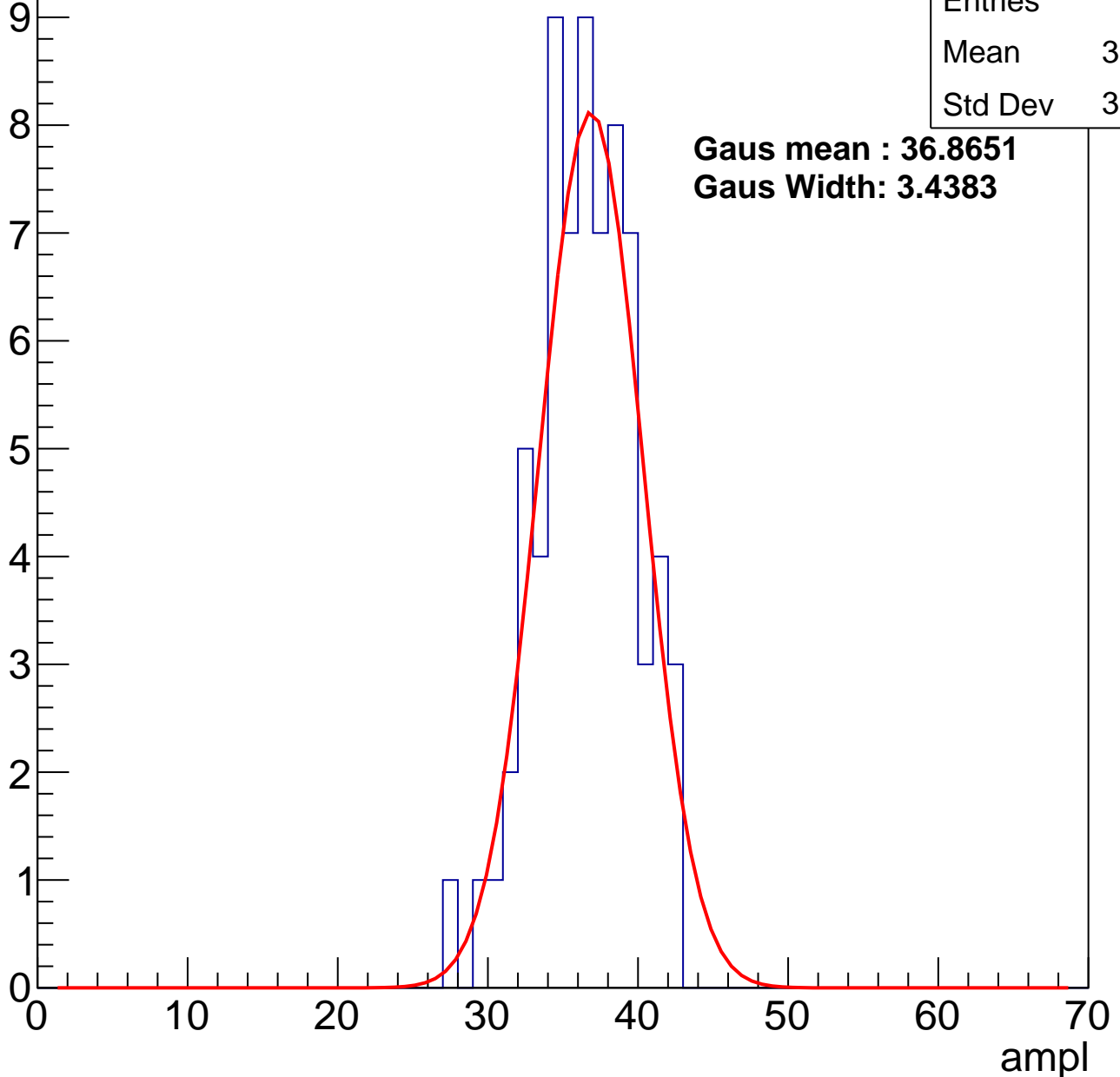
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.07
Std Dev	3.212

Gaus mean : 36.8651

Gaus Width: 3.4383



B1L102S, U22-ch30, adc2

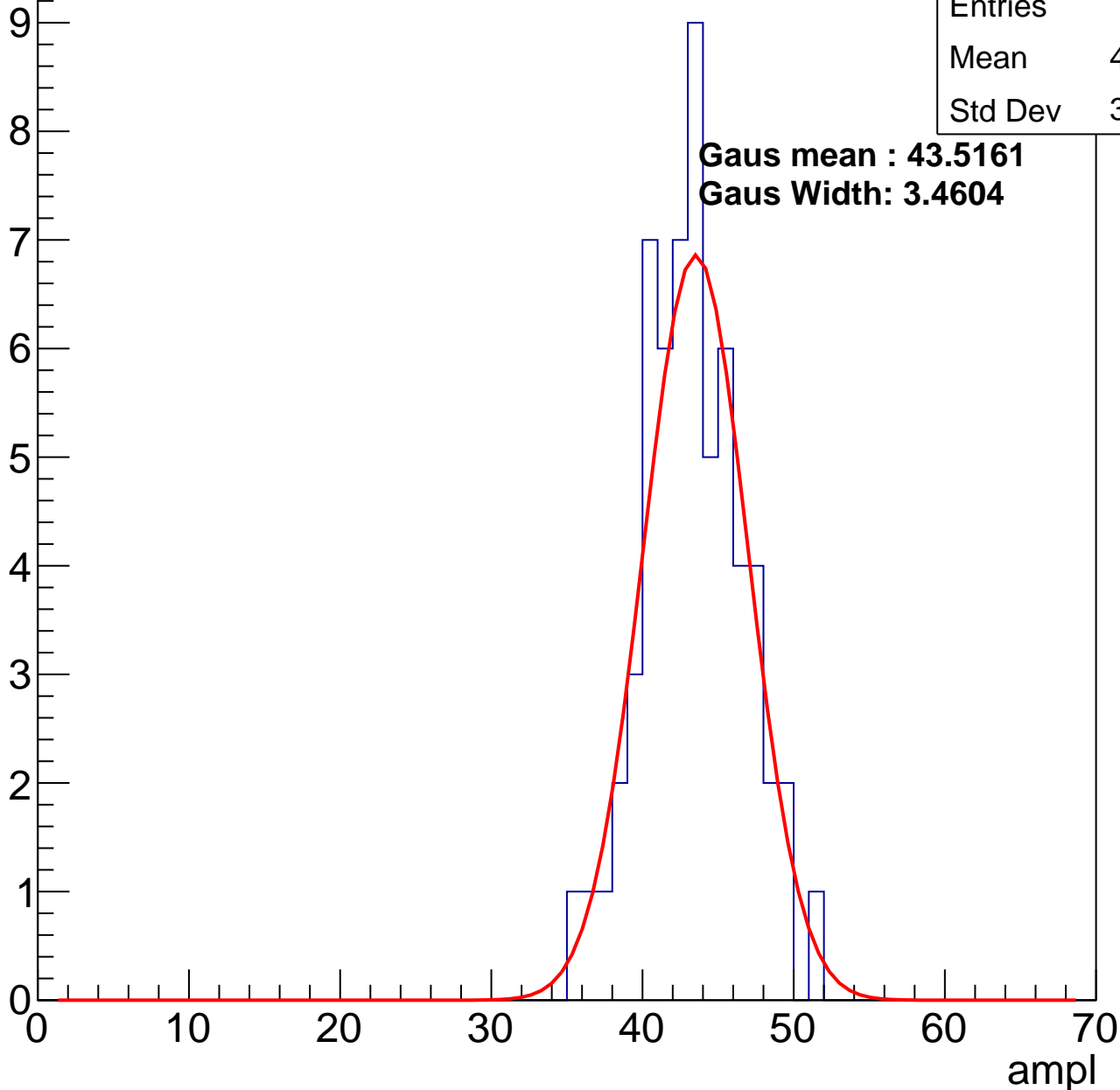
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.87
Std Dev	3.277

Gaus mean : 43.5161

Gaus Width: 3.4604

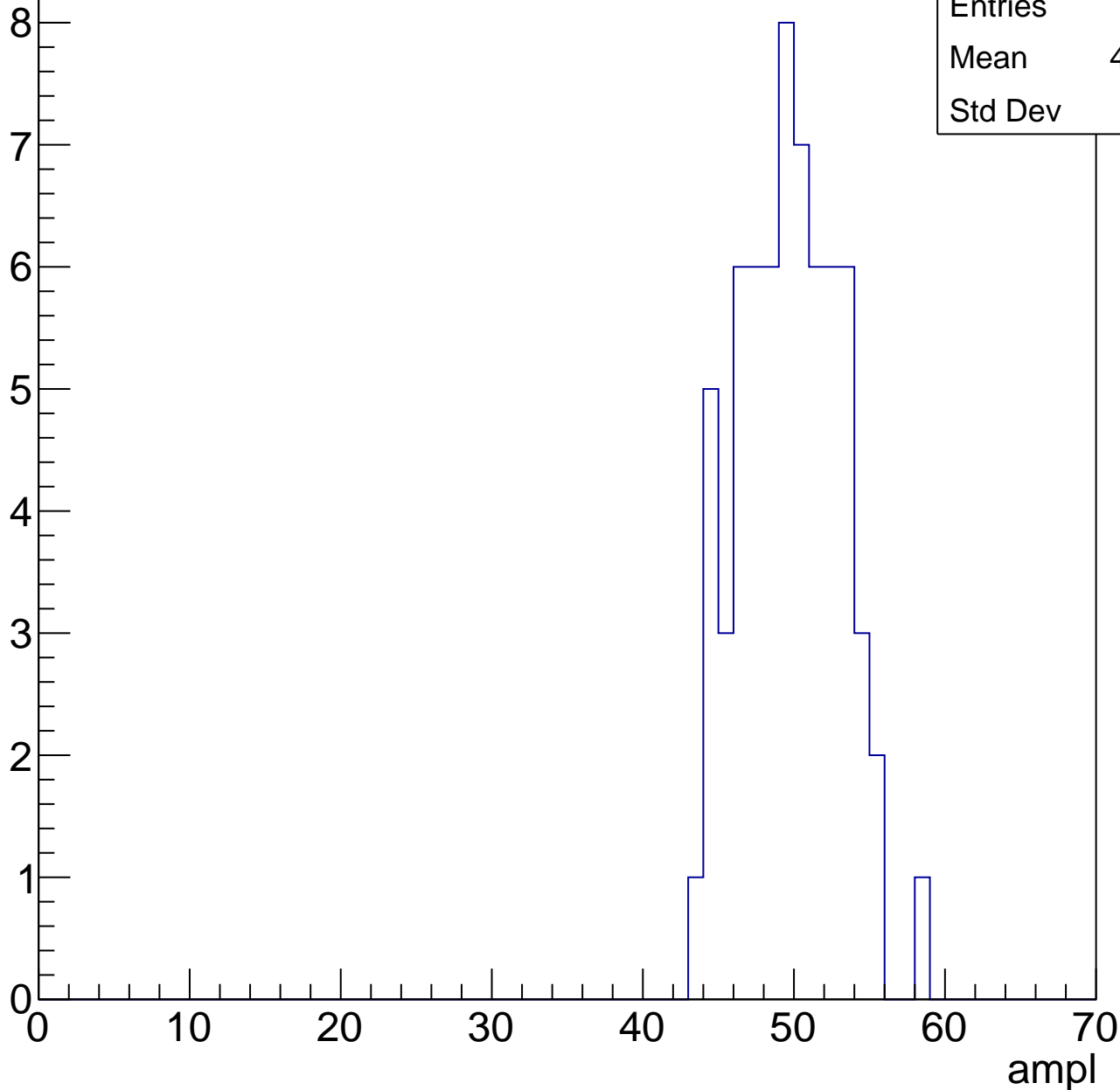


B1L102S, U22-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.27
Std Dev	3.25

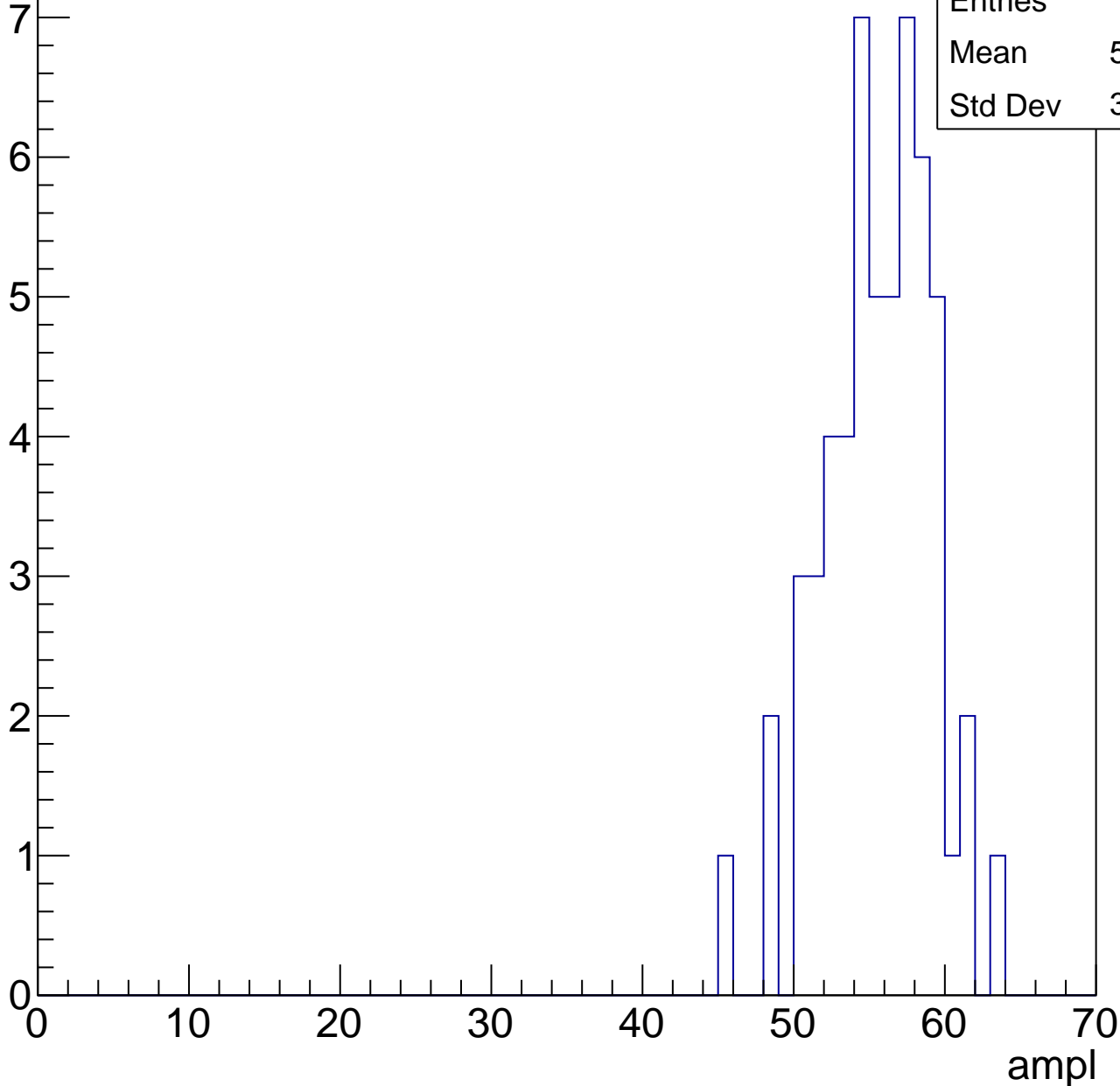


B1L102S, U22-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.07
Std Dev	3.555

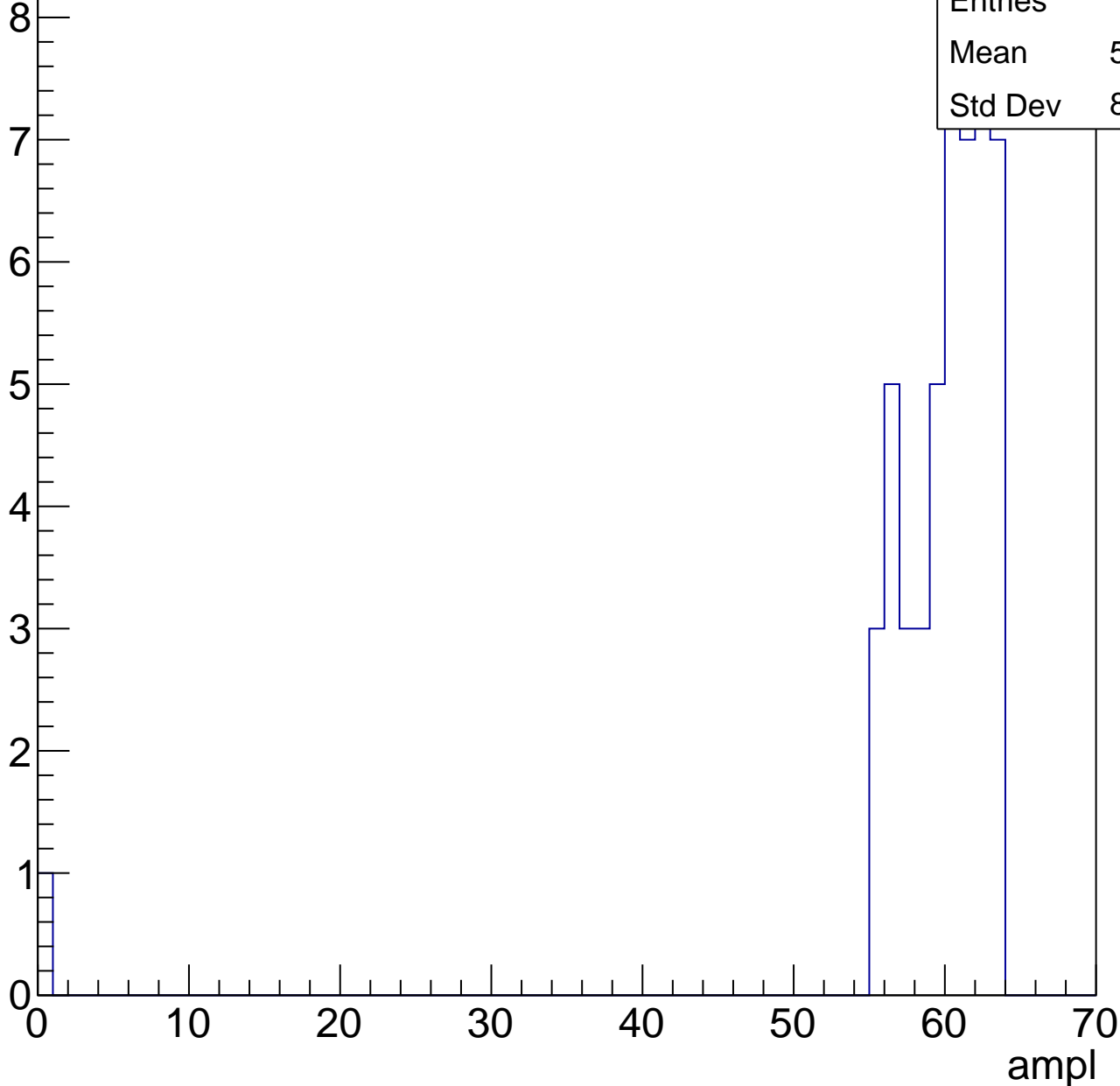


B1L102S, U22-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.58
Std Dev	8.718



B1L102S, U22-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch31, adc0

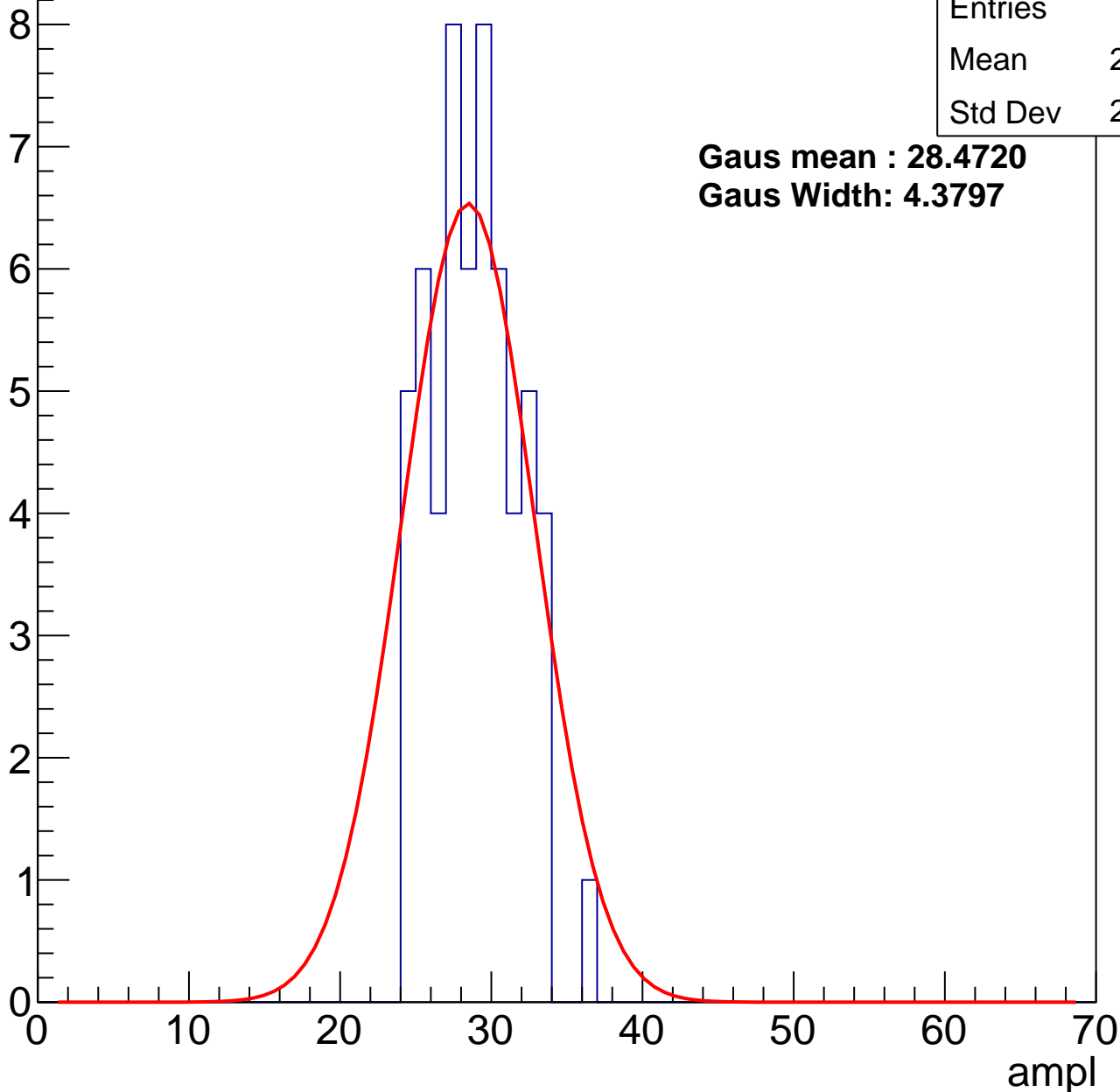
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	28.46
Std Dev	2.835

Gaus mean : 28.4720

Gaus Width: 4.3797



B1L102S, U22-ch31, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	35.44
Std Dev	3.353

Gaus mean : 35.8905

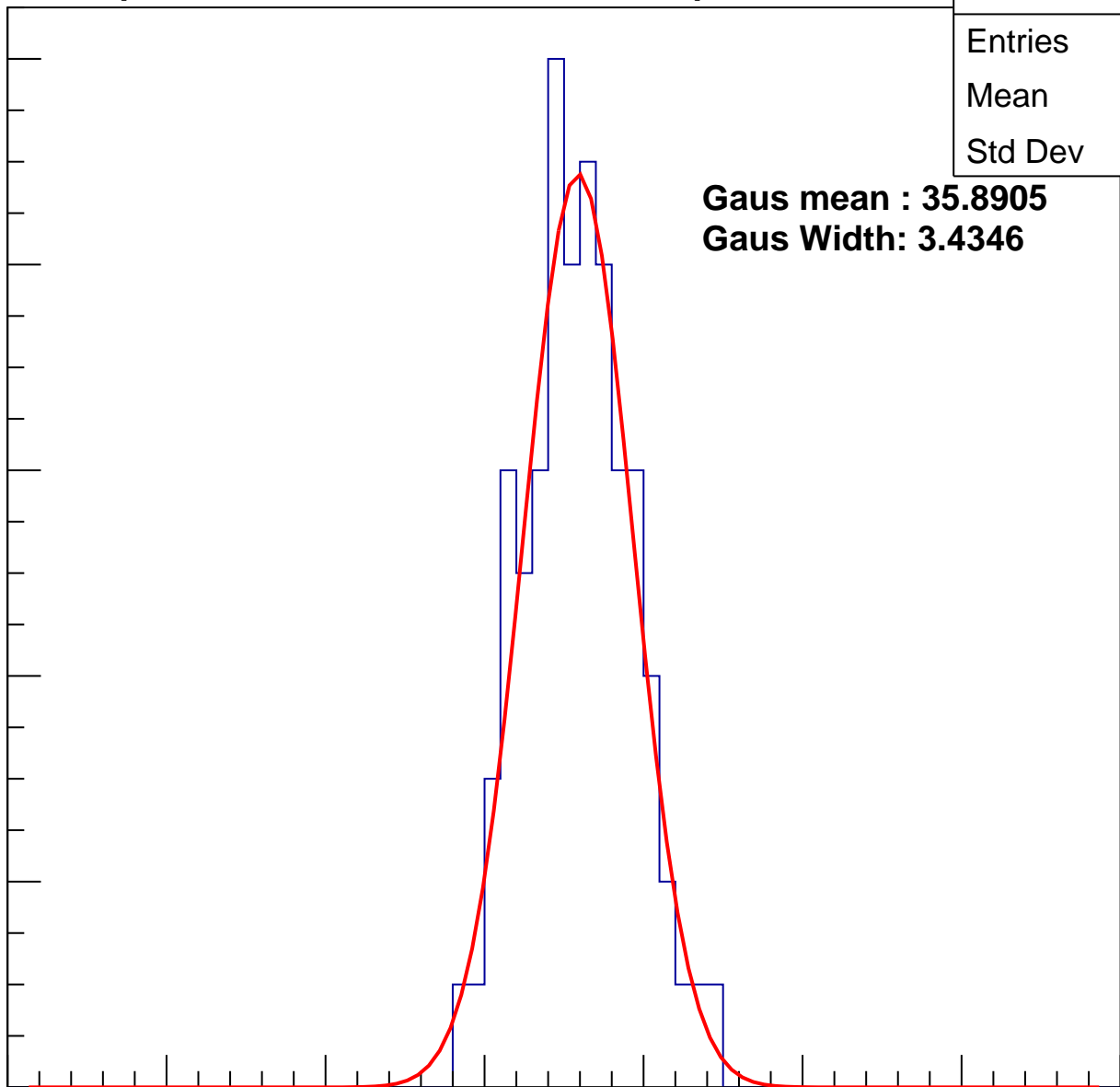
Gaus Width: 3.4346

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch31, adc2

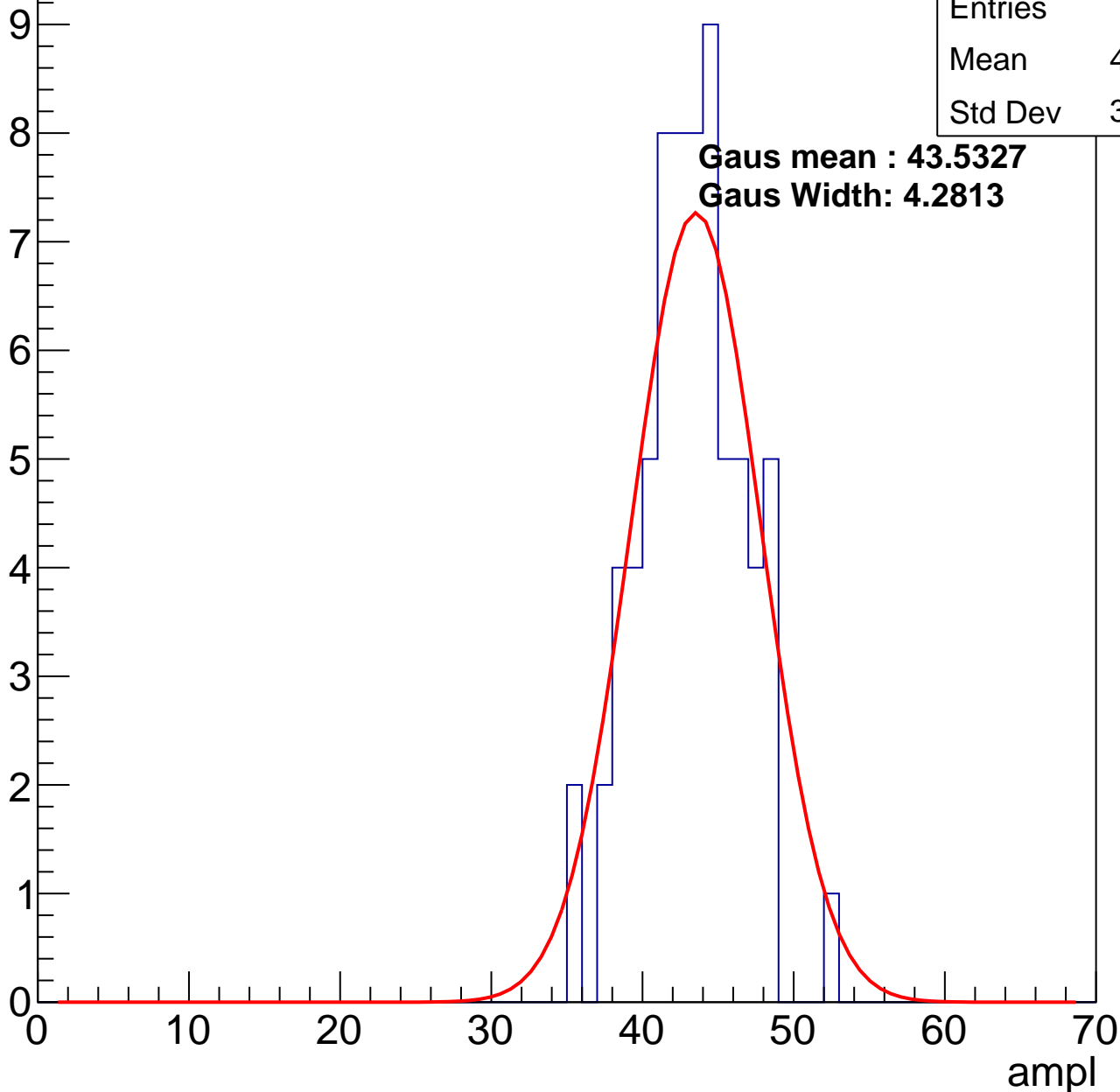
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.73
Std Dev	3.355

Gaus mean : 43.5327

Gaus Width: 4.2813

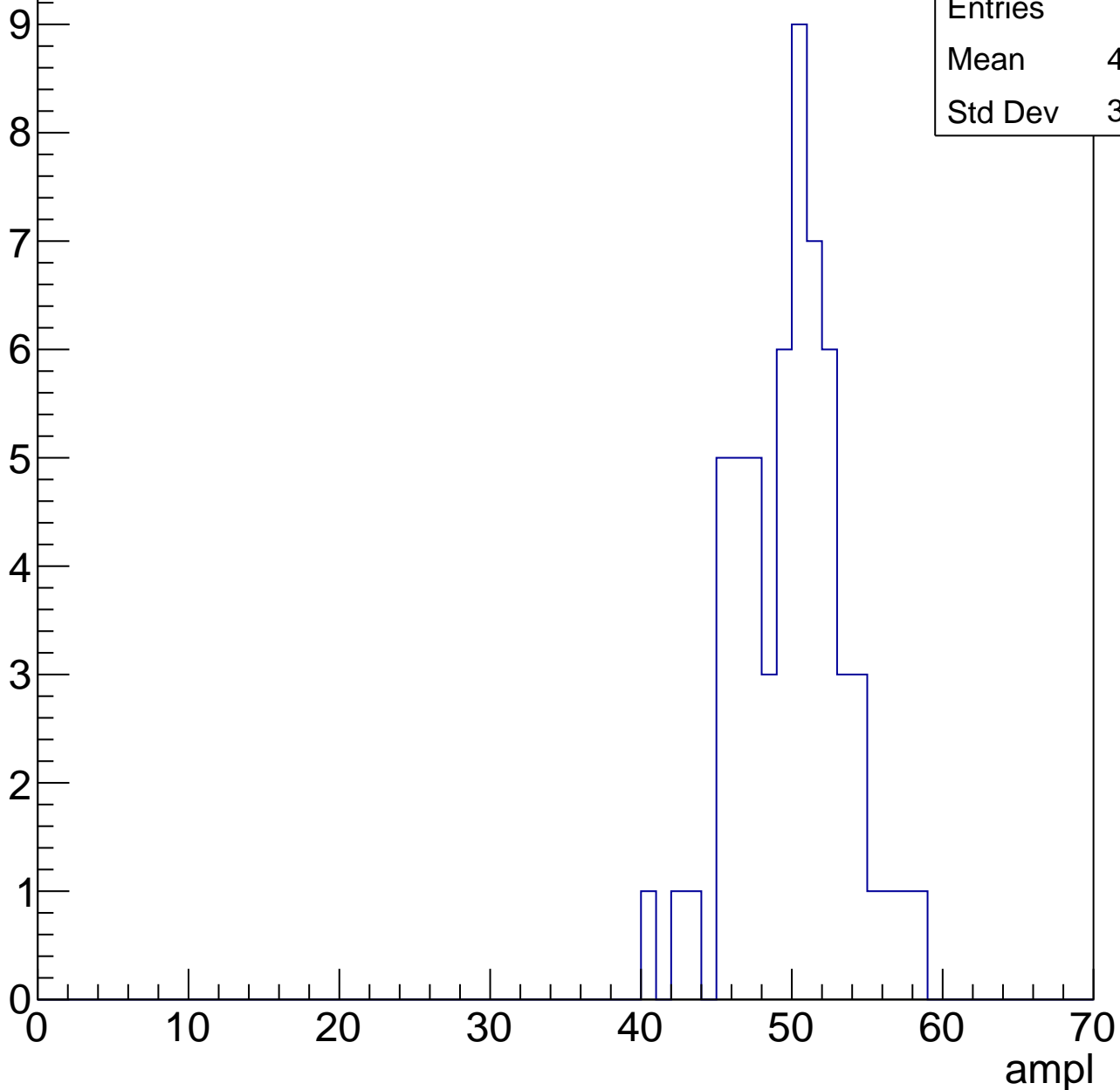


B1L102S, U22-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	49.47
Std Dev	3.553

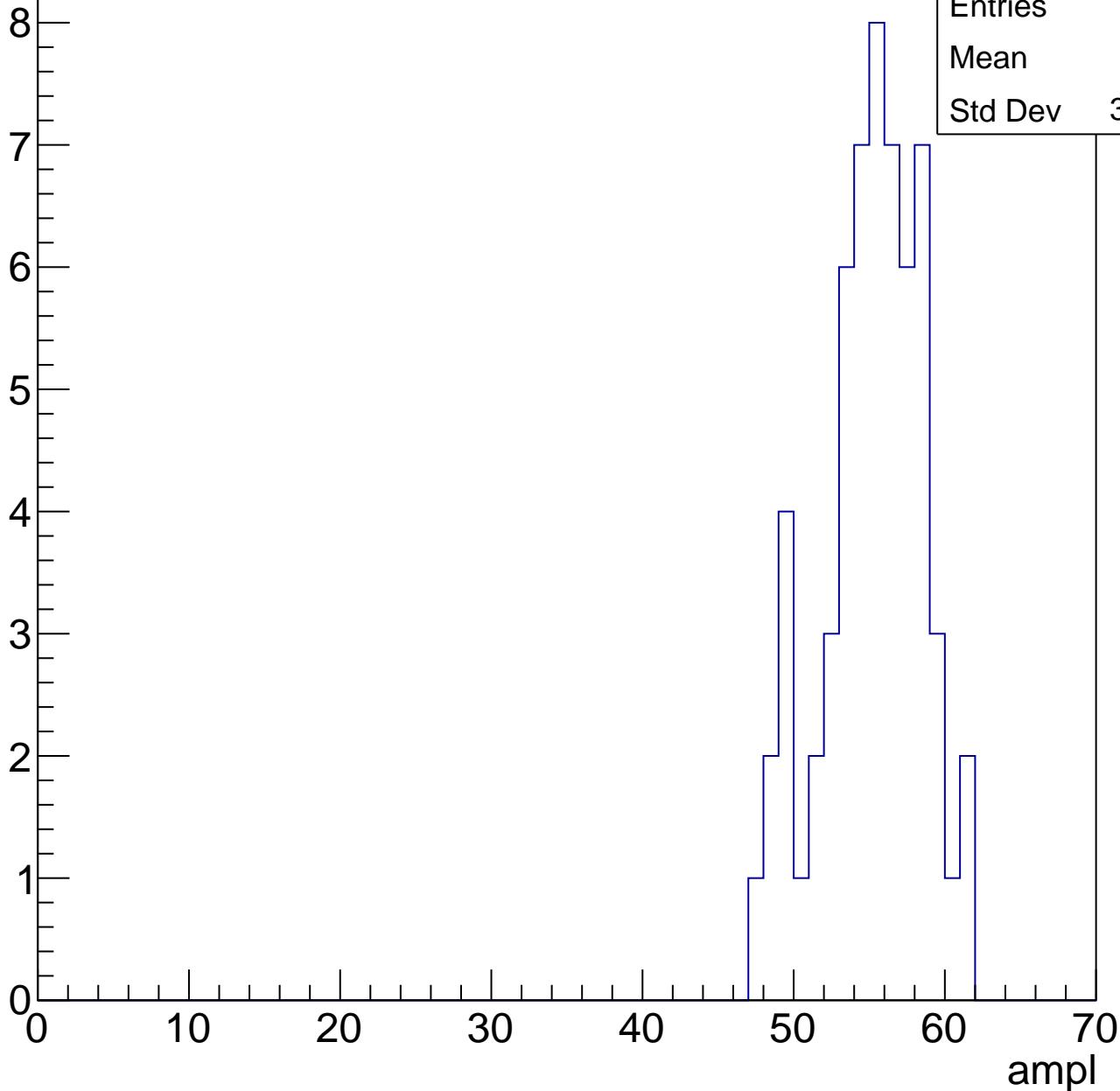


B1L102S, U22-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.7
Std Dev	3.303

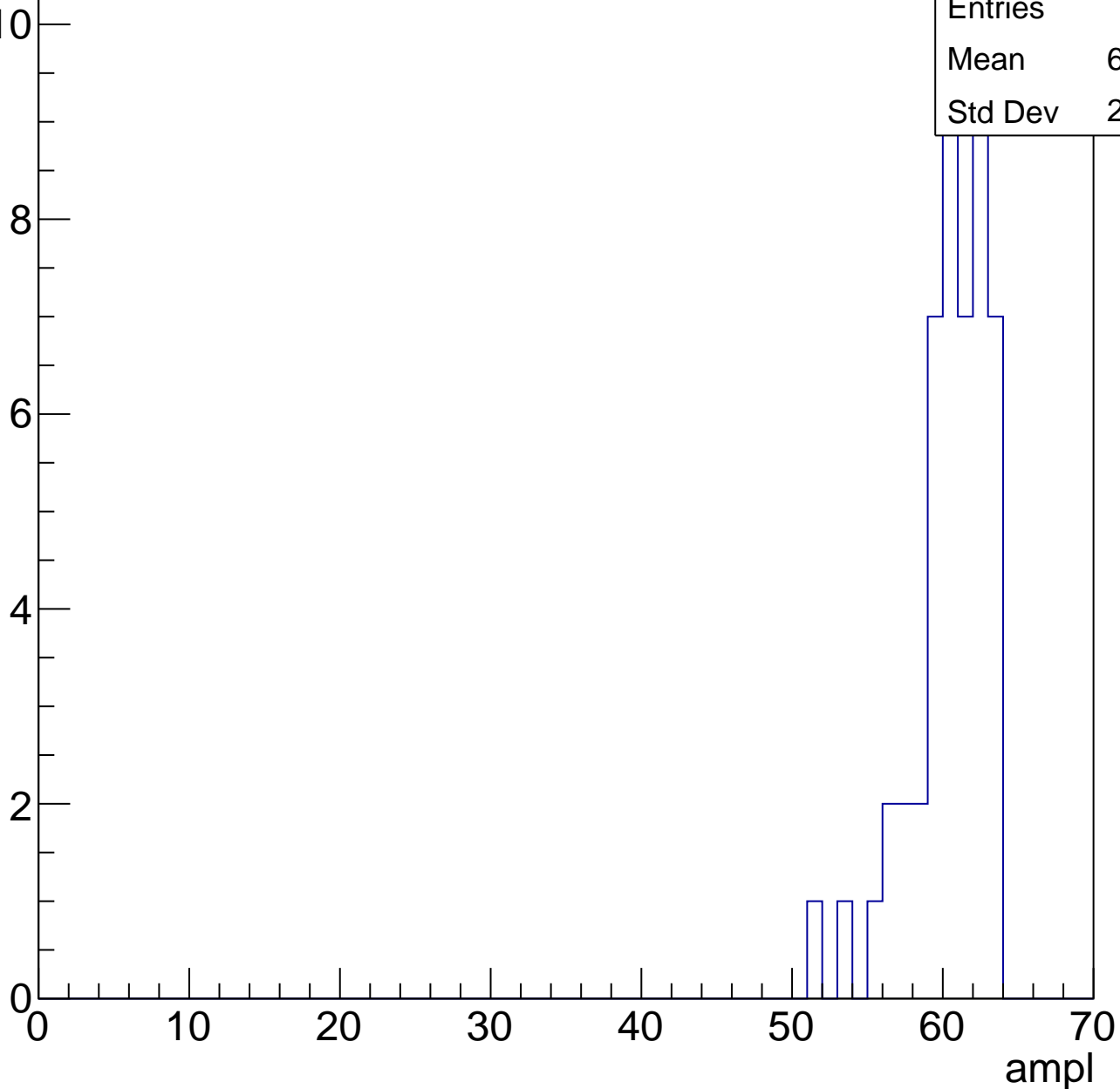


B1L102S, U22-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

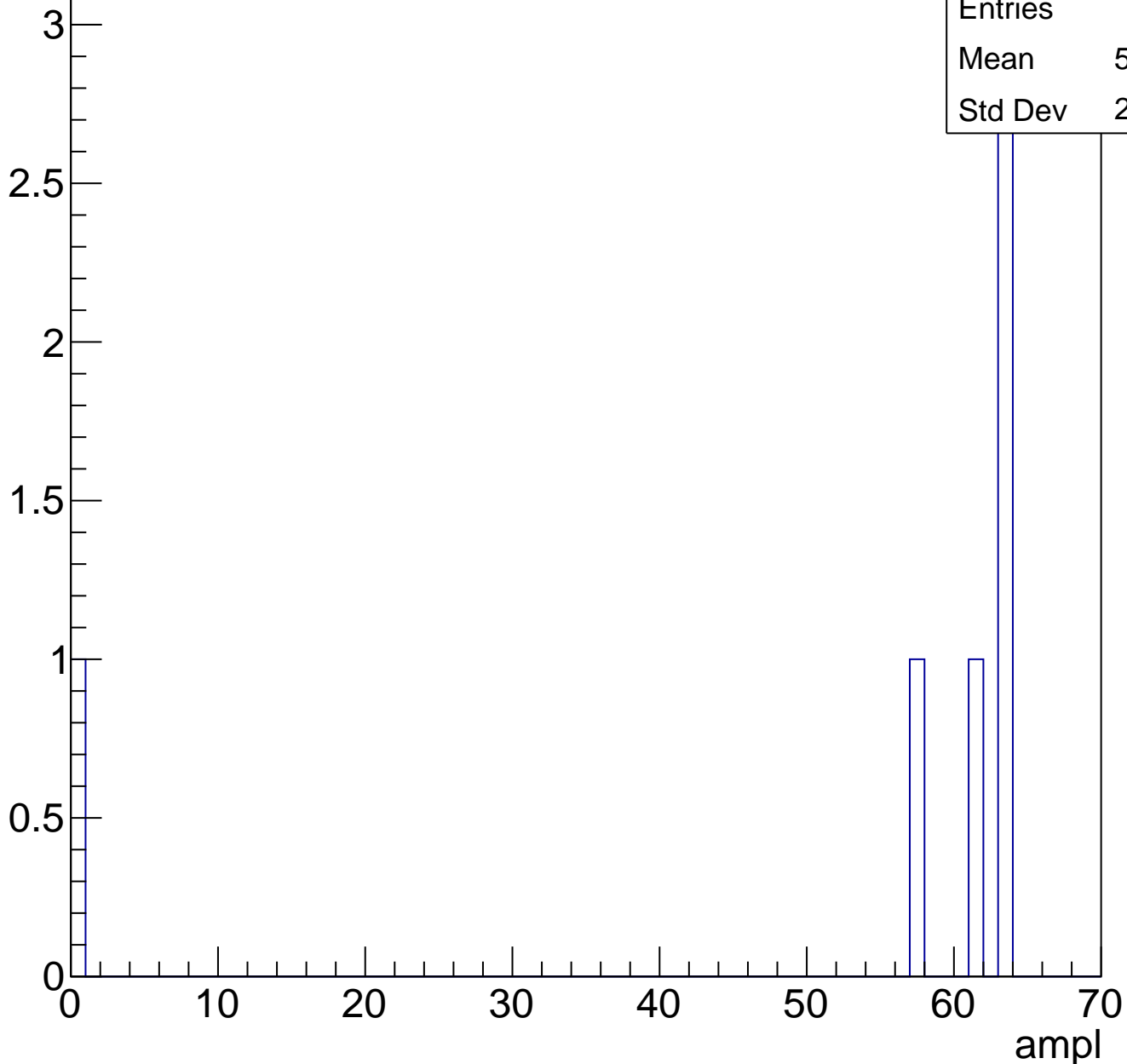
Entries	49
Mean	60.04
Std Dev	2.595



B1L102S, U22-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch32, adc0

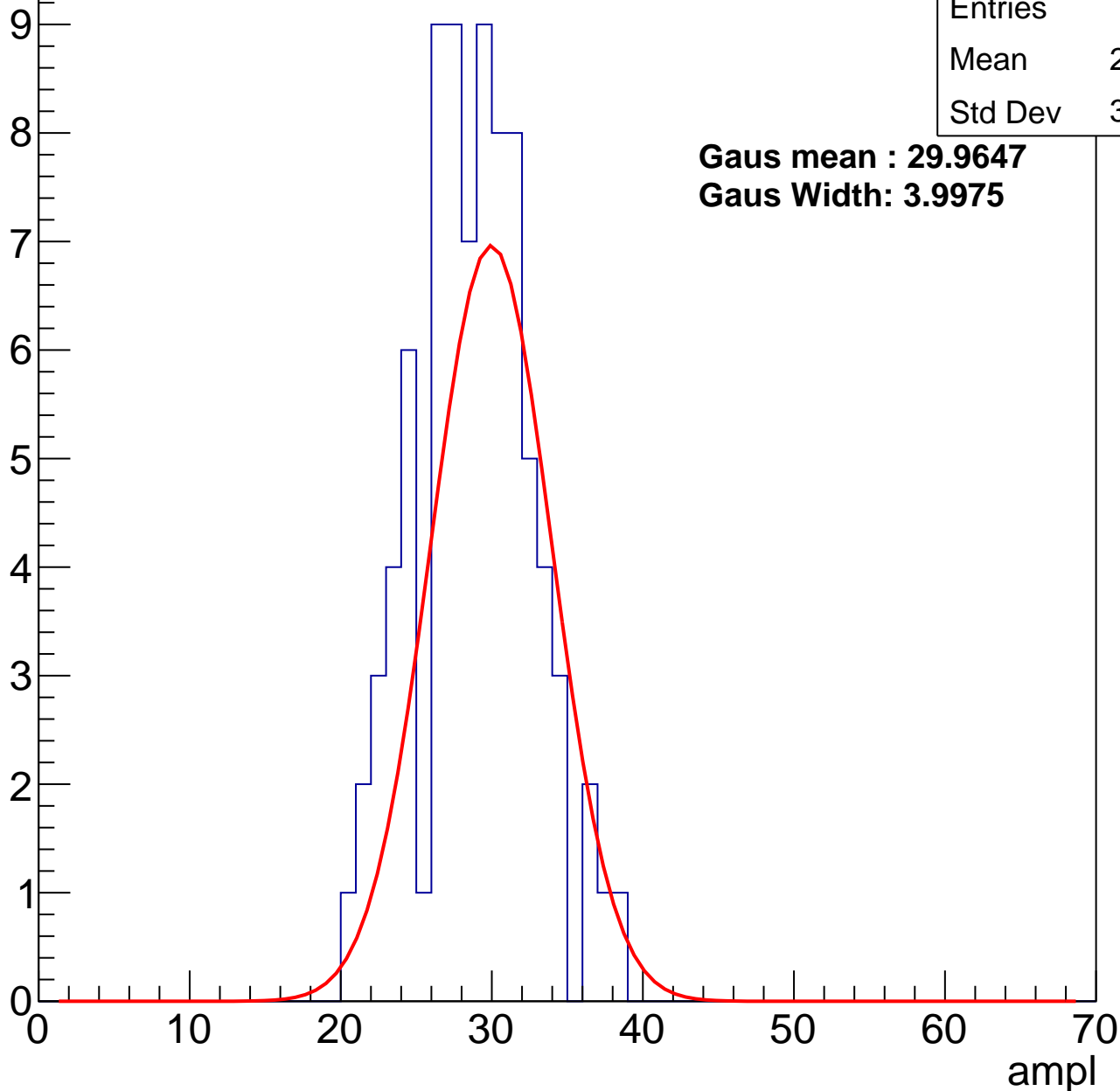
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.34
Std Dev	3.835

Gaus mean : 29.9647

Gaus Width: 3.9975



B1L102S, U22-ch32, adc1

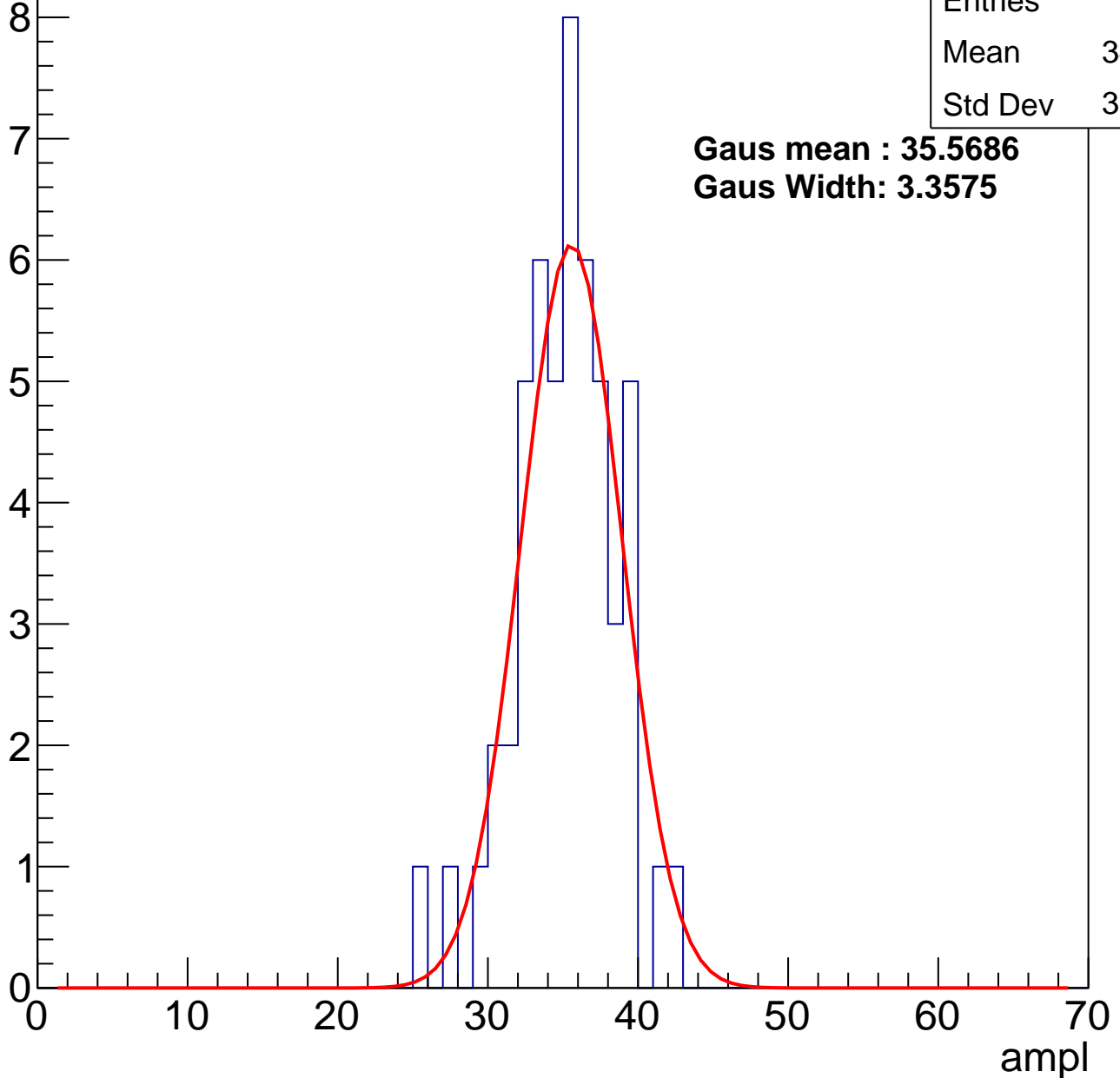
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	34.69
Std Dev	3.308

Gaus mean : 35.5686

Gaus Width: 3.3575



B1L102S, U22-ch32, adc2

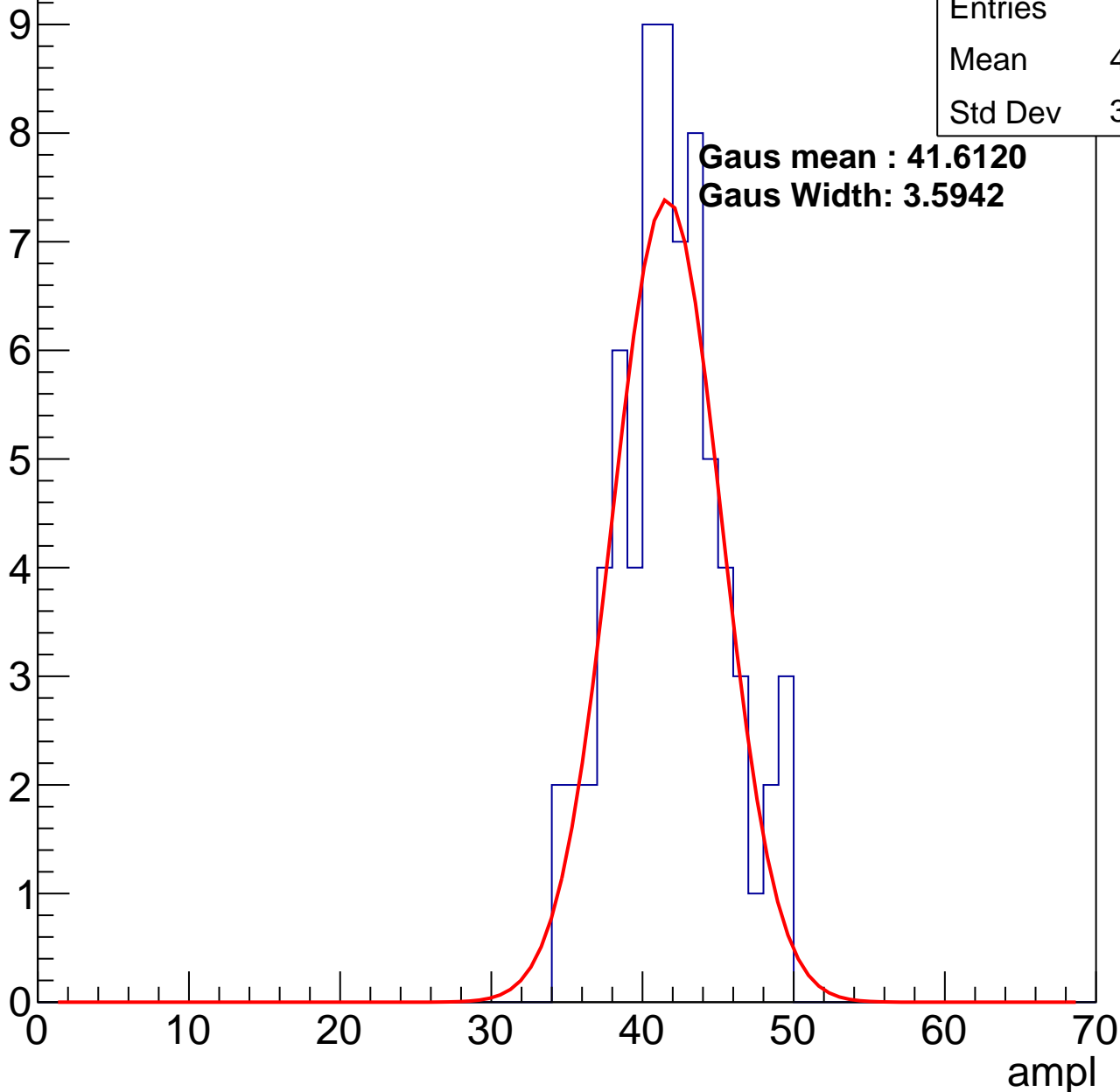
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.37
Std Dev	3.565

Gaus mean : 41.6120

Gaus Width: 3.5942

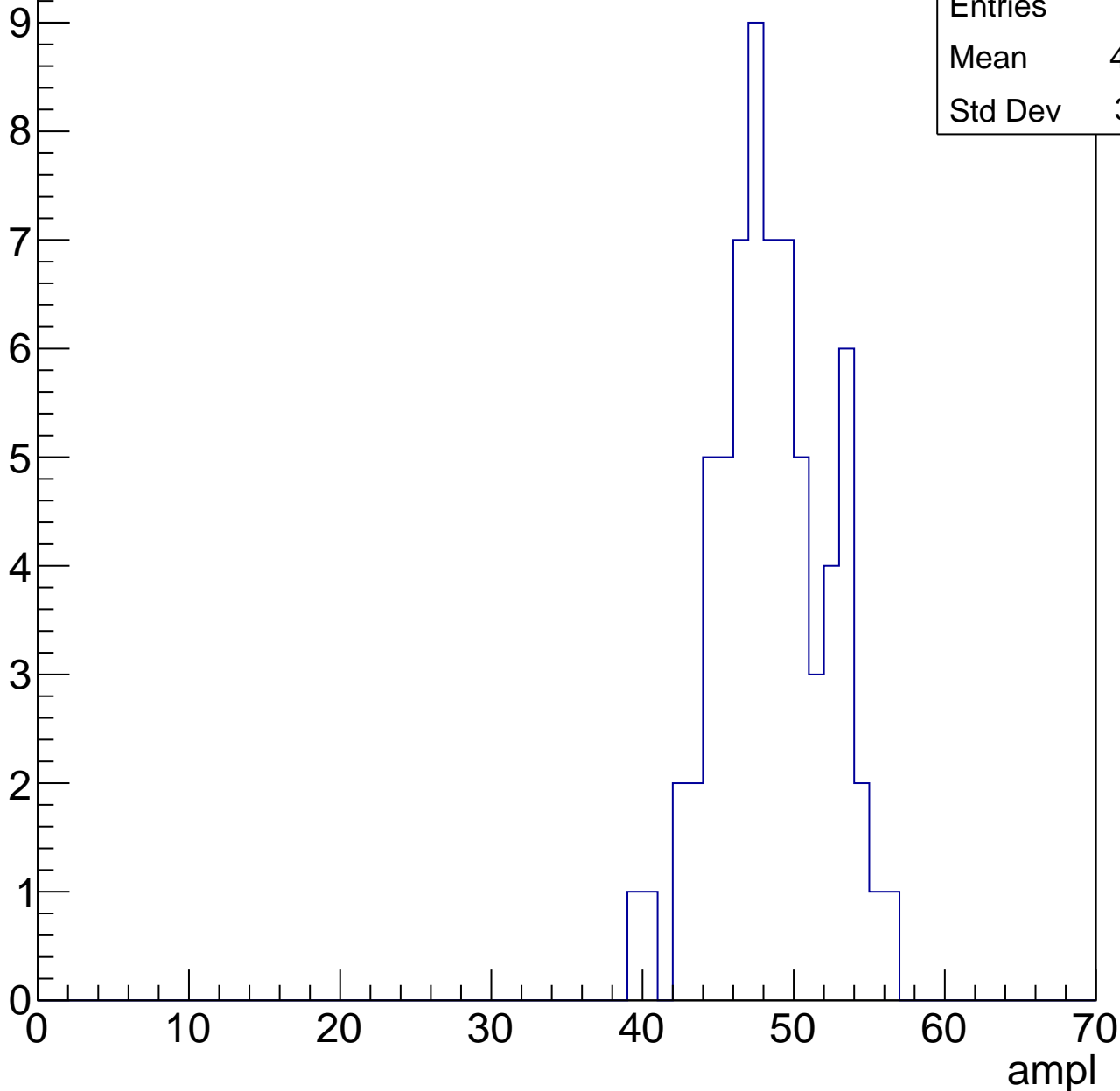


B1L102S, U22-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.03
Std Dev	3.601

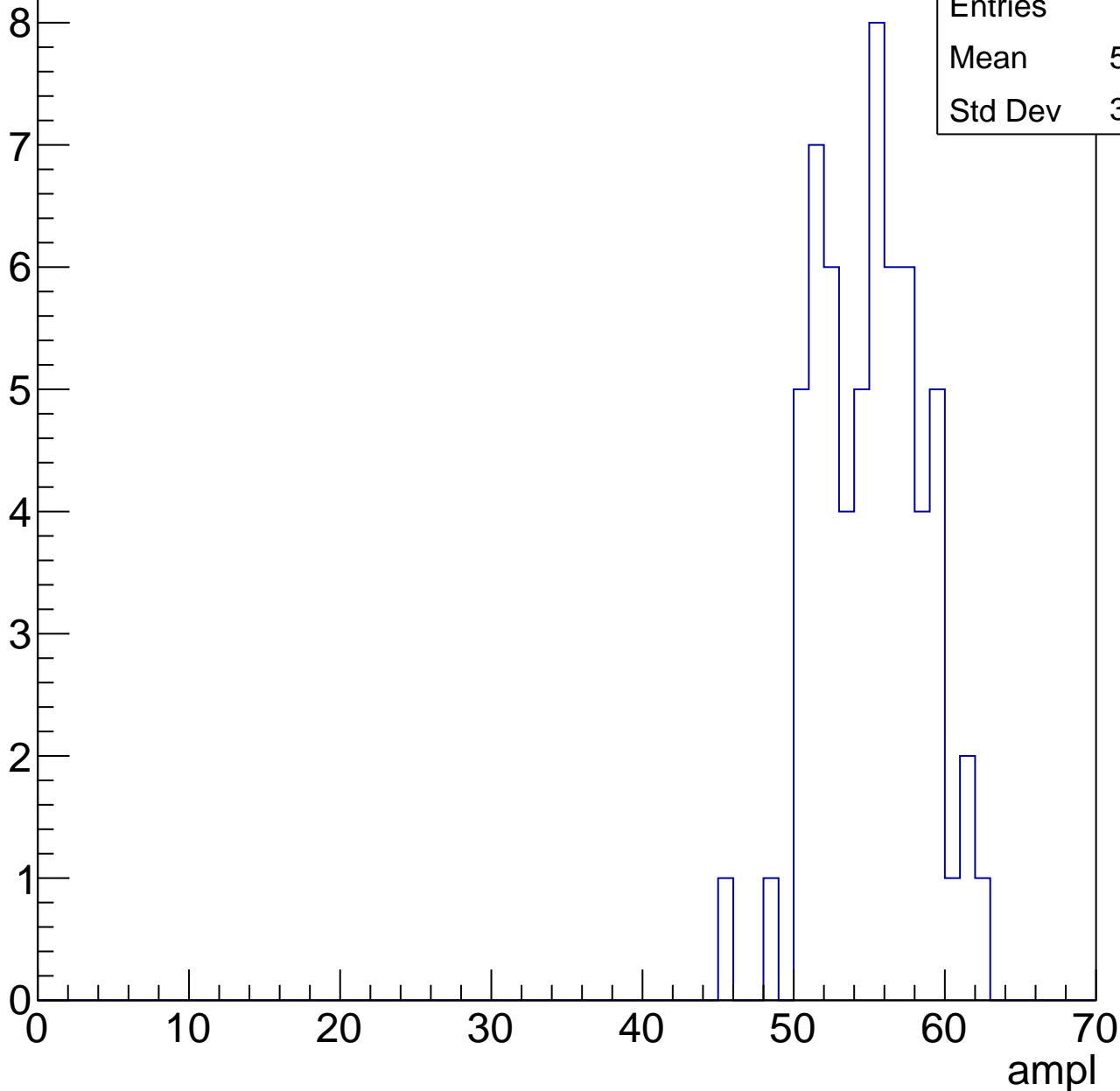


B1L102S, U22-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	54.56
Std Dev	3.458

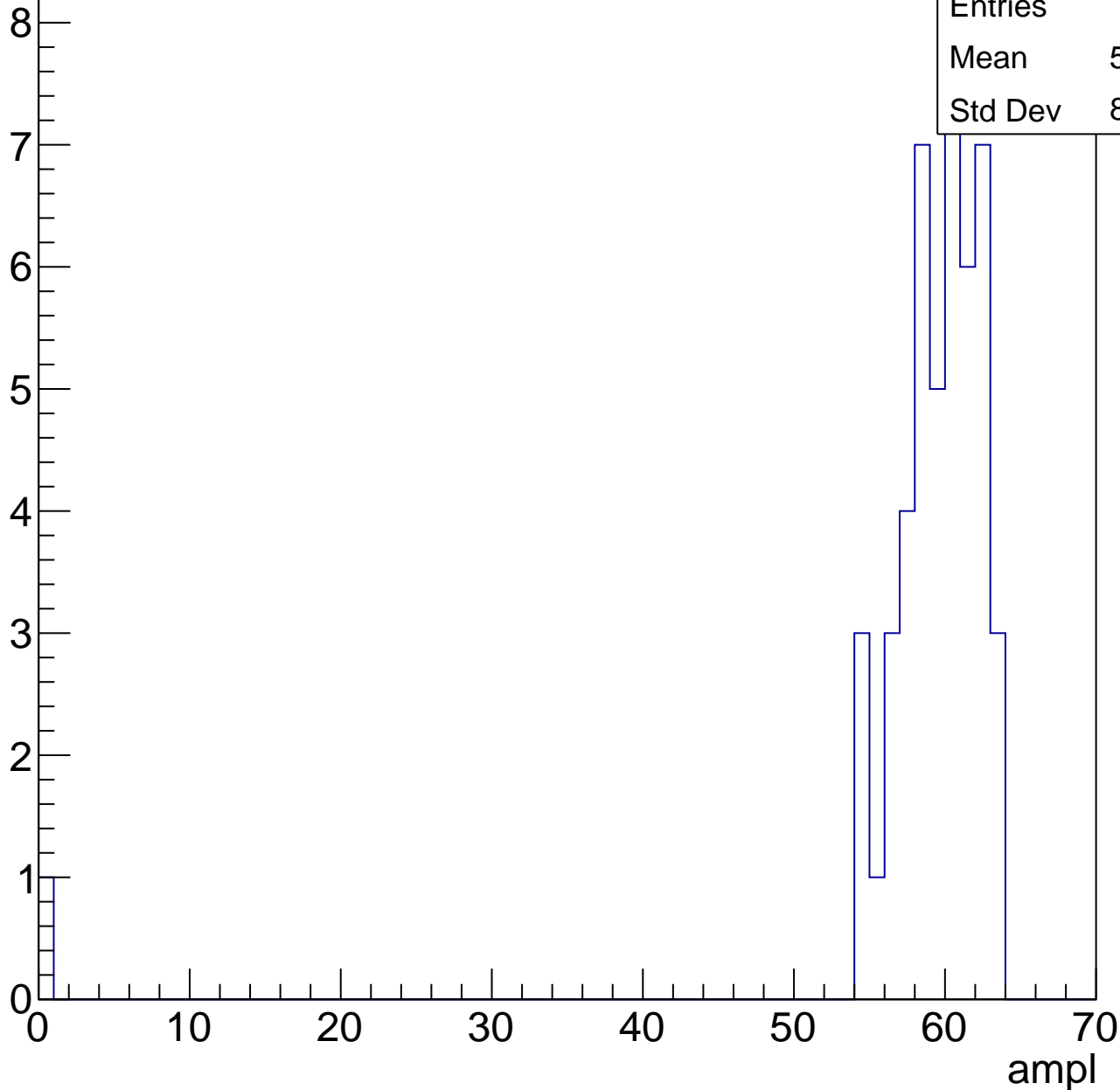


B1L102S, U22-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	57.98
Std Dev	8.797

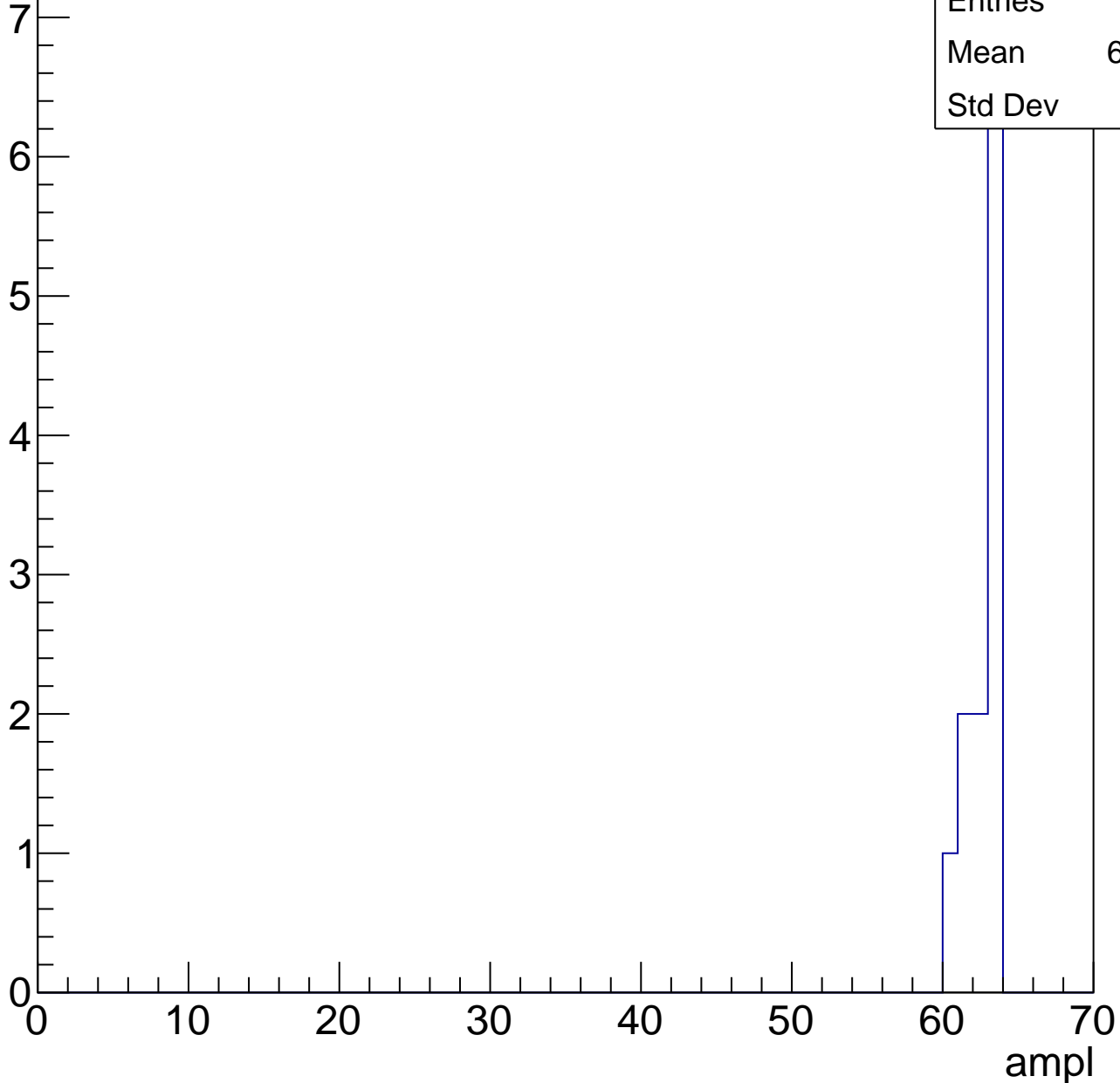


B1L102S, U22-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	62.25
Std Dev	1.01



B1L102S, U22-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch33, adc0

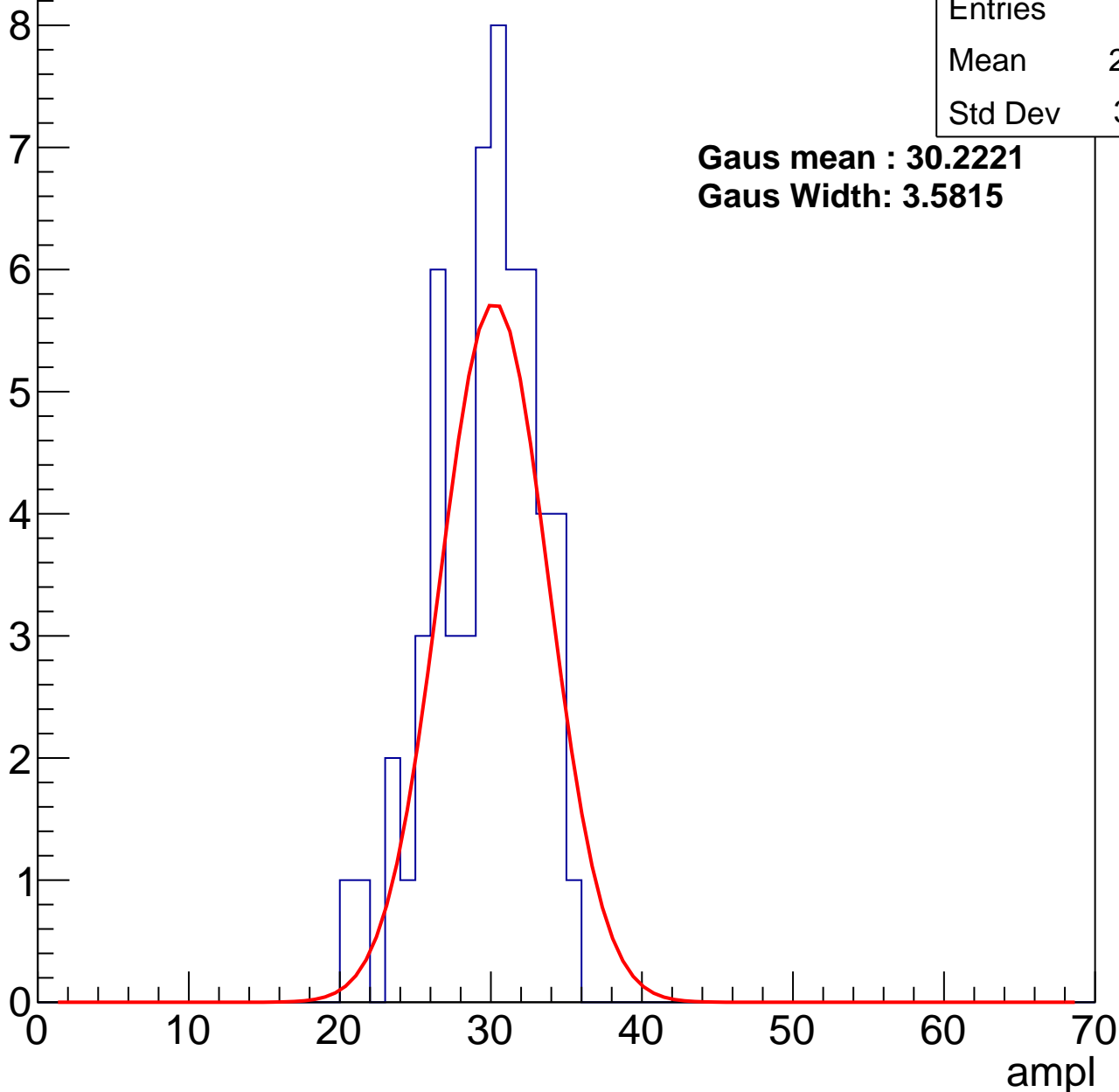
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	29.12
Std Dev	3.391

Gaus mean : 30.2221

Gaus Width: 3.5815

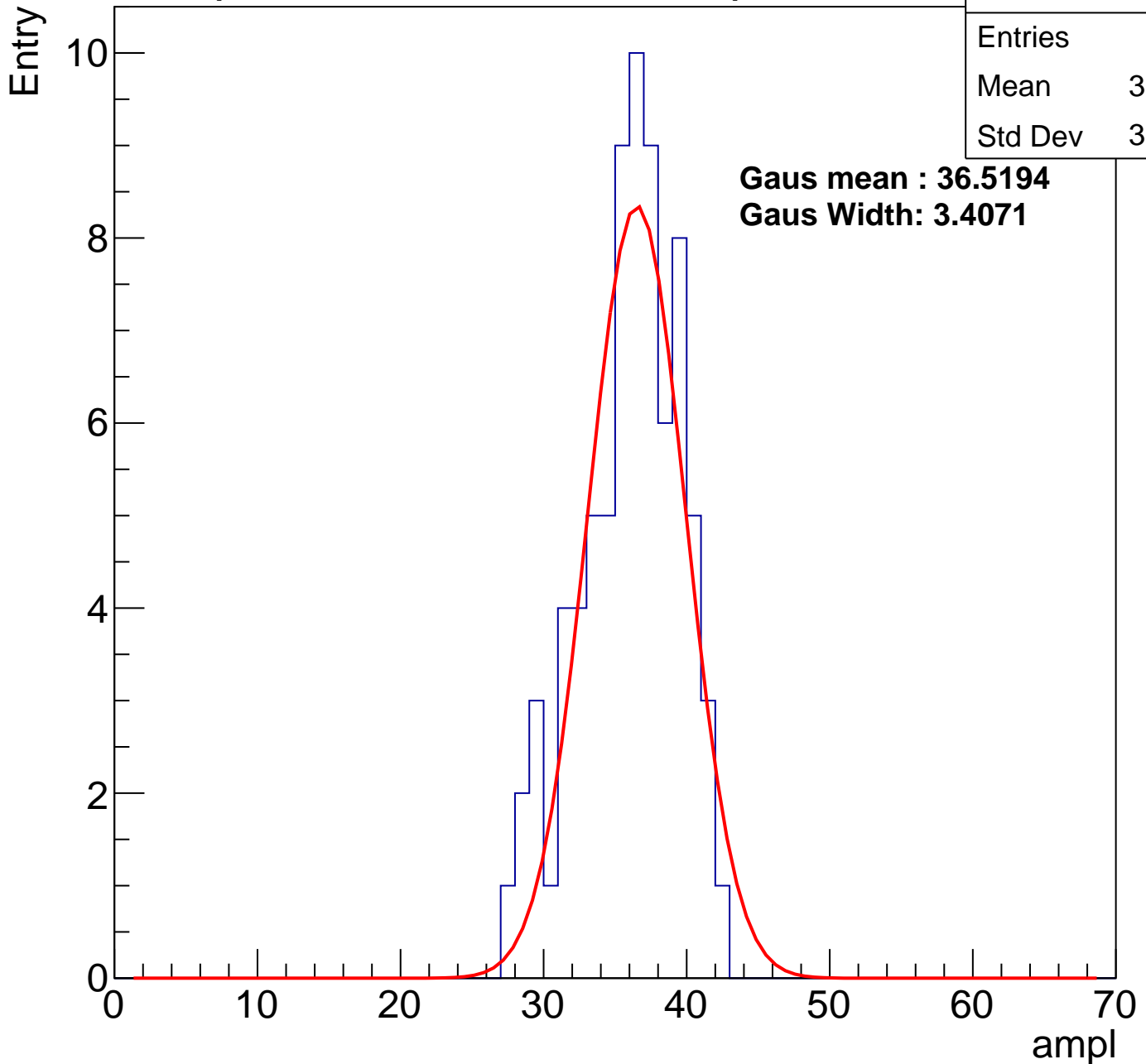


B1L102S, U22-ch33, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	35.53
Std Dev	3.458

Gaus mean : 36.5194
Gaus Width: 3.4071



B1L102S, U22-ch33, adc2

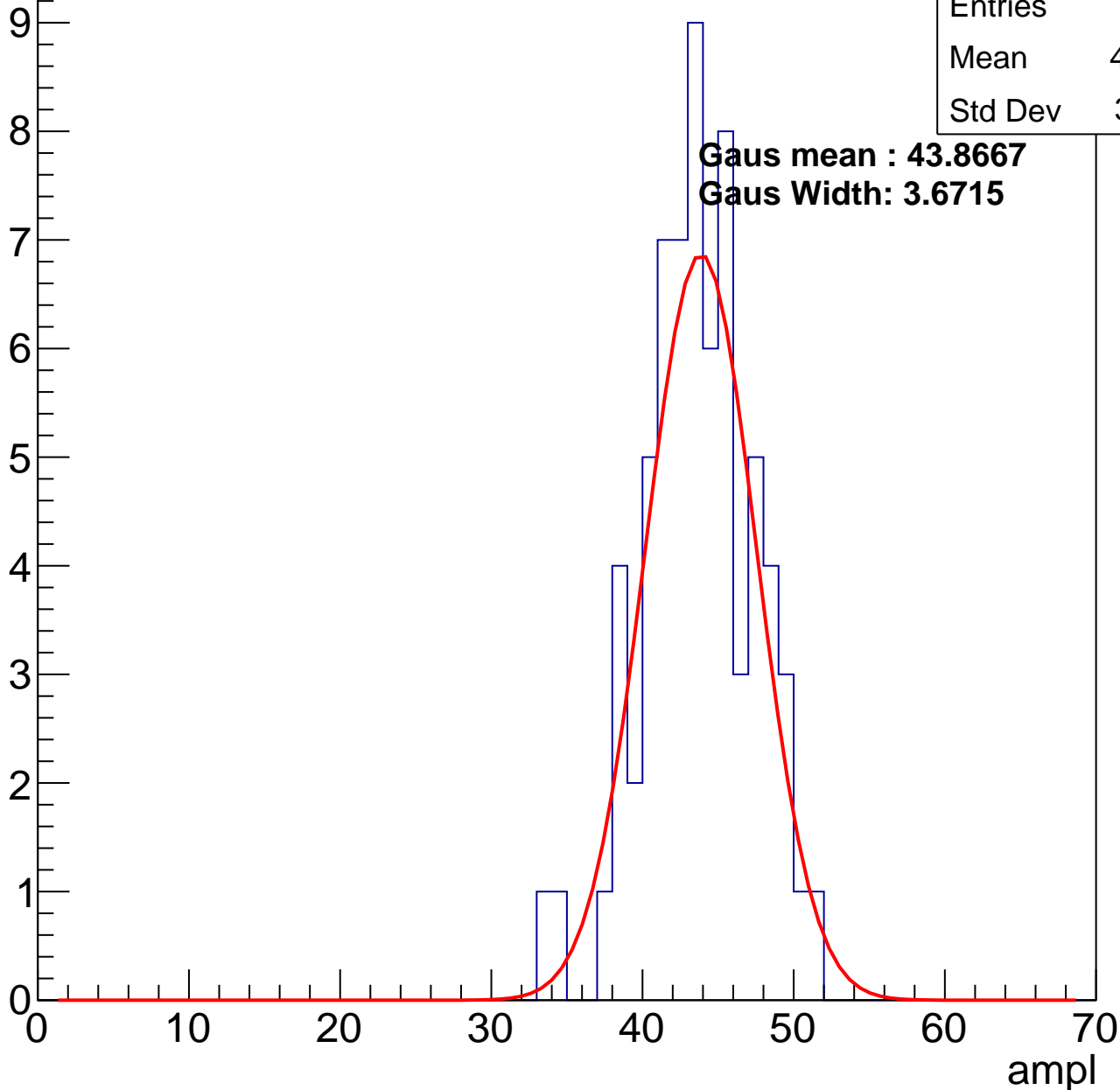
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	43.22
Std Dev	3.621

Gaus mean : 43.8667

Gaus Width: 3.6715

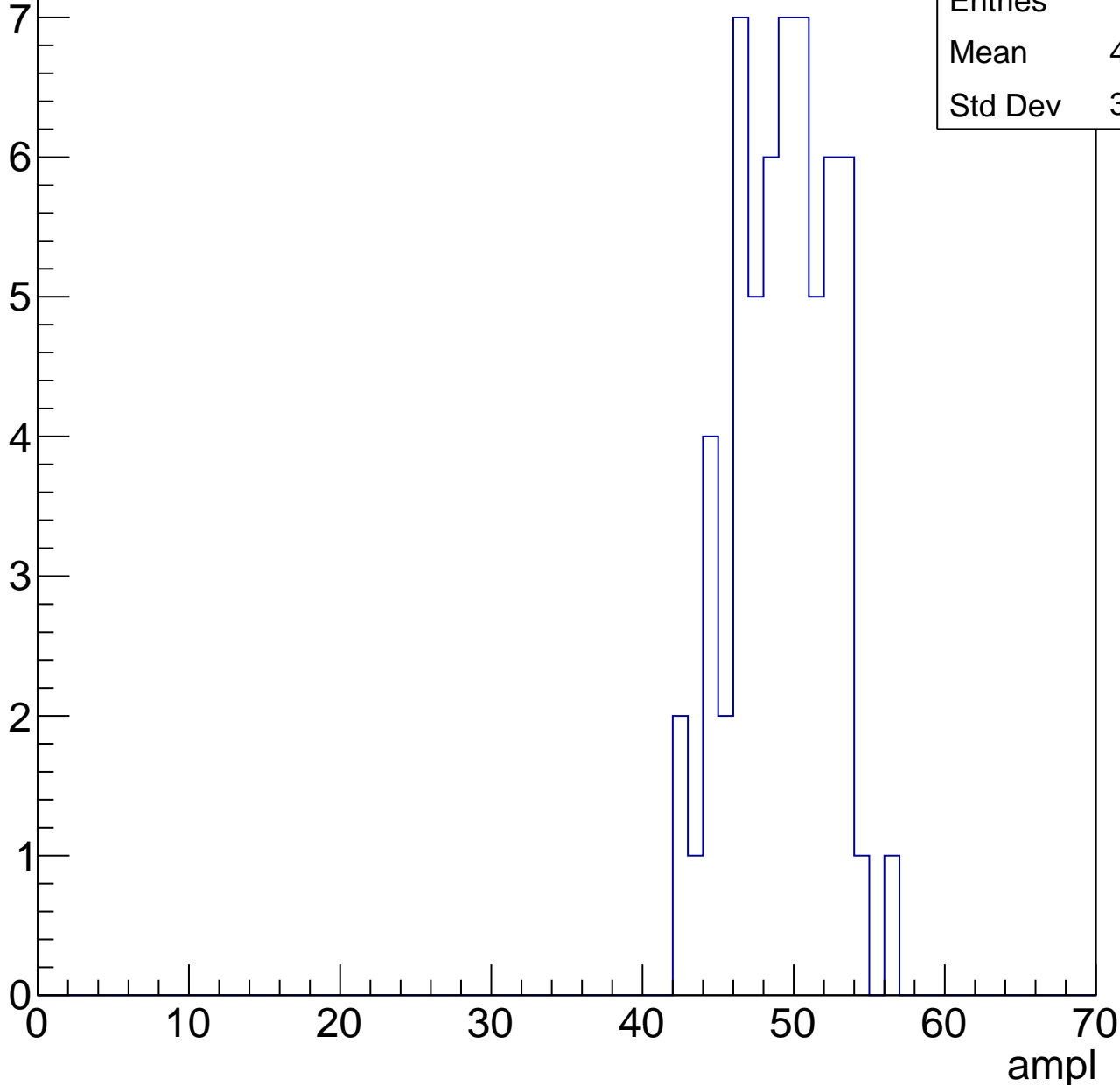


B1L102S, U22-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.77
Std Dev	3.169

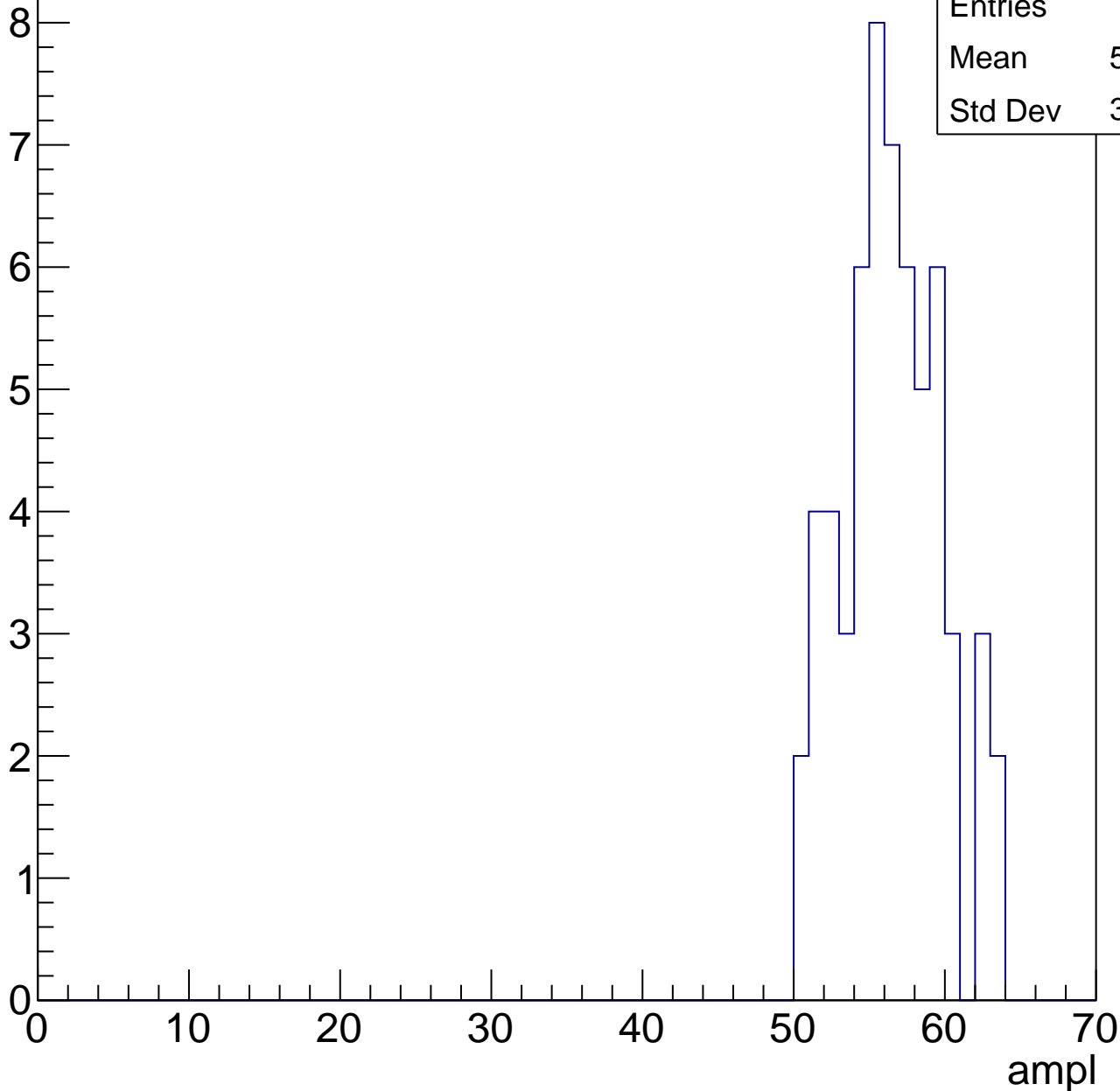


B1L102S, U22-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.02
Std Dev	3.265

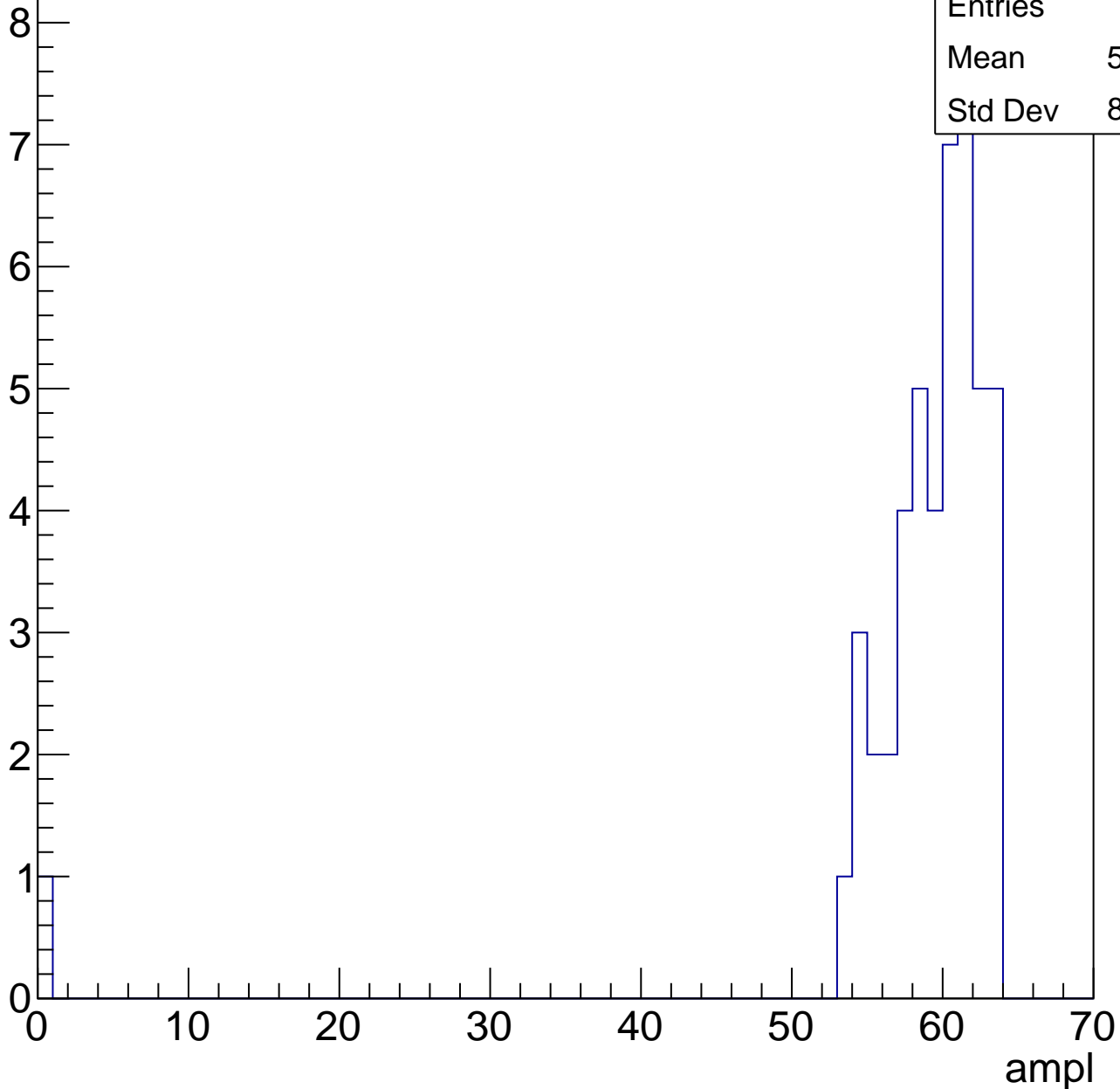


B1L102S, U22-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.96
Std Dev	8.963



B1L102S, U22-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch34, adc0

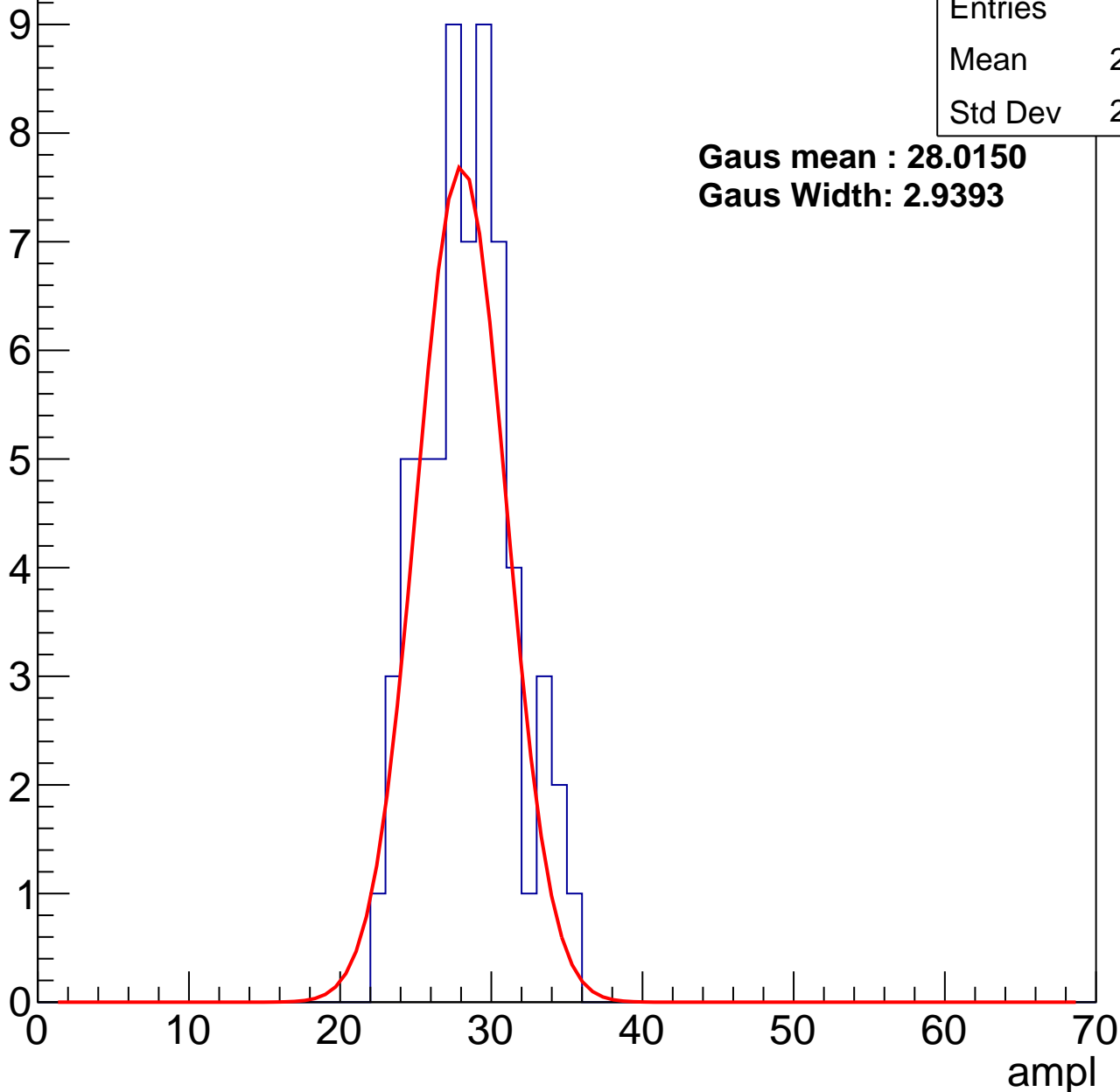
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.97
Std Dev	2.978

Gaus mean : 28.0150

Gaus Width: 2.9393



B1L102S, U22-ch34, adc1

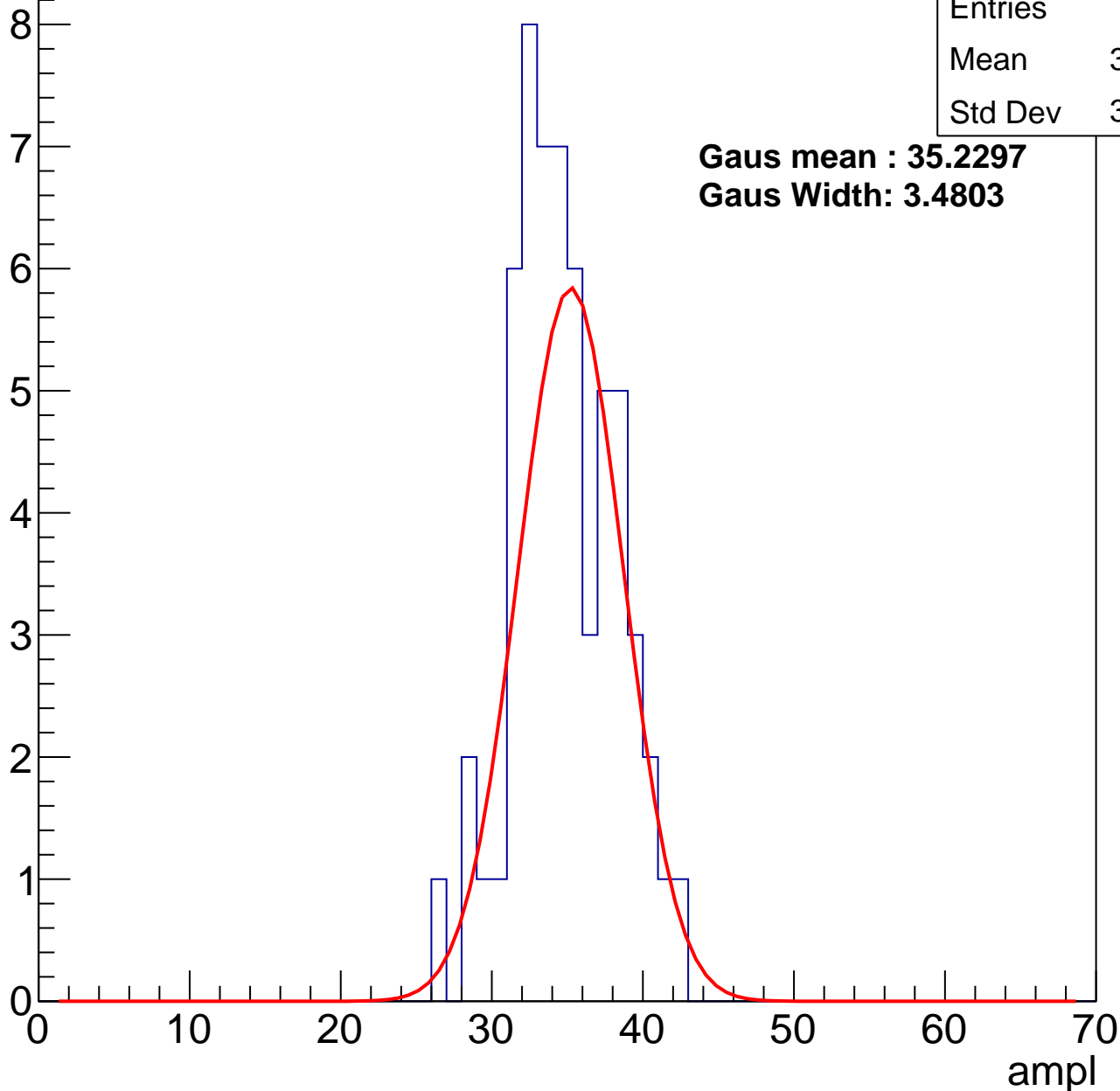
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	34.32
Std Dev	3.362

Gaus mean : 35.2297

Gaus Width: 3.4803



B1L102S, U22-ch34, adc2

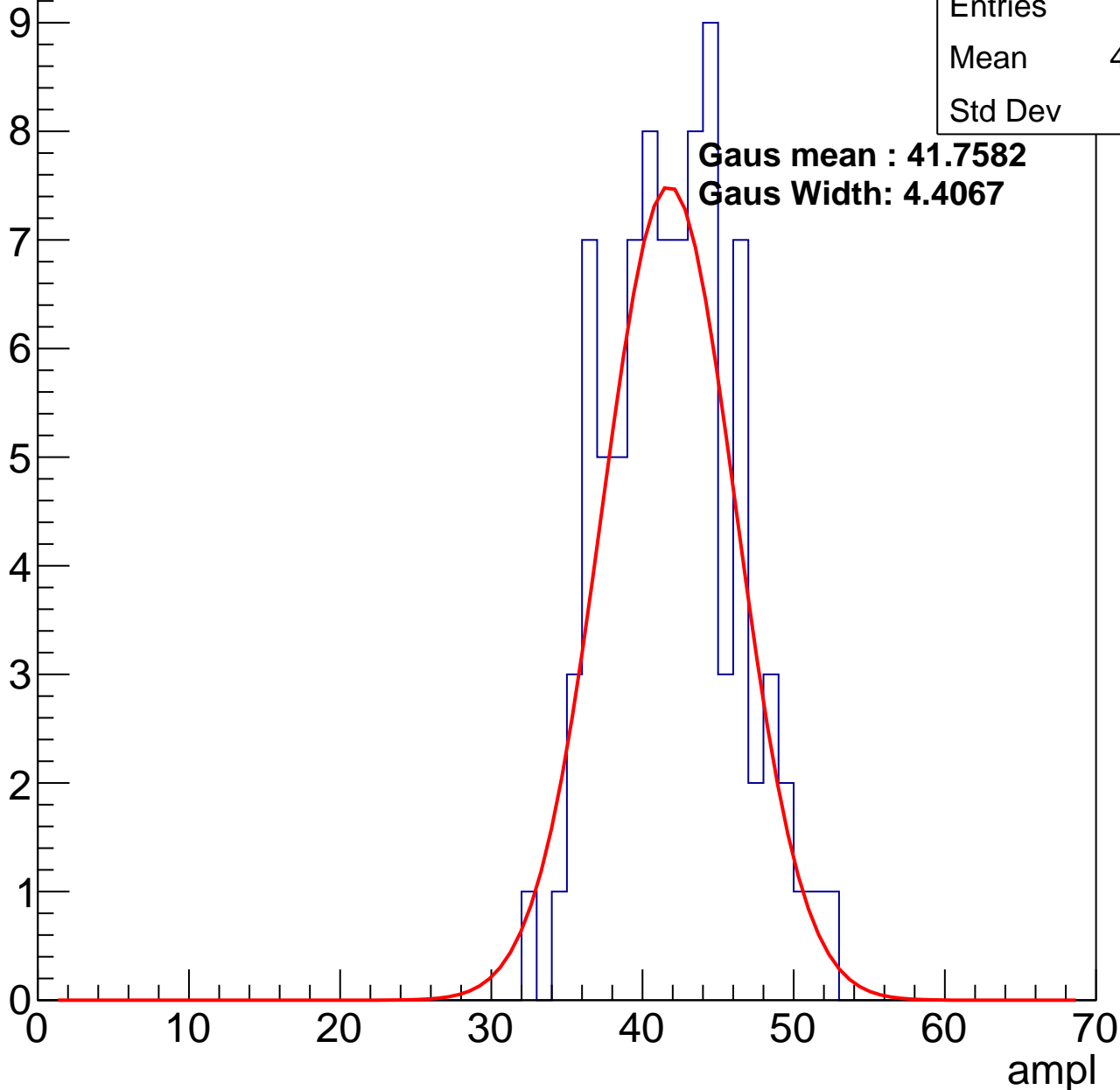
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	41.57
Std Dev	4.18

Gaus mean : 41.7582

Gaus Width: 4.4067

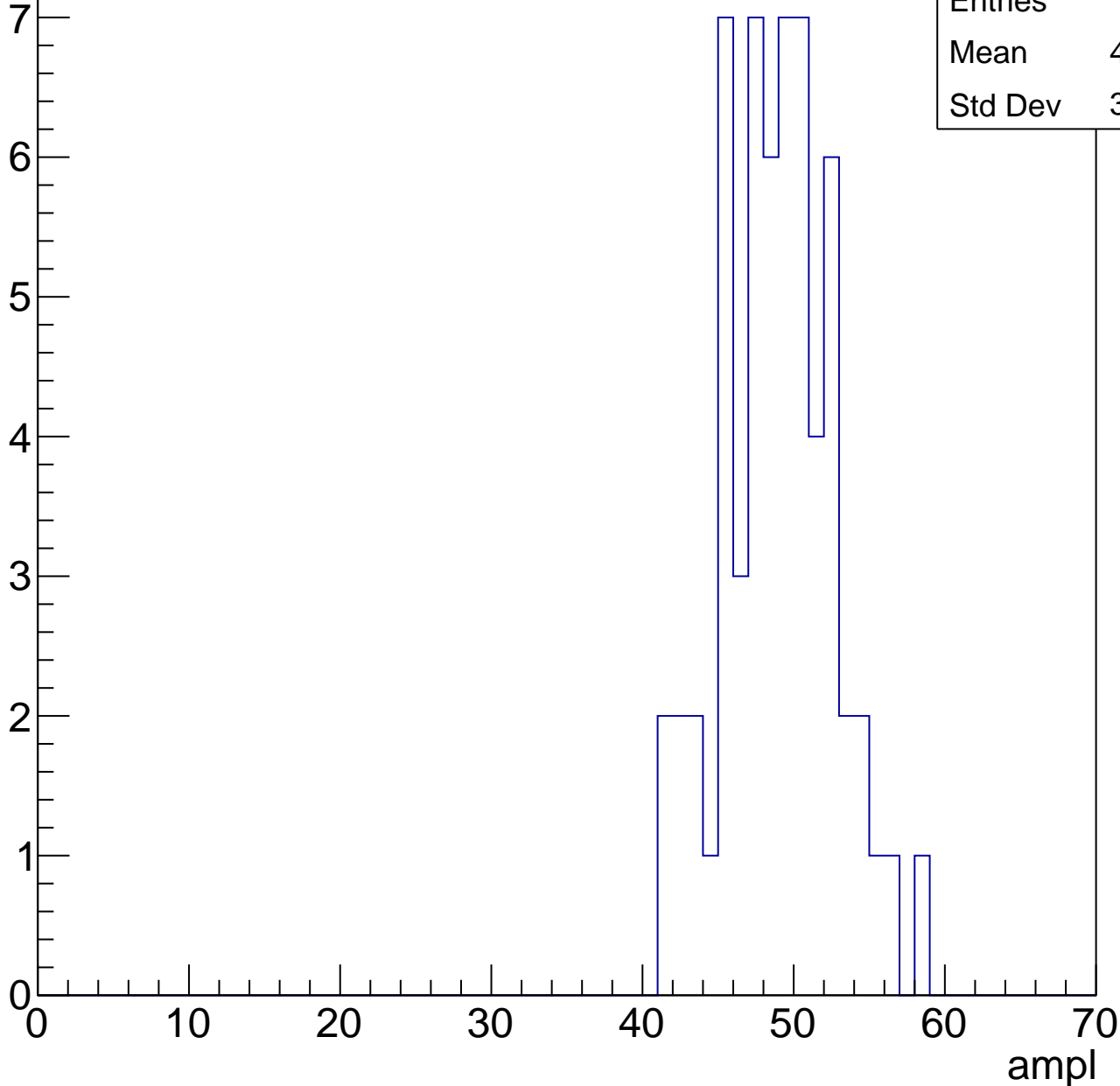


B1L102S, U22-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.49
Std Dev	3.633



B1L102S, U22-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

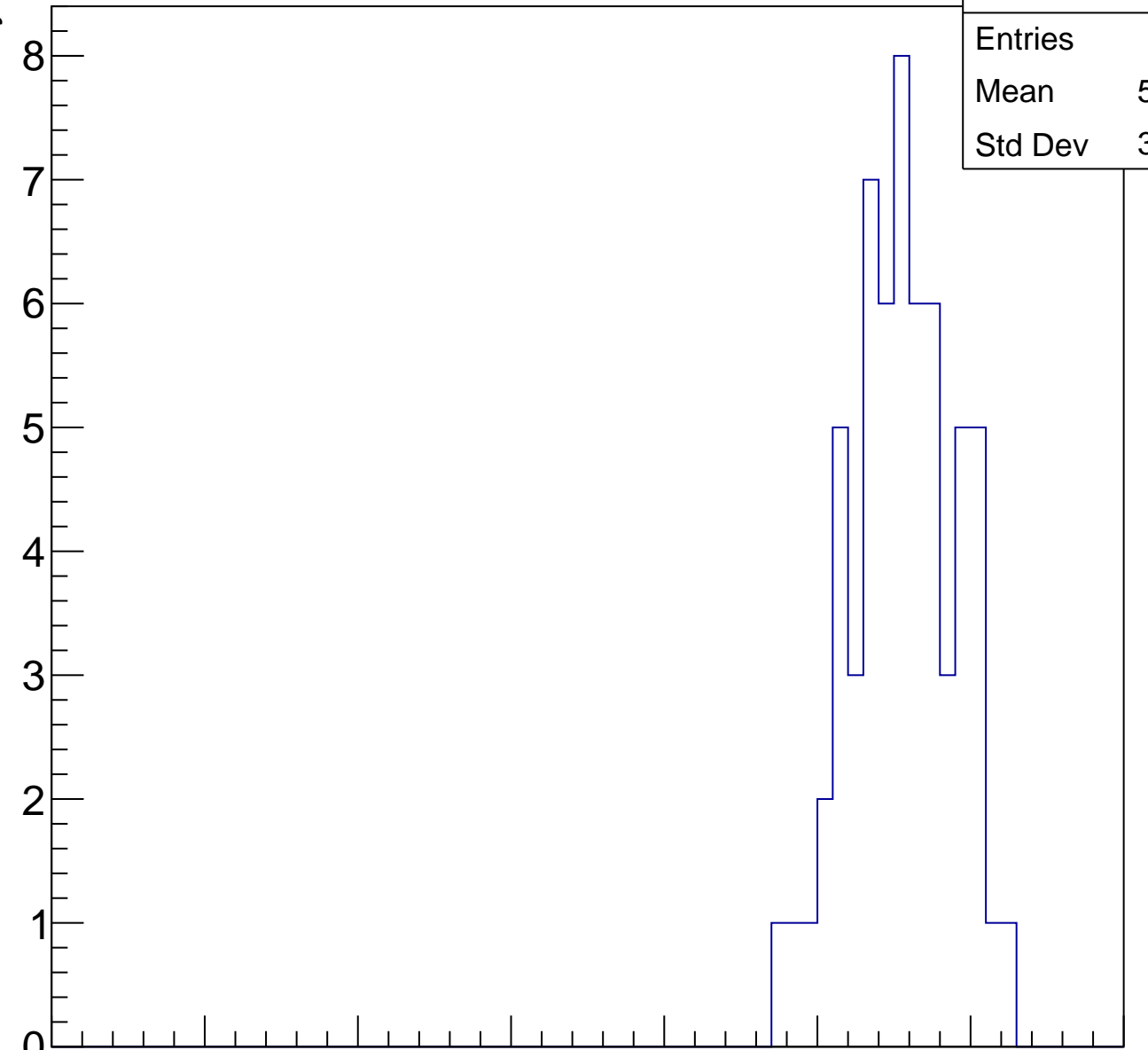
Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	55.08
Std Dev	3.355

ampl

0 10 20 30 40 50 60 70

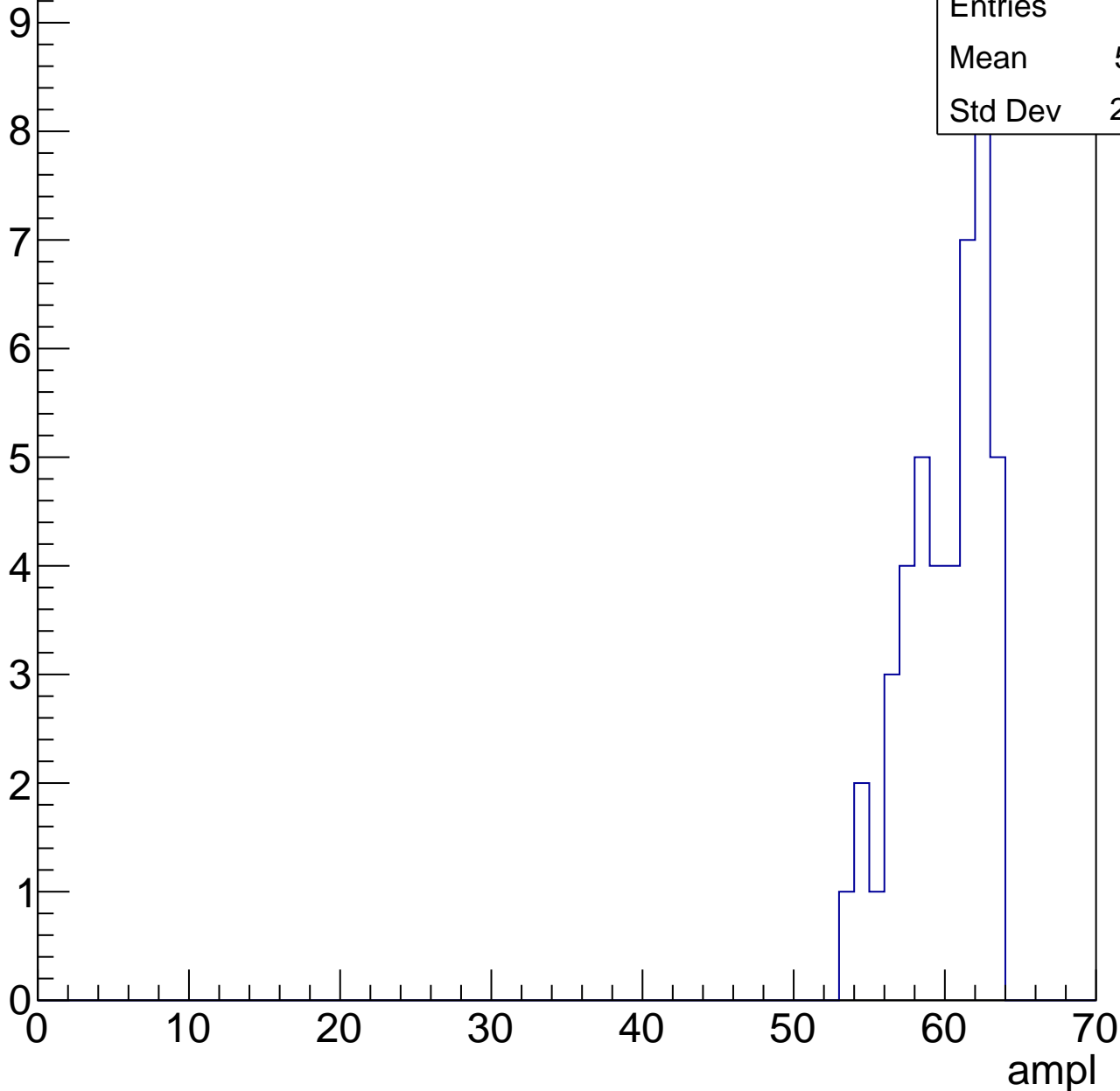


B1L102S, U22-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.51
Std Dev	2.713

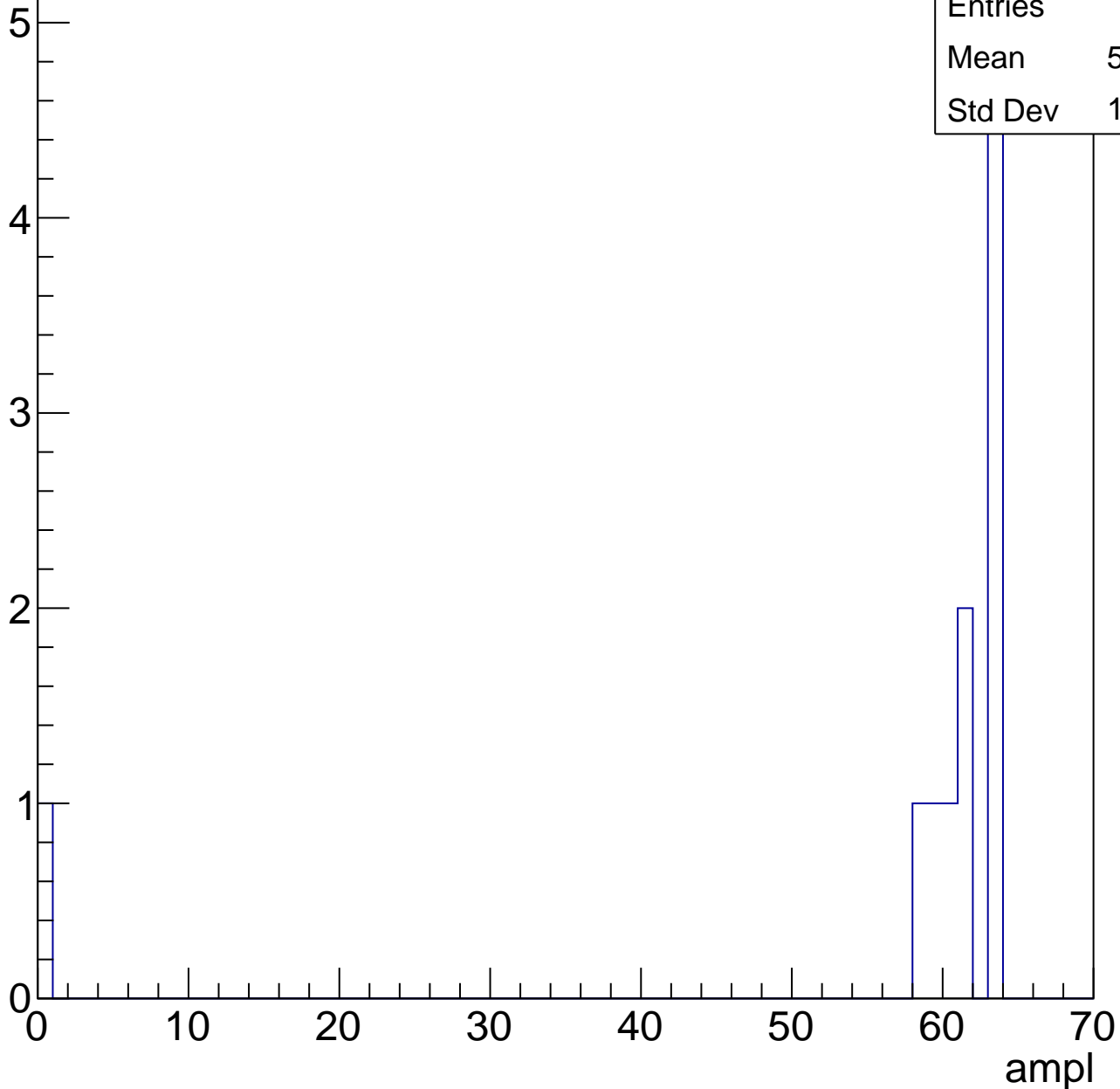


B1L102S, U22-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	55.82
Std Dev	17.73



B1L102S, U22-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch35, adc0

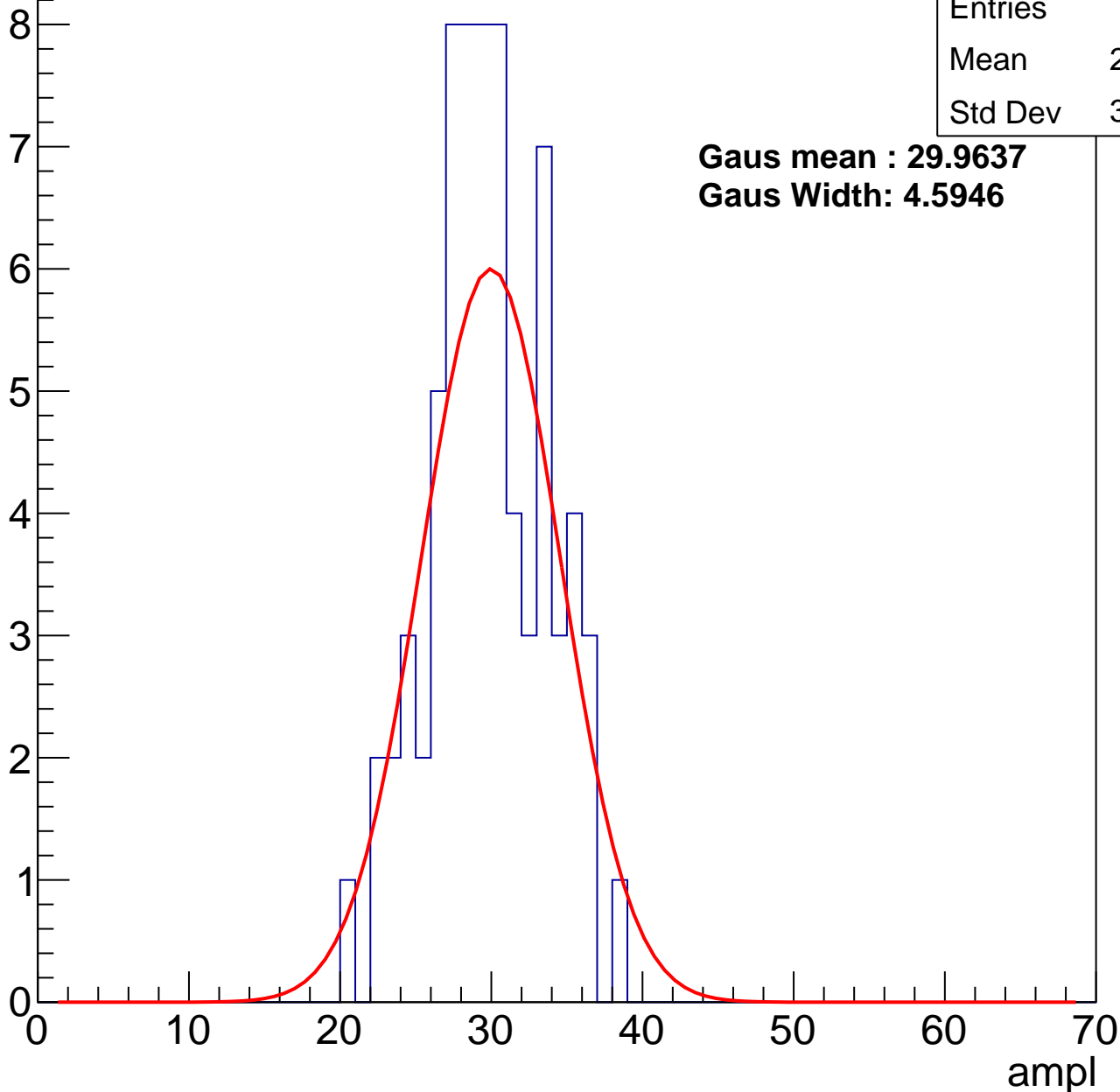
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	29.35
Std Dev	3.808

Gaus mean : 29.9637

Gaus Width: 4.5946



B1L102S, U22-ch35, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	93
Mean	37.08
Std Dev	4.41

Gaus mean : 37.8483

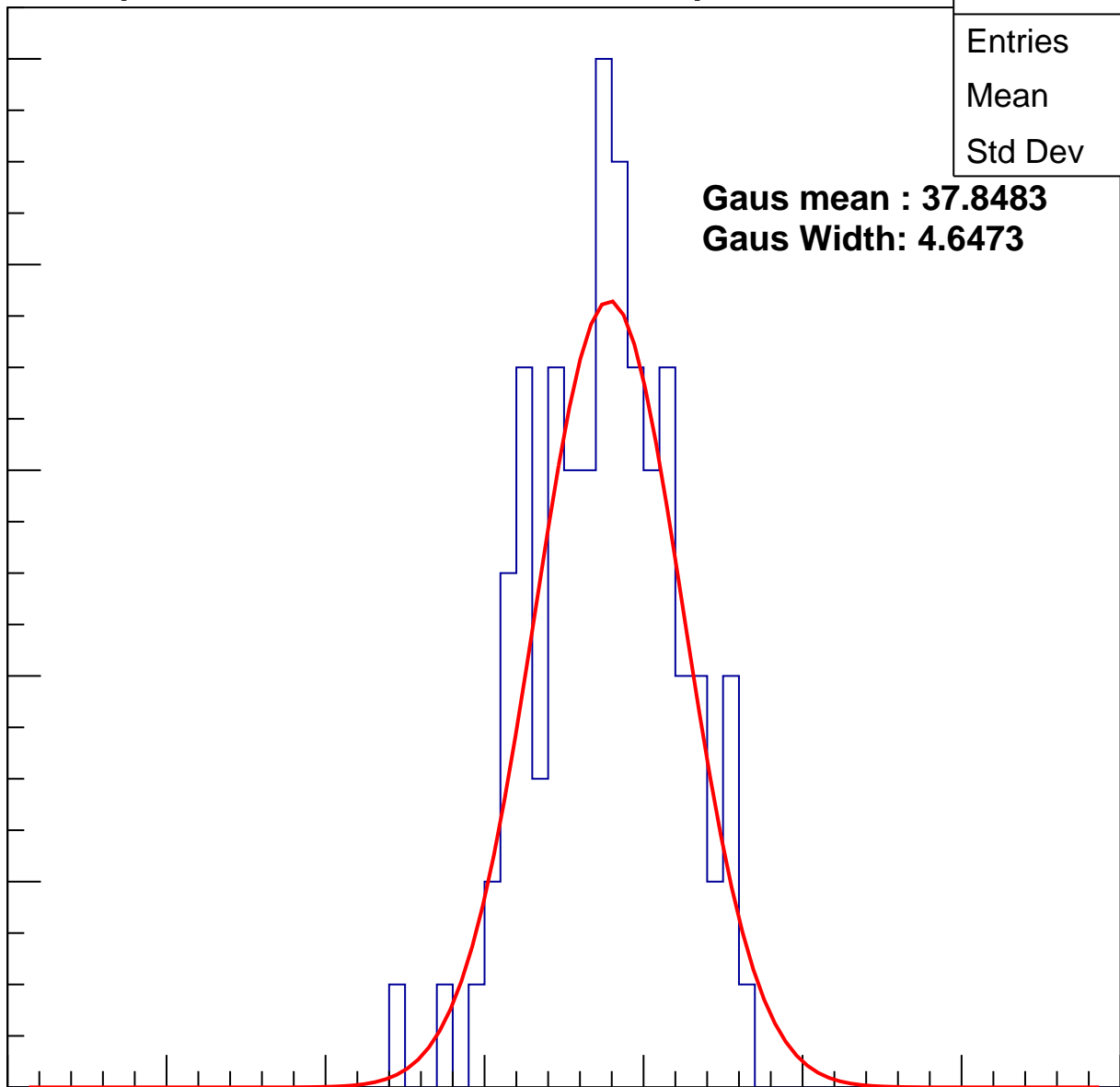
Gaus Width: 4.6473

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch35, adc2

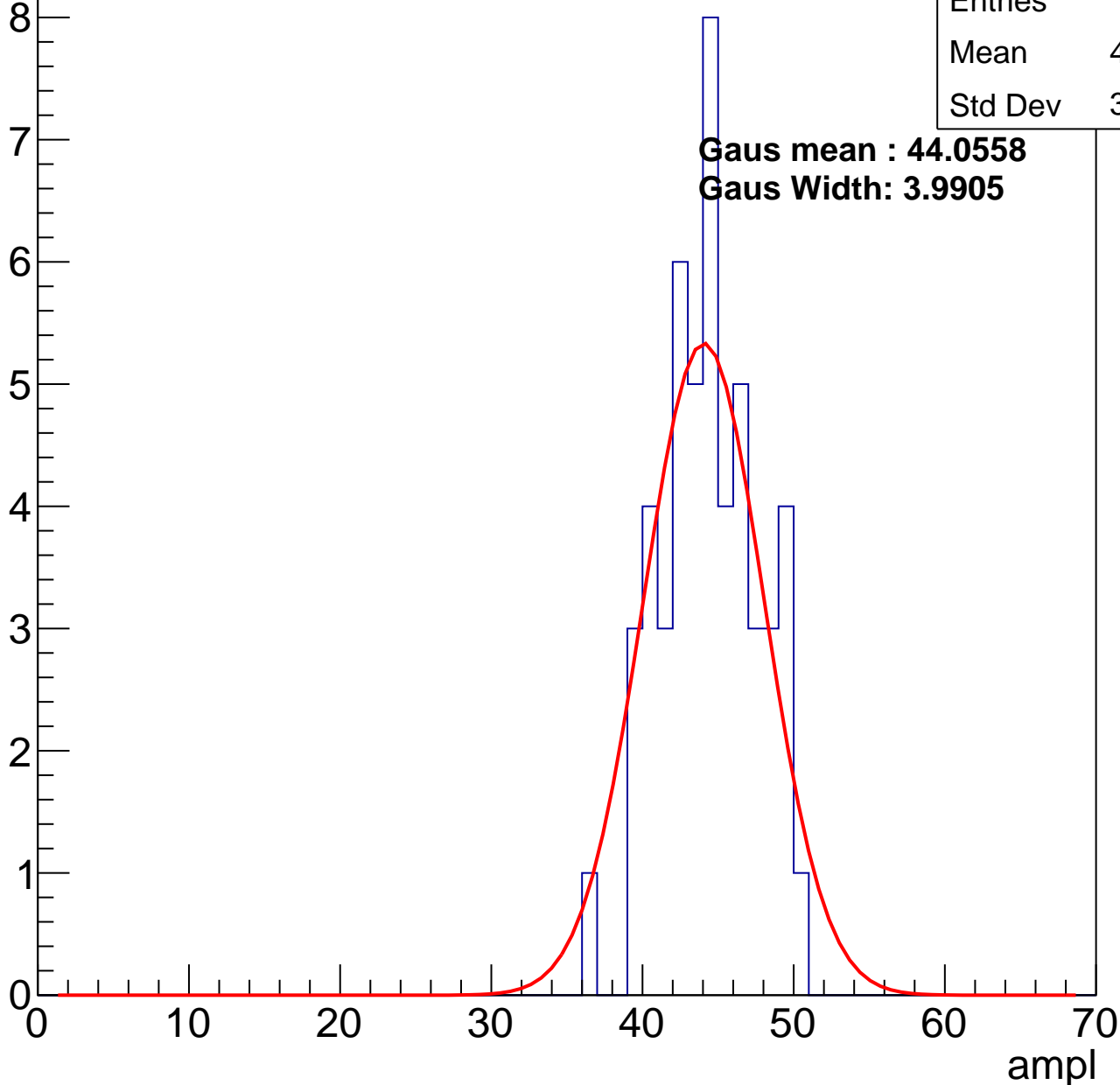
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	43.92
Std Dev	3.142

Gaus mean : 44.0558

Gaus Width: 3.9905

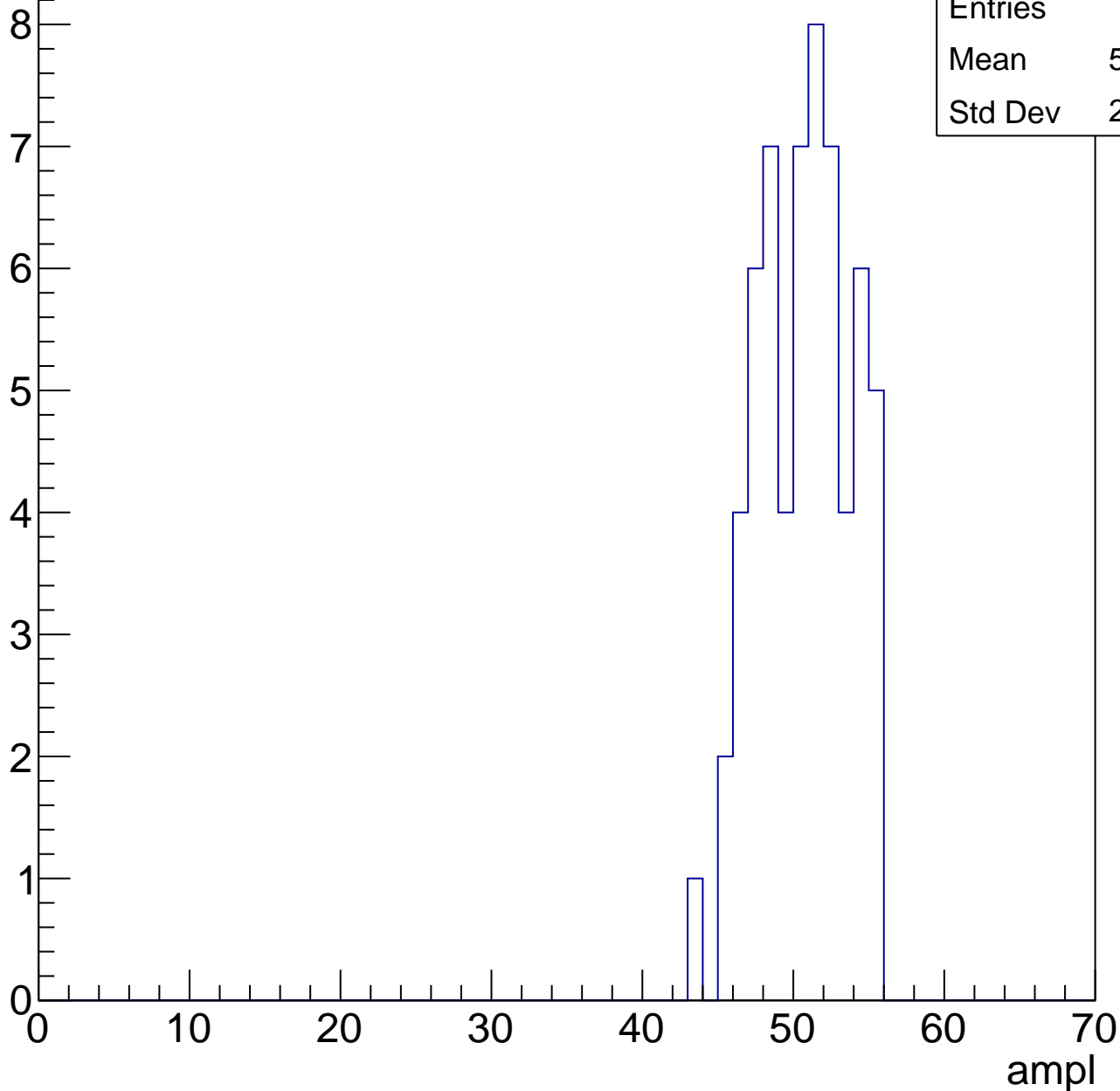


B1L102S, U22-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	50.23
Std Dev	2.972



B1L102S, U22-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	56.18
Std Dev	3.35

Entry

10

8

6

4

2

0

0

10

20

30

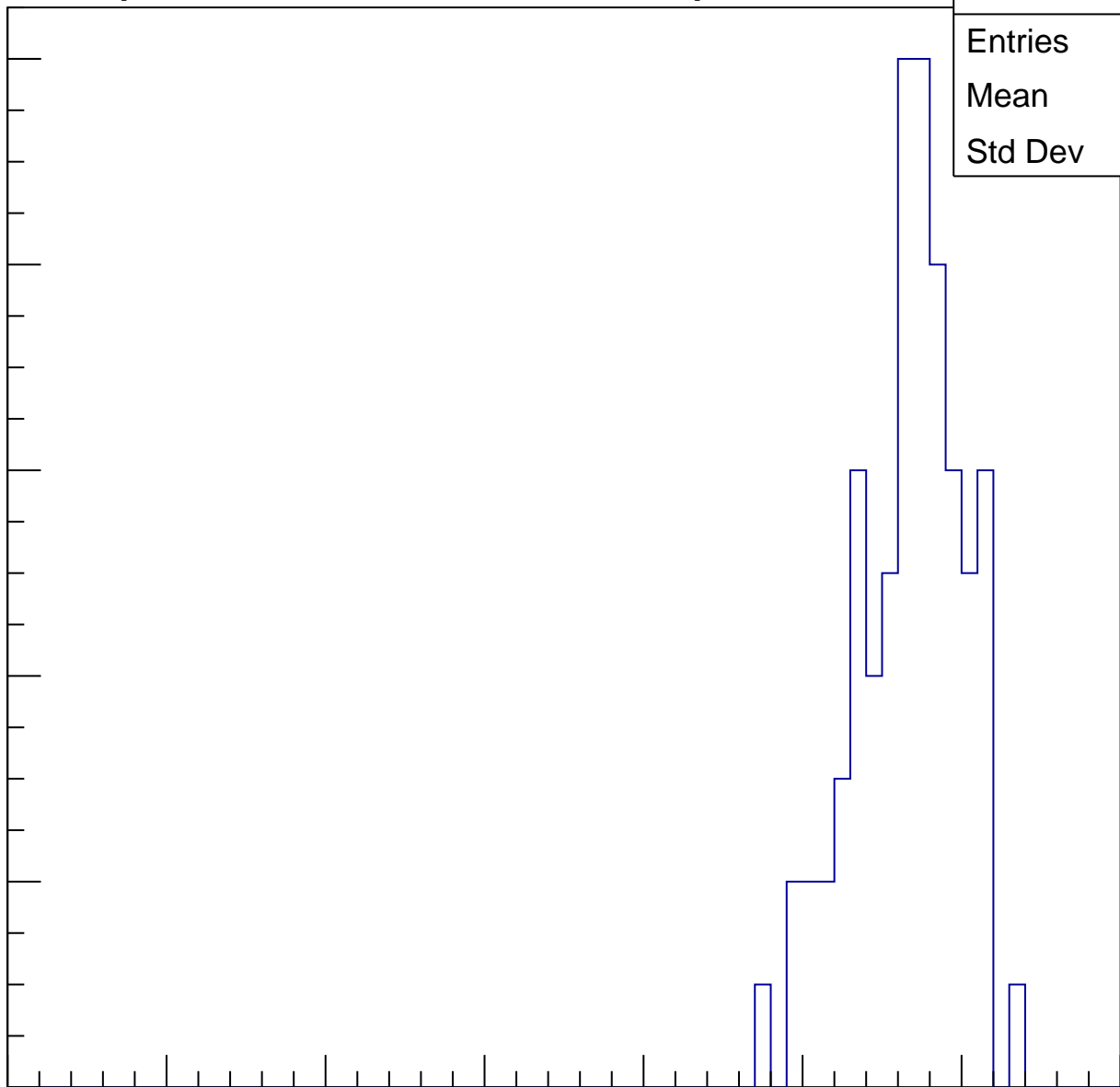
40

50

60

ampl

70

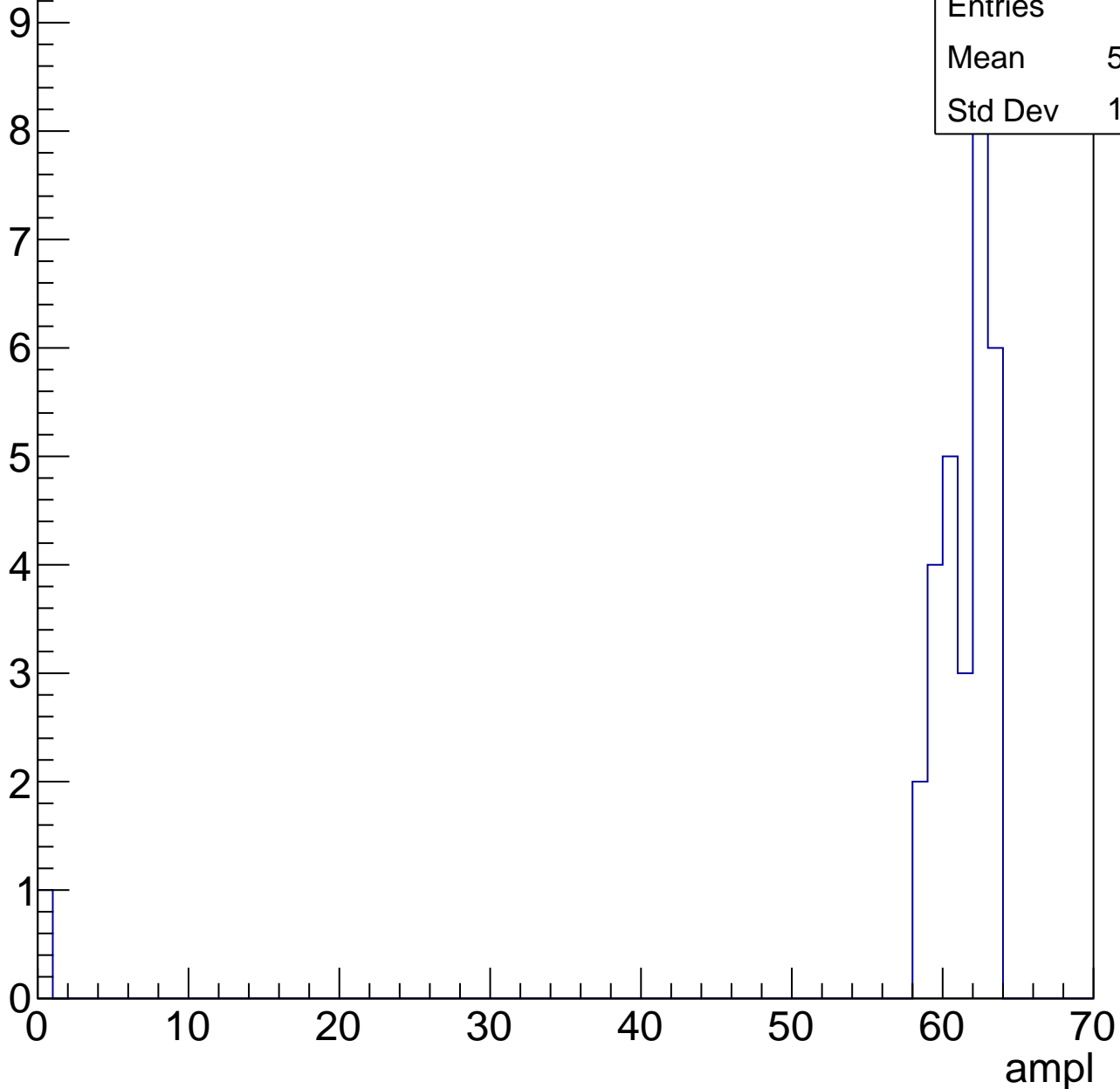


B1L102S, U22-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	59.03
Std Dev	11.07



B1L102S, U22-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	62.4
Std Dev	0.8

B1L102S, U22-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U22-ch36, adc0

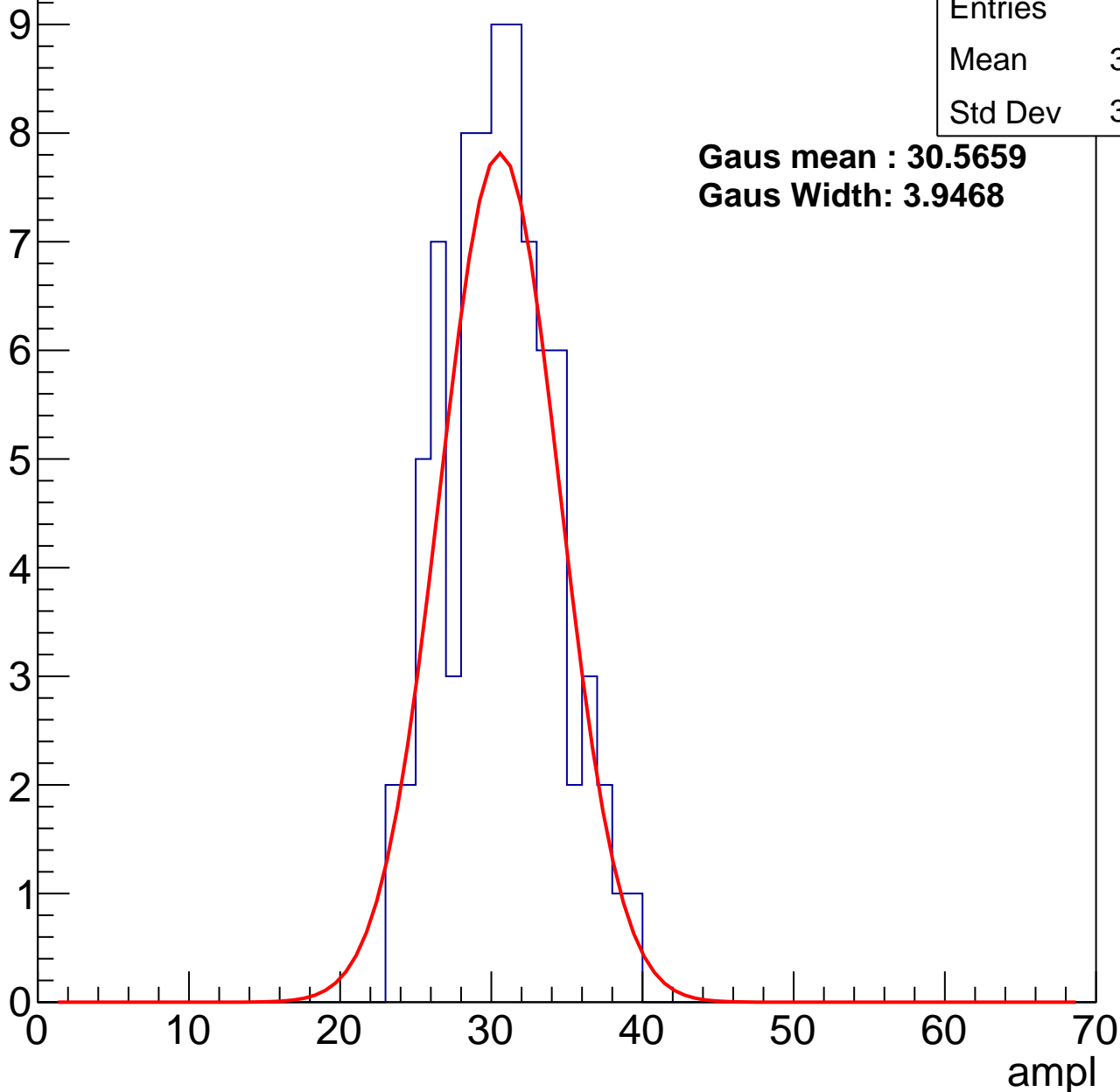
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	30.15
Std Dev	3.618

Gaus mean : 30.5659

Gaus Width: 3.9468



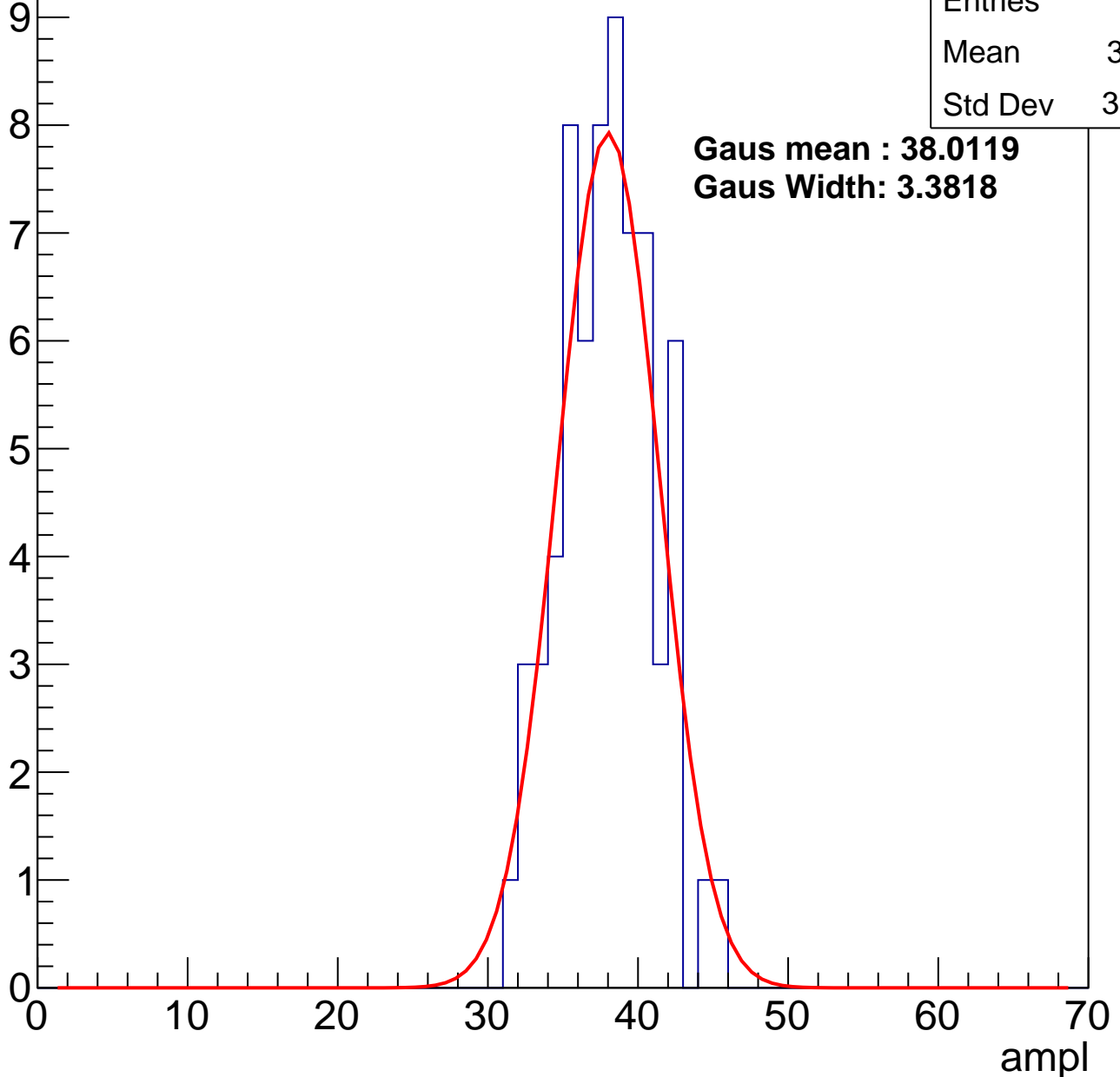
B1L102S, U22-ch36, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	37.51
Std Dev	3.058

Gaus mean : 38.0119
Gaus Width: 3.3818



B1L102S, U22-ch36, adc2

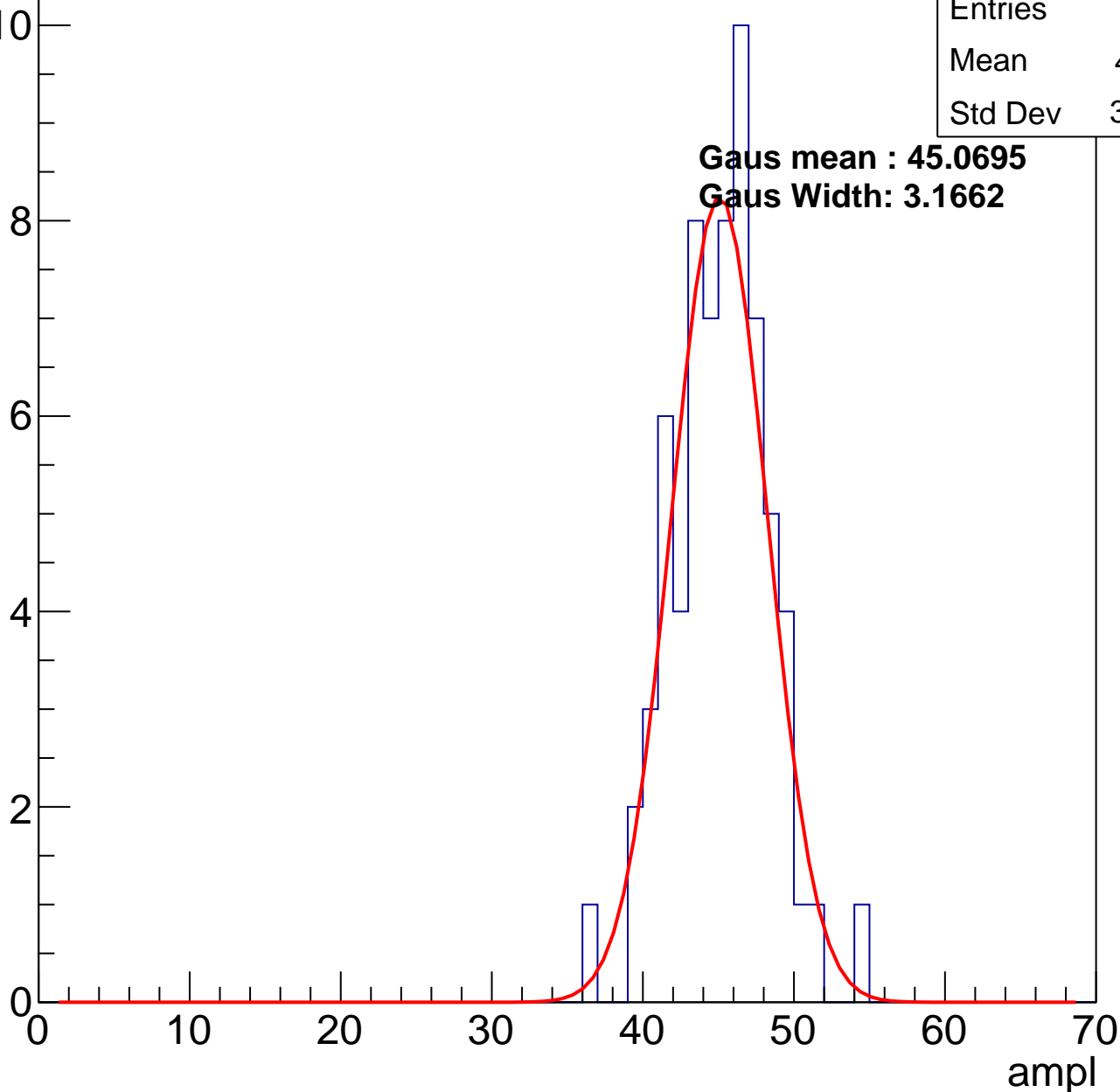
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	44.71
Std Dev	3.167

Gaus mean : 45.0695

Gaus Width: 3.1662

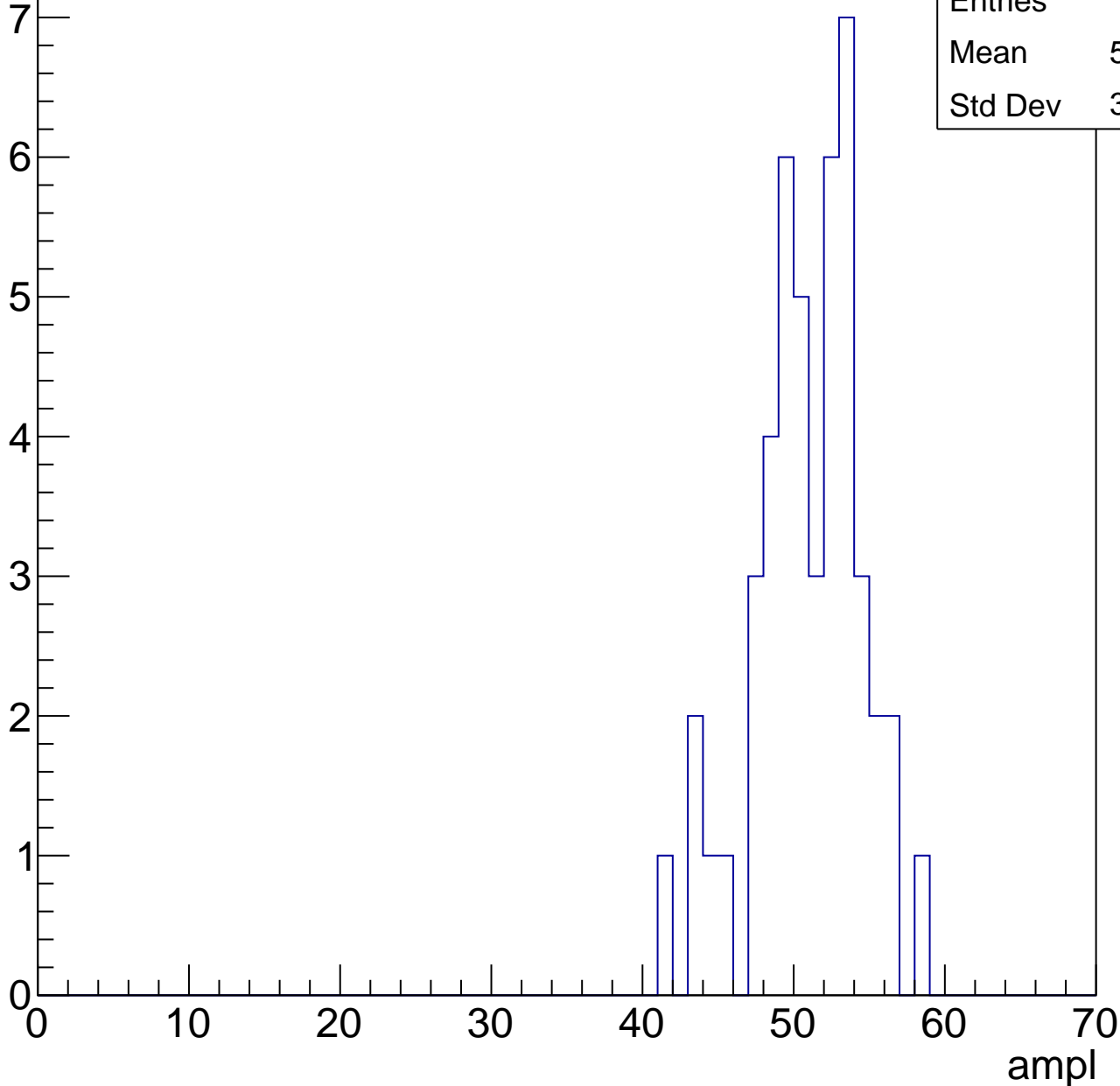


B1L102S, U22-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	50.45
Std Dev	3.584

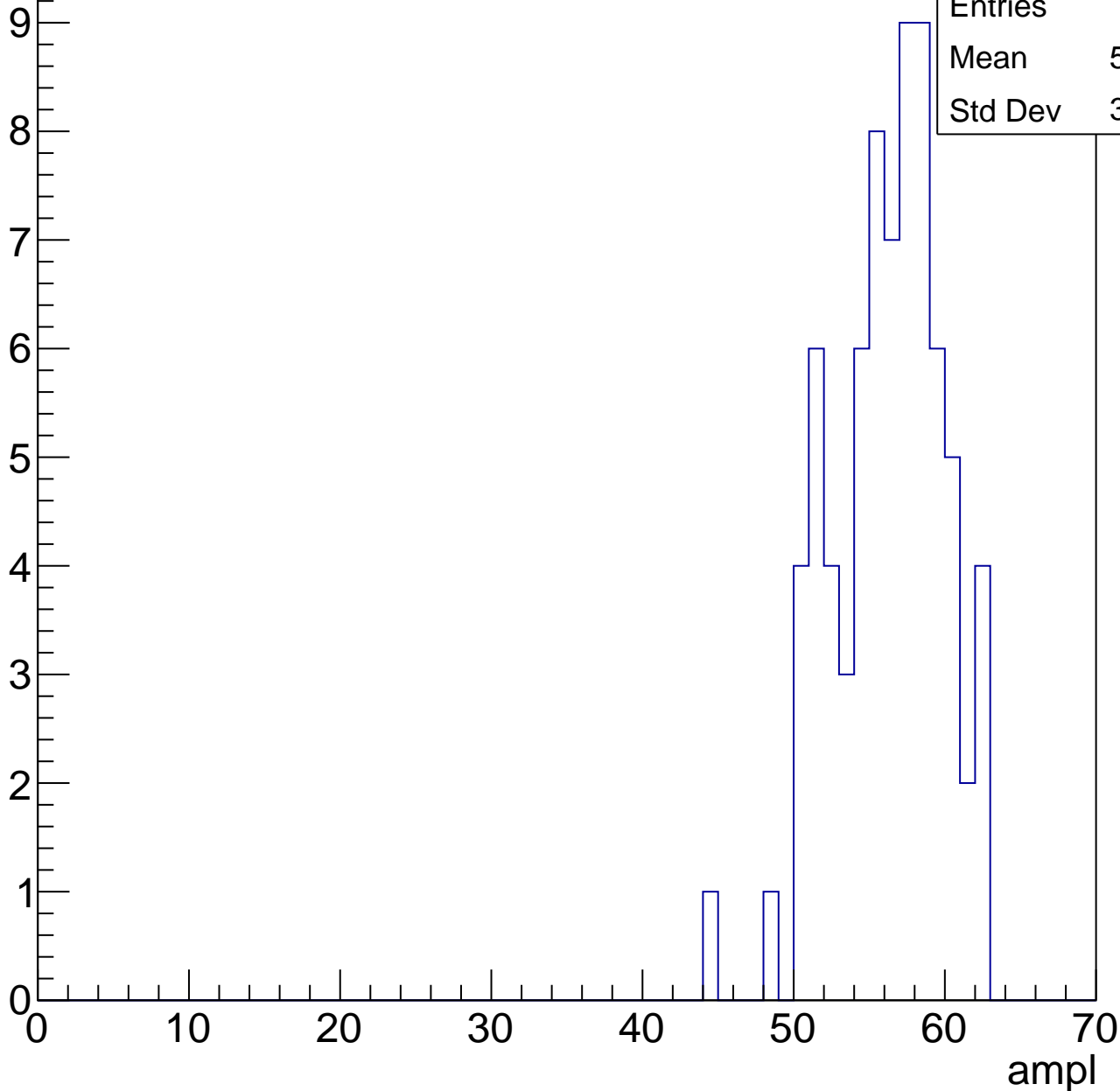


B1L102S, U22-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	55.73
Std Dev	3.638

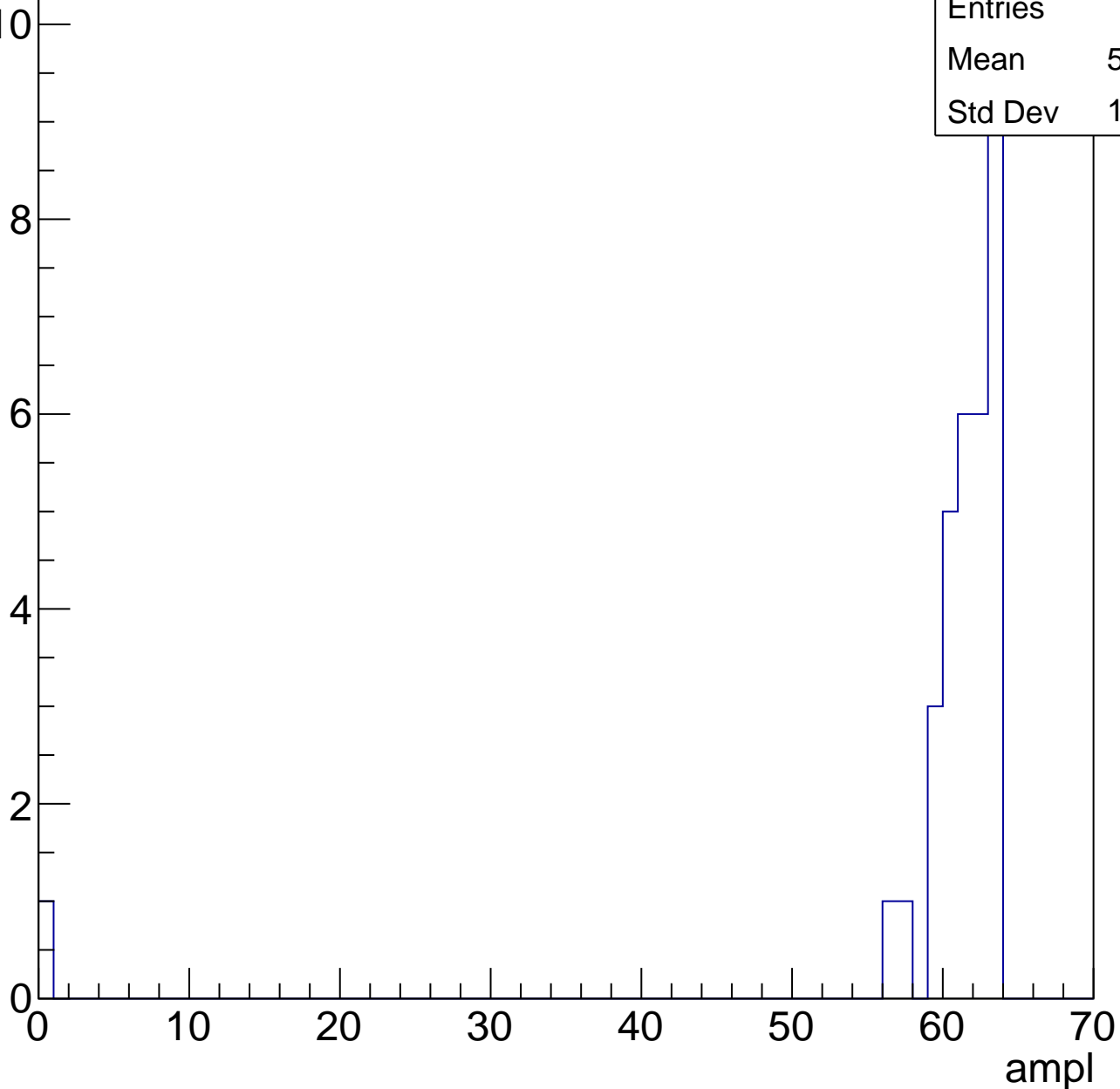


B1L102S, U22-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

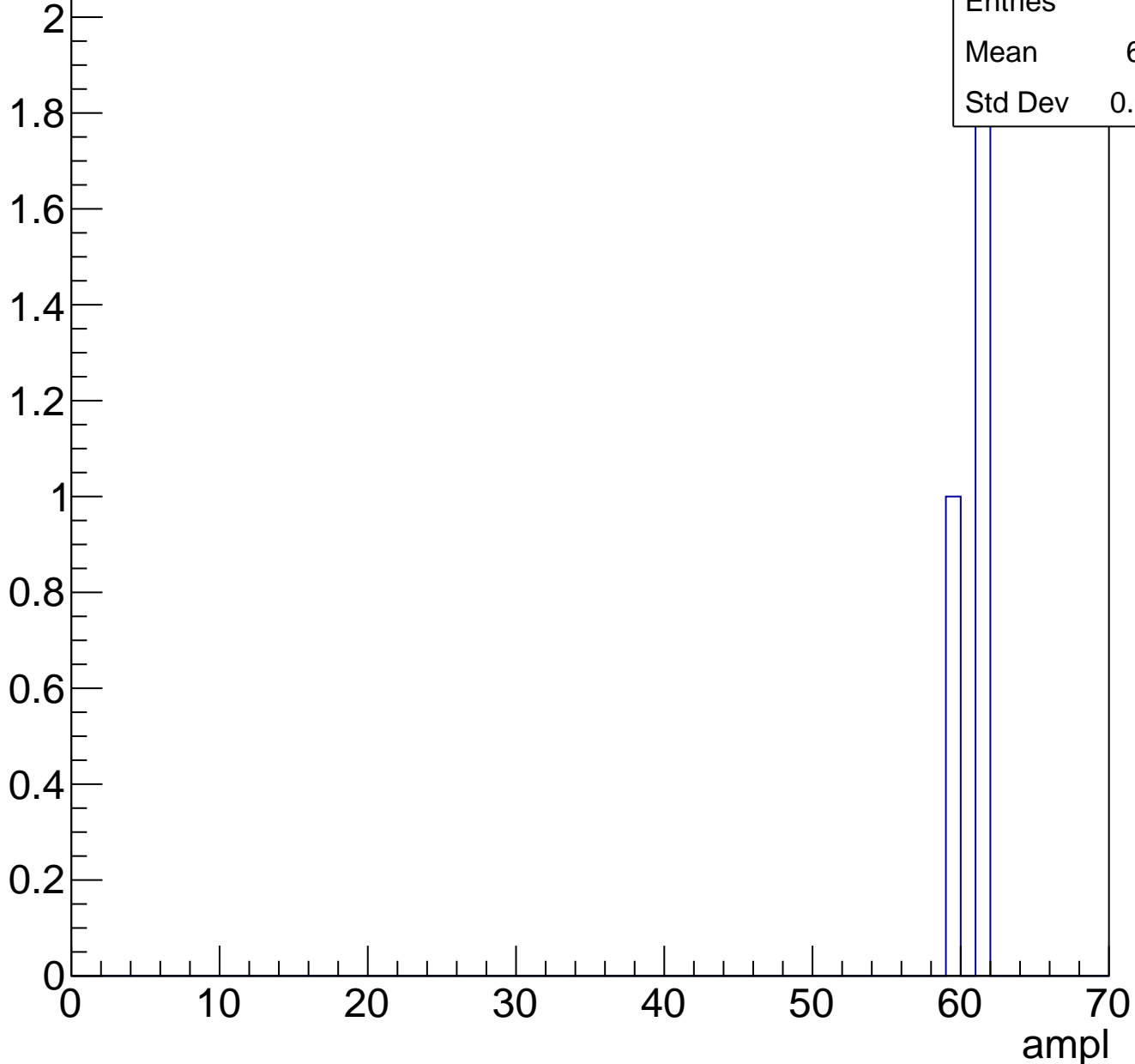
Entries	33
Mean	59.33
Std Dev	10.64



B1L102S, U22-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch37, adc0

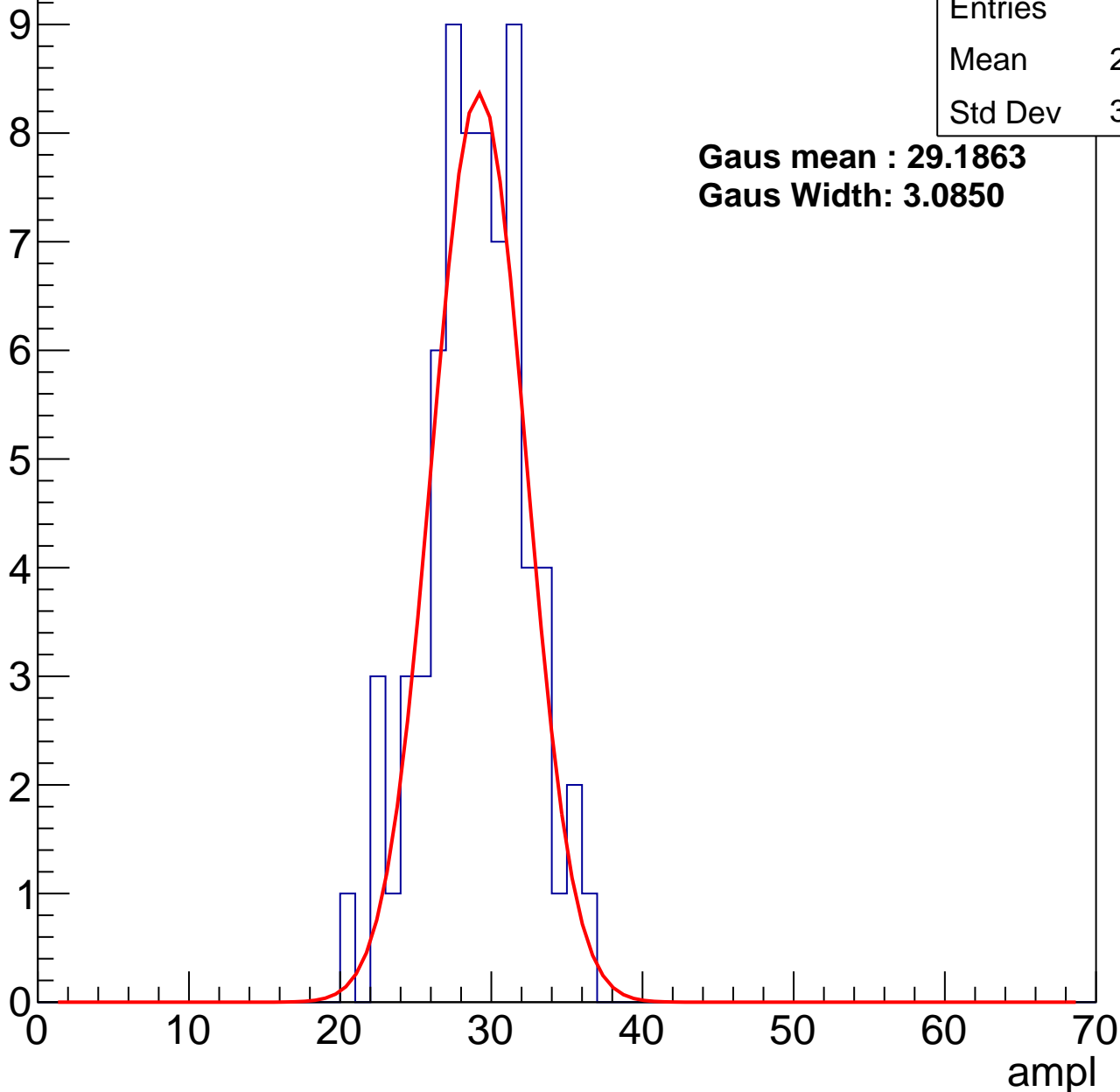
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.57
Std Dev	3.297

Gaus mean : 29.1863

Gaus Width: 3.0850



B1L102S, U22-ch37, adc1

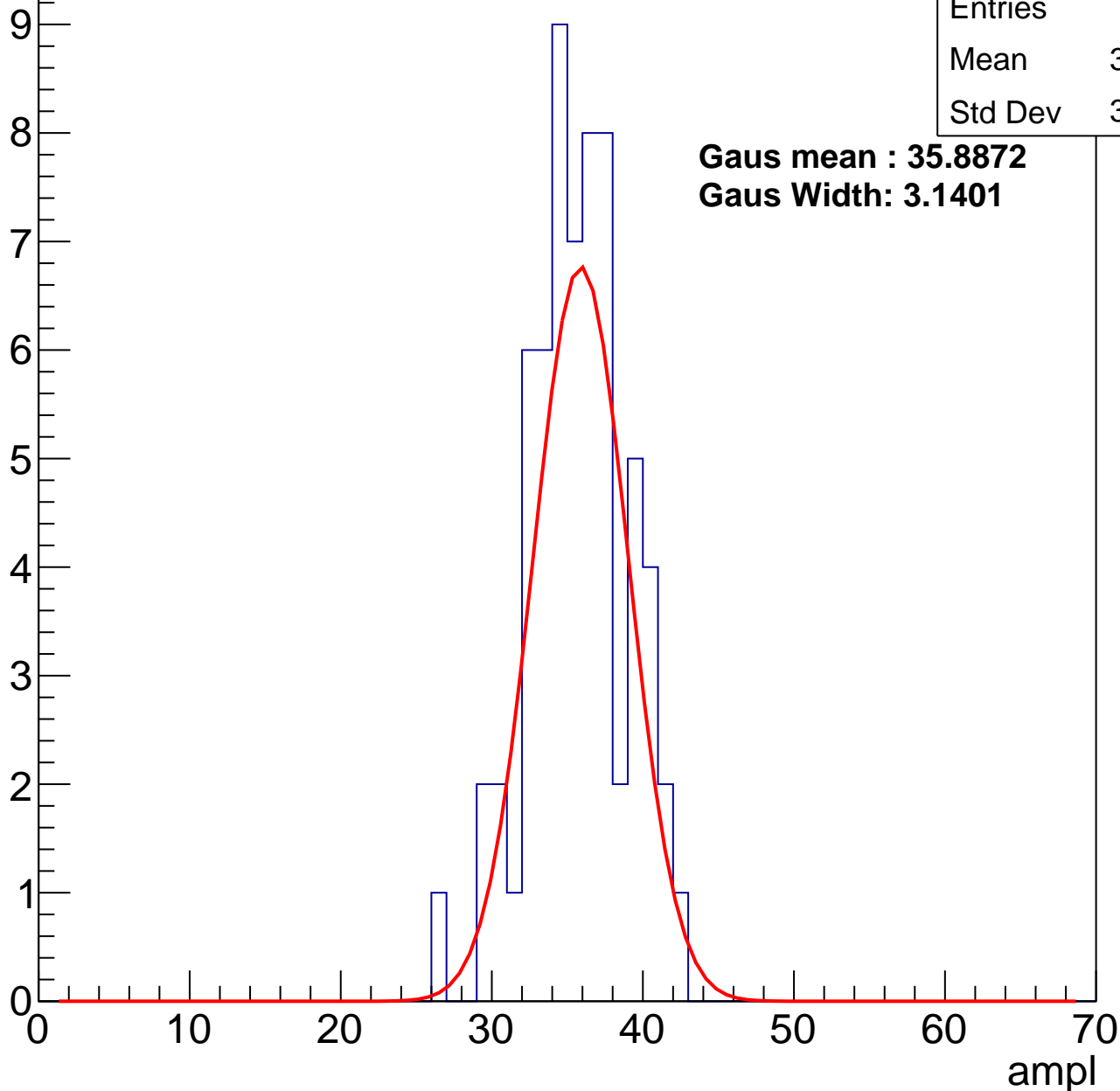
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.23
Std Dev	3.215

Gaus mean : 35.8872

Gaus Width: 3.1401



B1L102S, U22-ch37, adc2

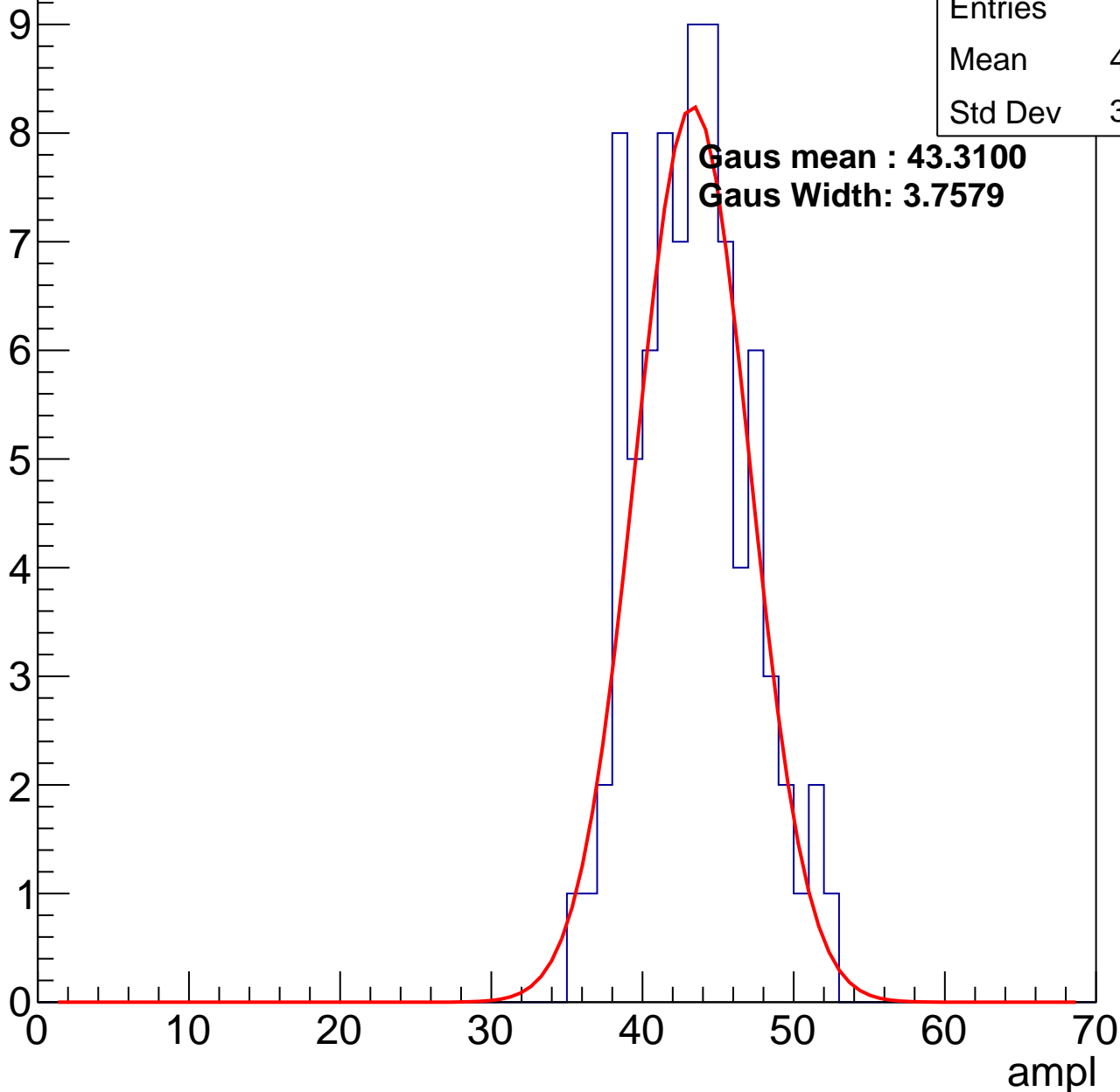
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	42.88
Std Dev	3.713

Gaus mean : 43.3100

Gaus Width: 3.7579

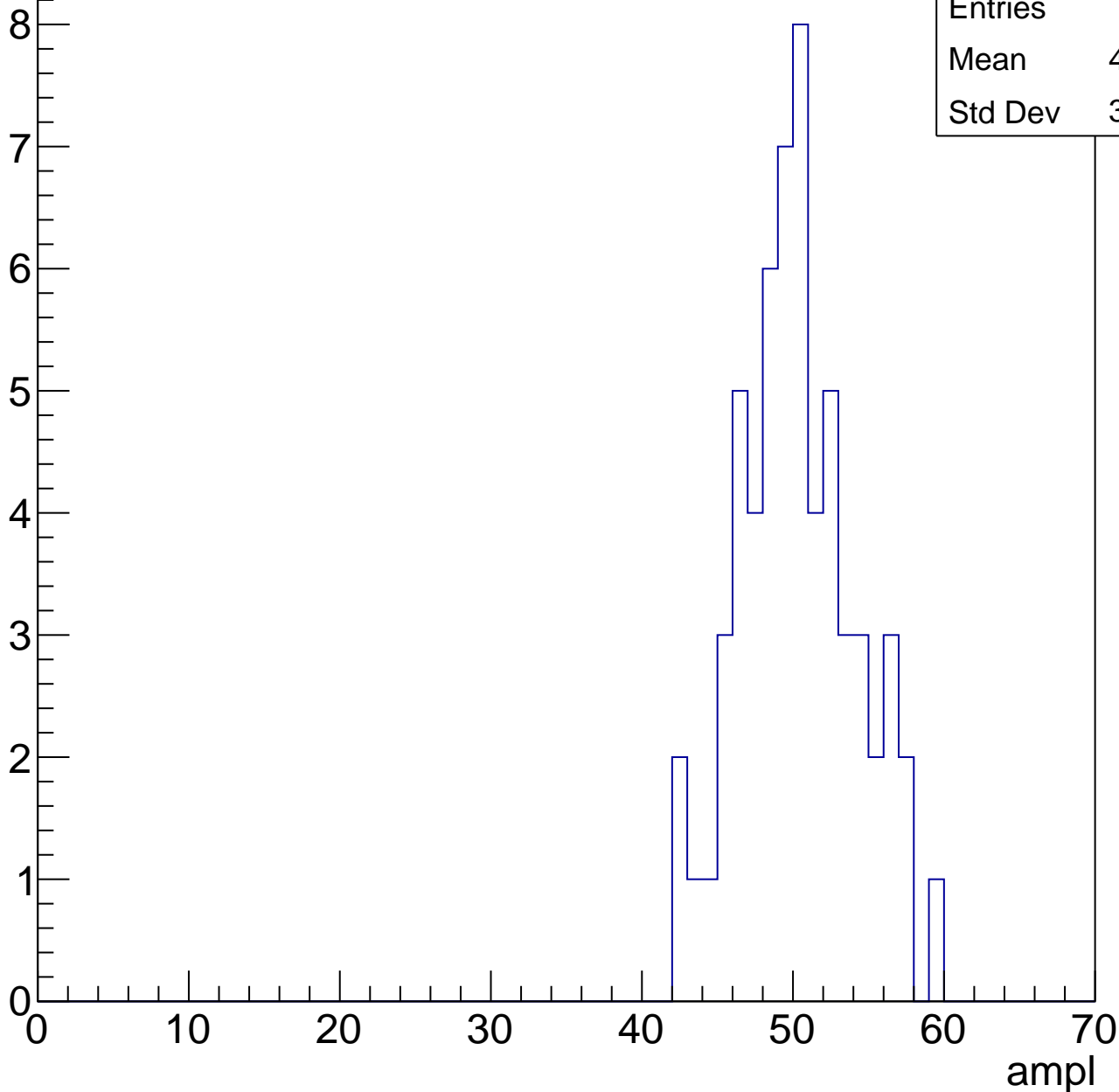


B1L102S, U22-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	49.85
Std Dev	3.807

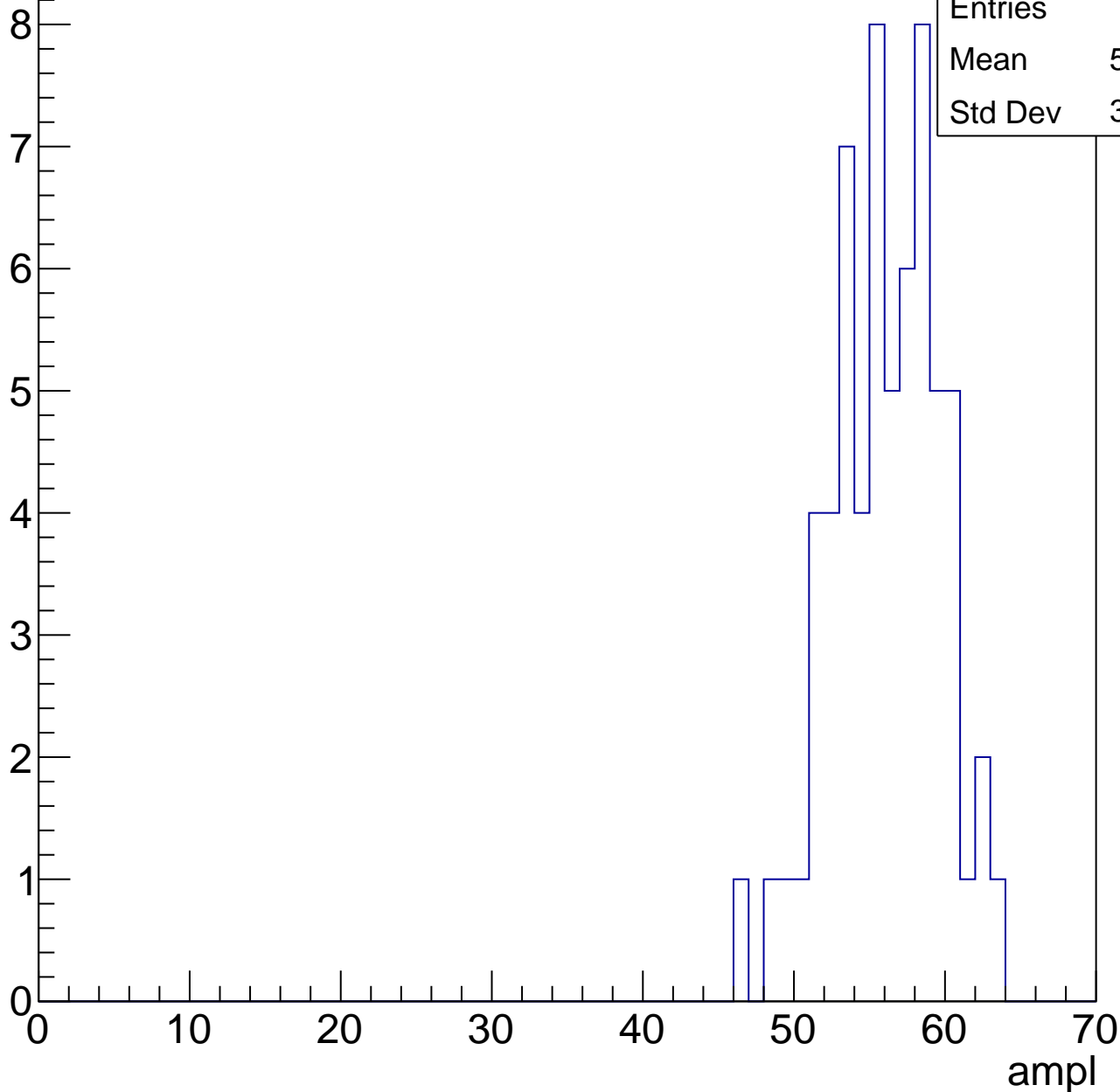


B1L102S, U22-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55.64
Std Dev	3.537

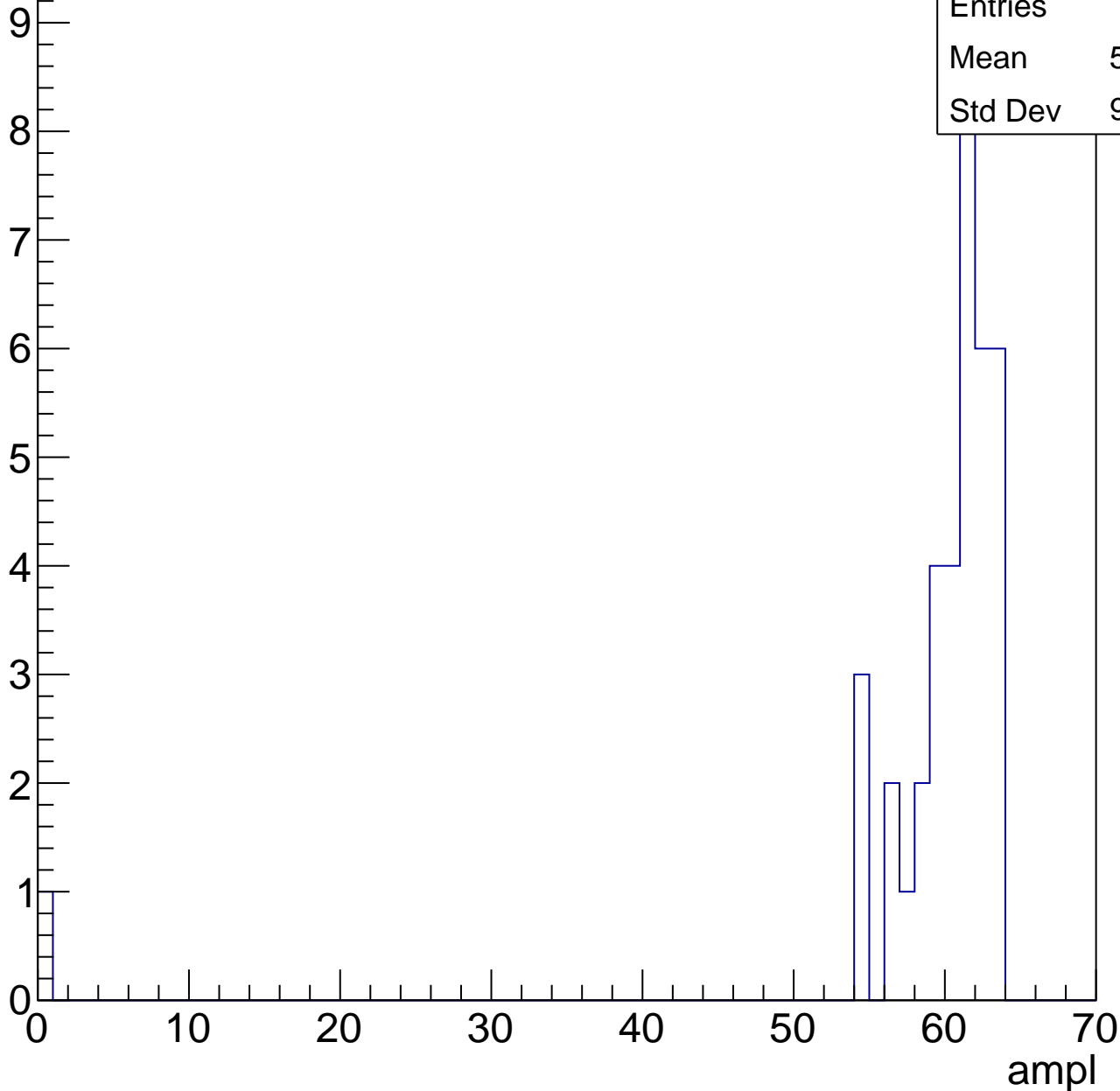


B1L102S, U22-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

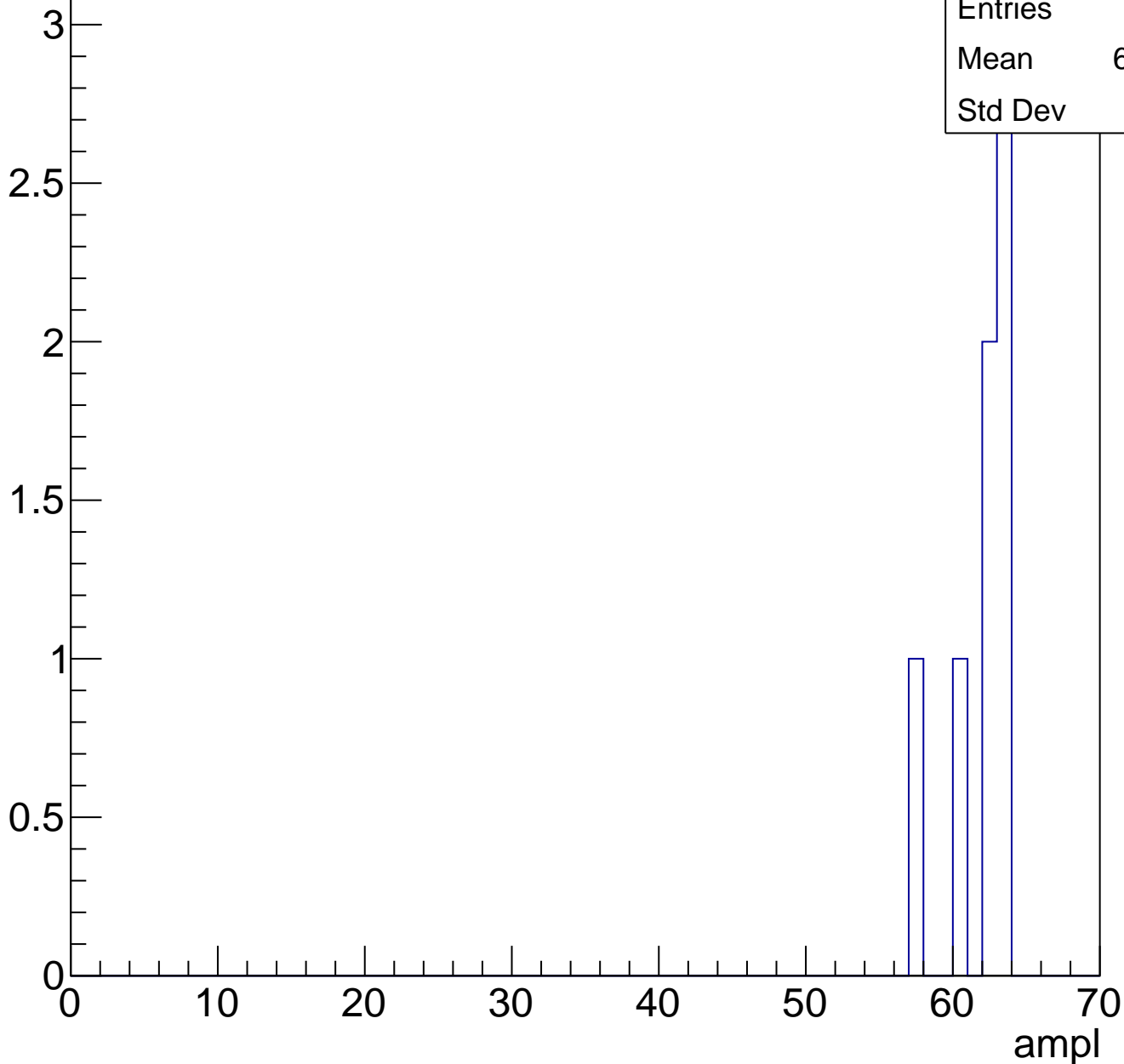
Entries	38
Mean	58.47
Std Dev	9.947



B1L102S, U22-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch38, adc0

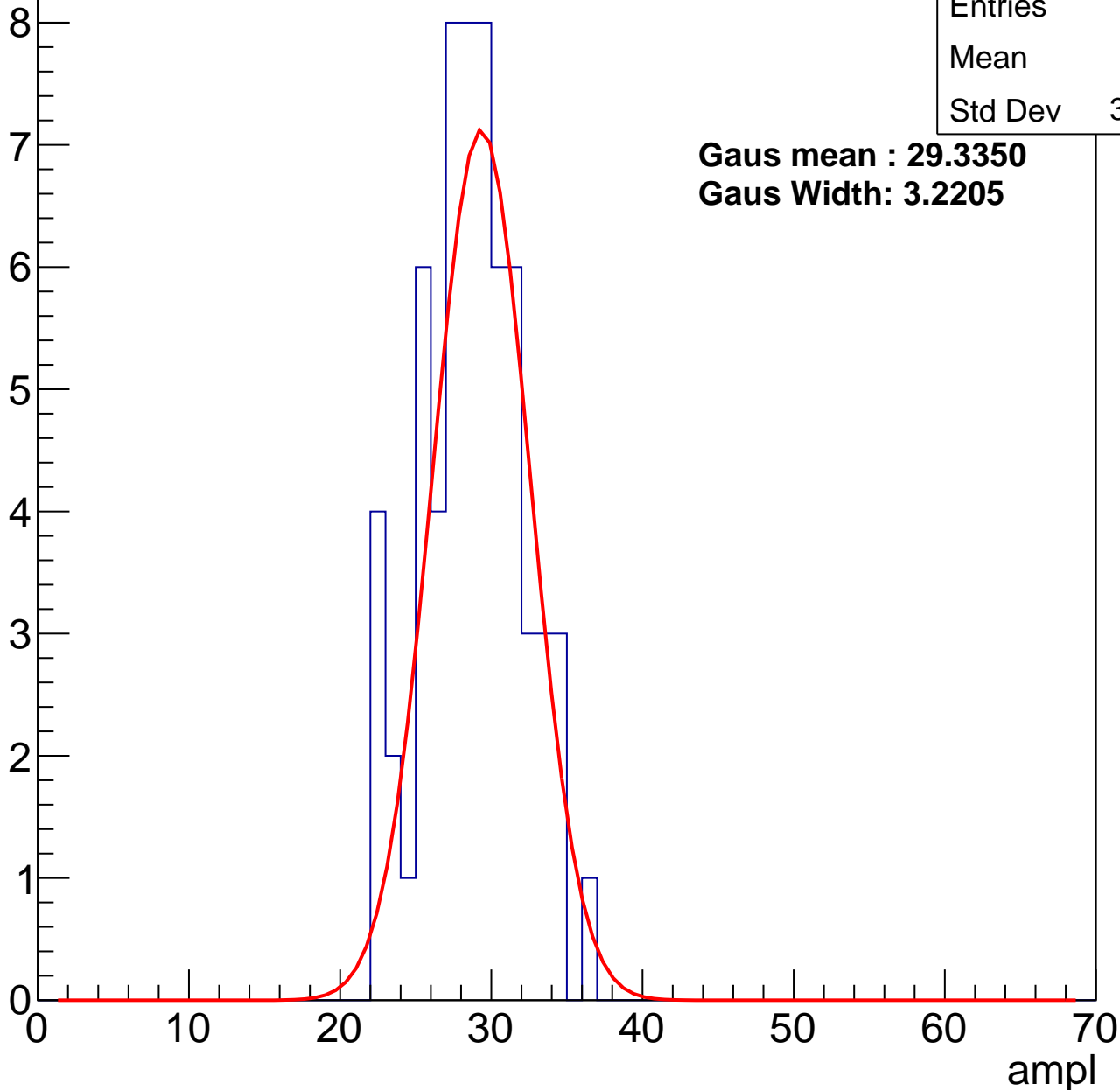
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.3
Std Dev	3.245

Gaus mean : 29.3350

Gaus Width: 3.2205



B1L102S, U22-ch38, adc1

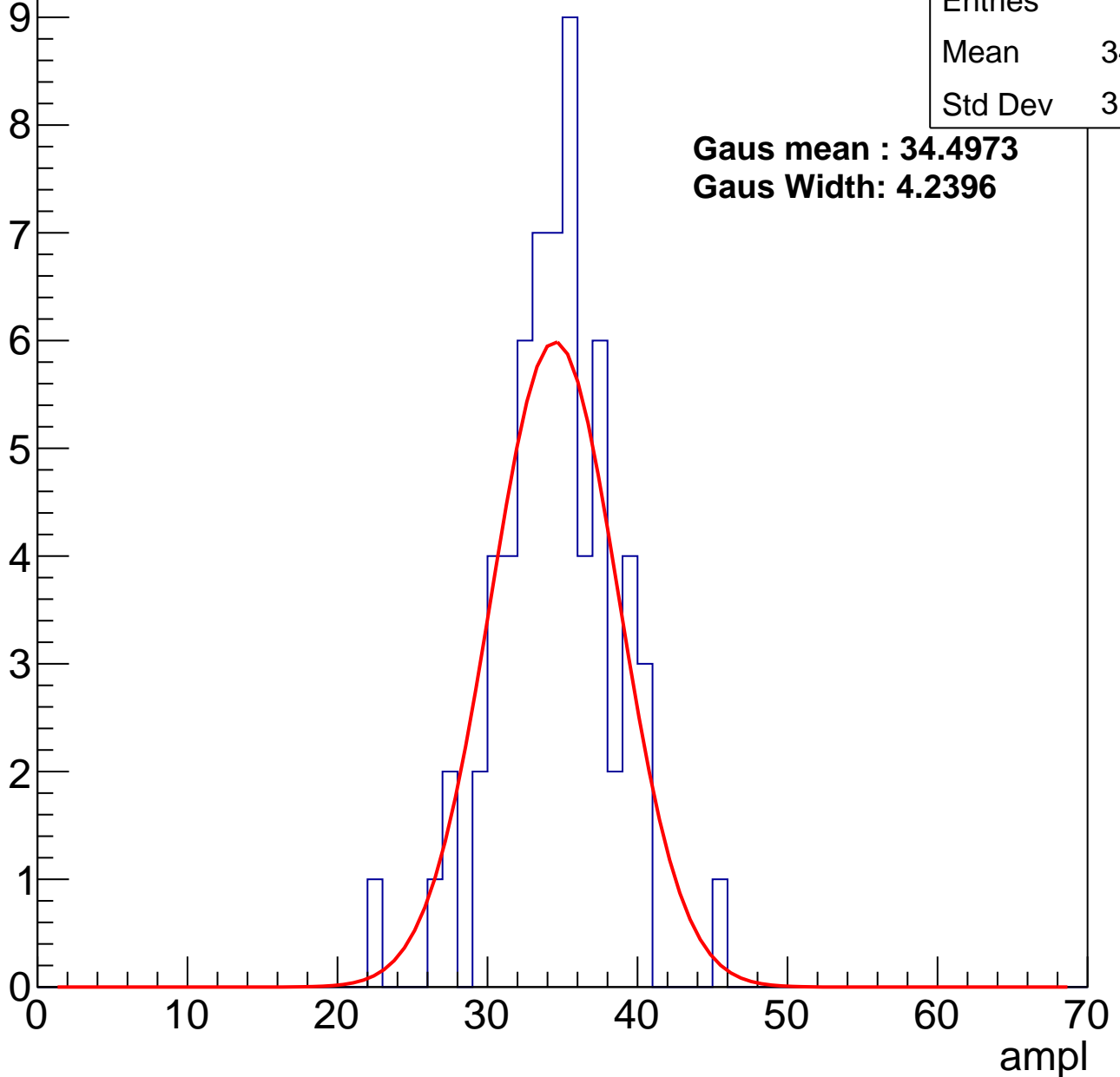
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.02
Std Dev	3.836

Gaus mean : 34.4973

Gaus Width: 4.2396



B1L102S, U22-ch38, adc2

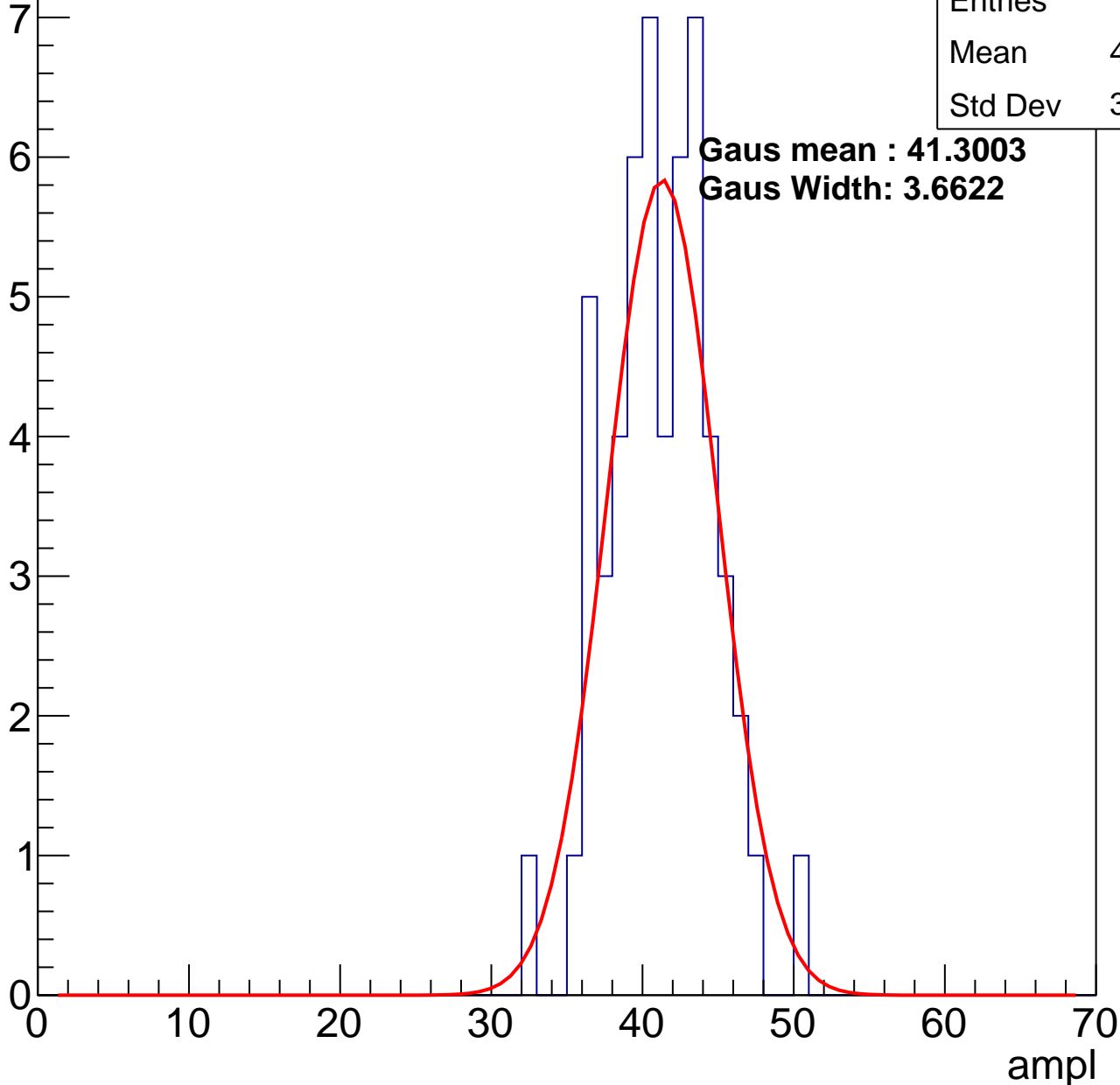
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	40.75
Std Dev	3.407

Gaus mean : 41.3003

Gaus Width: 3.6622

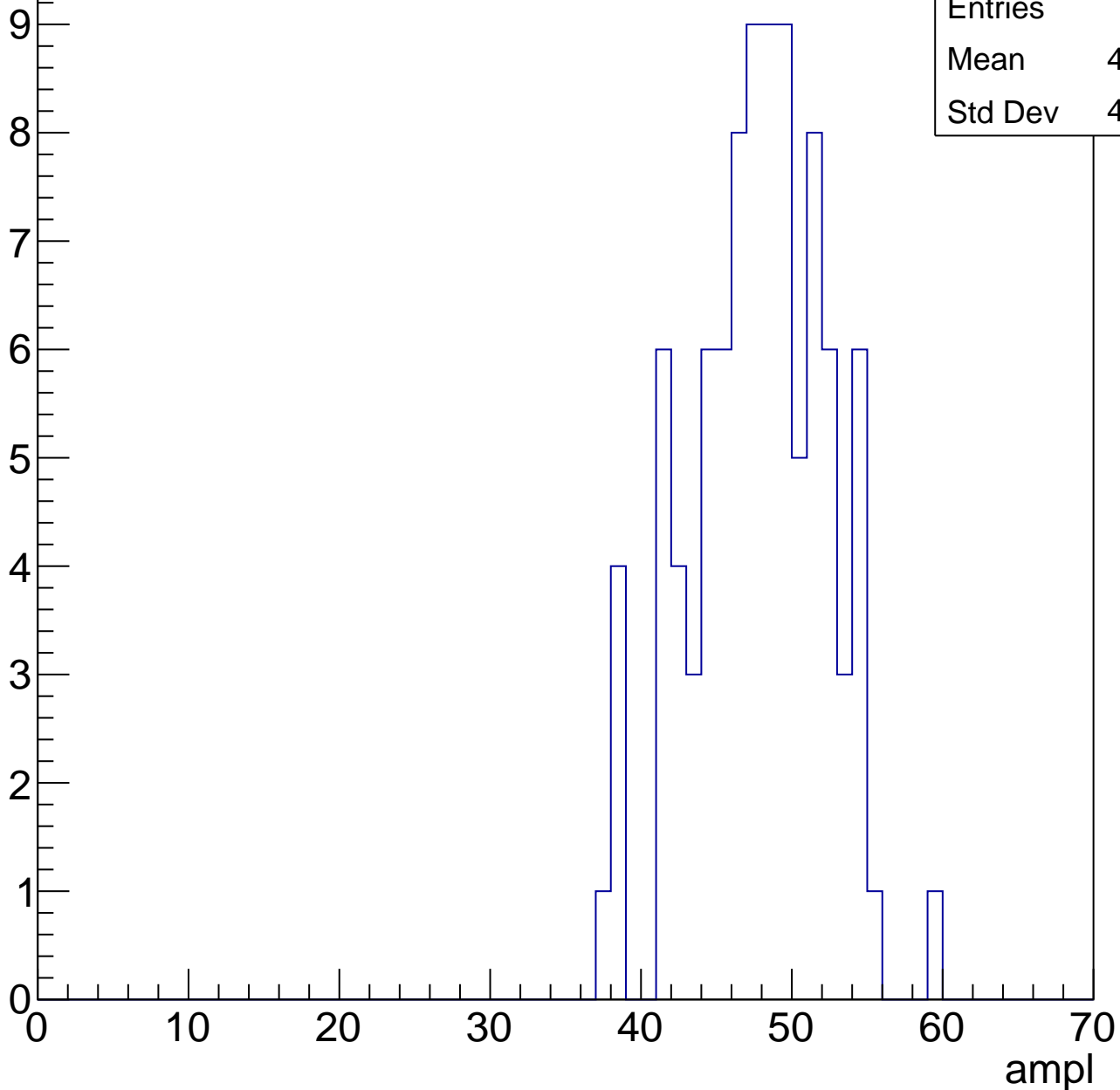


B1L102S, U22-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

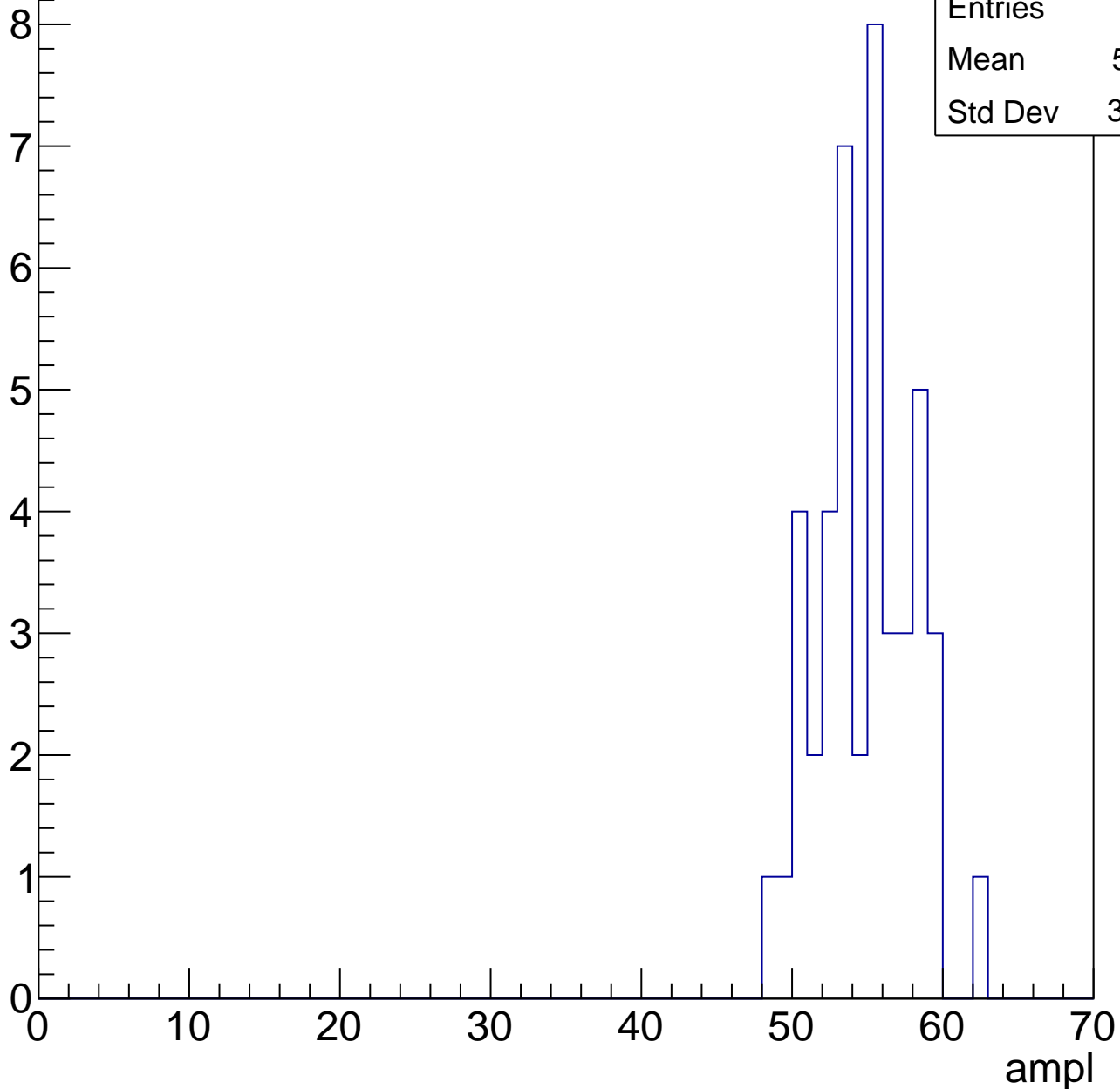
Entries	95
Mean	47.34
Std Dev	4.402



B1L102S, U22-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

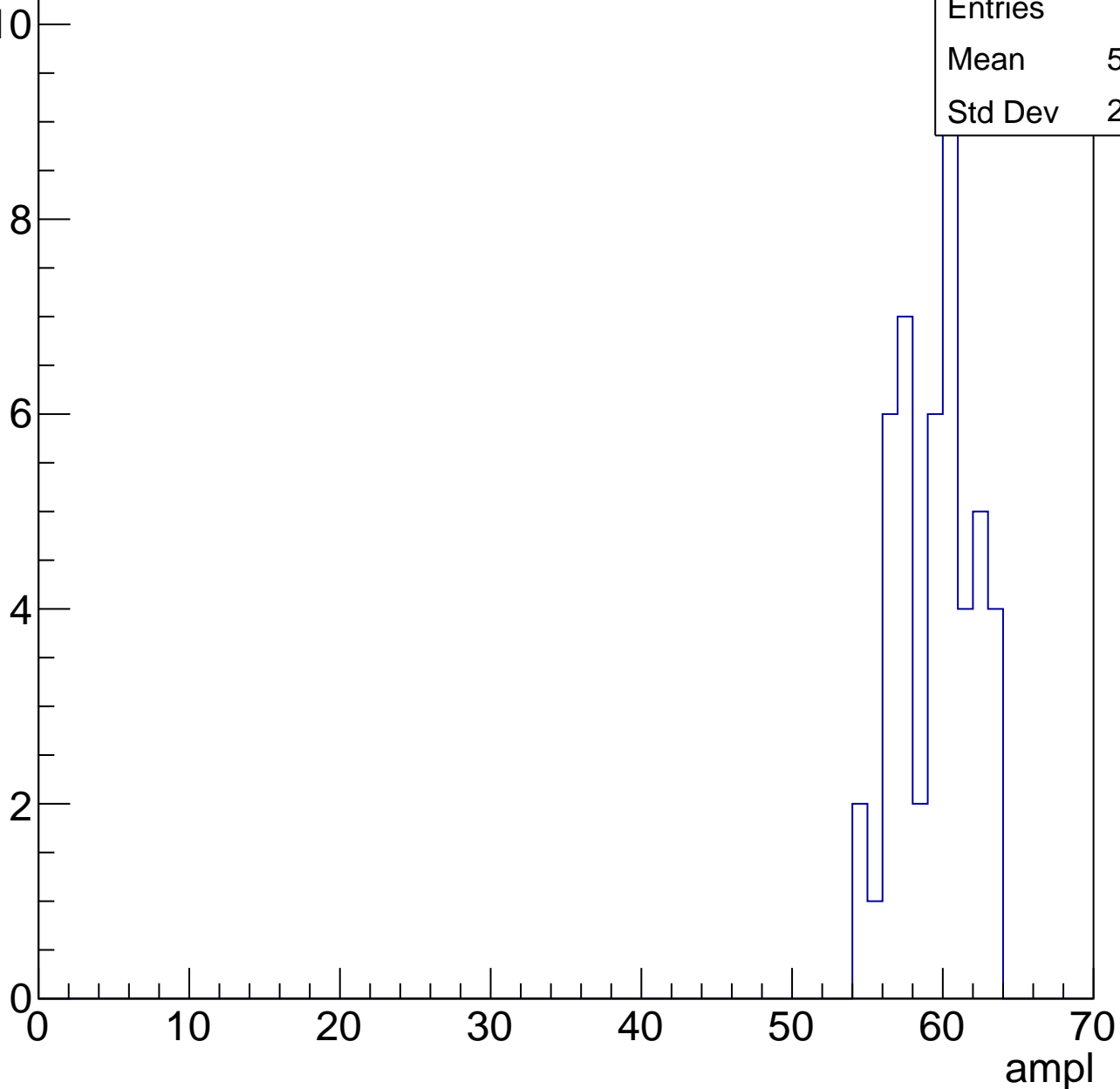


B1L102S, U22-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	59.02
Std Dev	2.462

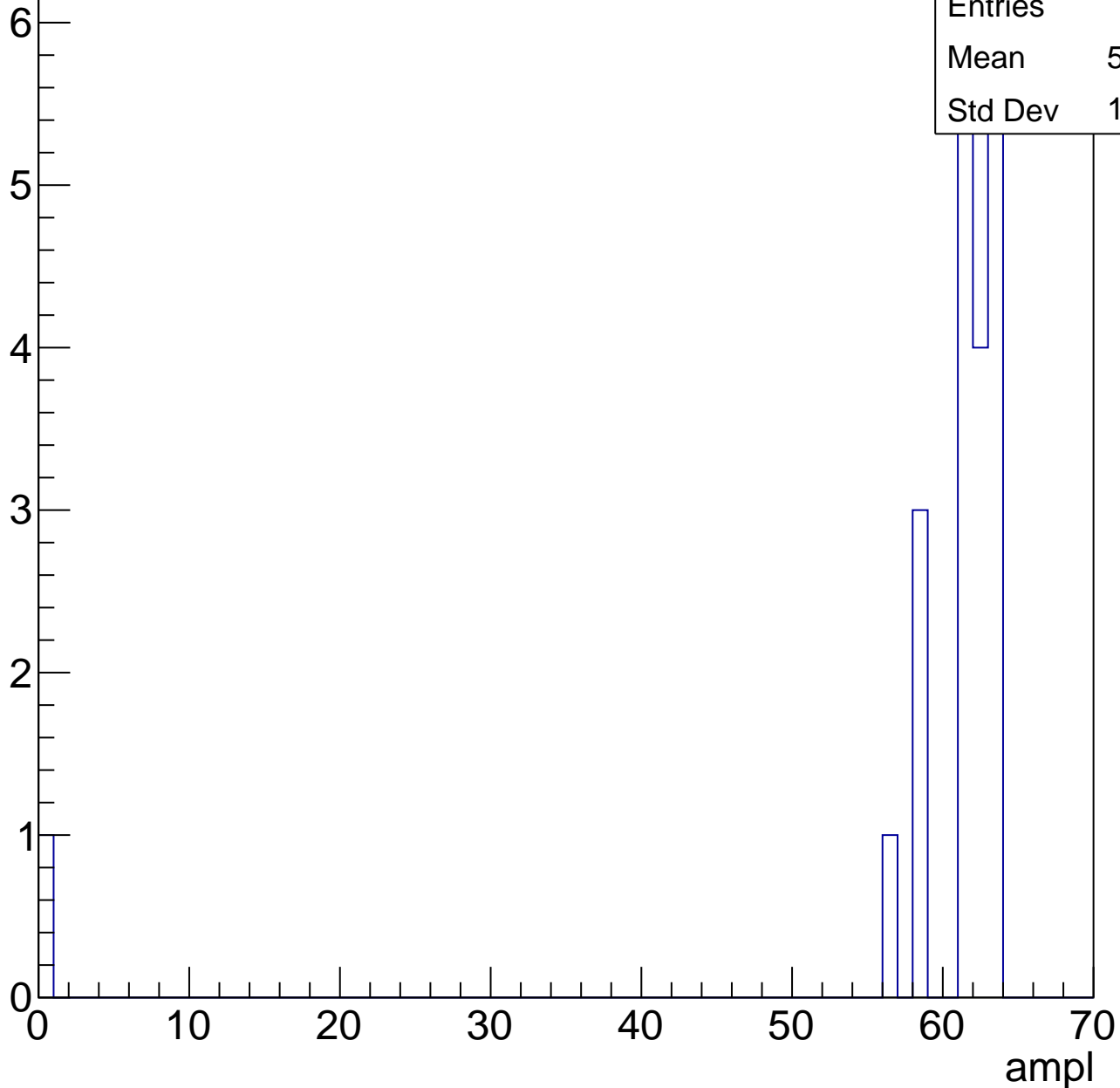


B1L102S, U22-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	58.19
Std Dev	13.16



B1L102S, U22-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

B1L102S, U22-ch39, adc0

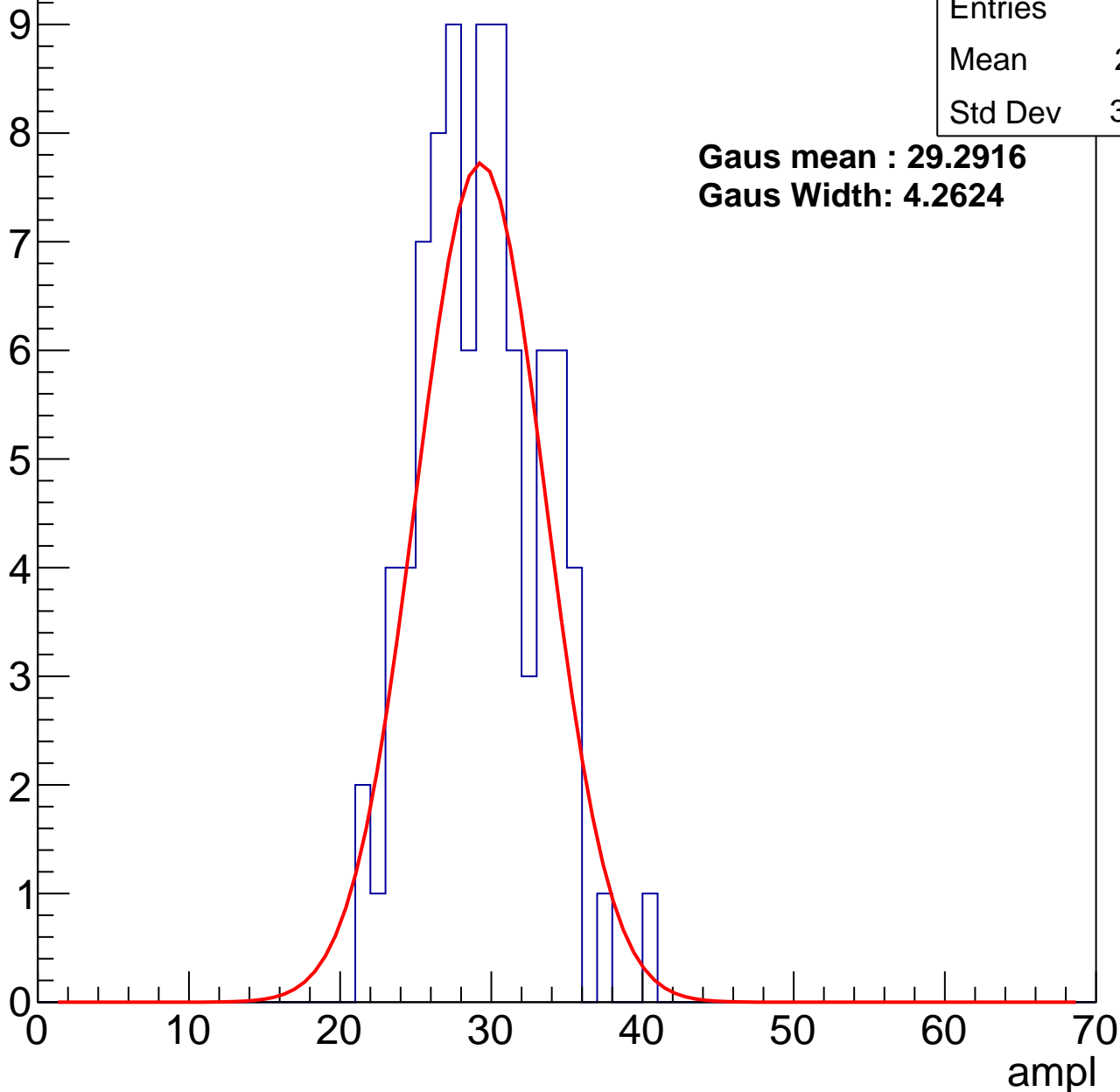
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	28.81
Std Dev	3.856

Gaus mean : 29.2916

Gaus Width: 4.2624



B1L102S, U22-ch39, adc1

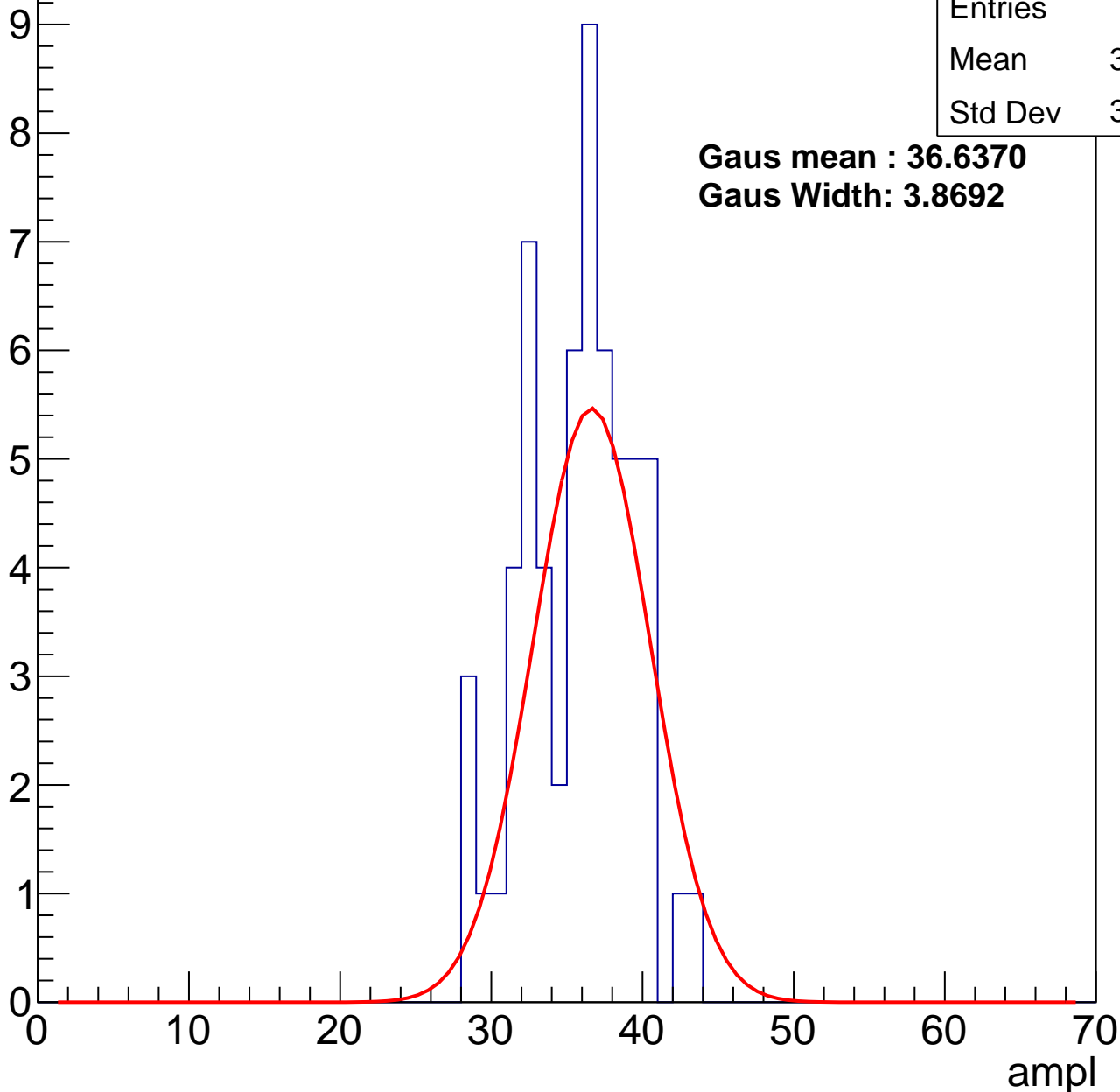
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.28
Std Dev	3.522

Gaus mean : 36.6370

Gaus Width: 3.8692



B1L102S, U22-ch39, adc2

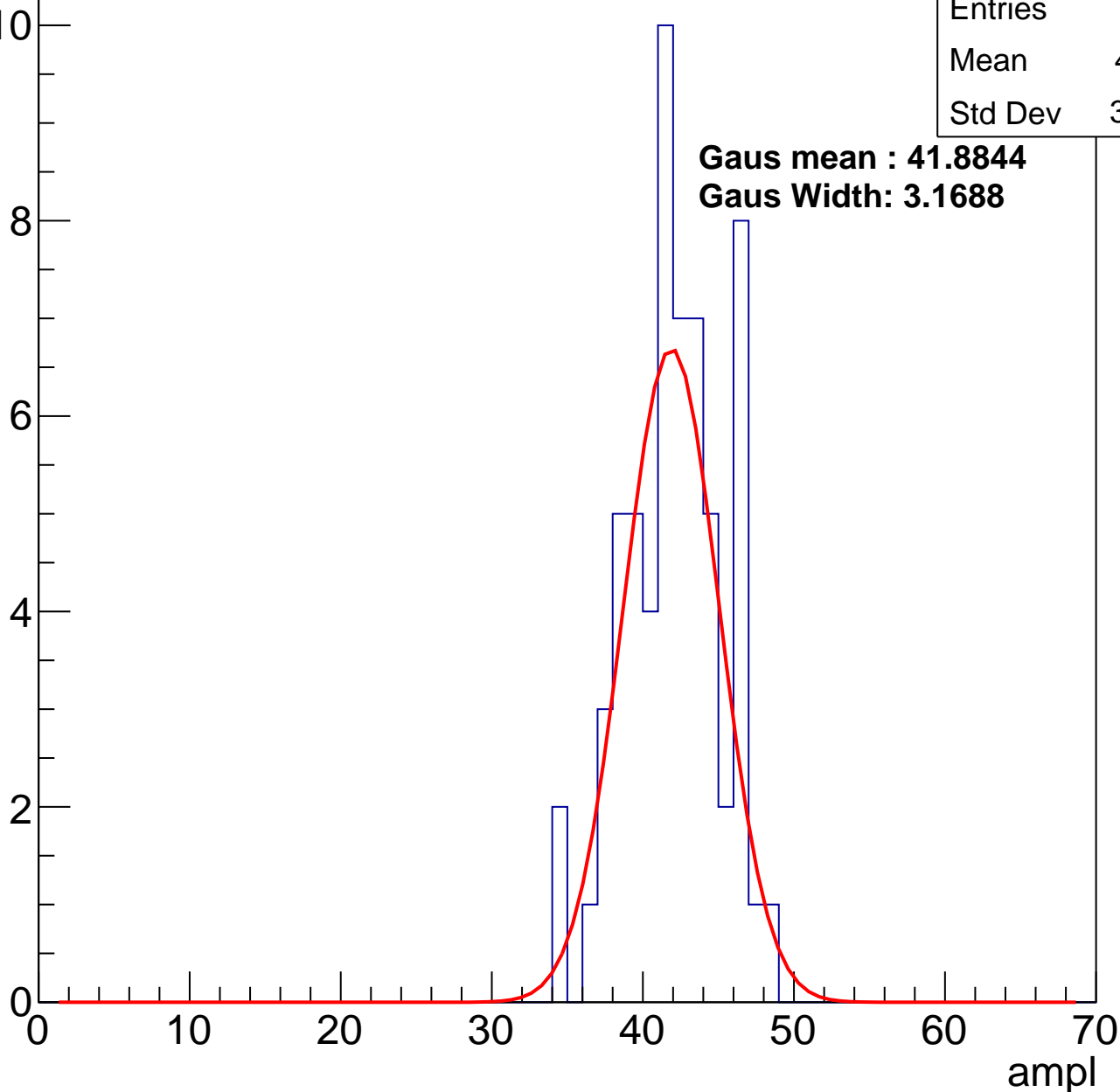
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.61
Std Dev	3.174

Gaus mean : 41.8844

Gaus Width: 3.1688



B1L102S, U22-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	48.53
Std Dev	3.21

Entry

10

8

6

4

2

0

0

10

20

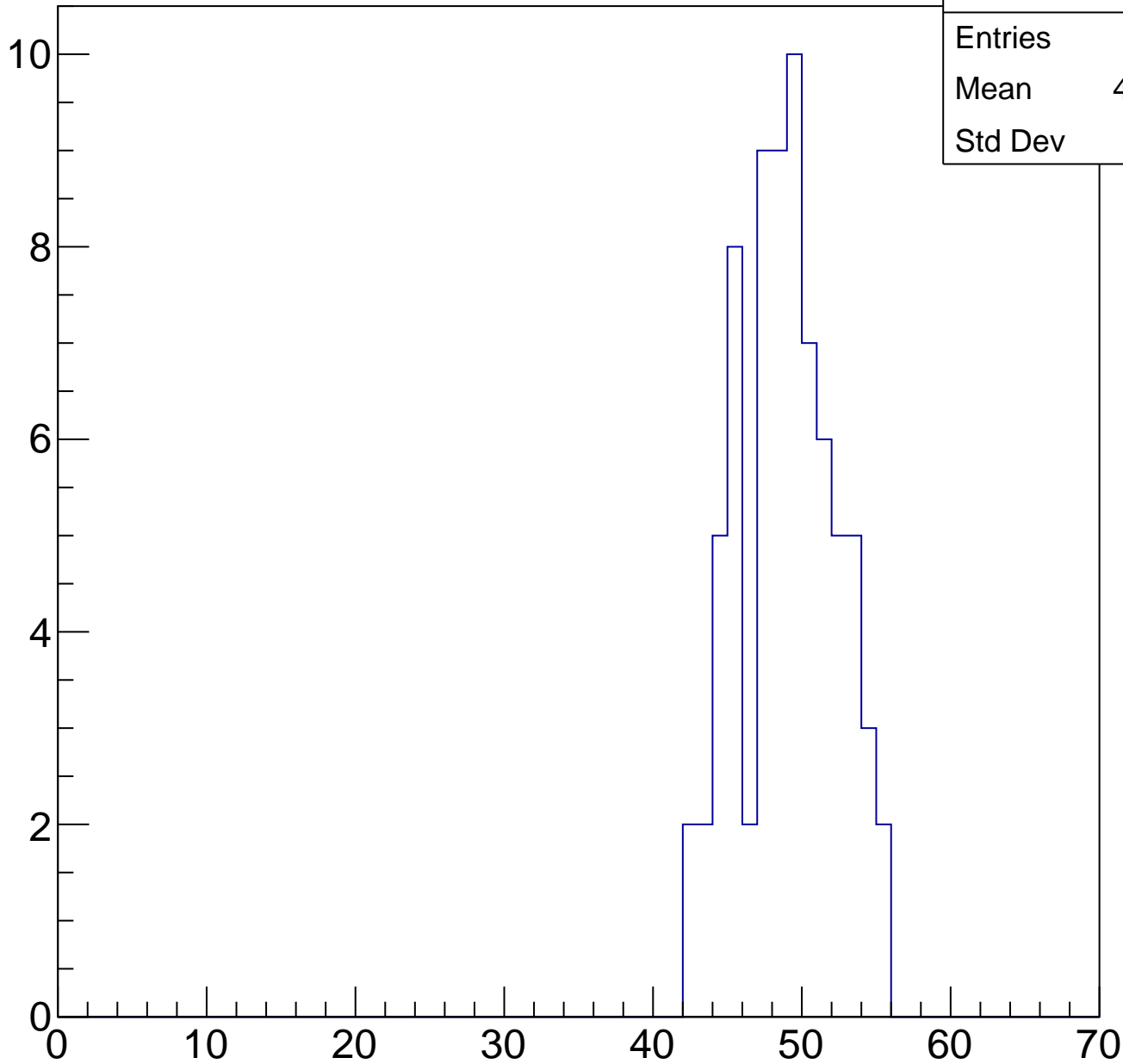
30

40

50

60

ampl

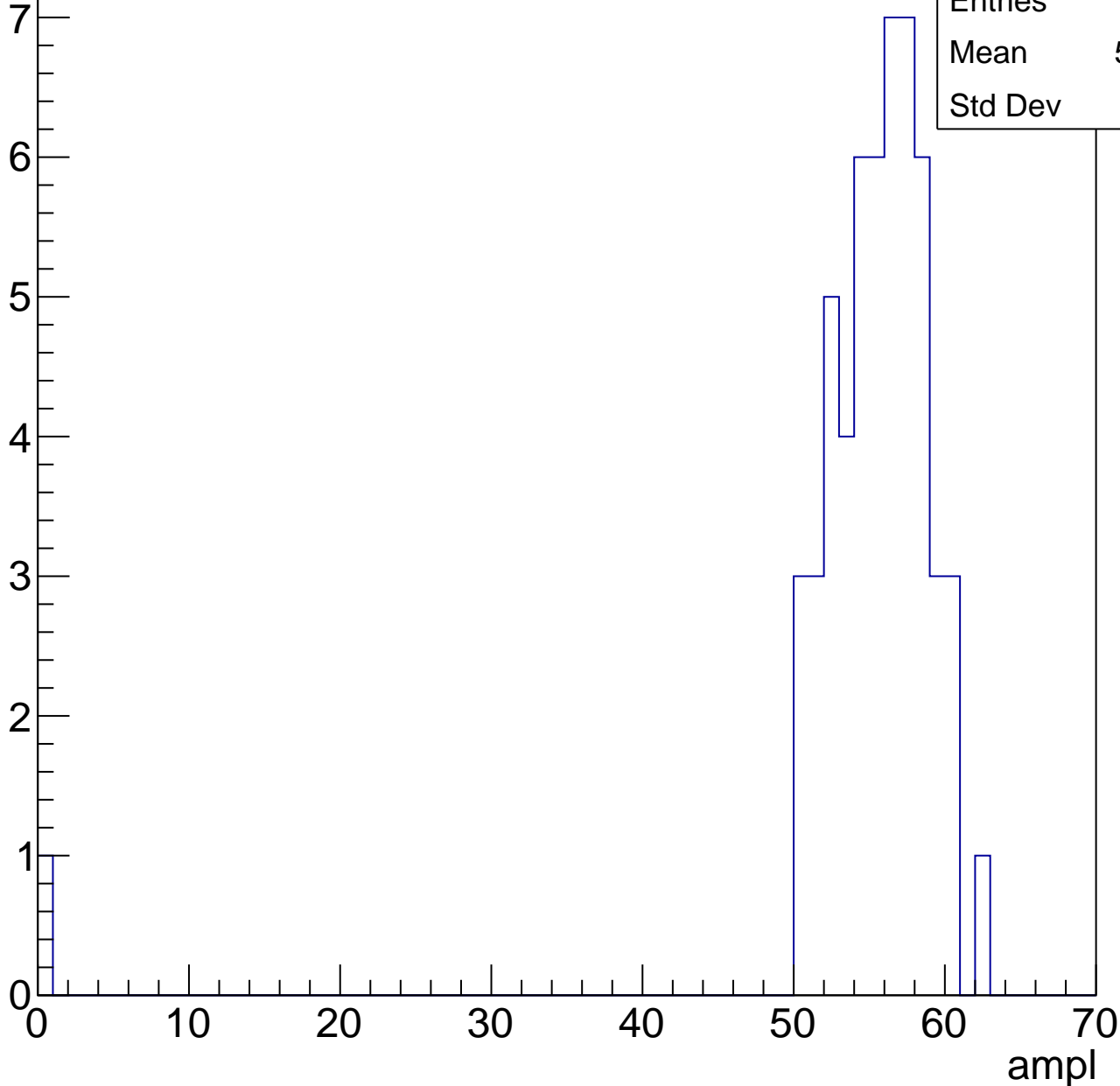


B1L102S, U22-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.31
Std Dev	7.92



B1L102S, U22-ch39, adc5

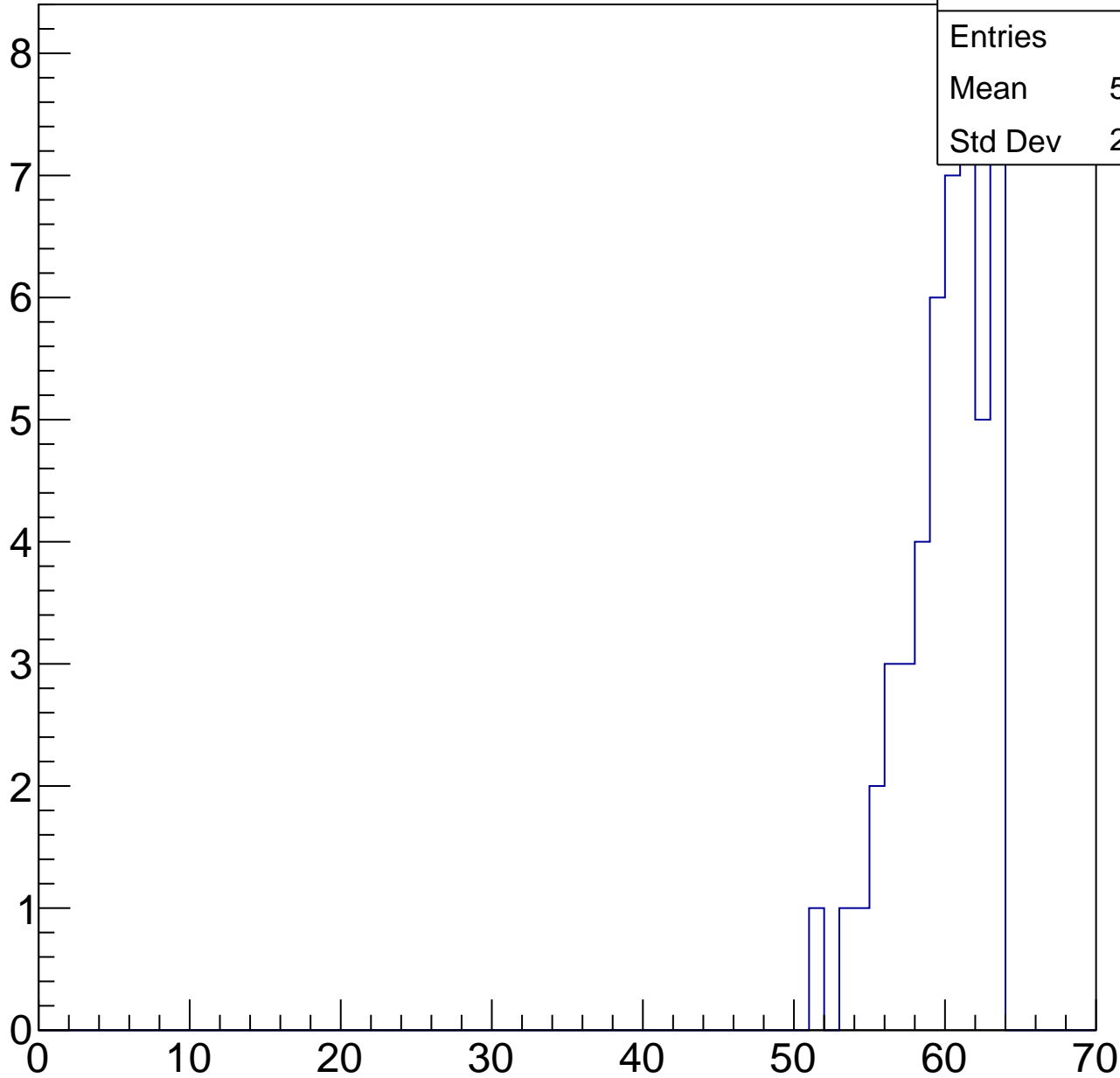
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.49
Std Dev	2.858

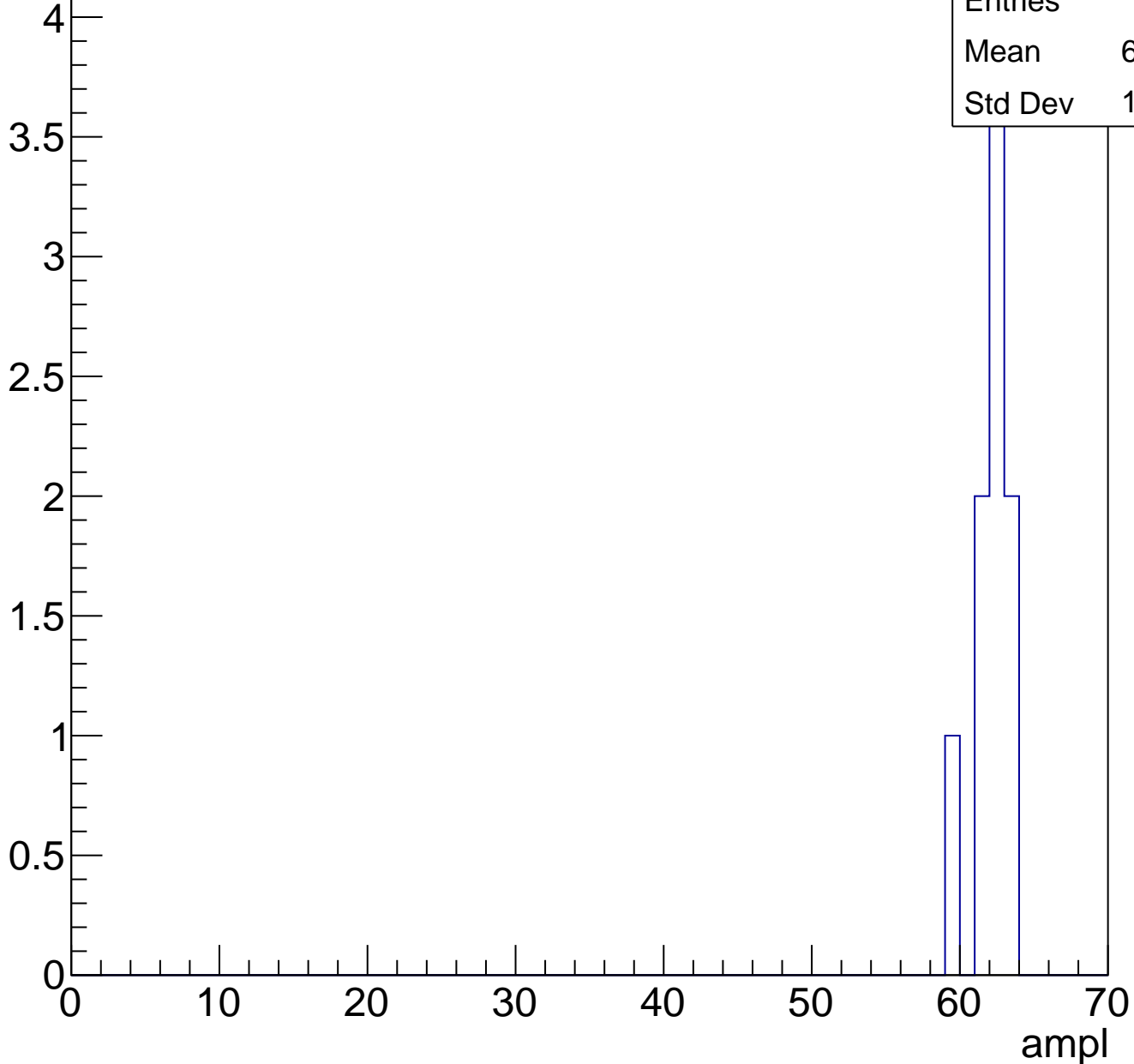
ampl



B1L102S, U22-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch40, adc0

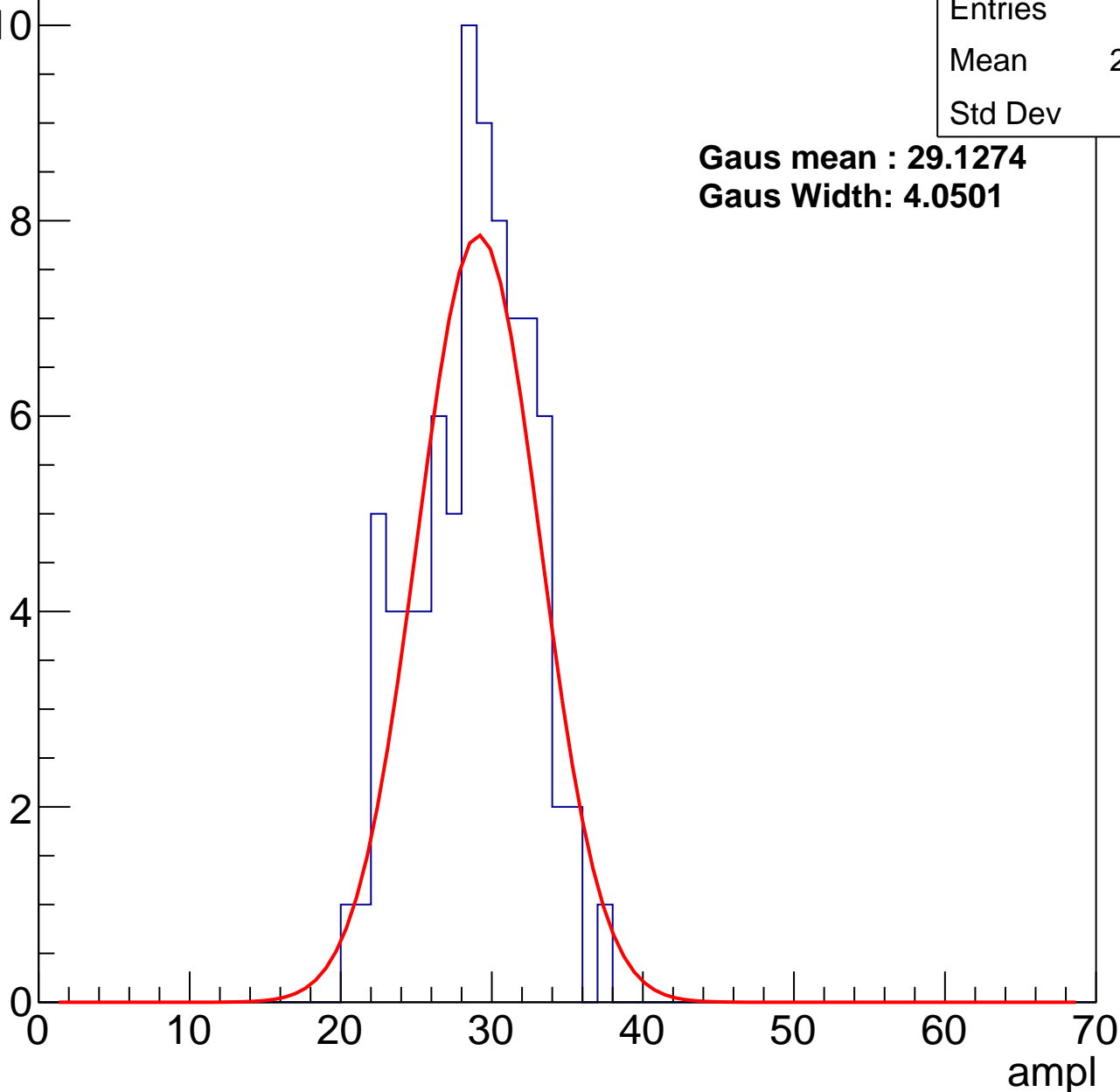
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.35
Std Dev	3.71

Gaus mean : 29.1274

Gaus Width: 4.0501



B1L102S, U22-ch40, adc1

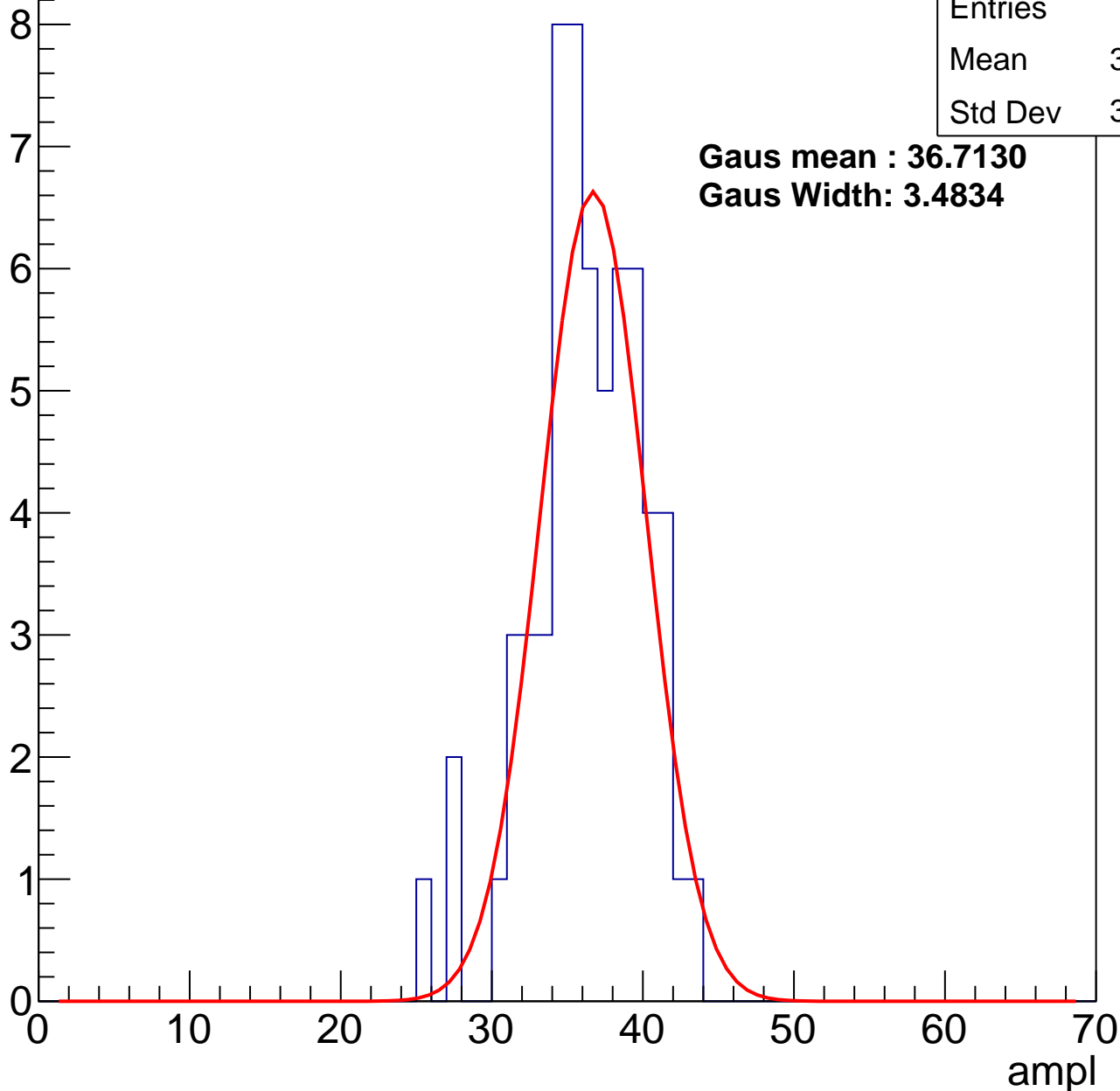
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.82
Std Dev	3.679

Gaus mean : 36.7130

Gaus Width: 3.4834



B1L102S, U22-ch40, adc2

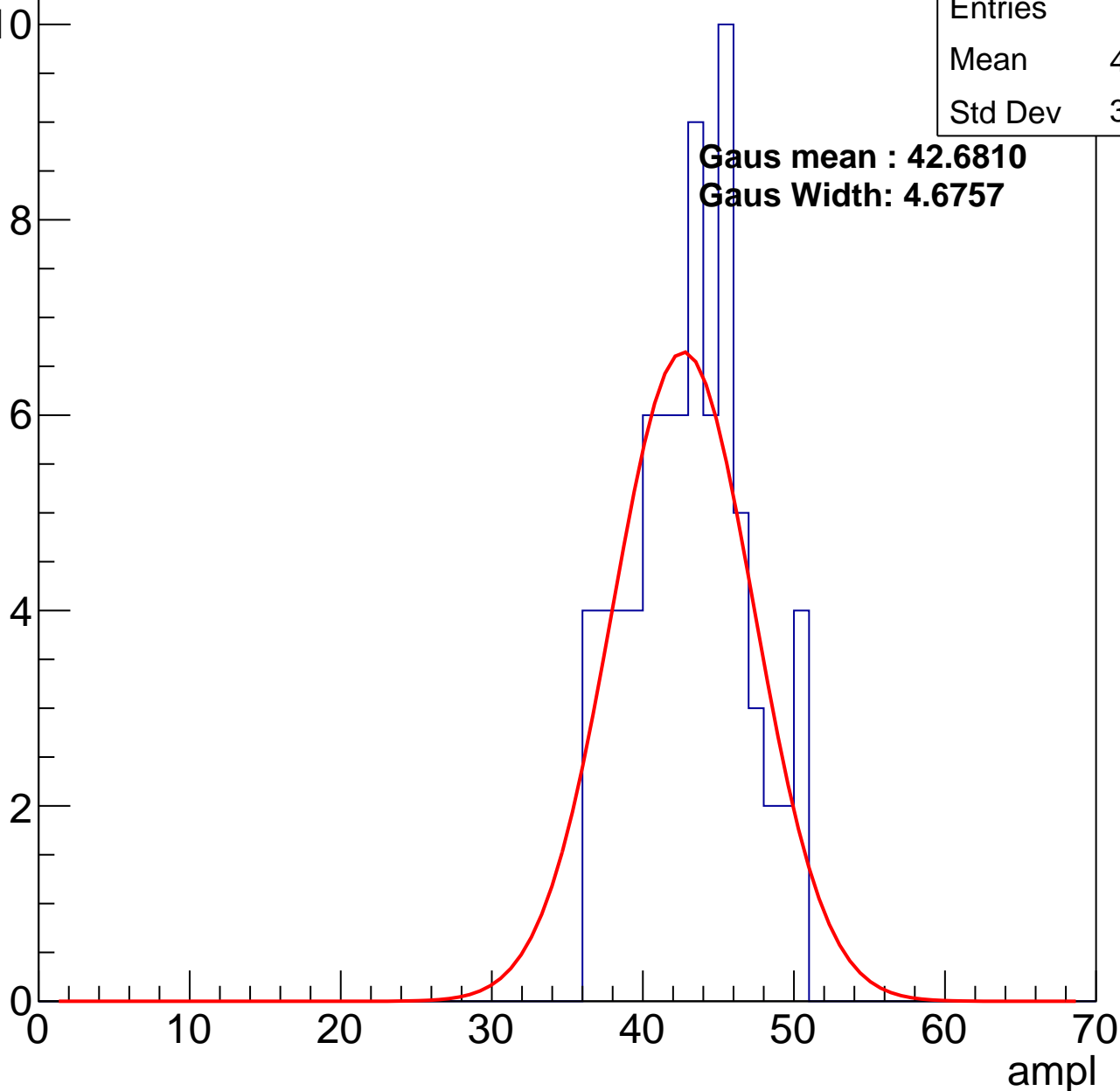
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	42.72
Std Dev	3.722

Gaus mean : 42.6810

Gaus Width: 4.6757

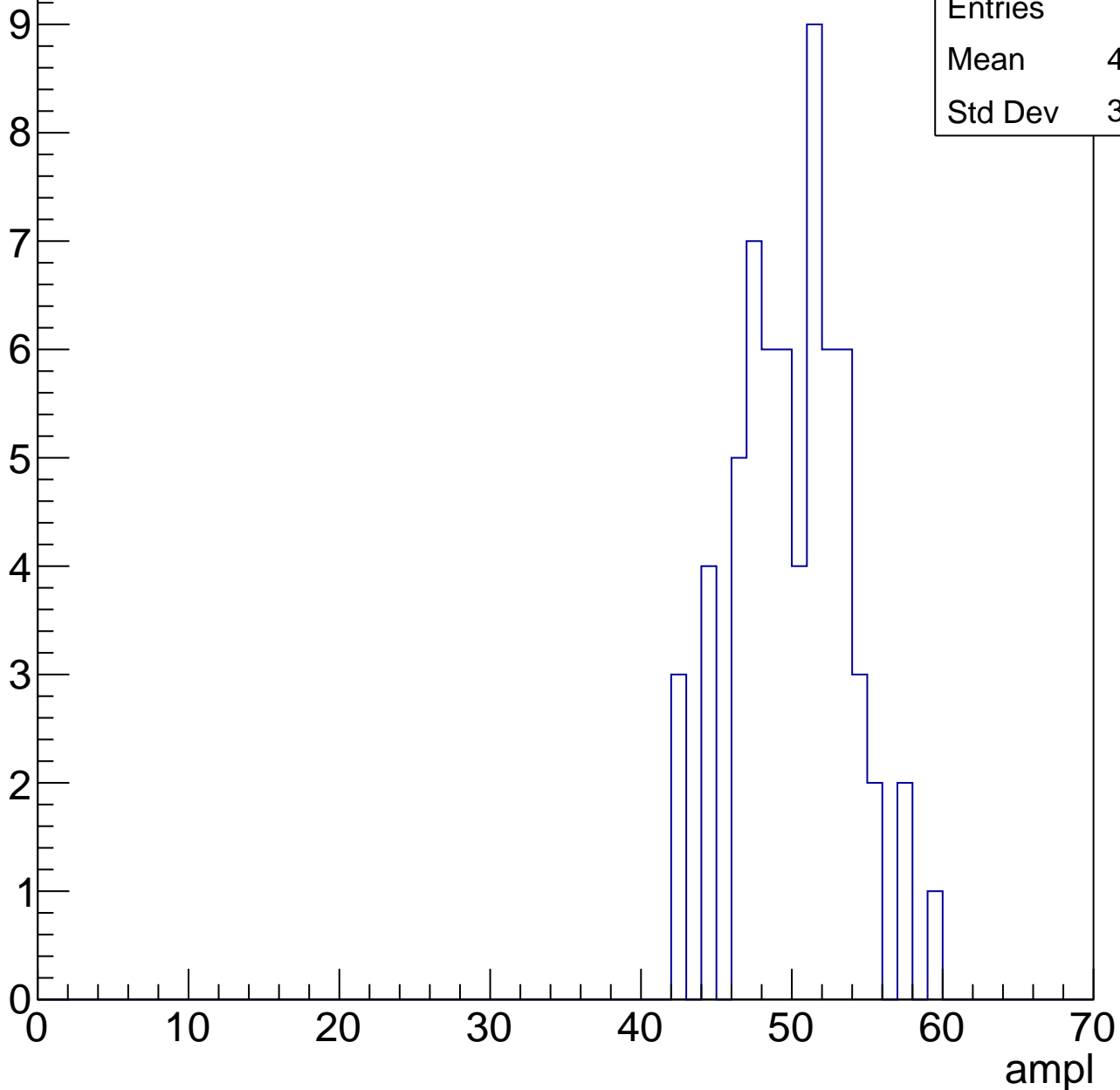


B1L102S, U22-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.64
Std Dev	3.676

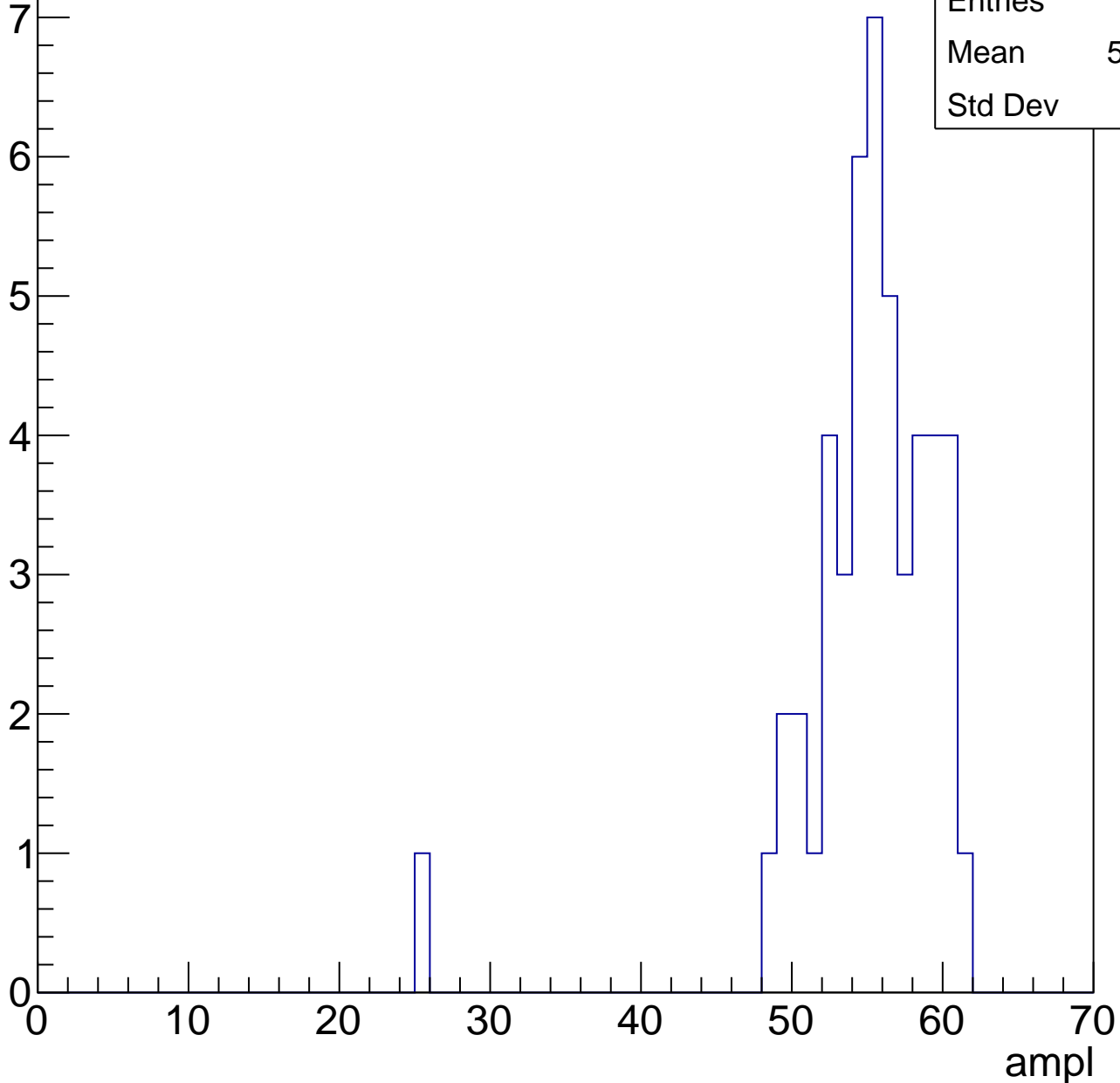


B1L102S, U22-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	54.54
Std Dev	5.37

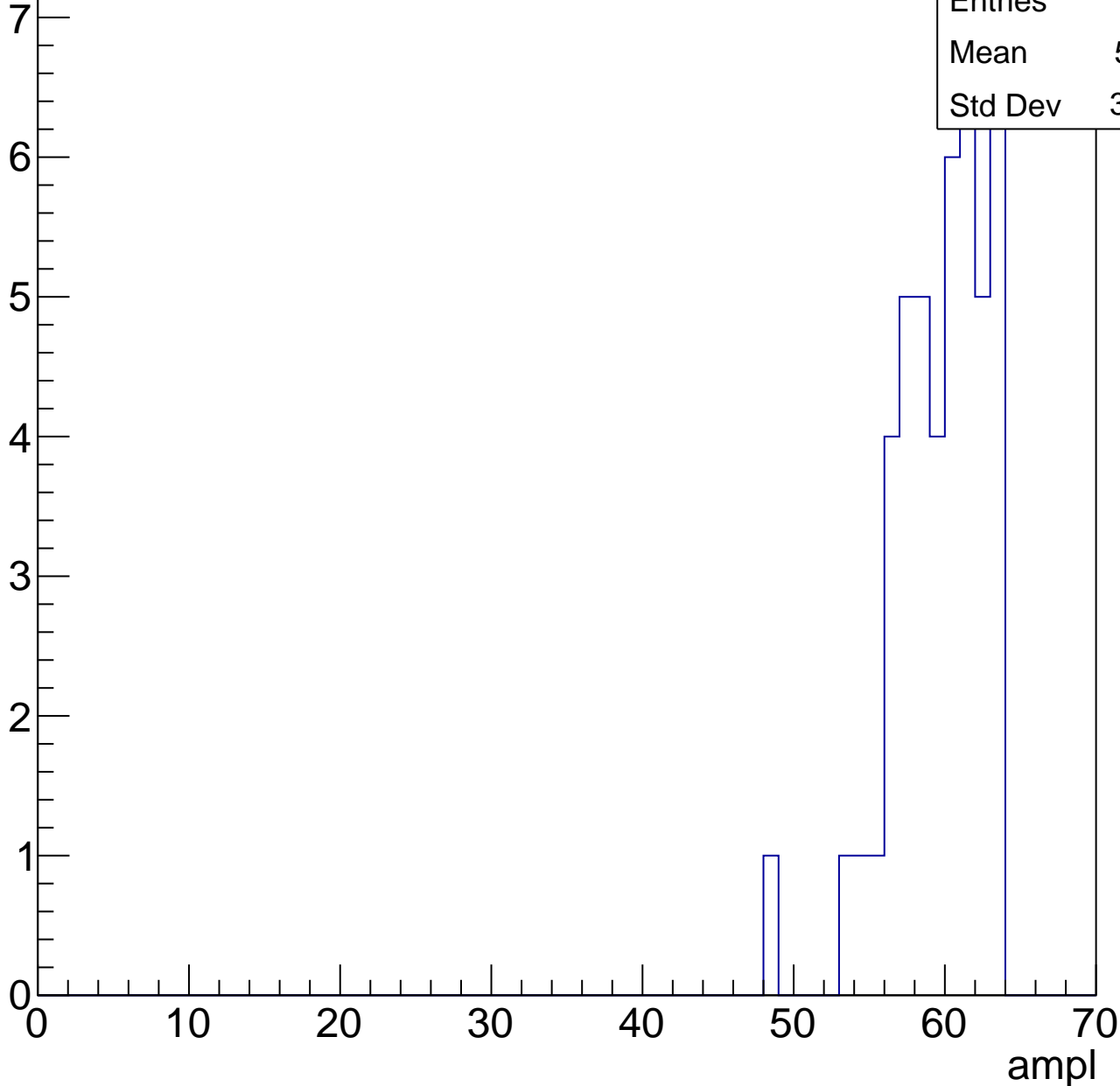


B1L102S, U22-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	59.21
Std Dev	3.087

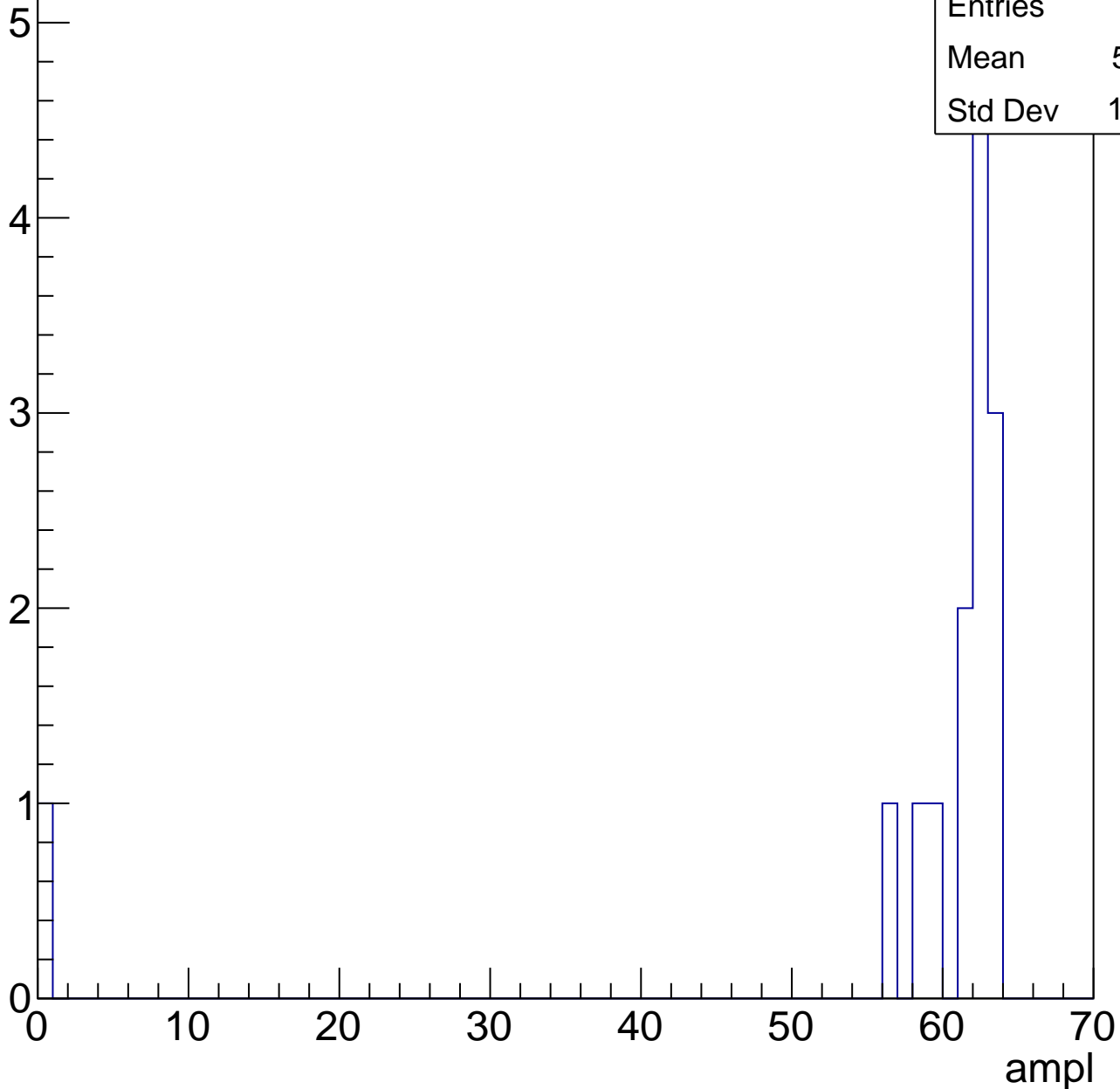


B1L102S, U22-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

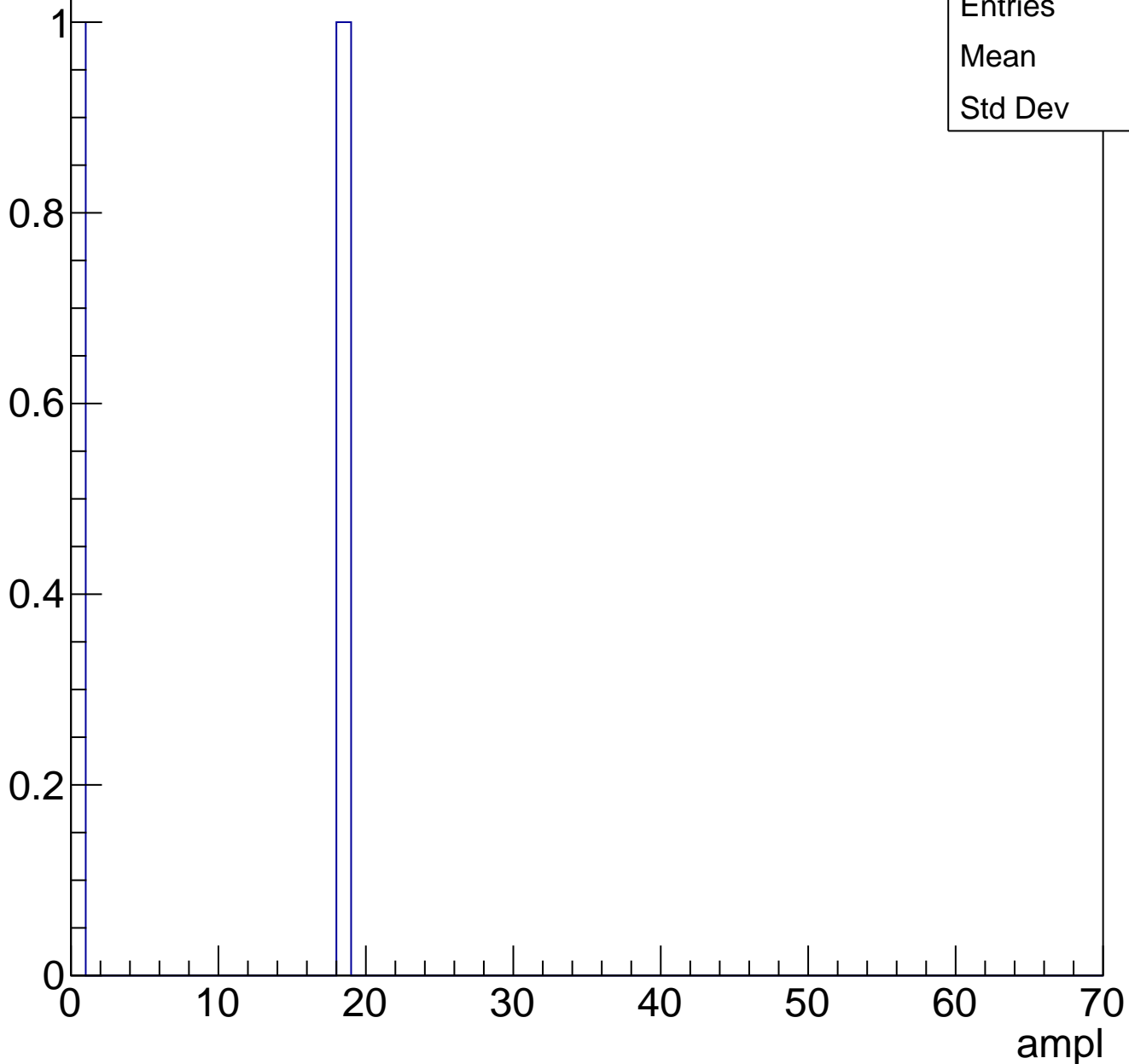
Entries	14
Mean	56.71
Std Dev	15.85



B1L102S, U22-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9
Std Dev	9

B1L102S, U22-ch41, adc0

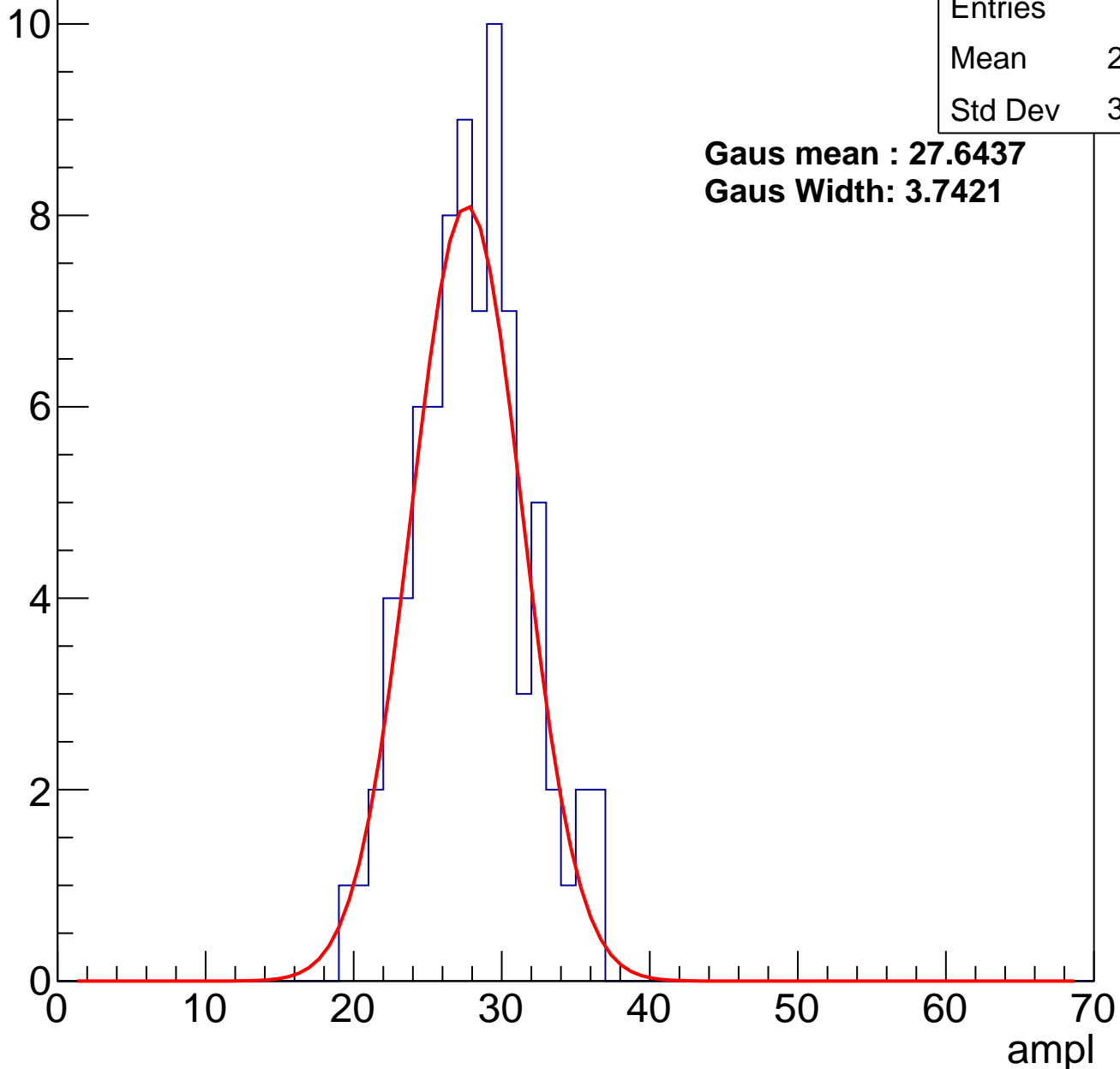
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	27.46
Std Dev	3.728

Gaus mean : 27.6437

Gaus Width: 3.7421

Entry



B1L102S, U22-ch41, adc1

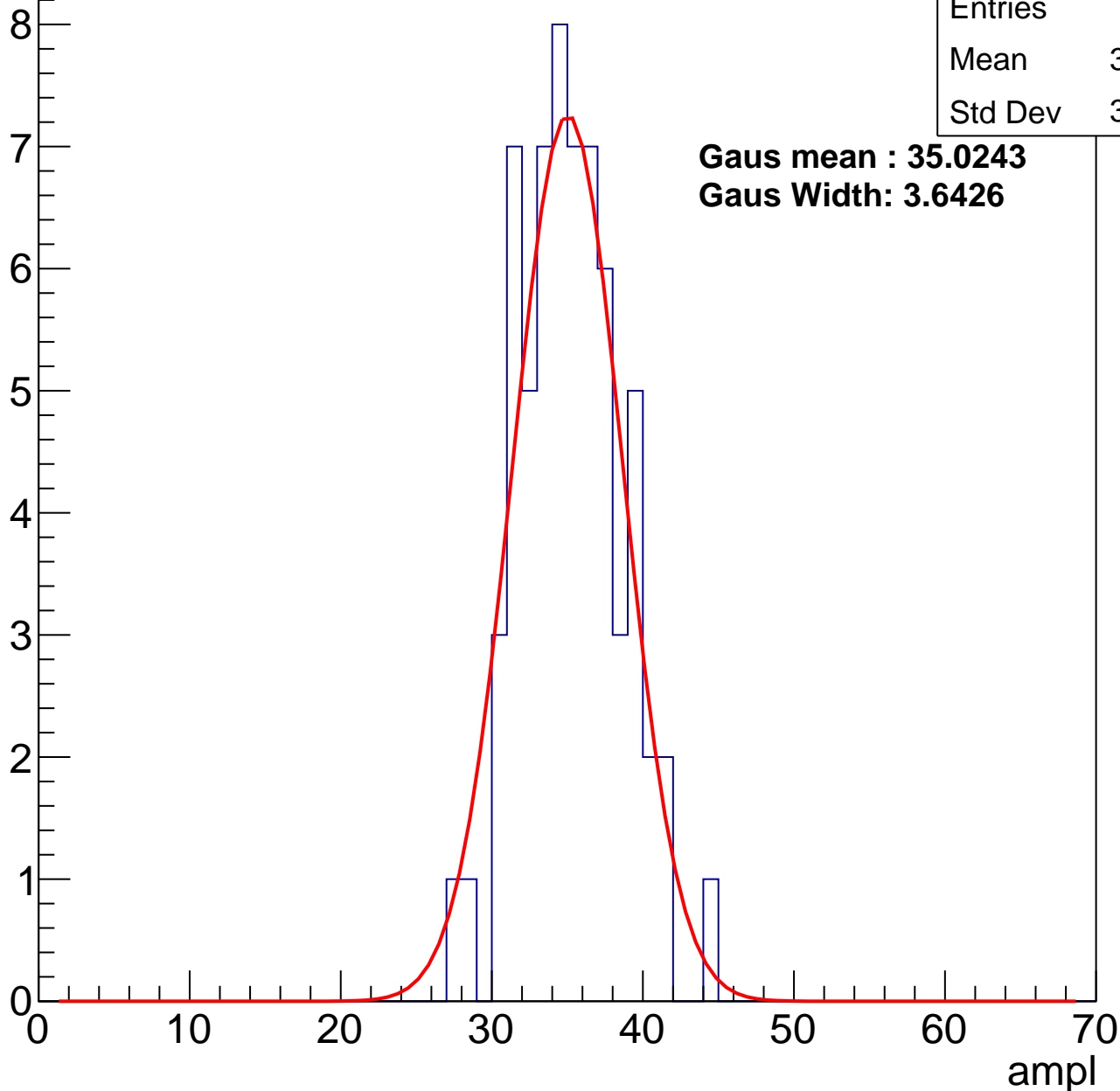
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.75
Std Dev	3.324

Gaus mean : 35.0243

Gaus Width: 3.6426



B1L102S, U22-ch41, adc2

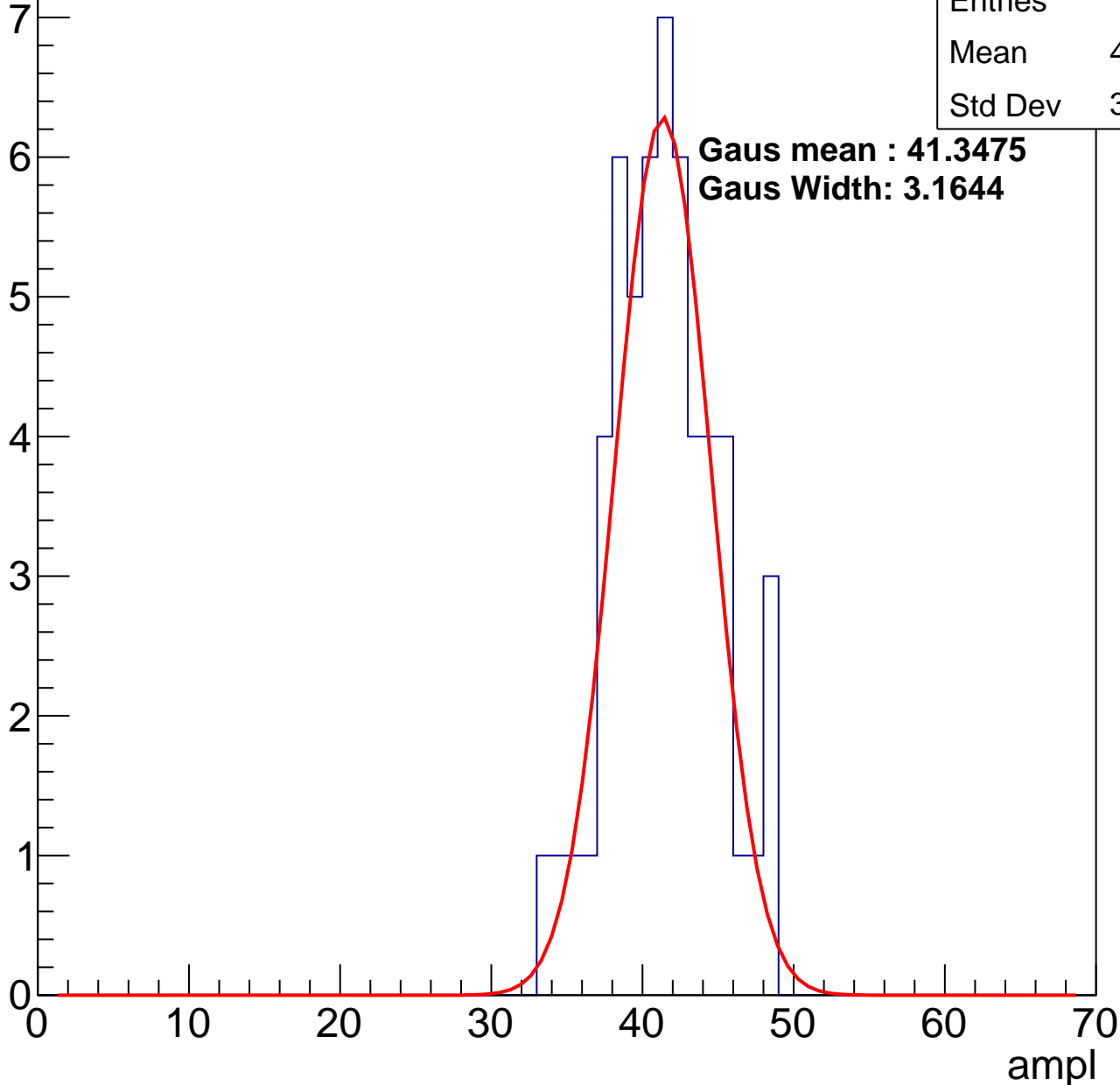
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	40.96
Std Dev	3.432

Gaus mean : 41.3475

Gaus Width: 3.1644

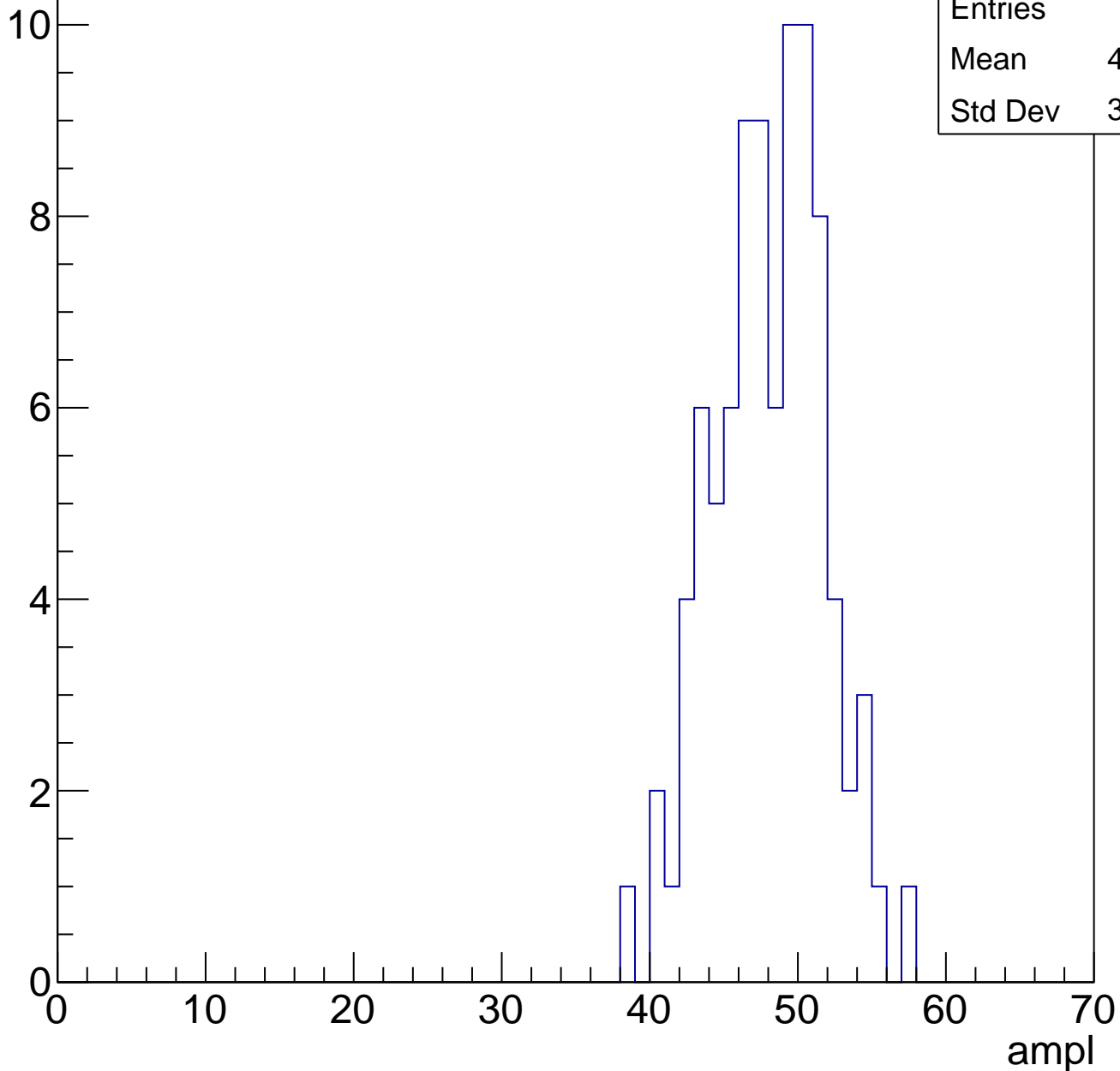


B1L102S, U22-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	47.57
Std Dev	3.704

Entry

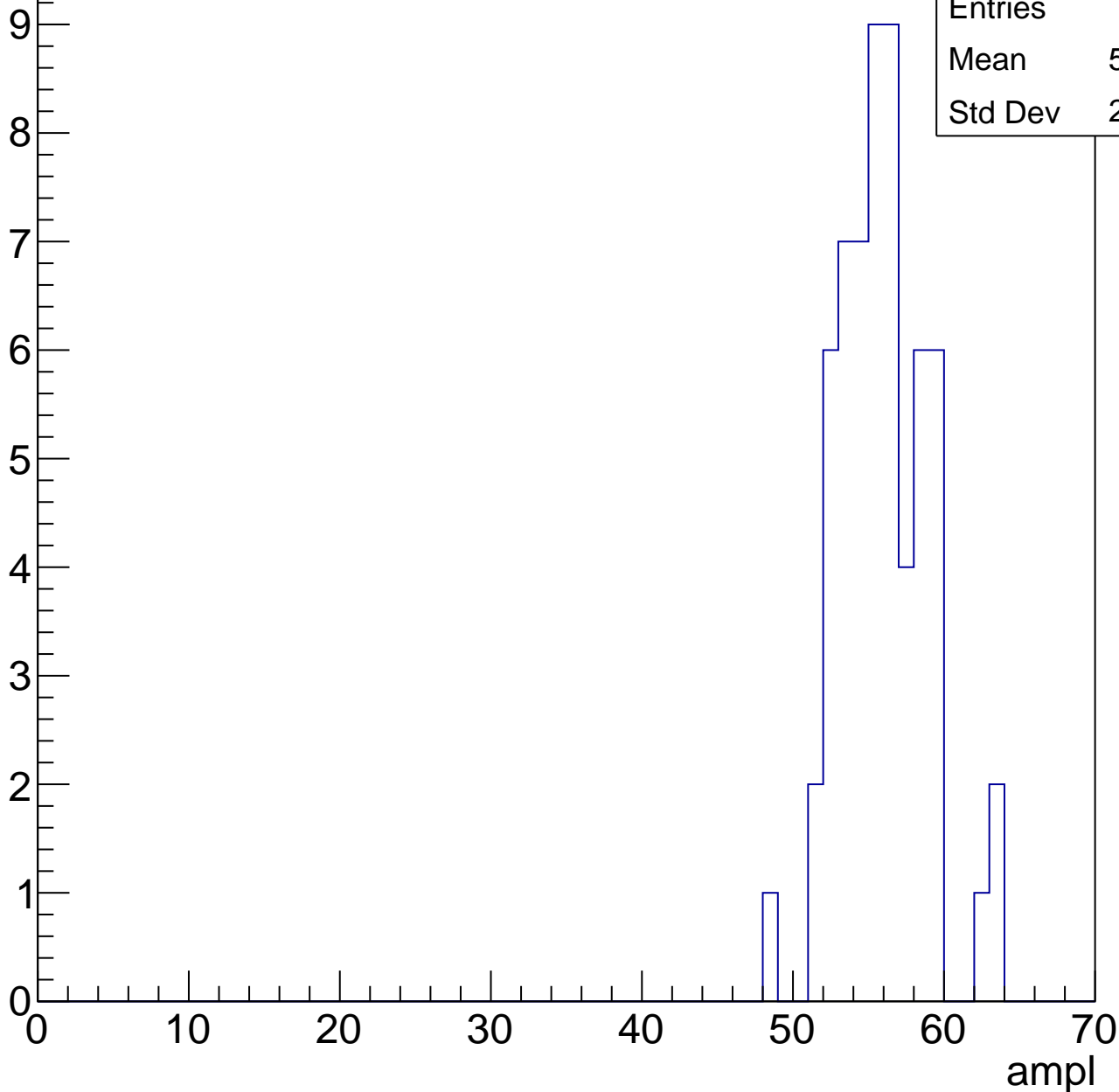


B1L102S, U22-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.47
Std Dev	2.912

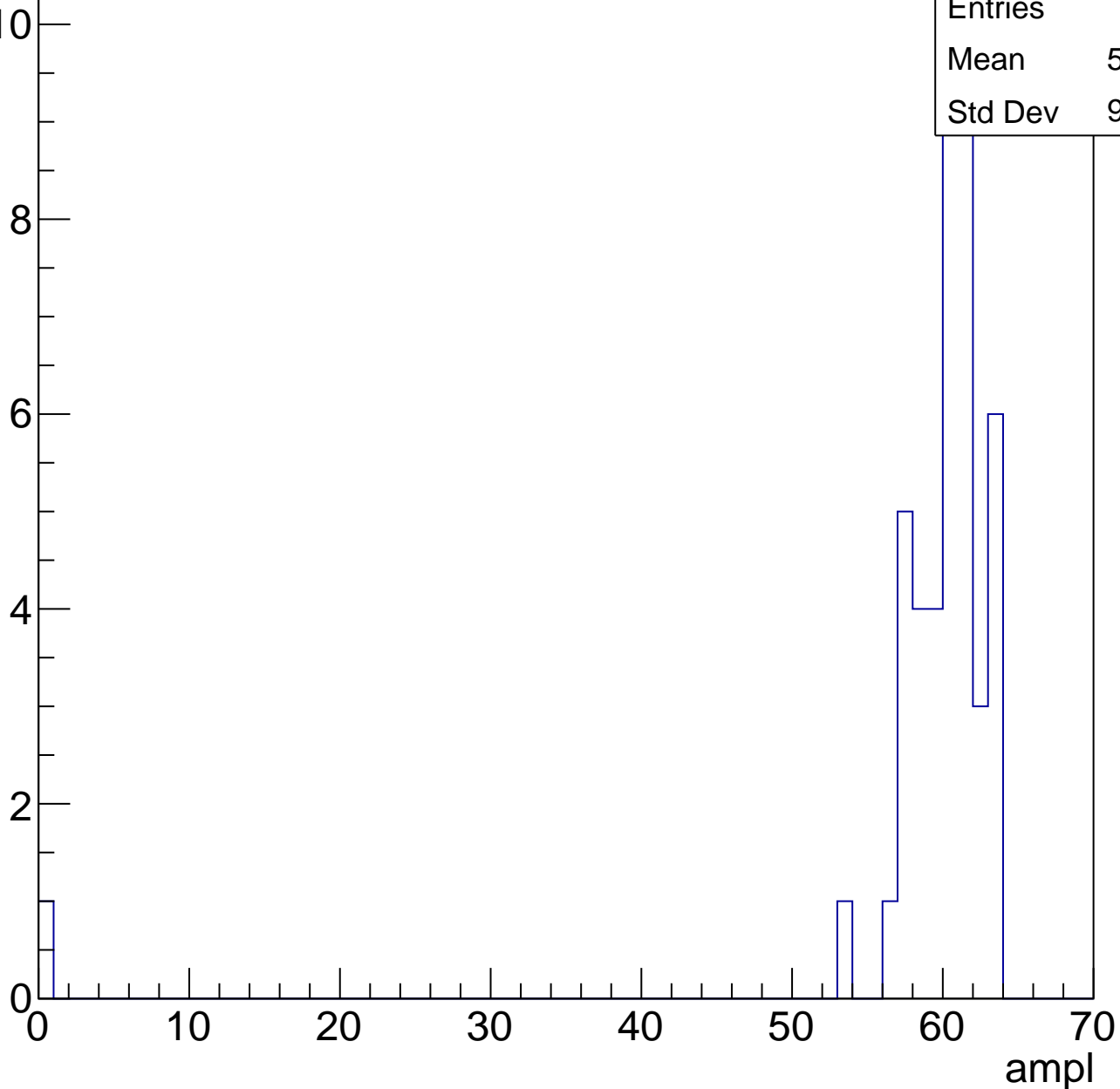


B1L102S, U22-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.55
Std Dev	9.186

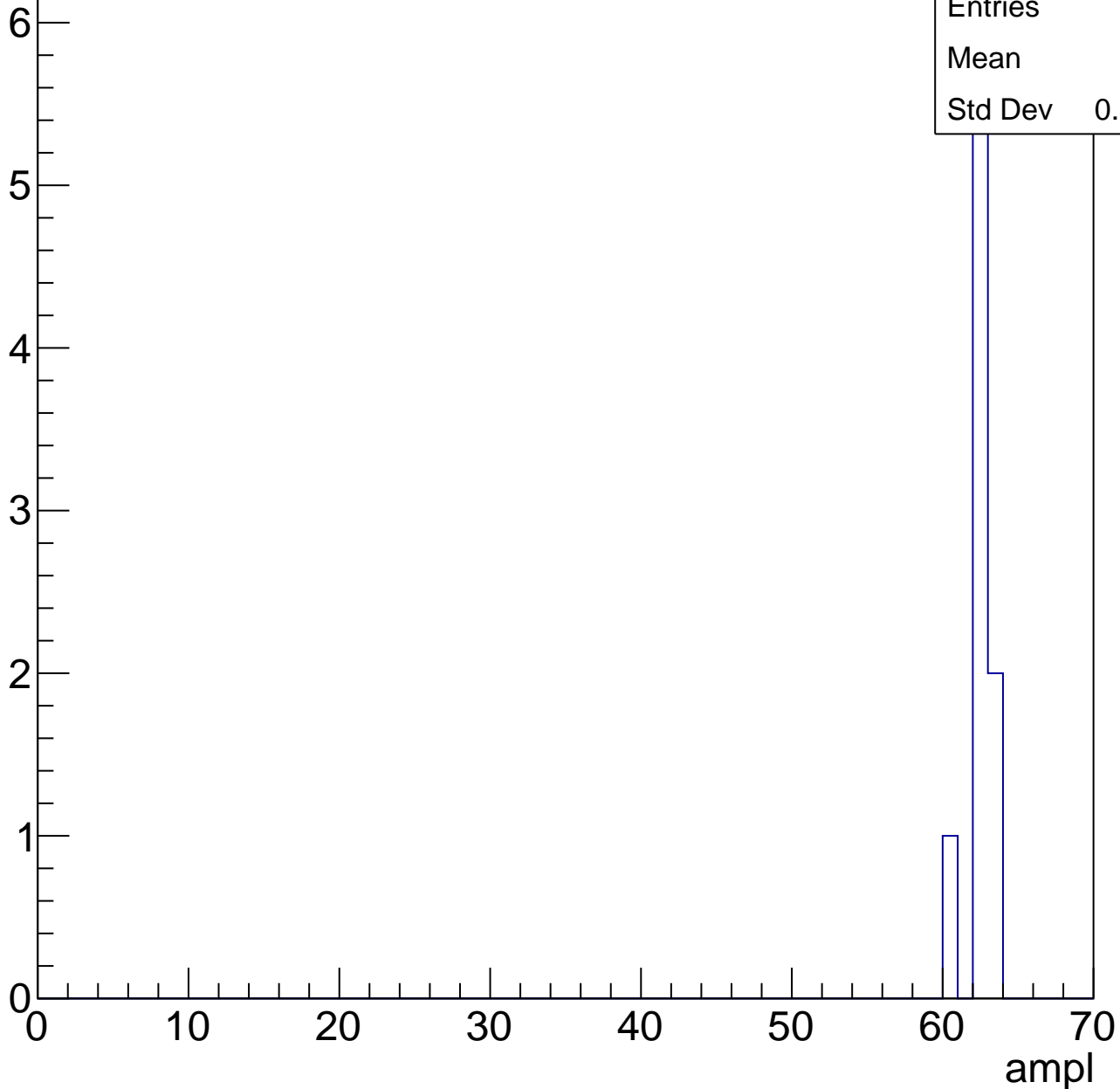


B1L102S, U22-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62
Std Dev	0.8165



B1L102S, U22-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch42, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	27.41
Std Dev	5.004

Gaus mean : 27.8065

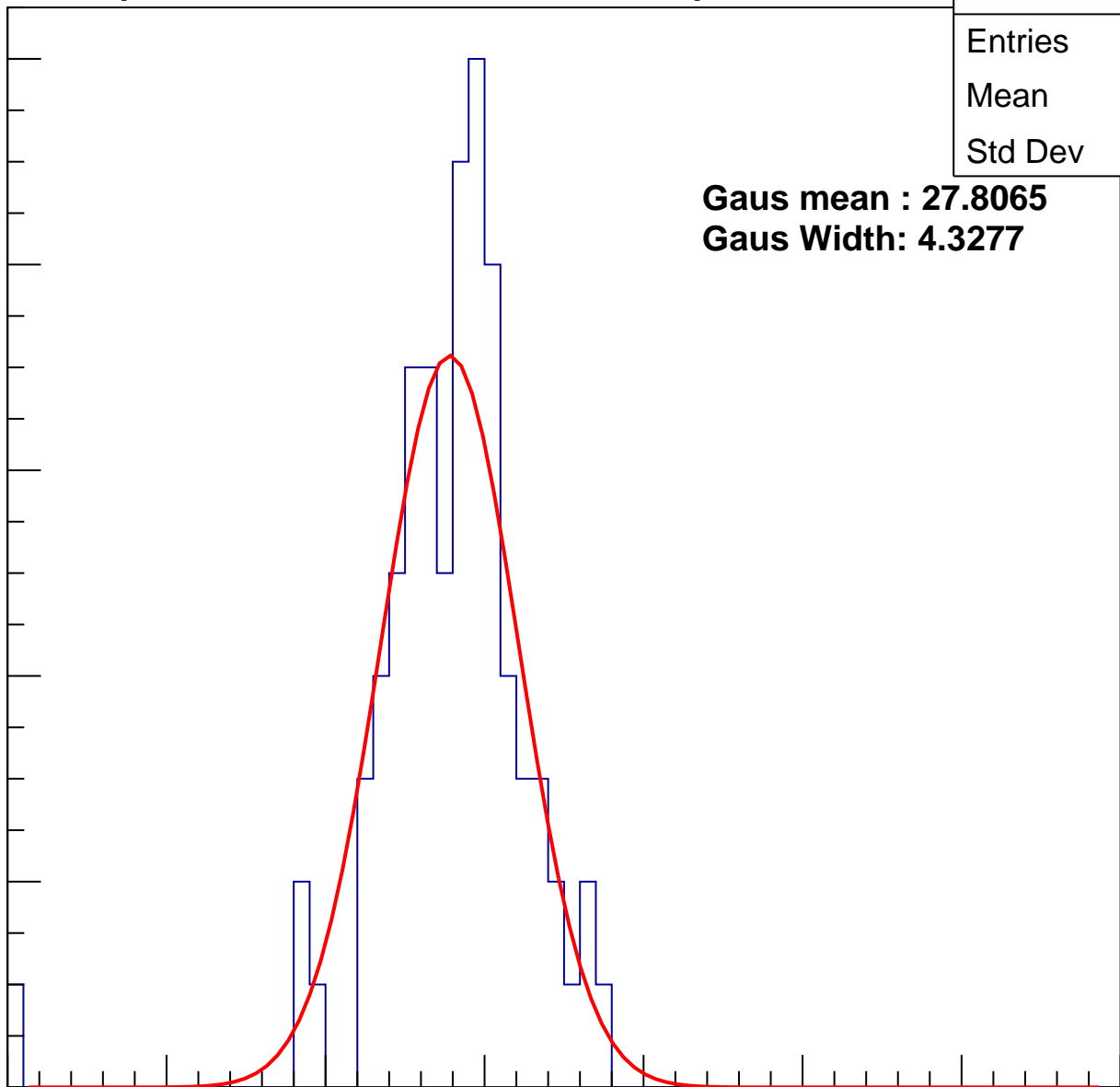
Gaus Width: 4.3277

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch42, adc1

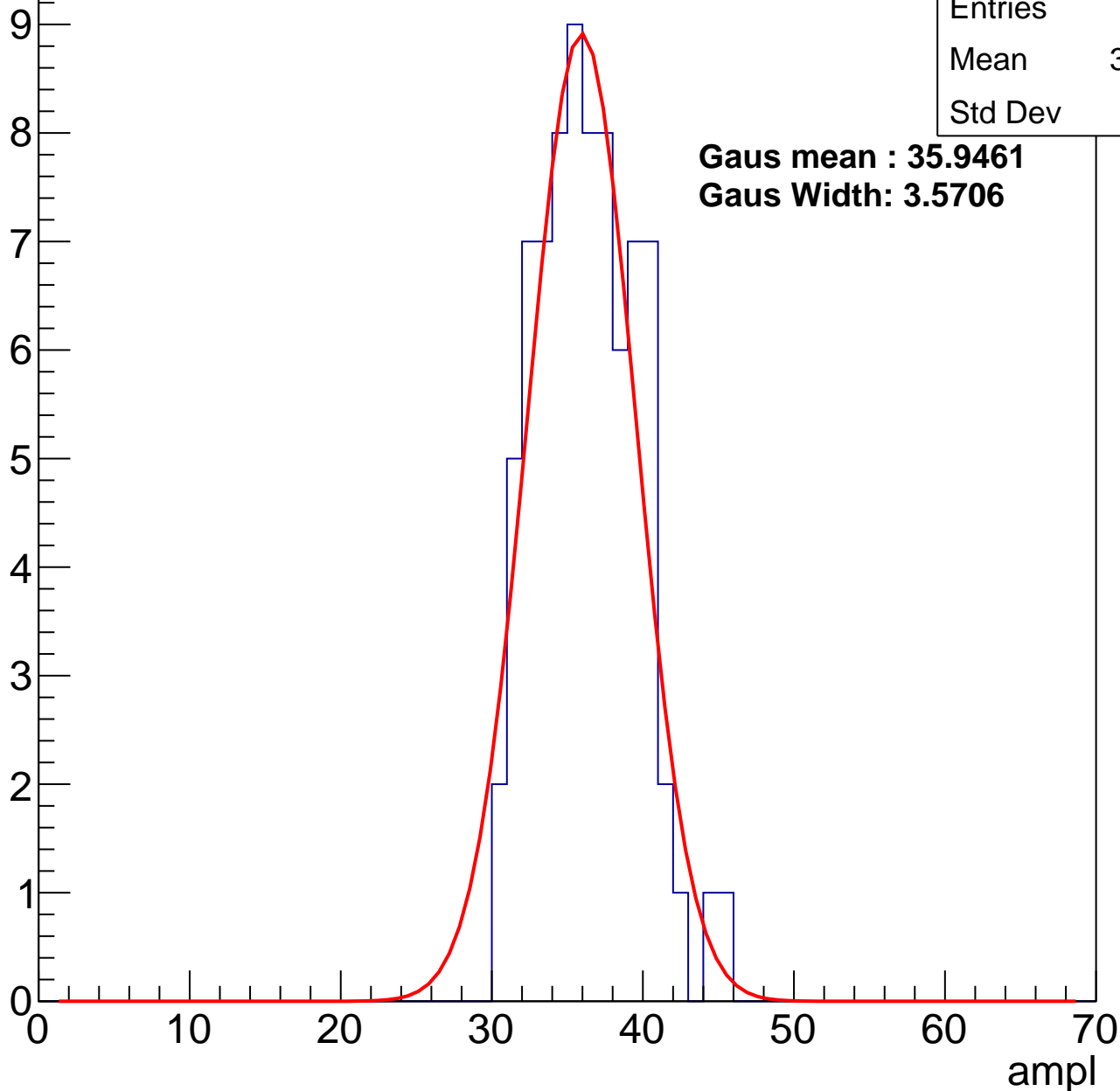
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.89
Std Dev	3.28

Gaus mean : 35.9461

Gaus Width: 3.5706



B1L102S, U22-ch42, adc2

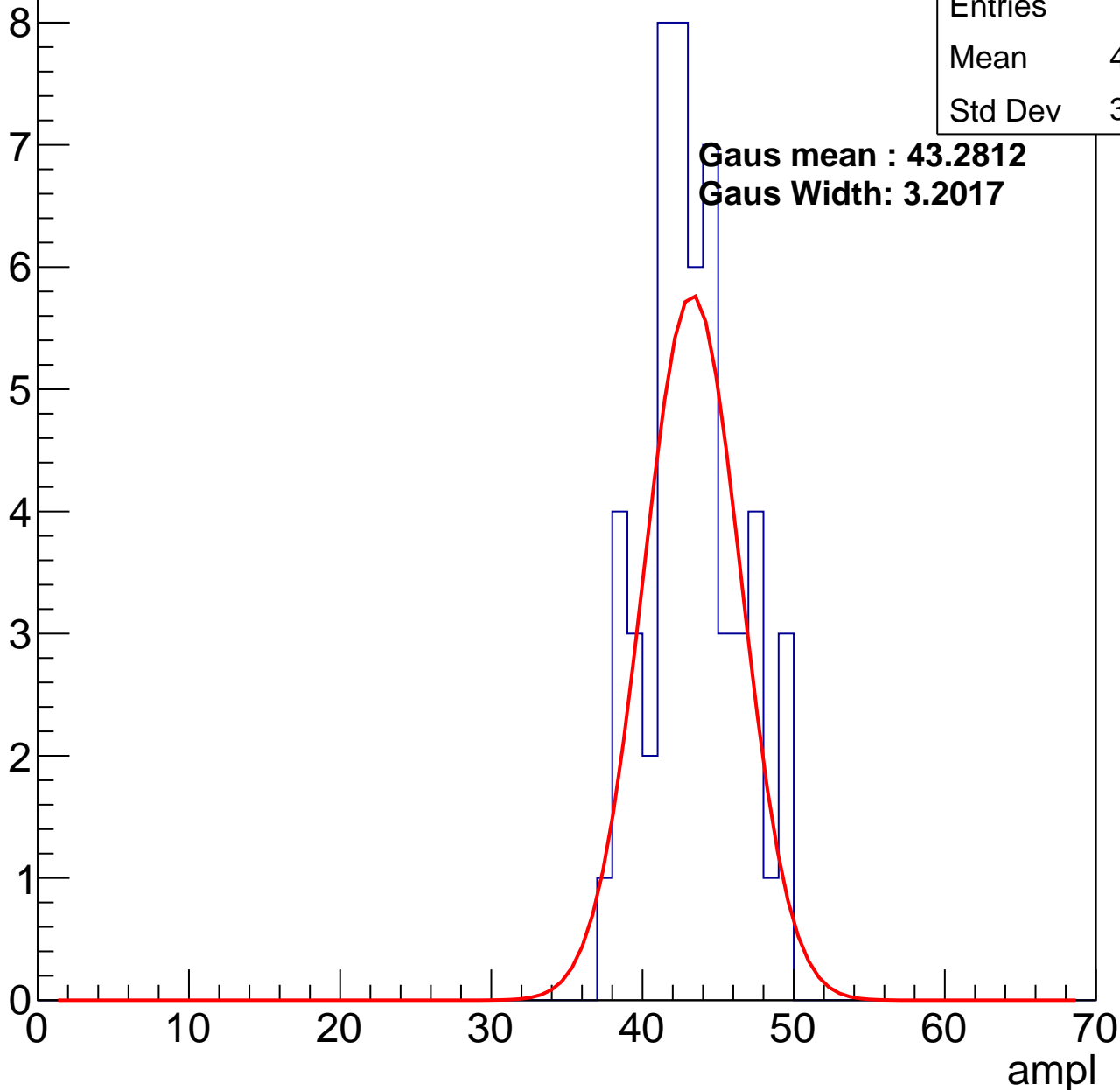
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.87
Std Dev	3.022

Gaus mean : 43.2812

Gaus Width: 3.2017



B1L102S, U22-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	48.36
Std Dev	3.327

Entry

10

8

6

4

2

0

0

10

20

30

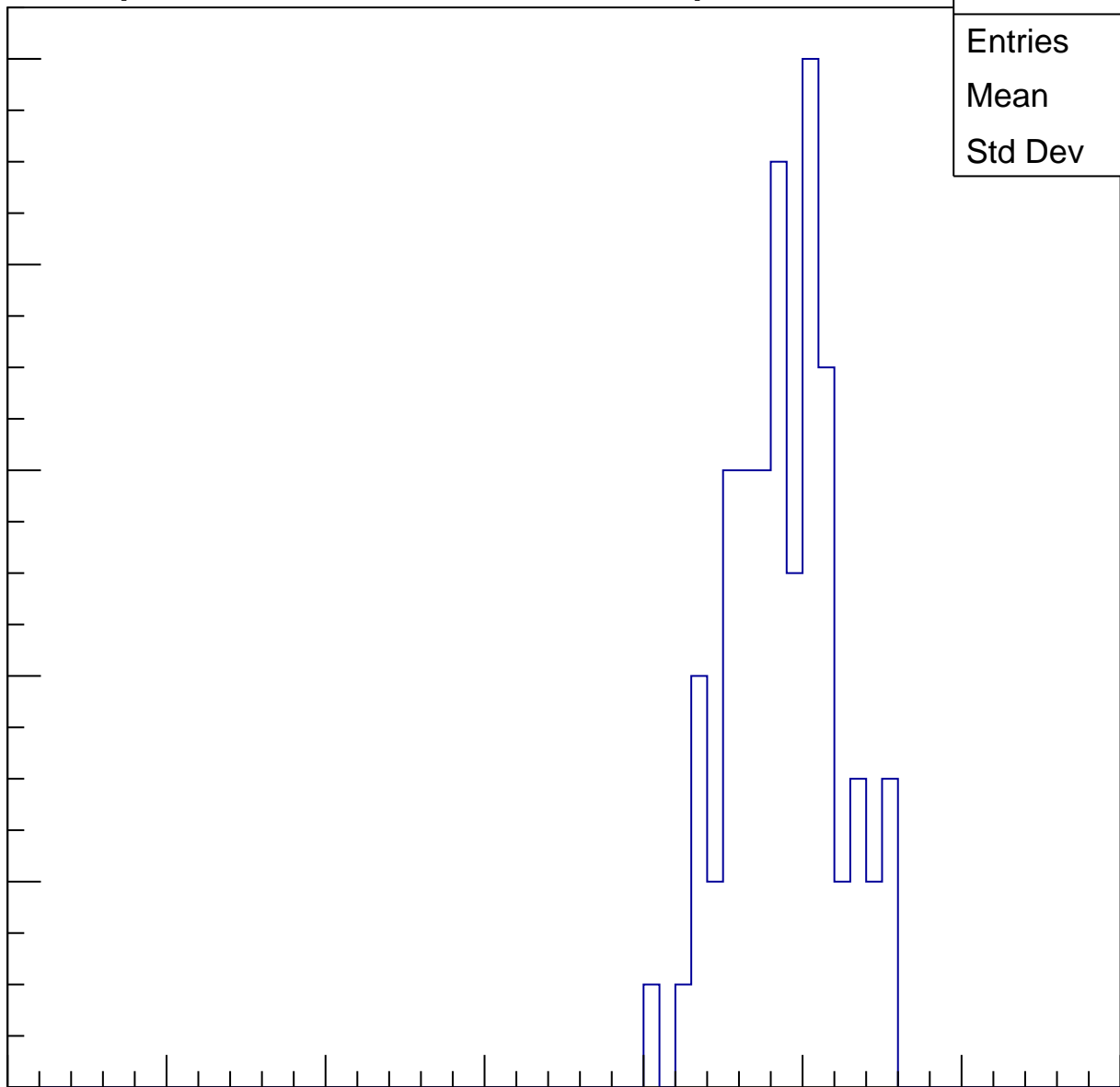
40

50

60

70

ampl

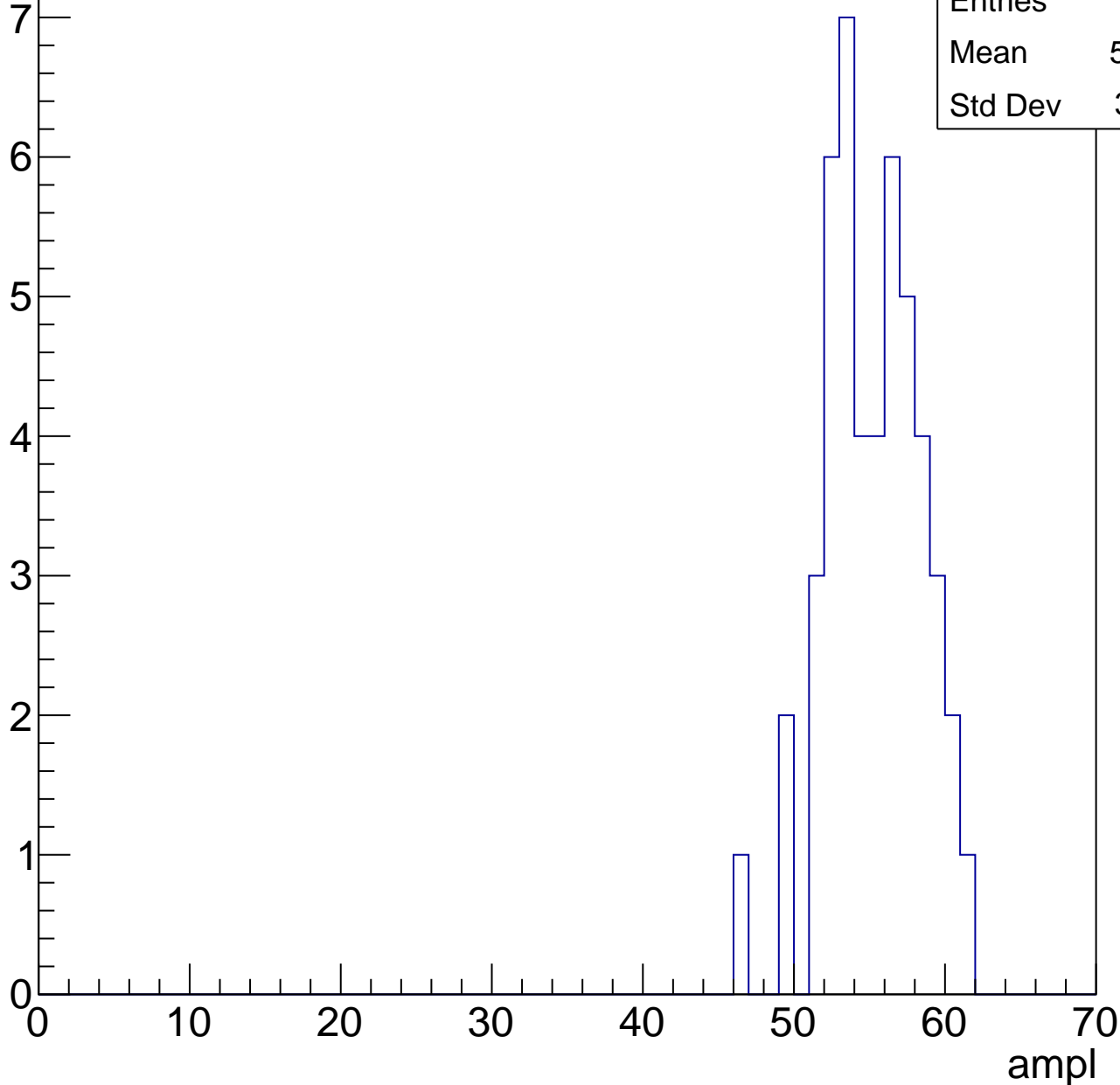


B1L102S, U22-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

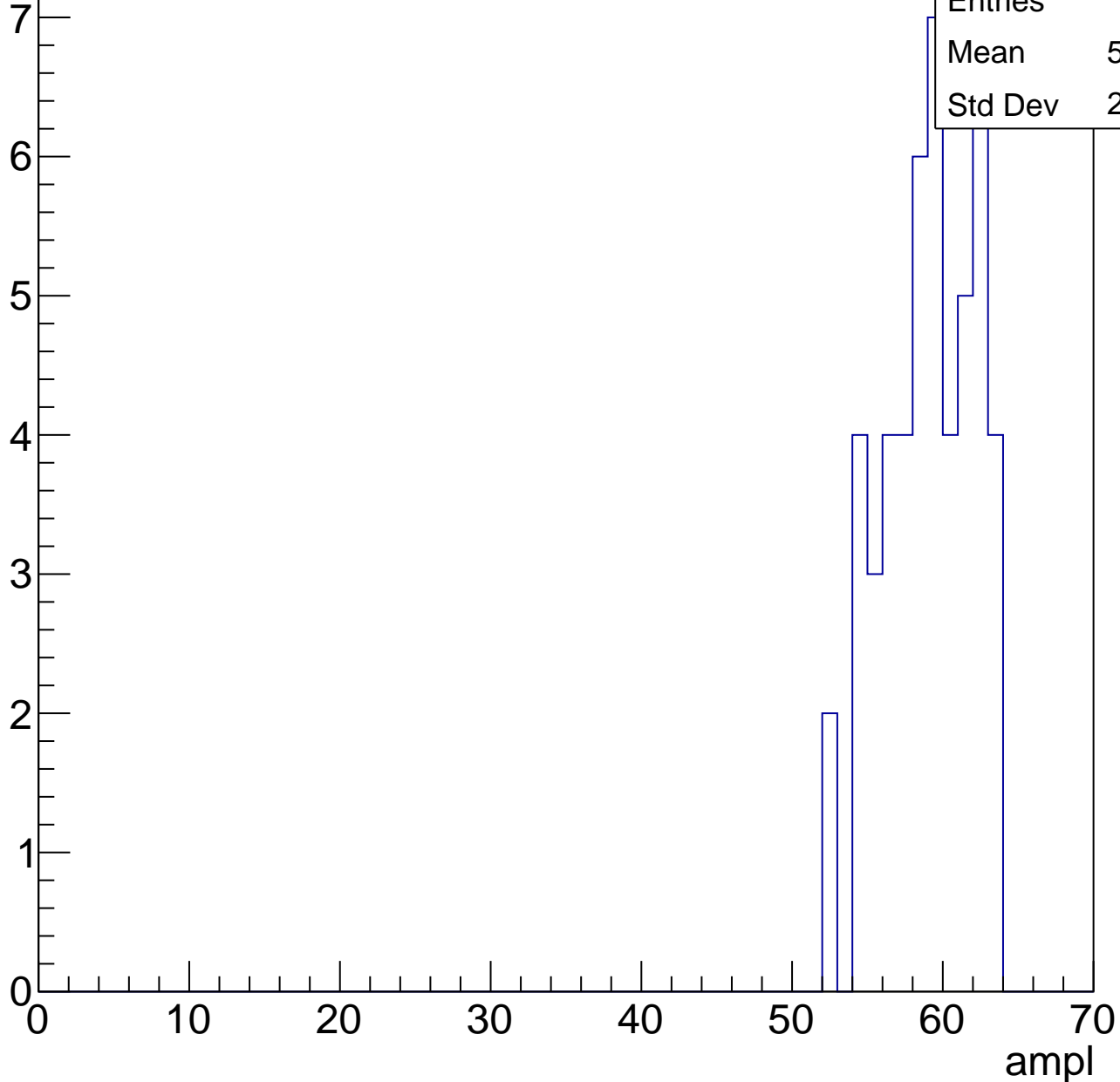
Entries	48
Mean	54.73
Std Dev	3.161



B1L102S, U22-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

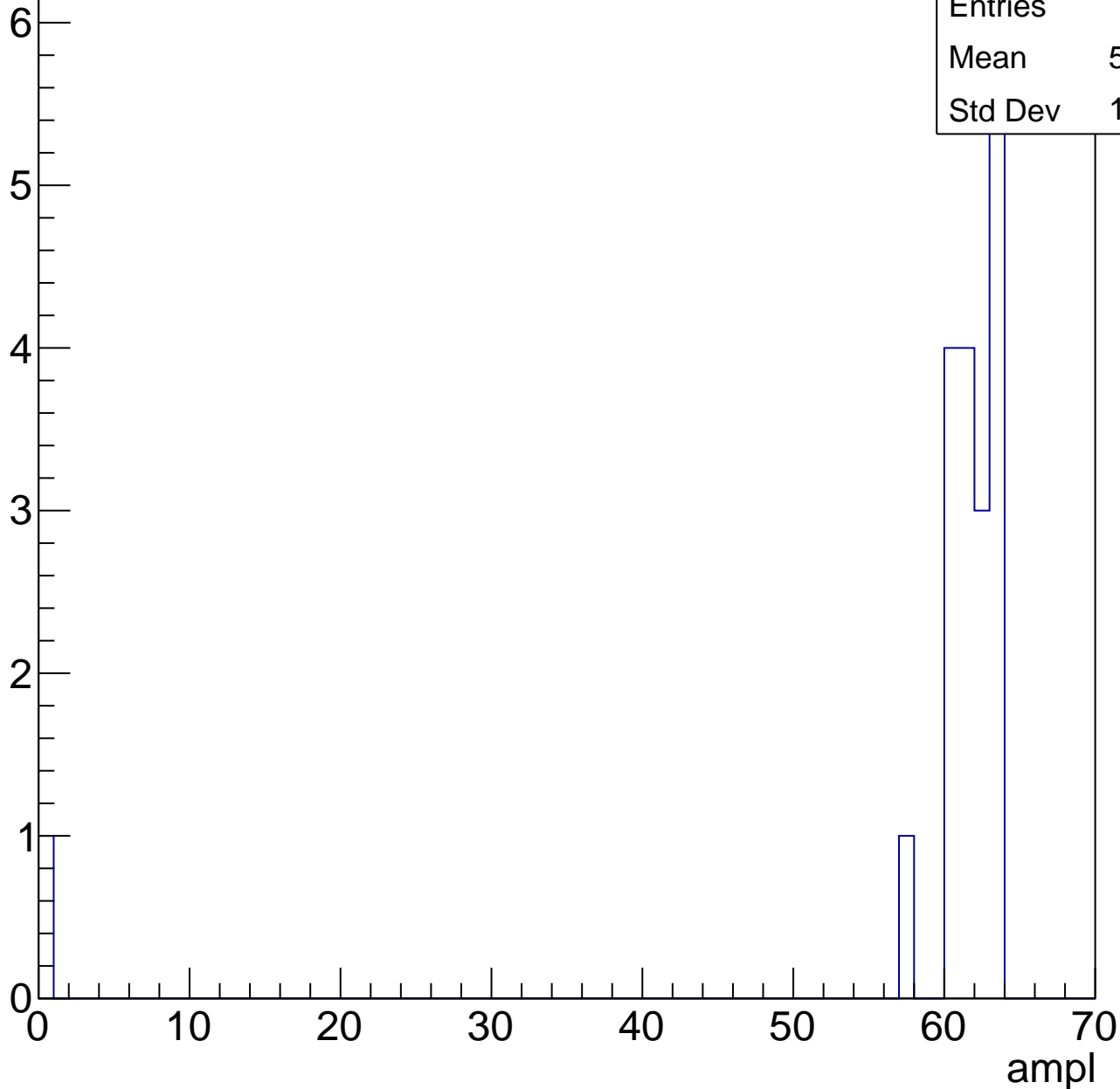


B1L102S, U22-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	58.16
Std Dev	13.79



B1L102S, U22-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch43, adc0

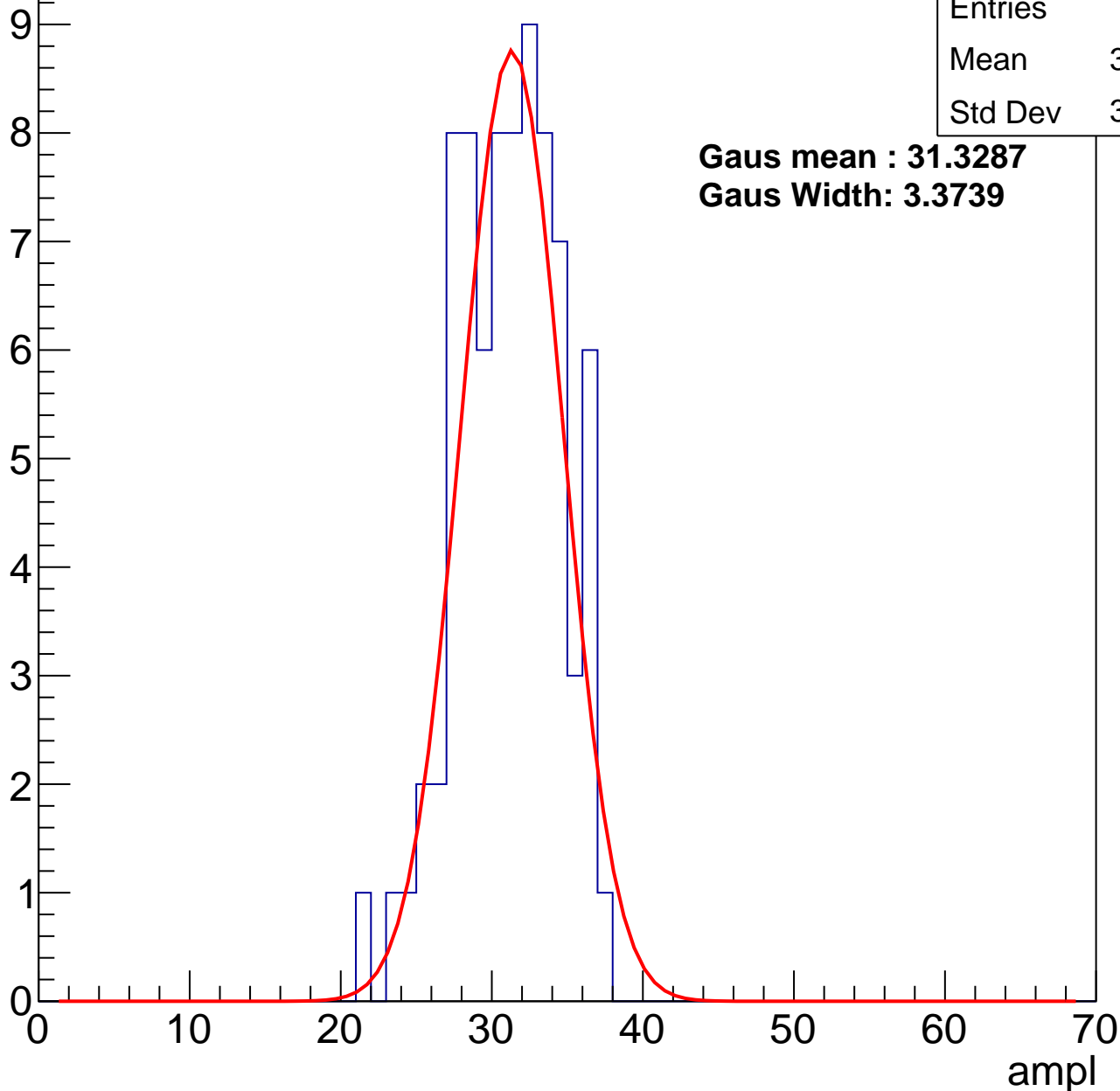
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	30.63
Std Dev	3.365

Gaus mean : 31.3287

Gaus Width: 3.3739



B1L102S, U22-ch43, adc1

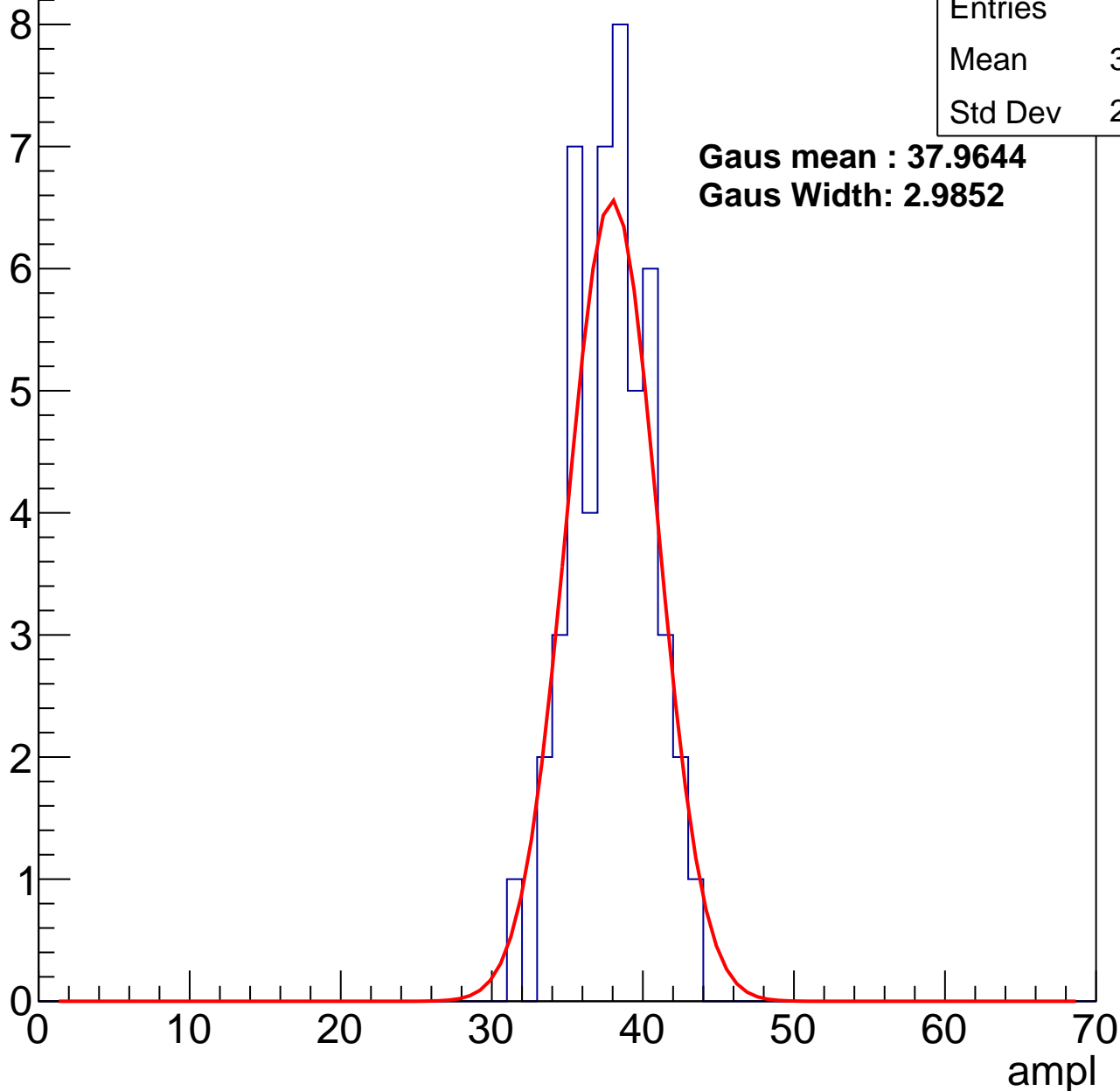
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	37.47
Std Dev	2.604

Gaus mean : 37.9644

Gaus Width: 2.9852



B1L102S, U22-ch43, adc2

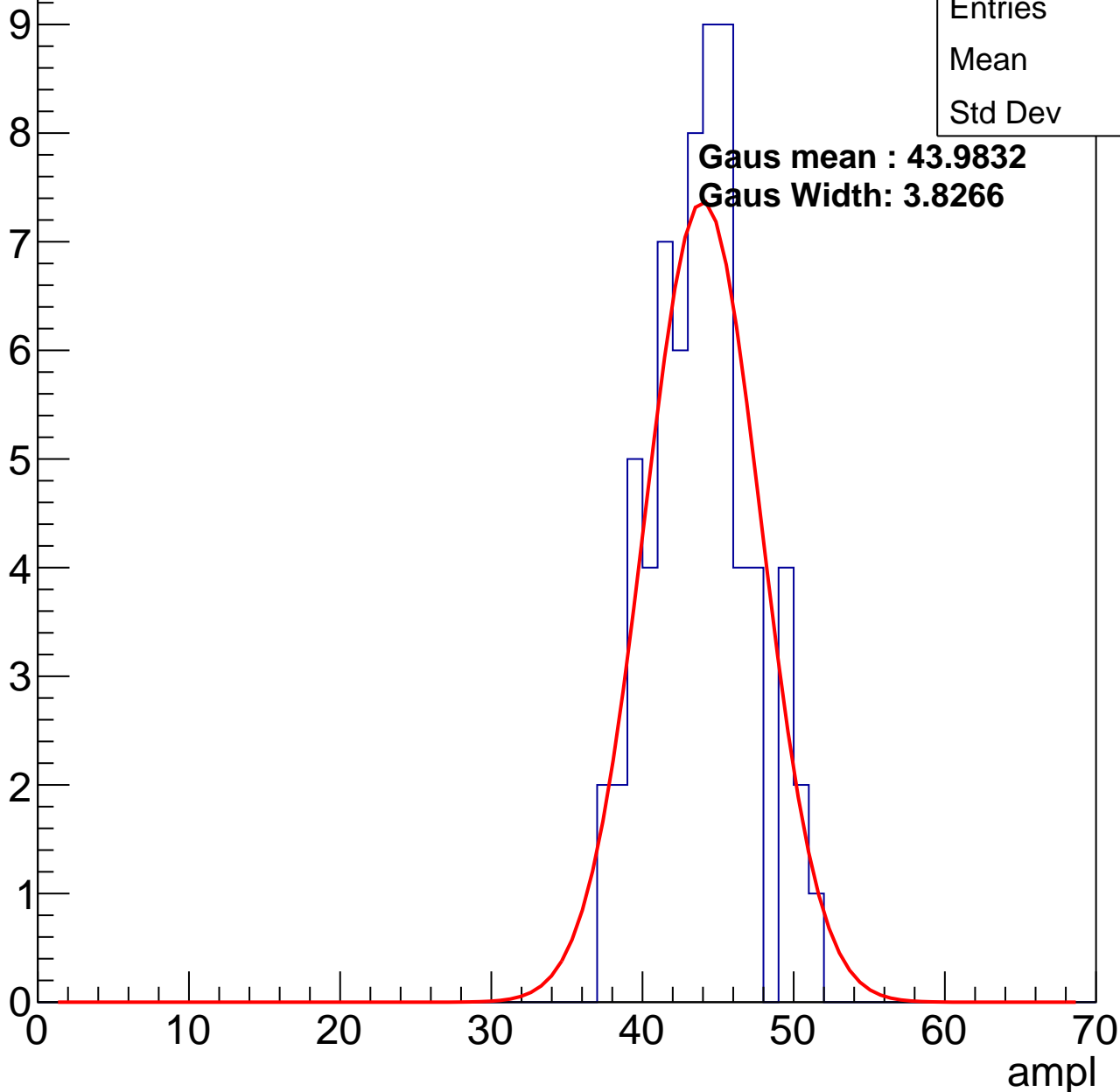
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.4
Std Dev	3.26

Gaus mean : 43.9832

Gaus Width: 3.8266

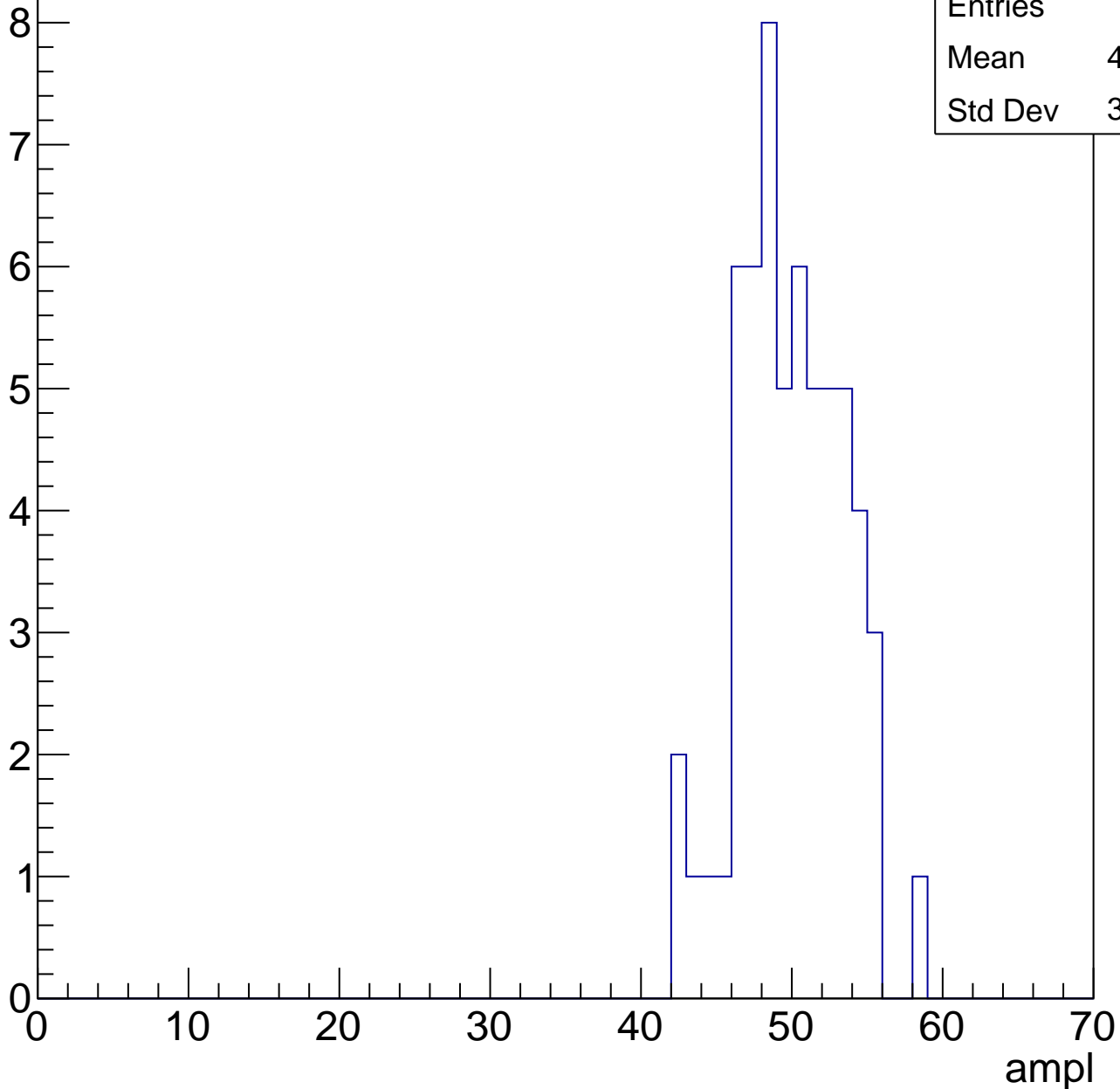


B1L102S, U22-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	49.53
Std Dev	3.407



B1L102S, U22-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	56
Std Dev	3.56

Entry

10

8

6

4

2

0

0

10

20

30

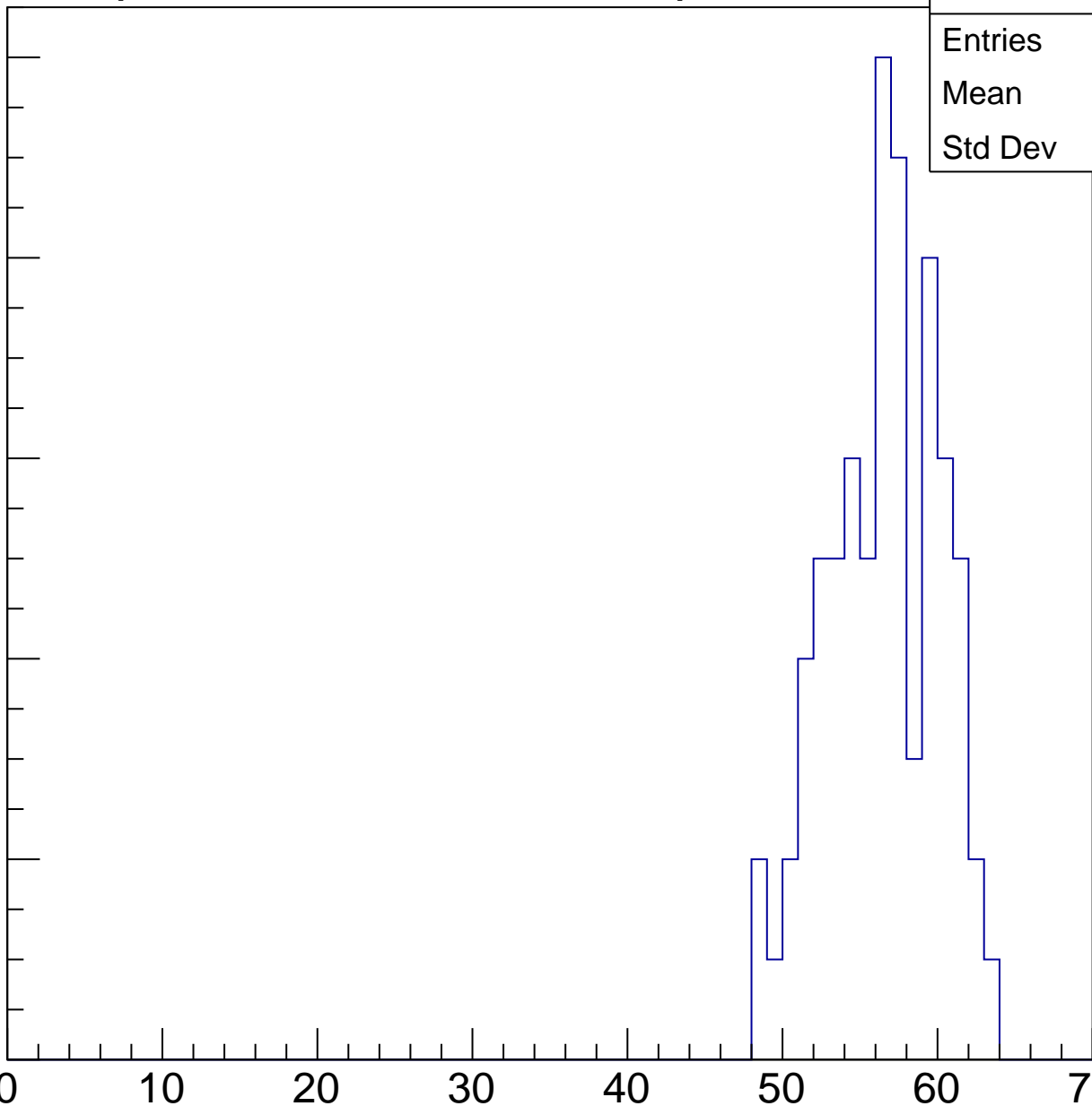
40

50

60

ampl

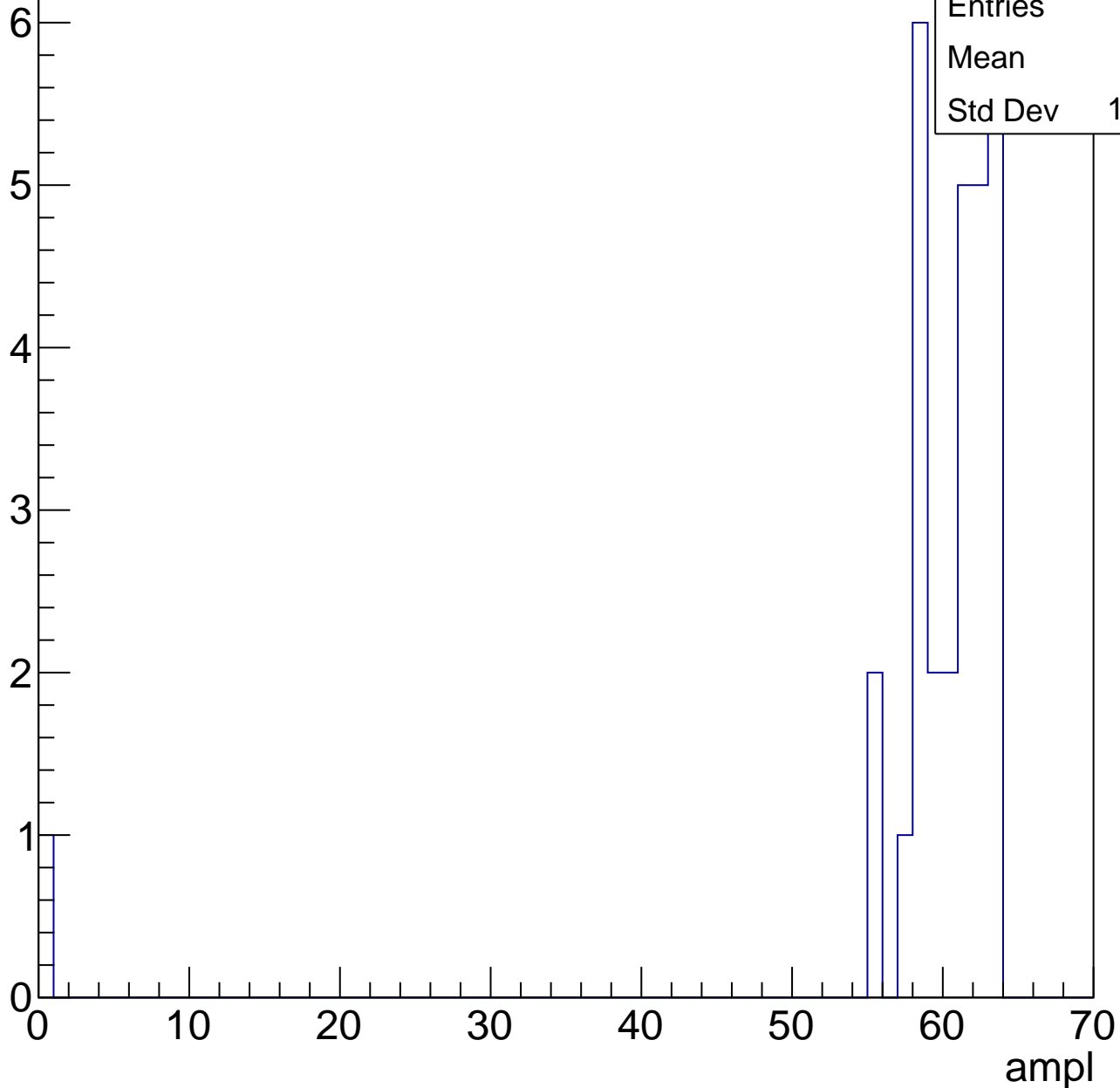
70



B1L102S, U22-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

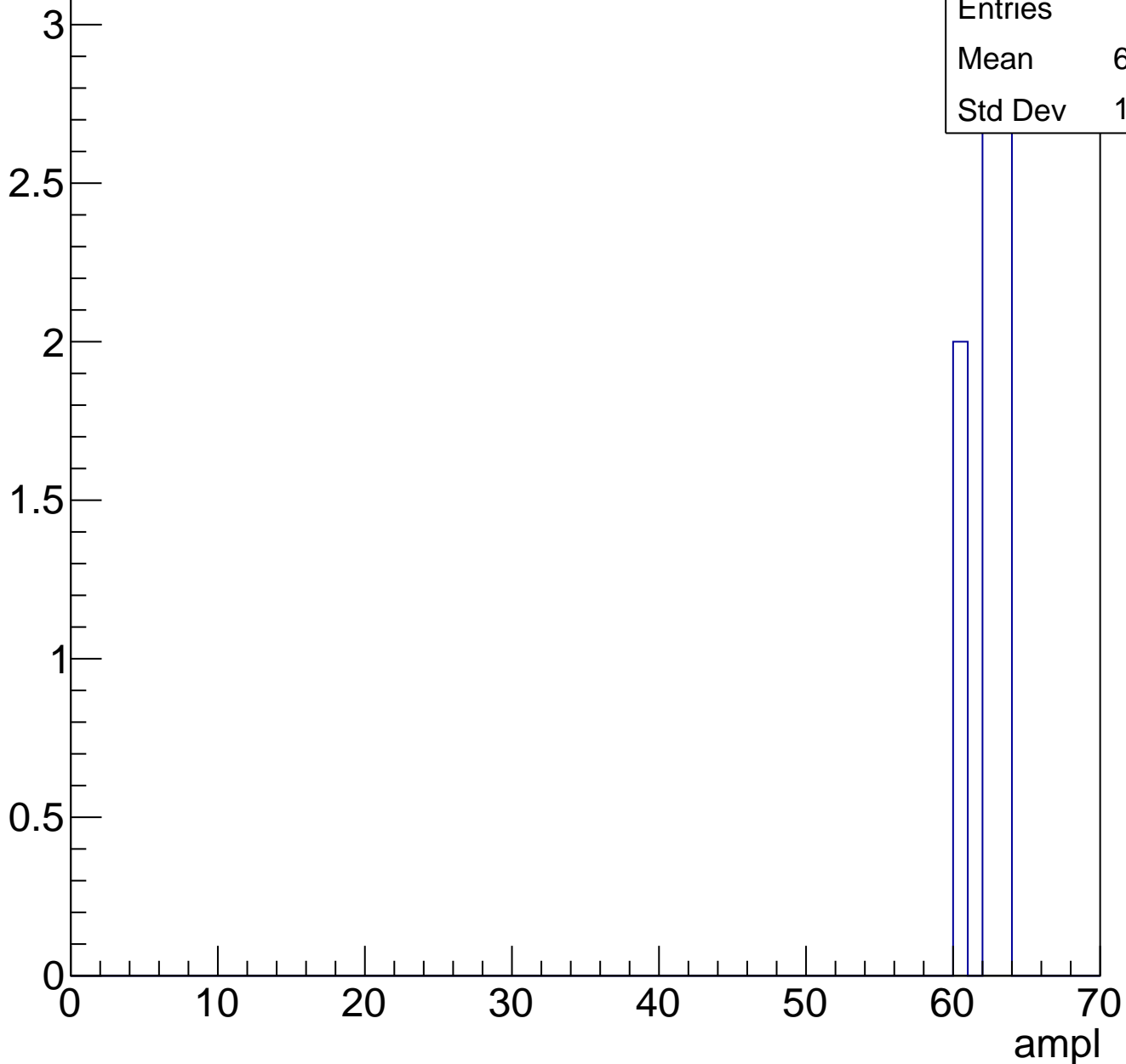
Entry



B1L102S, U22-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch44, adc0

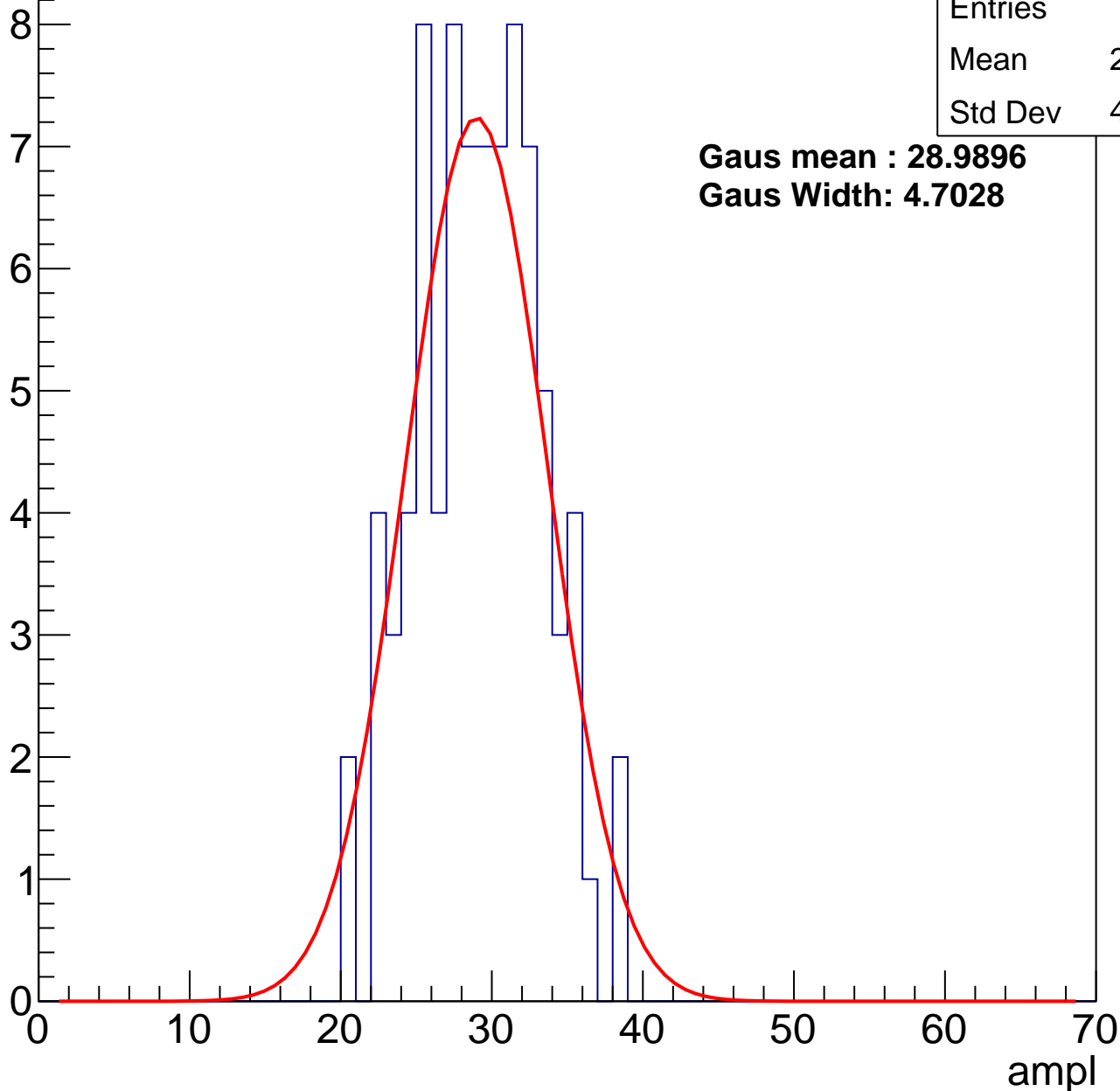
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.73
Std Dev	4.045

Gaus mean : 28.9896

Gaus Width: 4.7028



B1L102S, U22-ch44, adc1

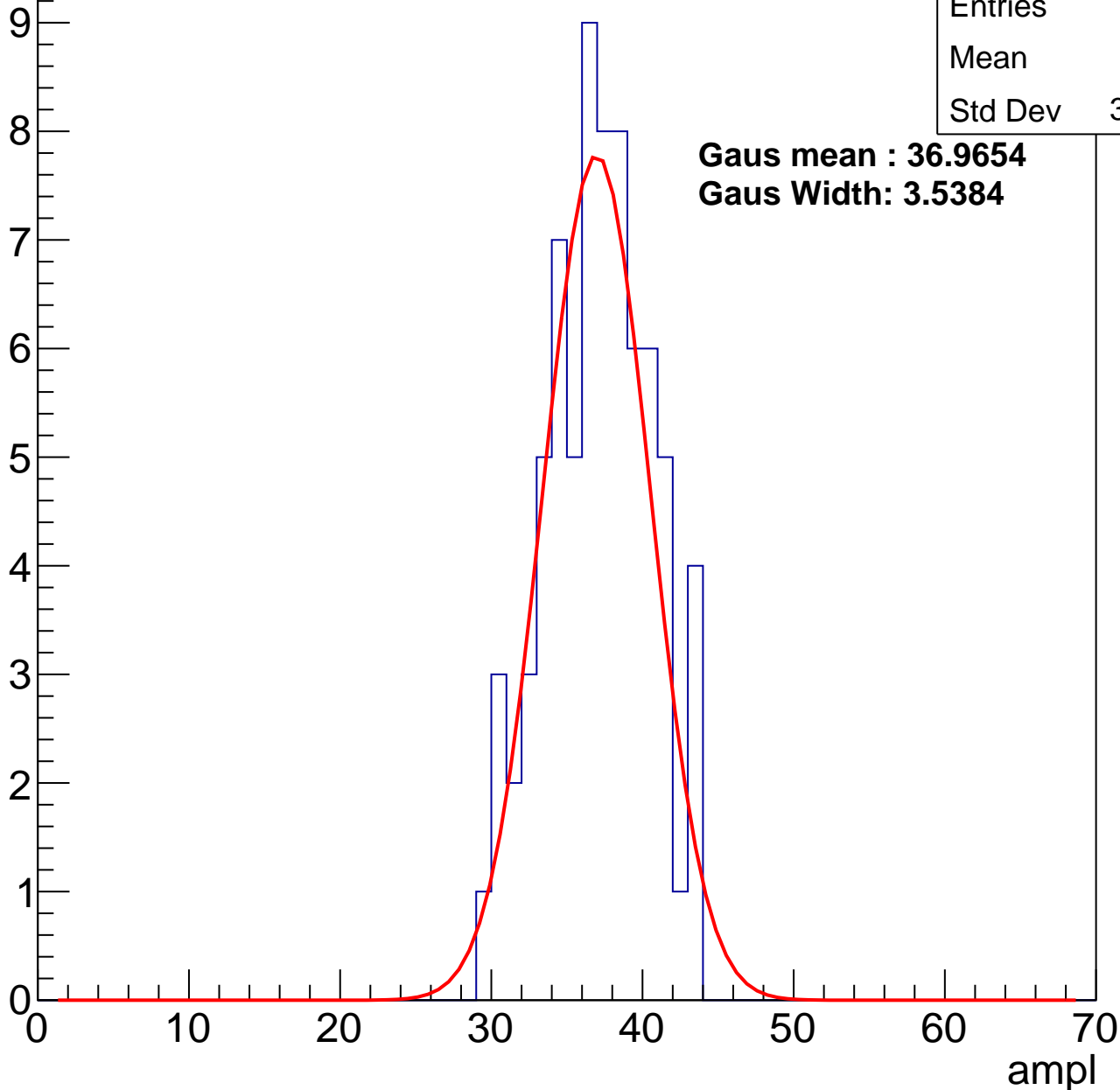
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.6
Std Dev	3.419

Gaus mean : 36.9654

Gaus Width: 3.5384



B1L102S, U22-ch44, adc2

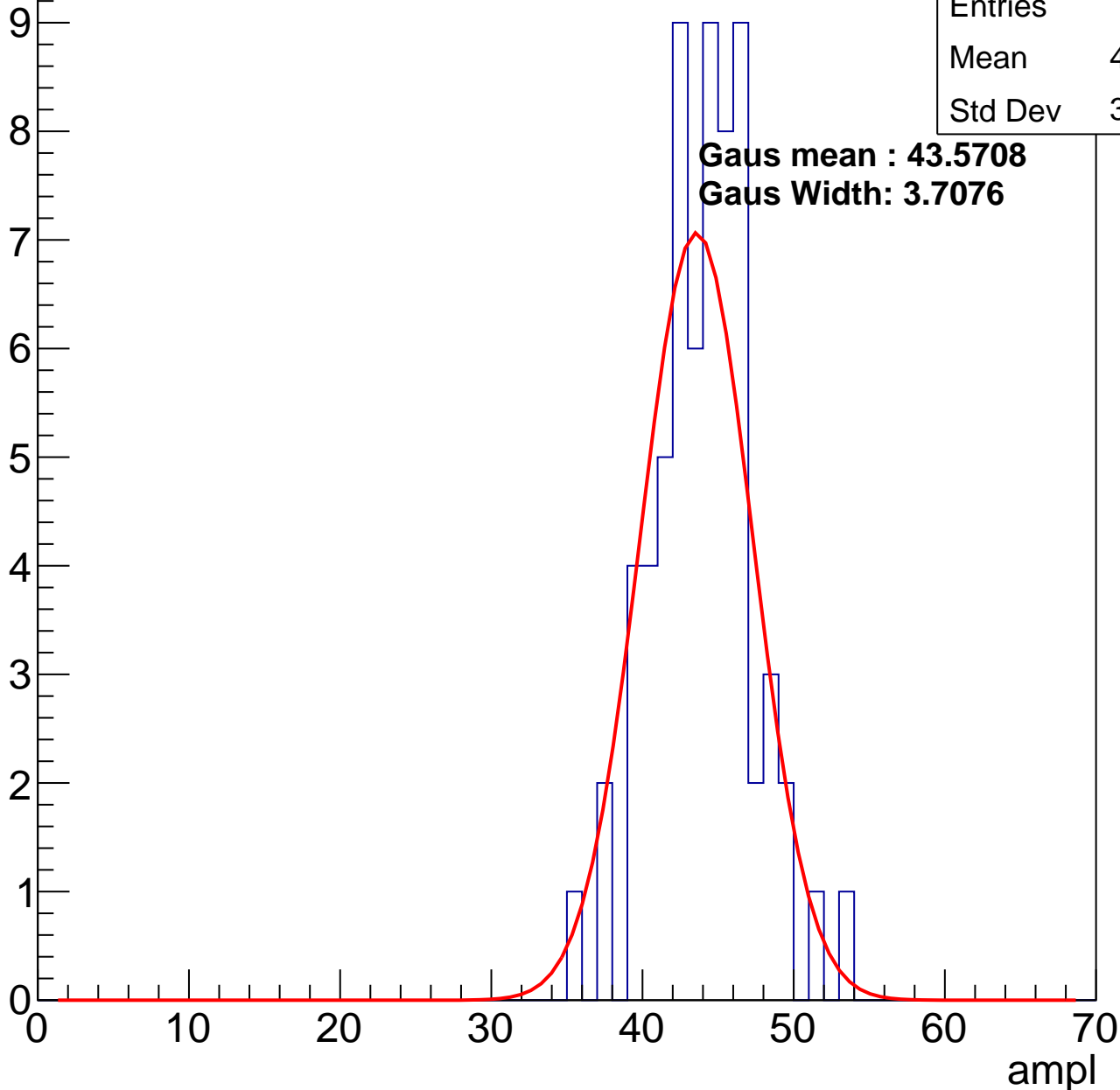
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.58
Std Dev	3.276

Gaus mean : 43.5708

Gaus Width: 3.7076

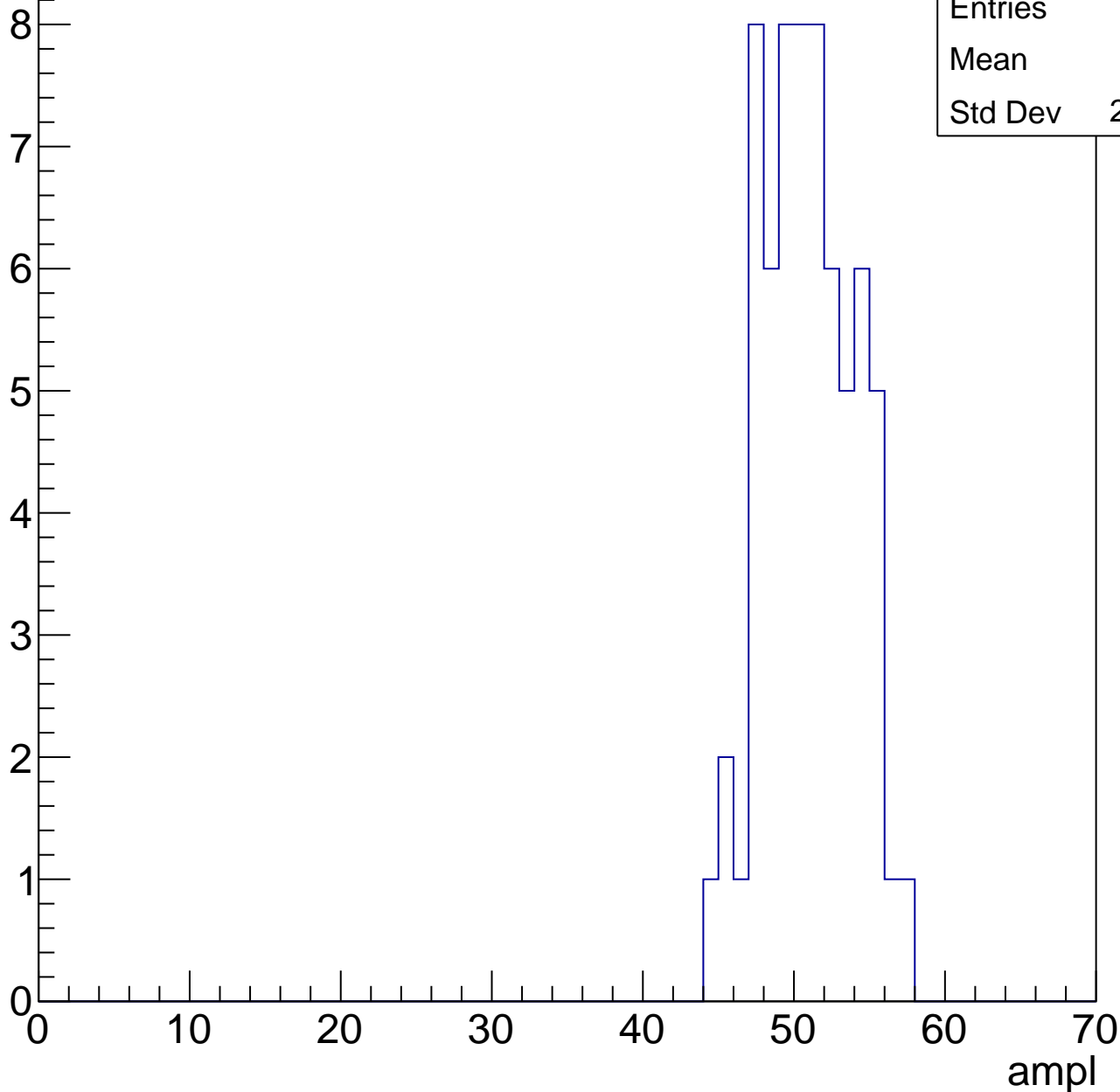


B1L102S, U22-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.5
Std Dev	2.945

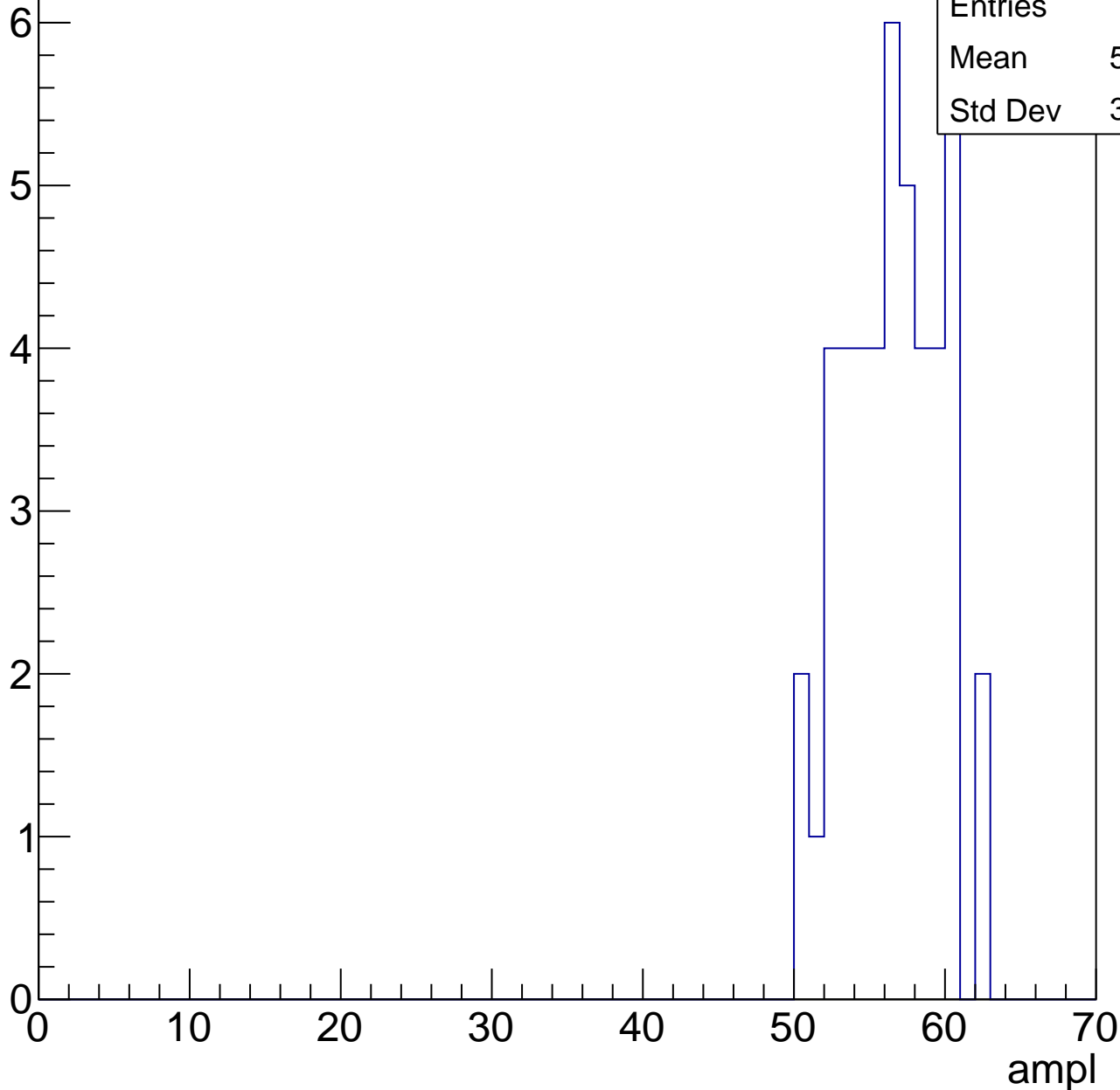


B1L102S, U22-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	56.09
Std Dev	3.099

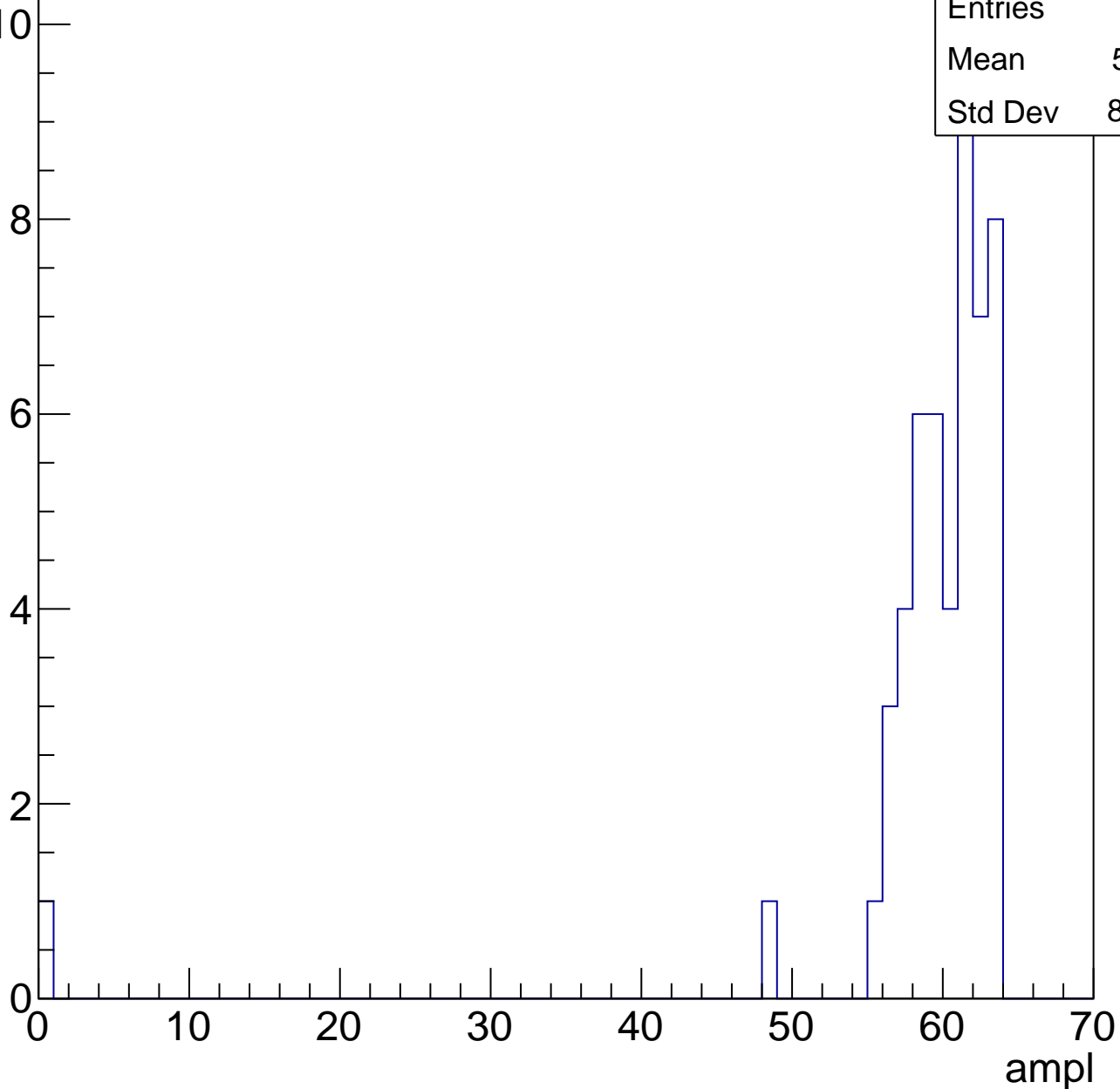


B1L102S, U22-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.61
Std Dev	8.738



B1L102S, U22-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L102S, U22-ch45, adc0

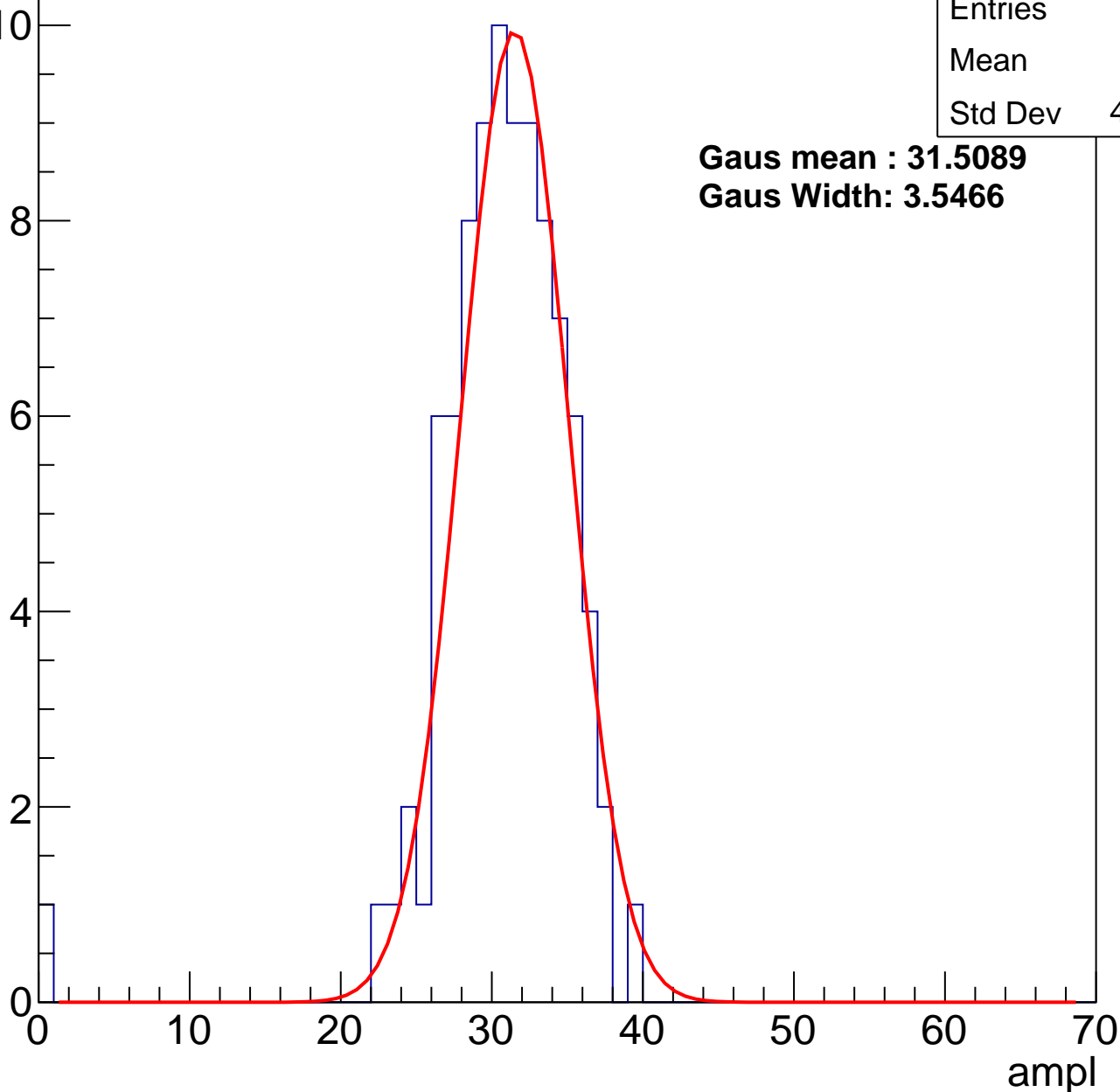
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	91
Mean	30.3
Std Dev	4.682

Gaus mean : 31.5089

Gaus Width: 3.5466



B1L102S, U22-ch45, adc1

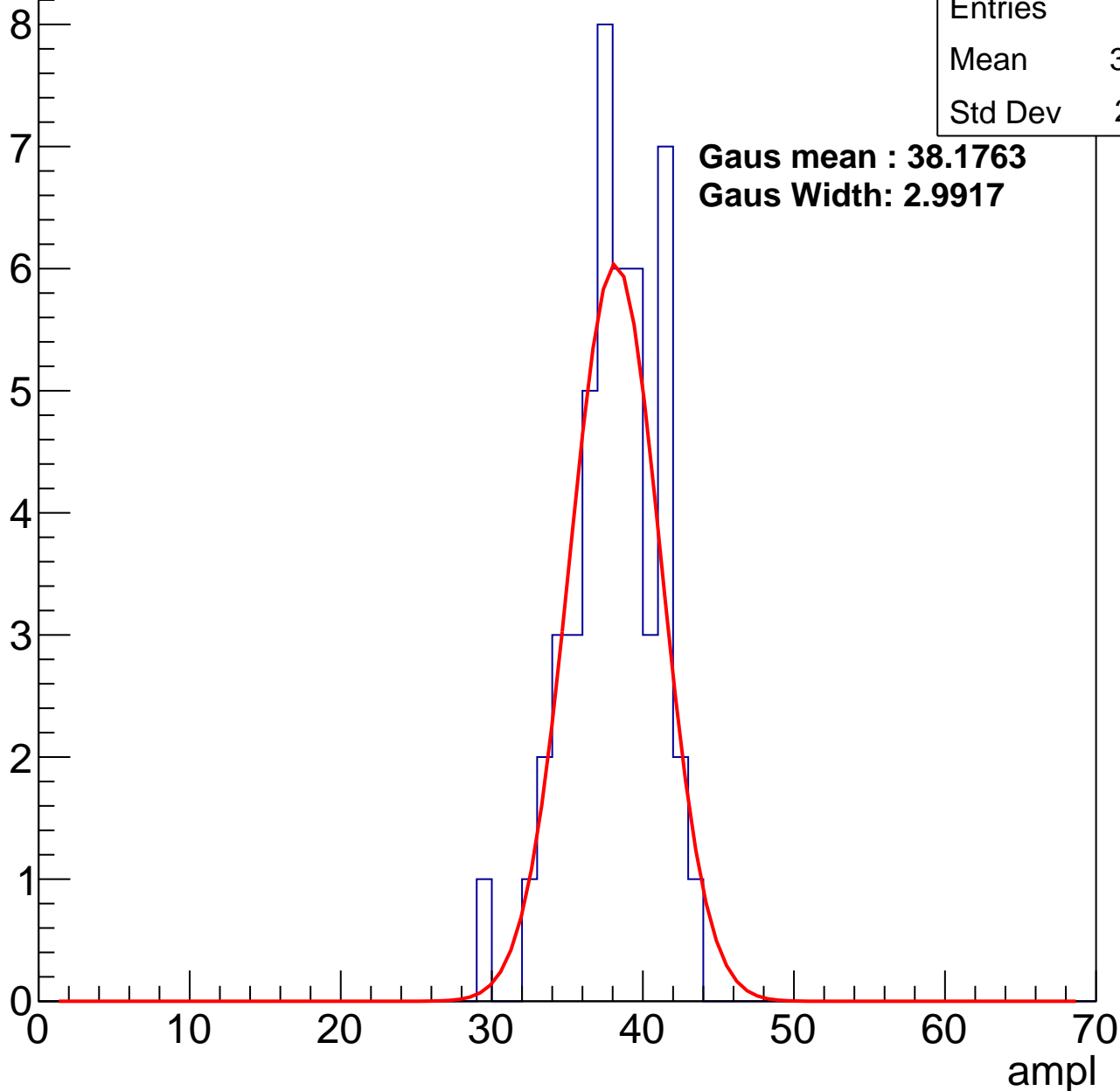
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	37.62
Std Dev	2.891

Gaus mean : 38.1763

Gaus Width: 2.9917



B1L102S, U22-ch45, adc2

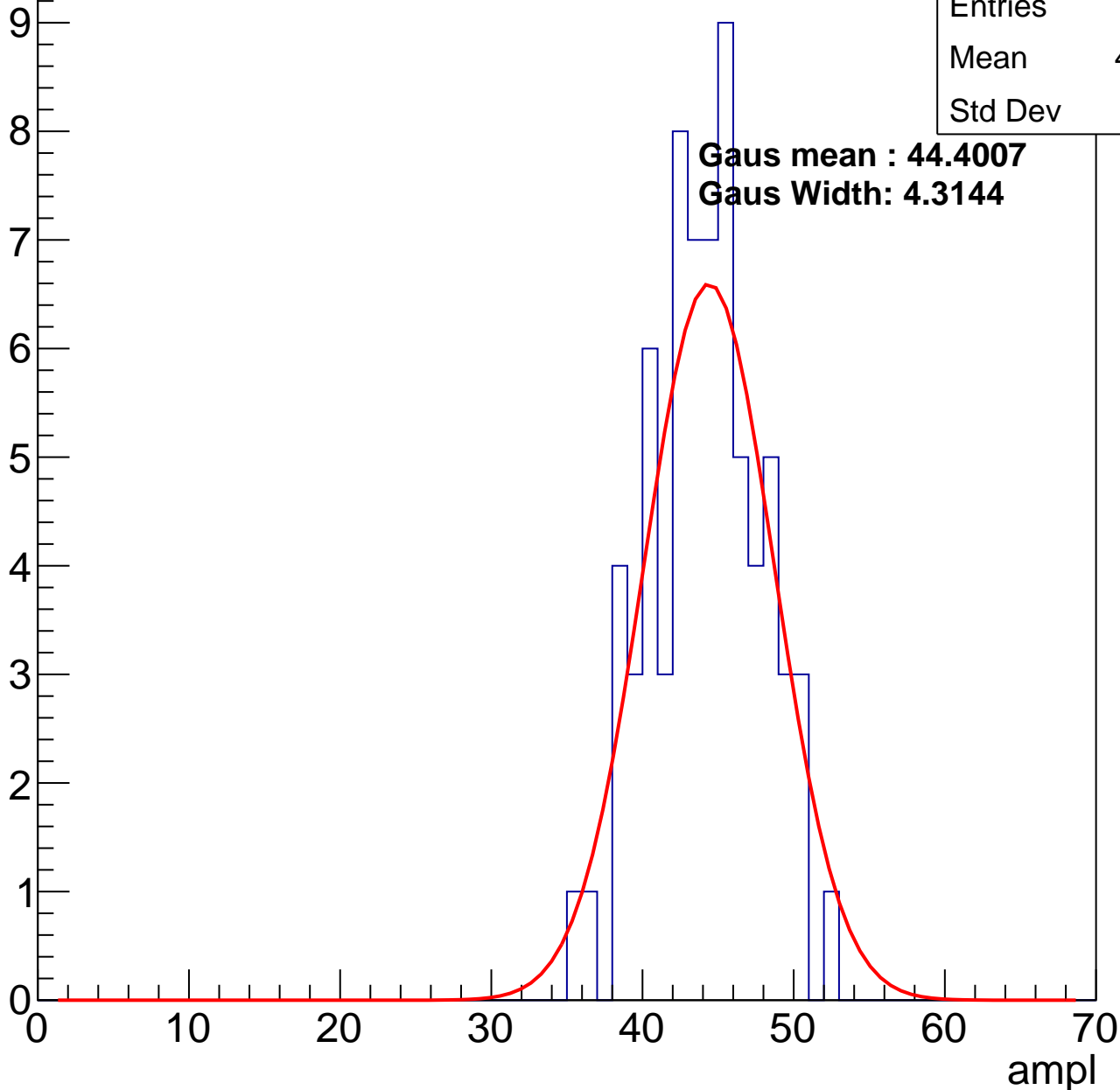
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.71
Std Dev	3.61

Gaus mean : 44.4007

Gaus Width: 4.3144

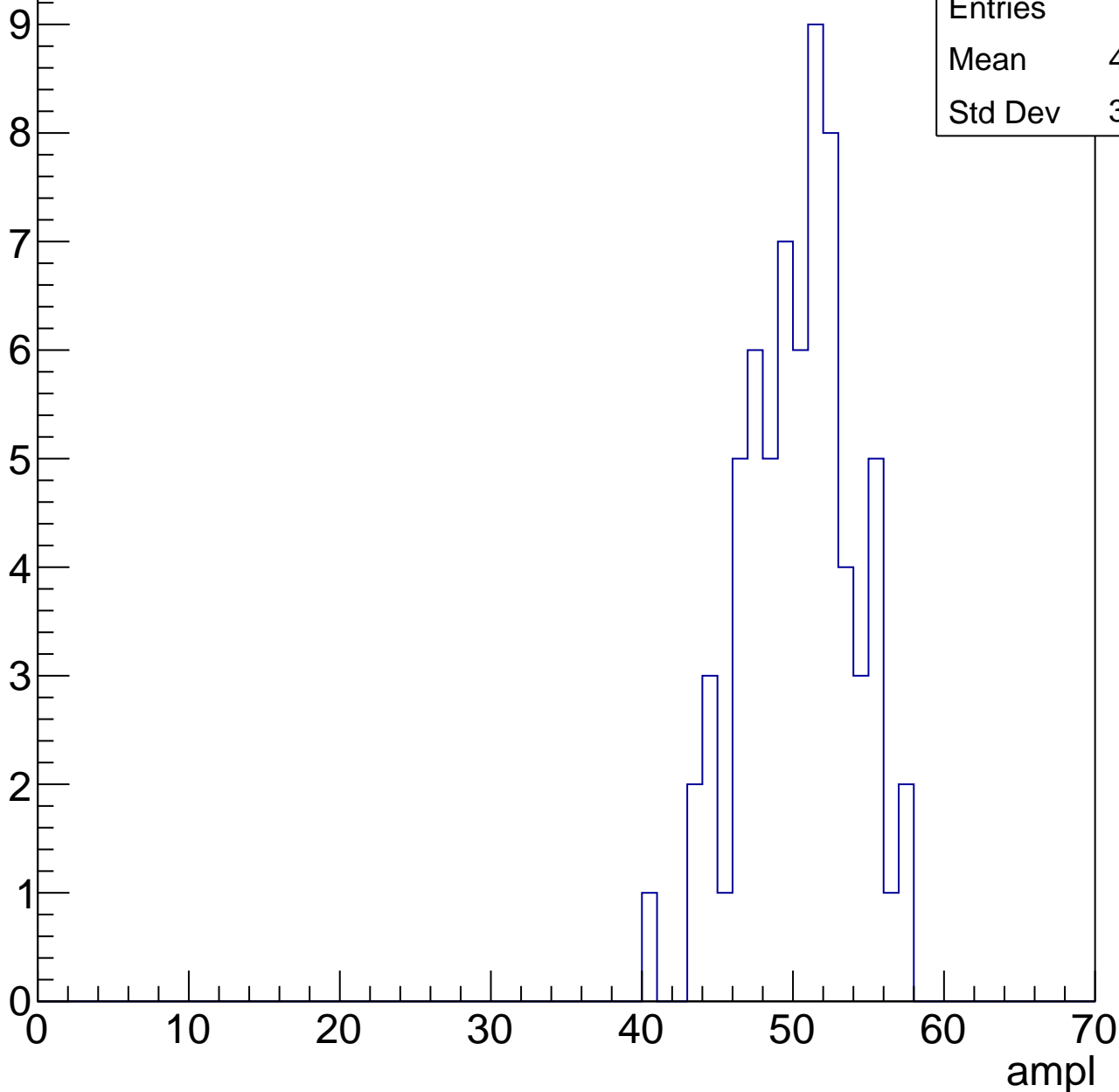


B1L102S, U22-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.88
Std Dev	3.587



B1L102S, U22-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	55
Mean	56.51
Std Dev	2.86

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

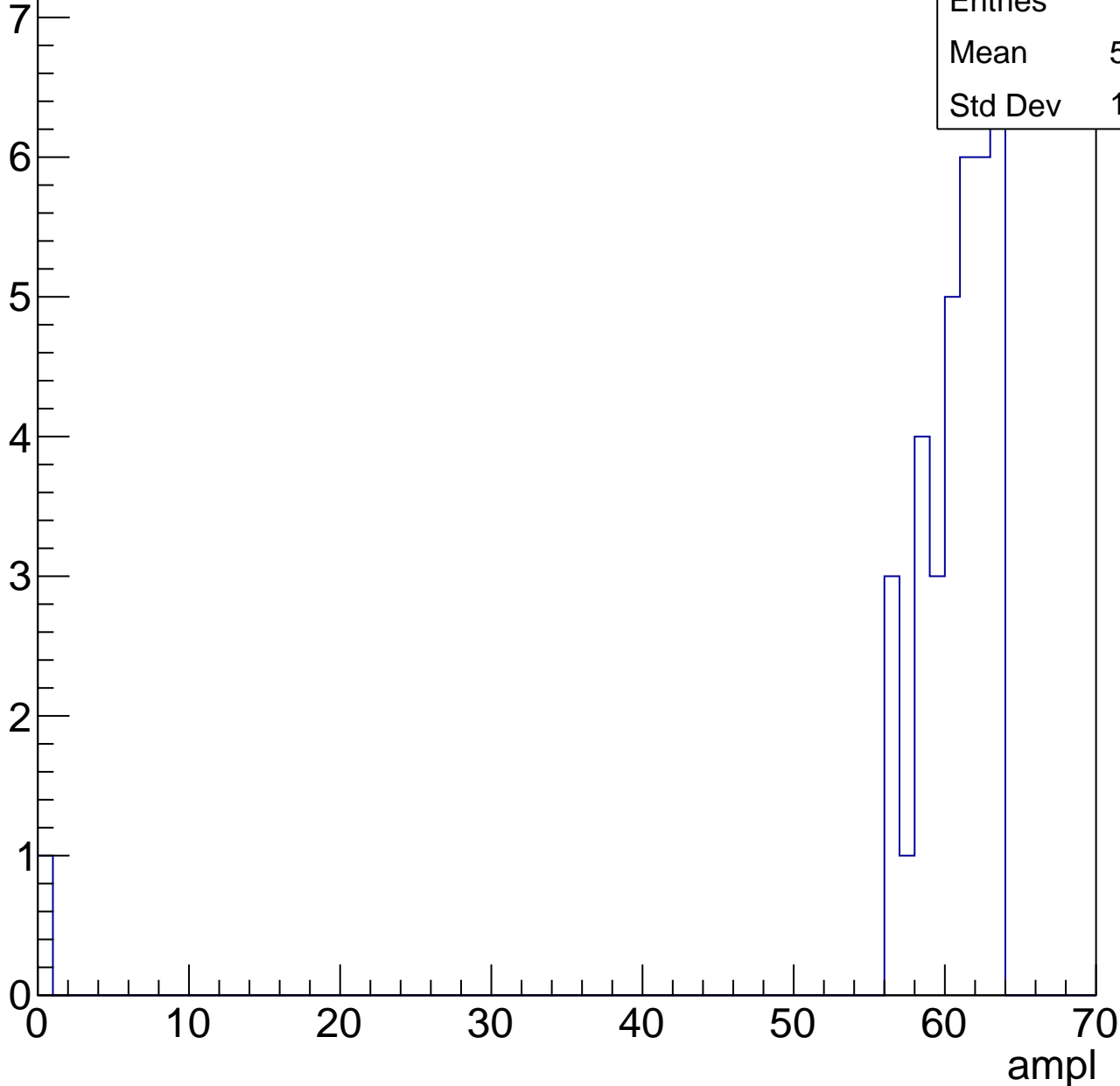
7

B1L102S, U22-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

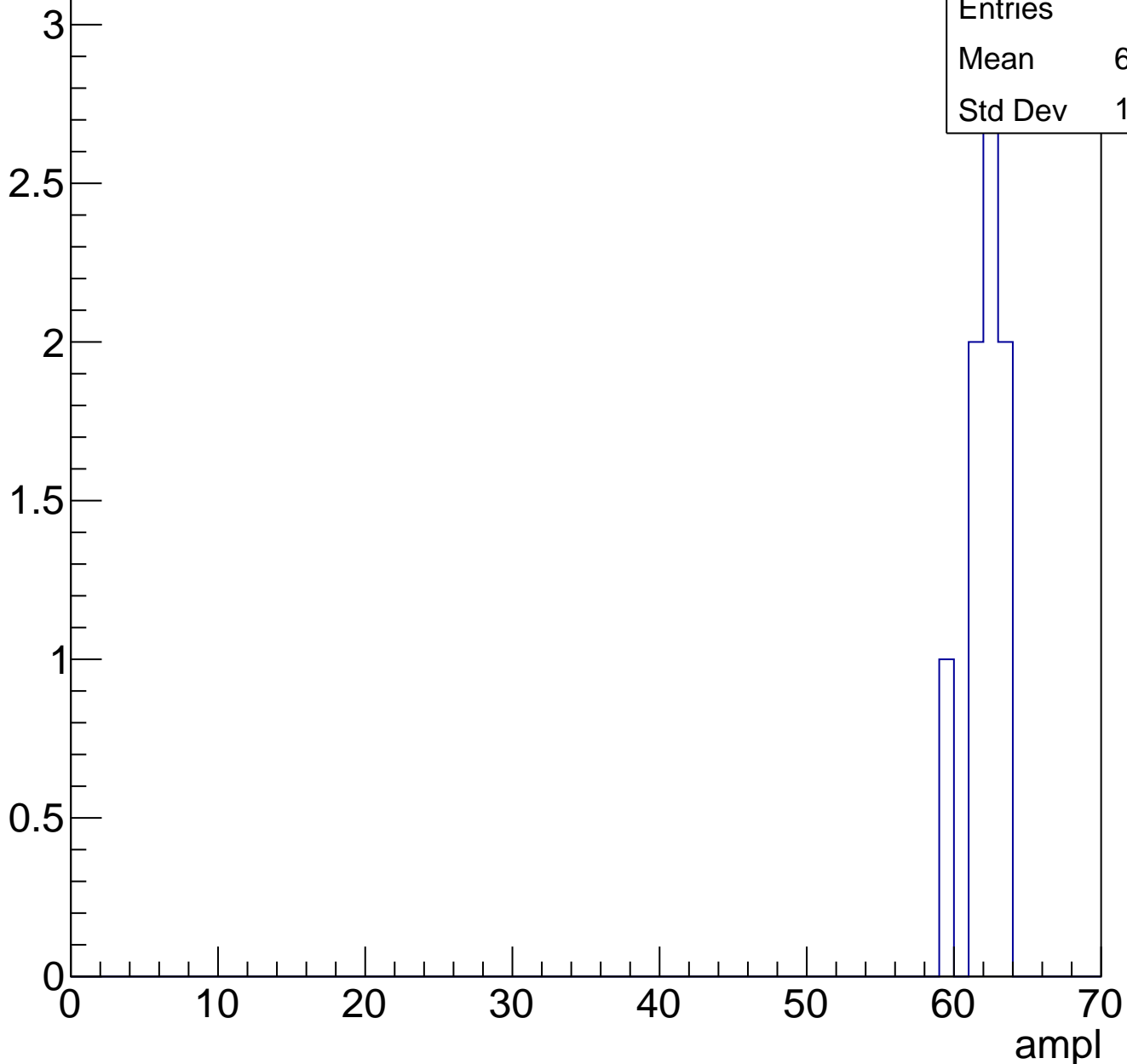
Entries	36
Mean	58.69
Std Dev	10.15



B1L102S, U22-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch46, adc0

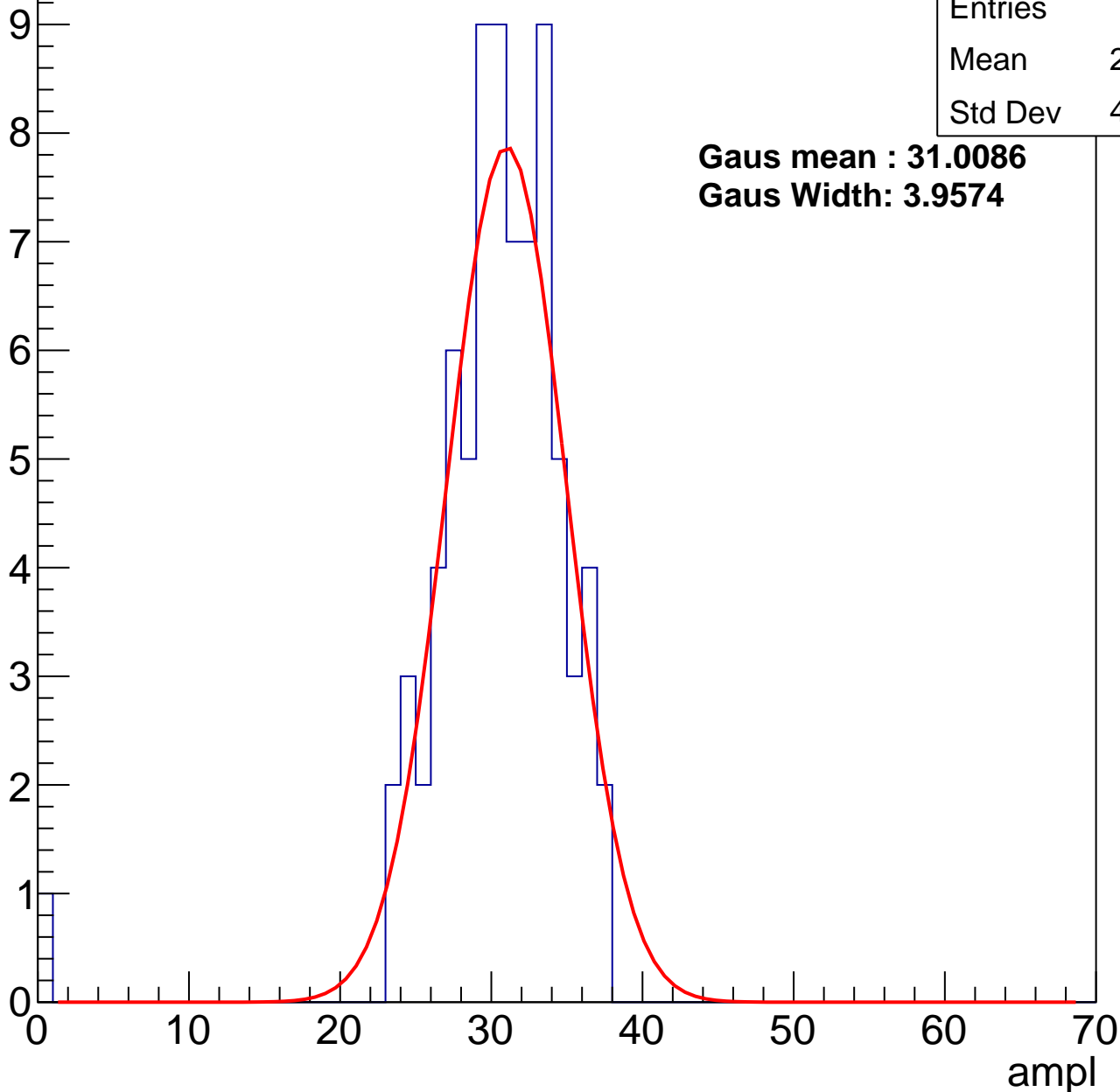
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	29.95
Std Dev	4.825

Gaus mean : 31.0086

Gaus Width: 3.9574



B1L102S, U22-ch46, adc1

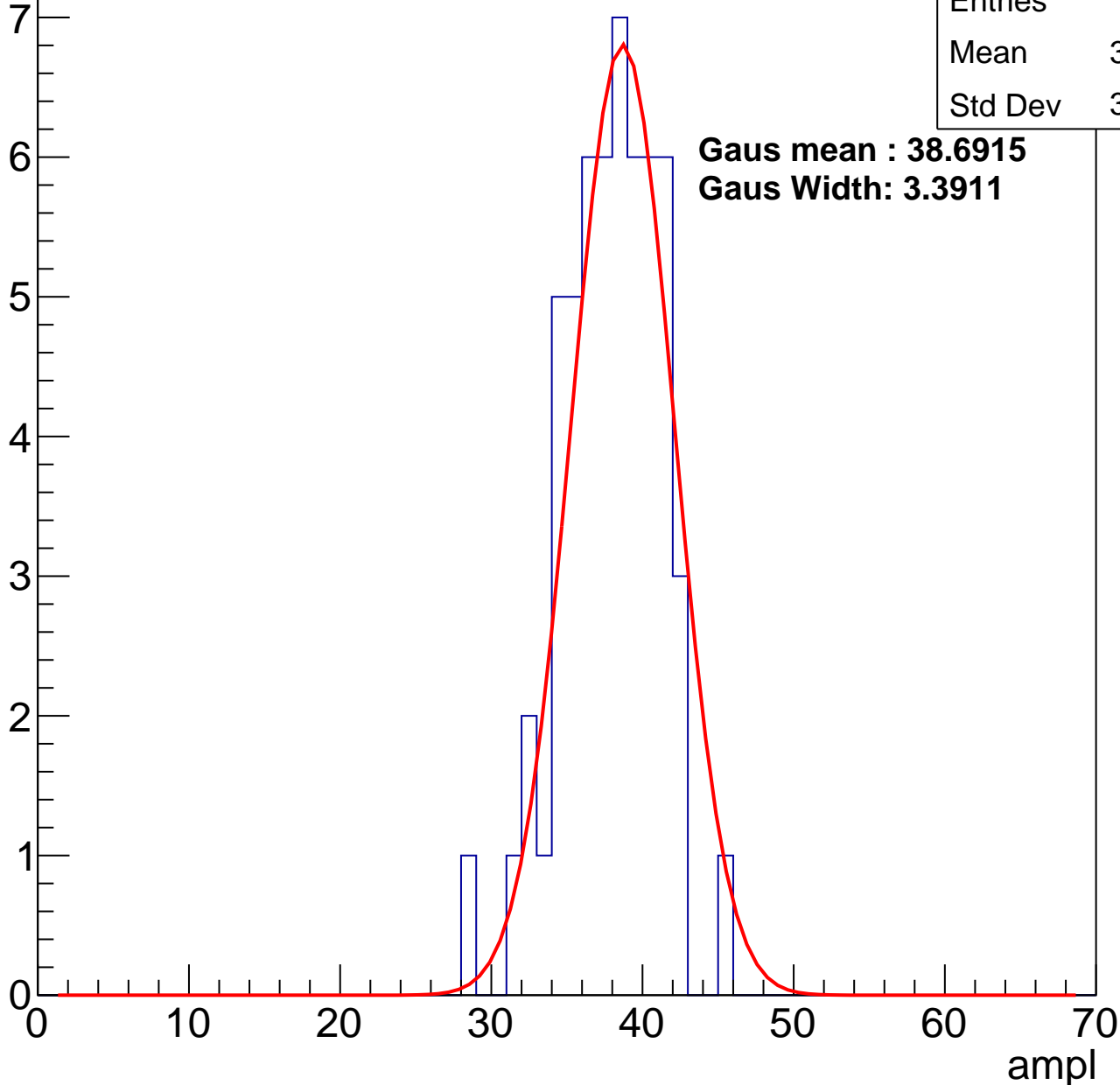
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	37.43
Std Dev	3.173

Gaus mean : 38.6915

Gaus Width: 3.3911



B1L102S, U22-ch46, adc2

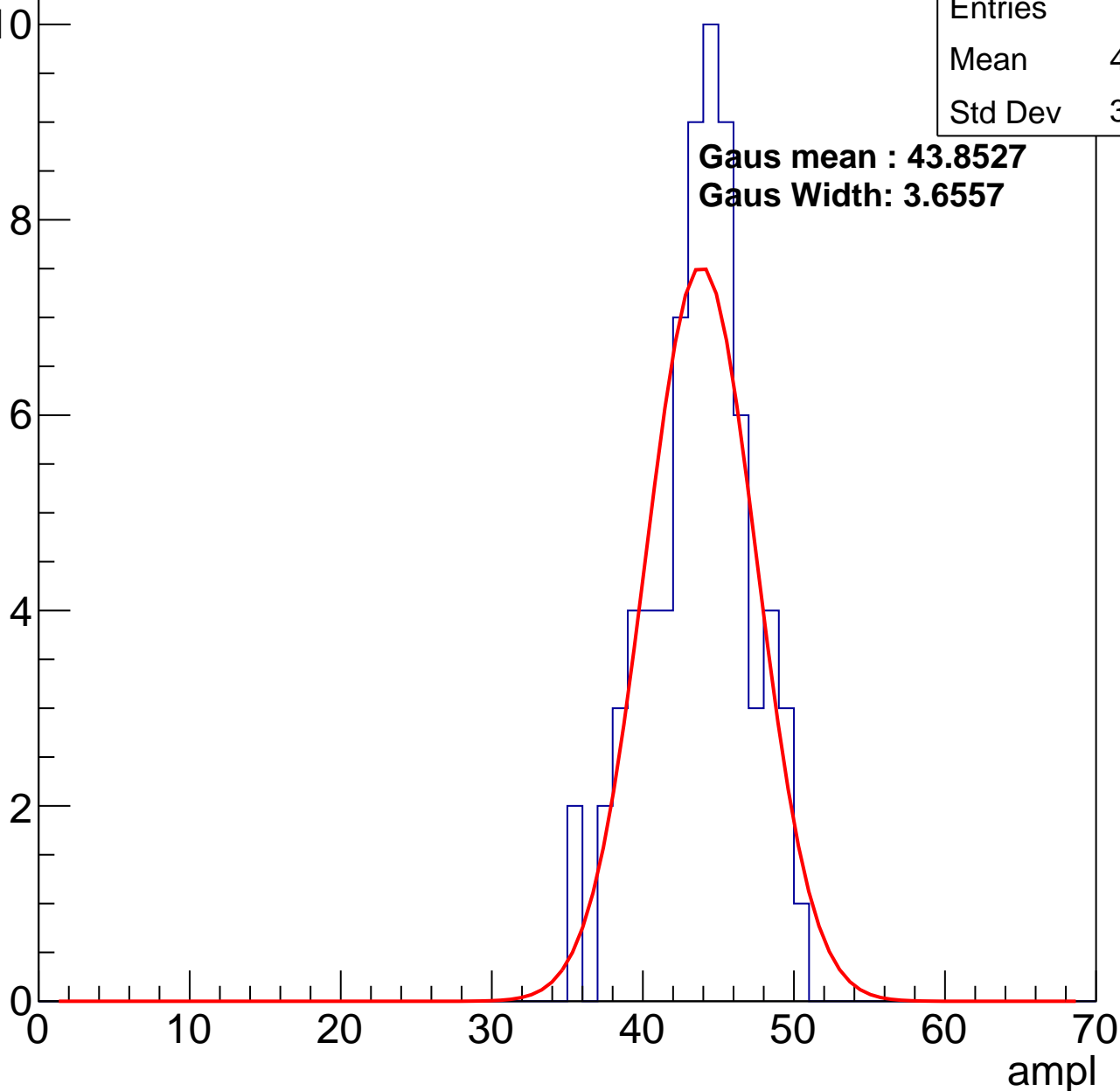
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.24
Std Dev	3.354

Gaus mean : 43.8527

Gaus Width: 3.6557

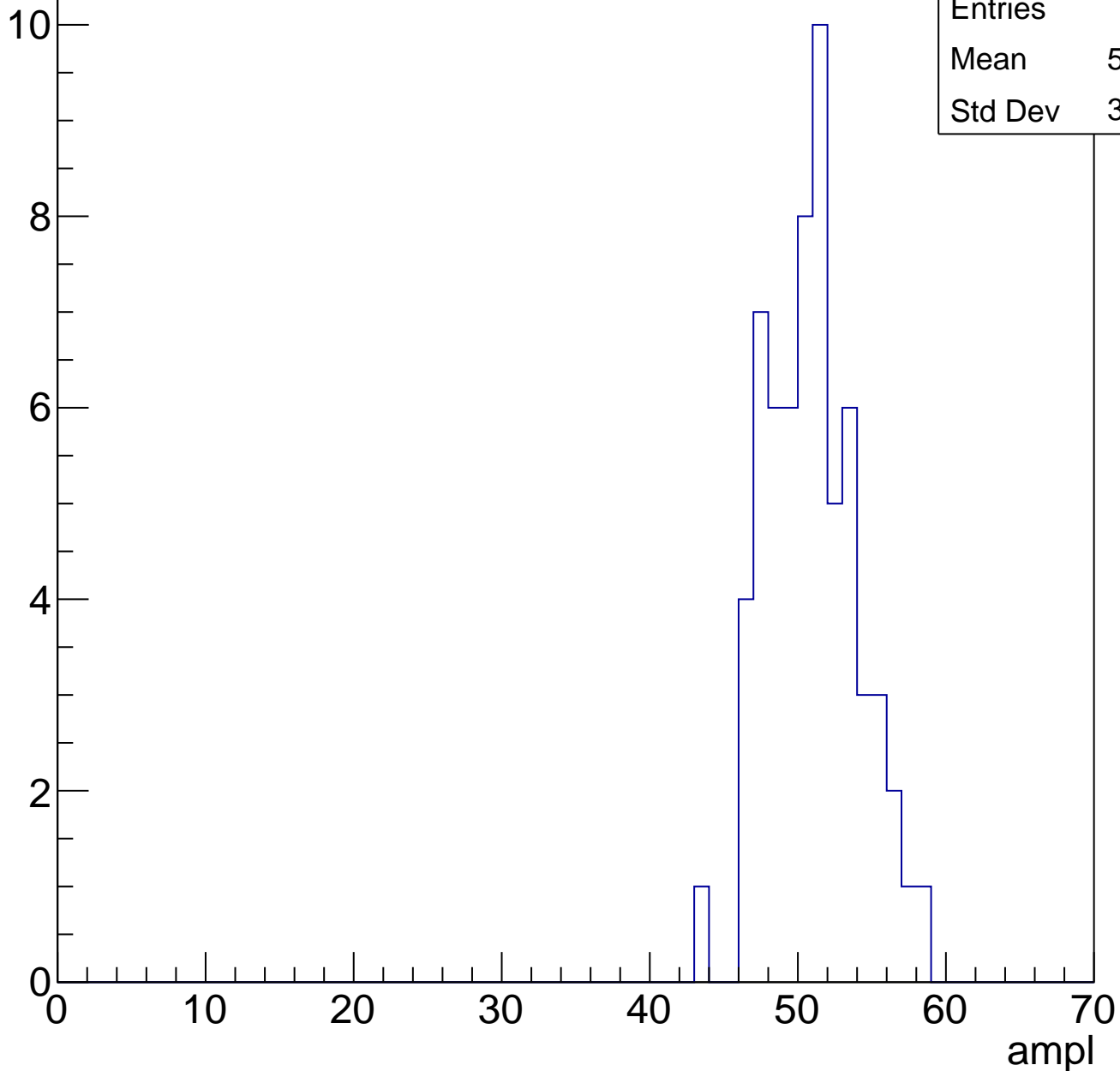


B1L102S, U22-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	50.48
Std Dev	3.044

Entry

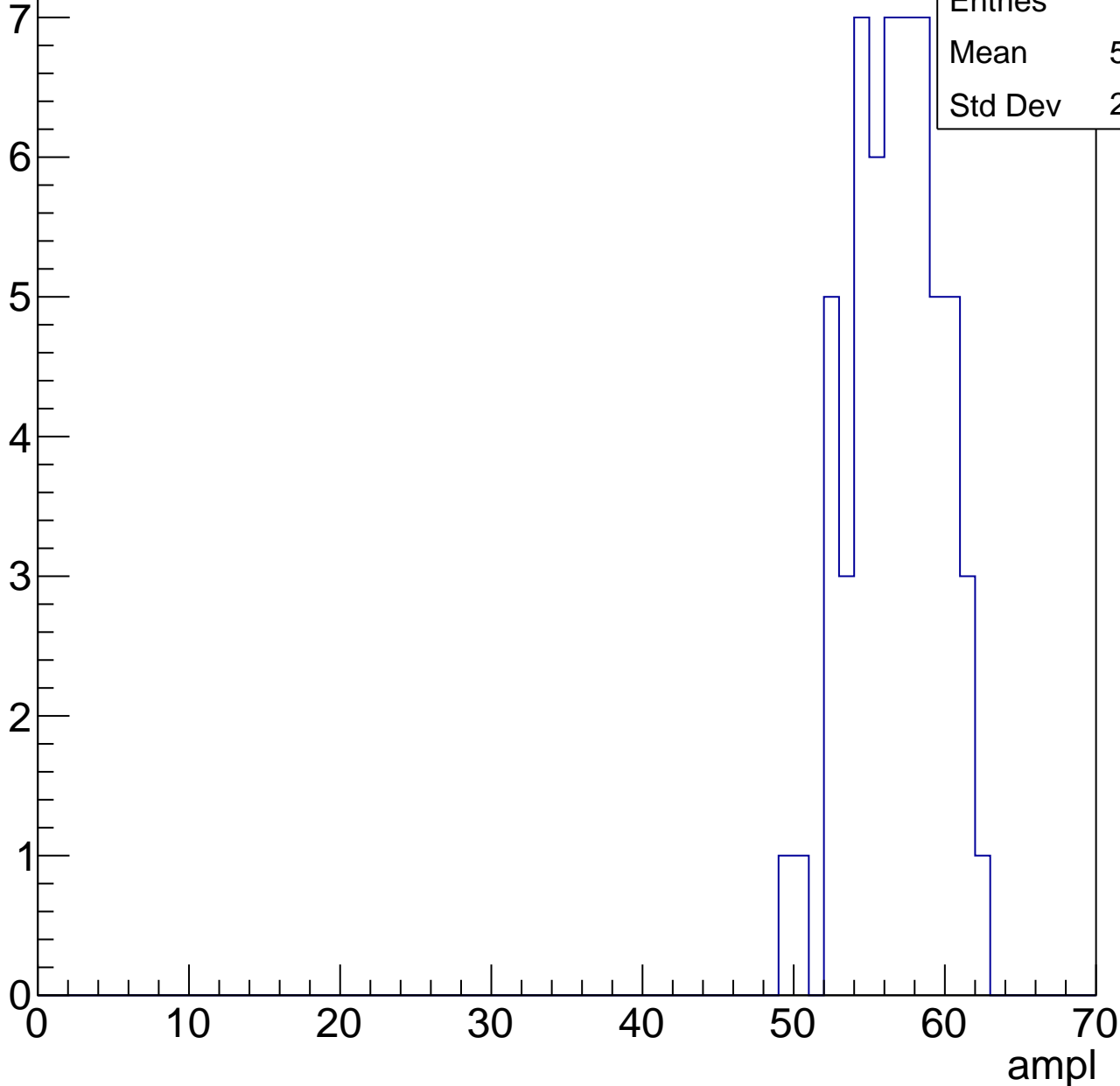


B1L102S, U22-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.26
Std Dev	2.916

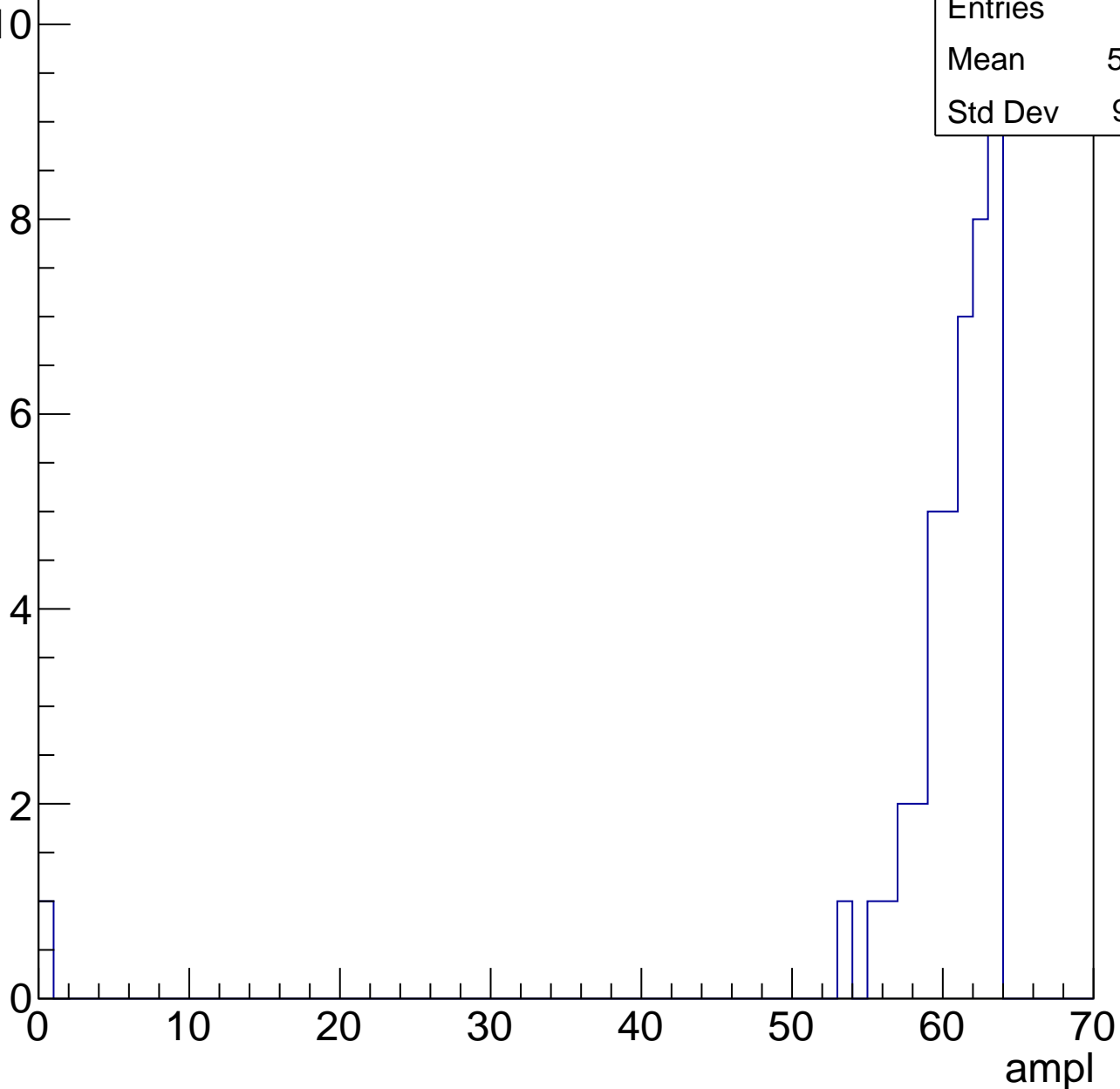


B1L102S, U22-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.12
Std Dev	9.421



B1L102S, U22-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

62

Std Dev

0

B1L102S, U22-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L102S, U22-ch47, adc0

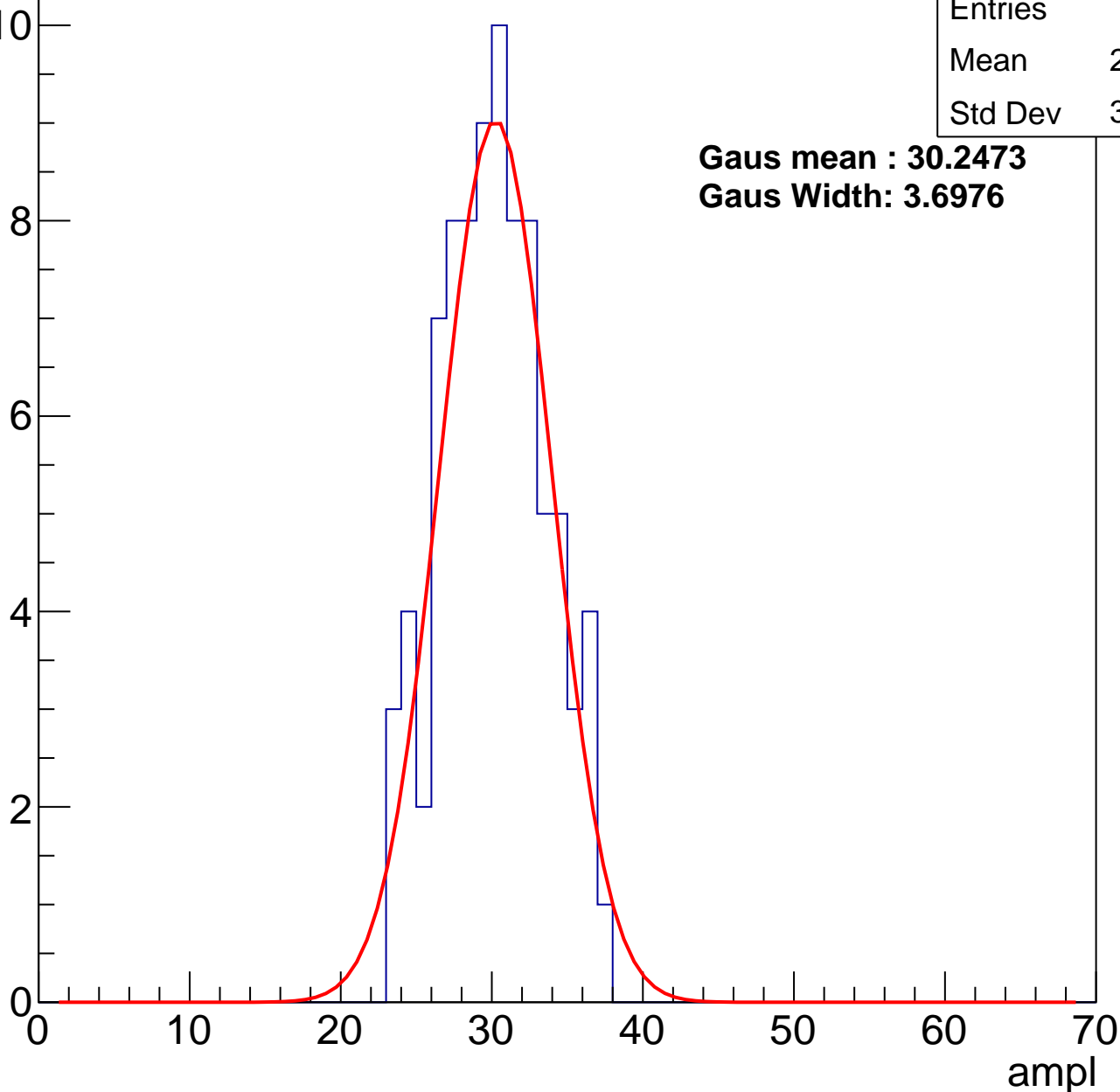
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	29.68
Std Dev	3.414

Gaus mean : 30.2473

Gaus Width: 3.6976



B1L102S, U22-ch47, adc1

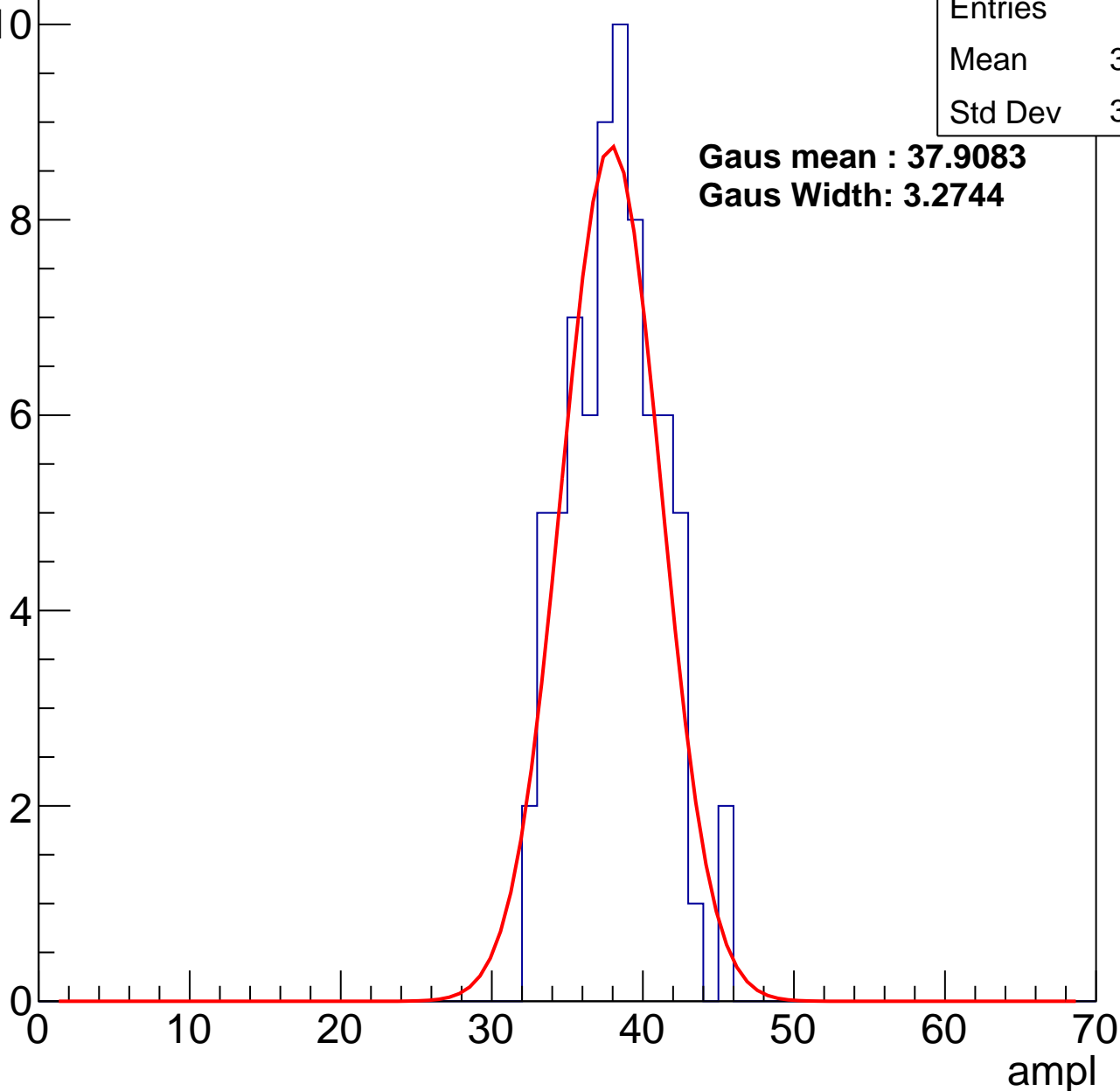
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	37.69
Std Dev	3.017

Gaus mean : 37.9083

Gaus Width: 3.2744



B1L102S, U22-ch47, adc2

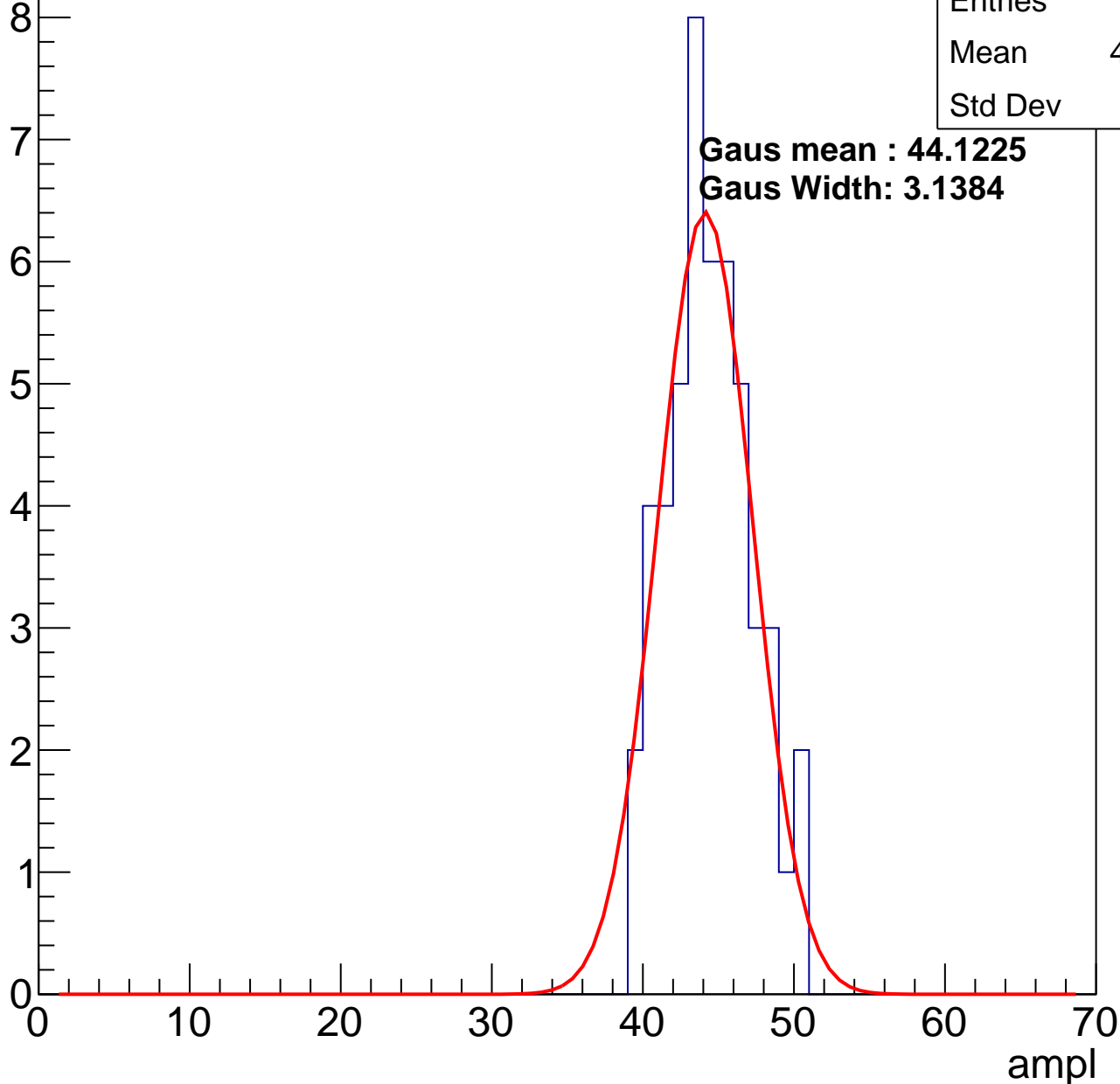
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	43.96
Std Dev	2.77

Gaus mean : 44.1225

Gaus Width: 3.1384

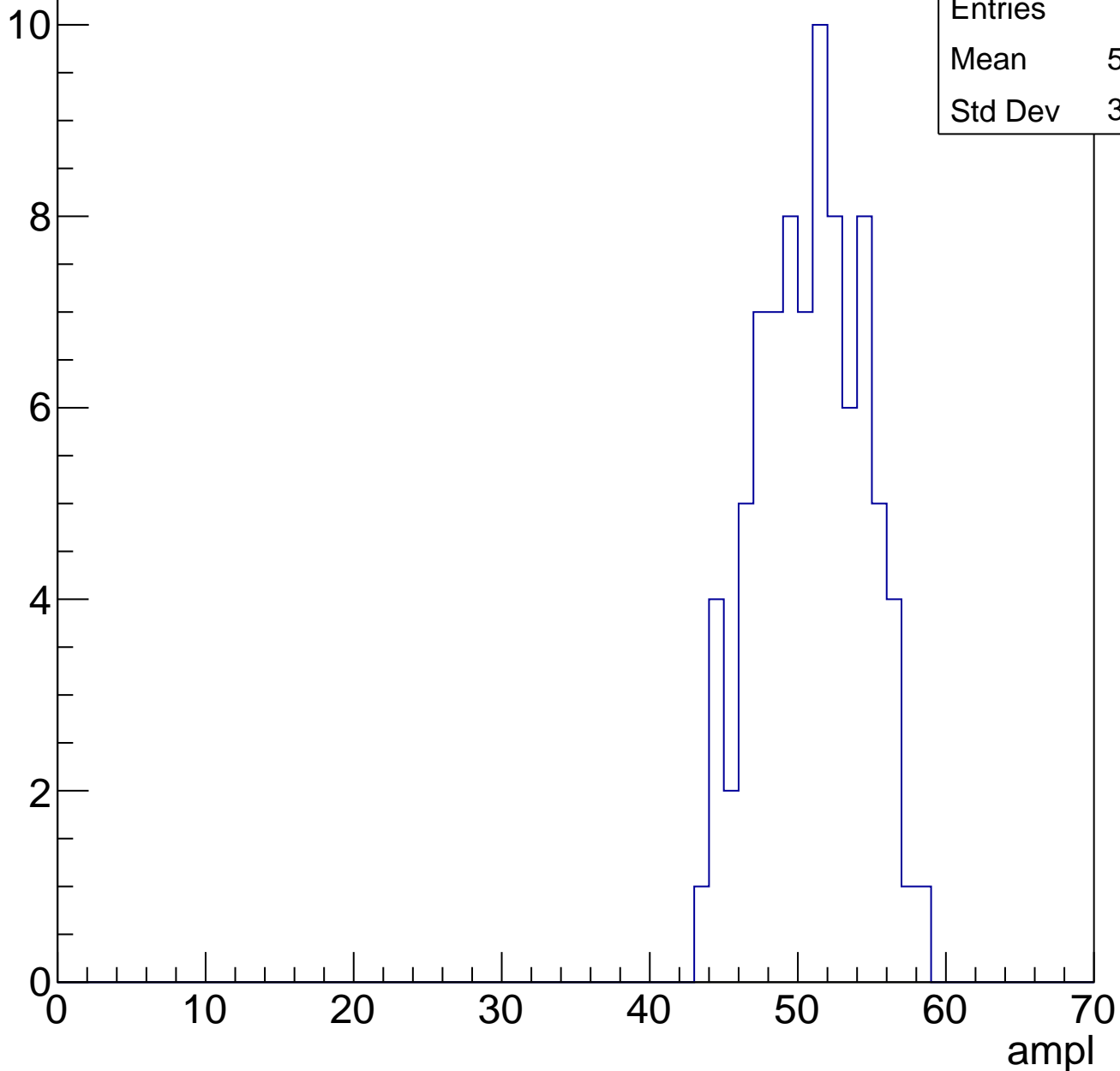


B1L102S, U22-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	50.43
Std Dev	3.472

Entry

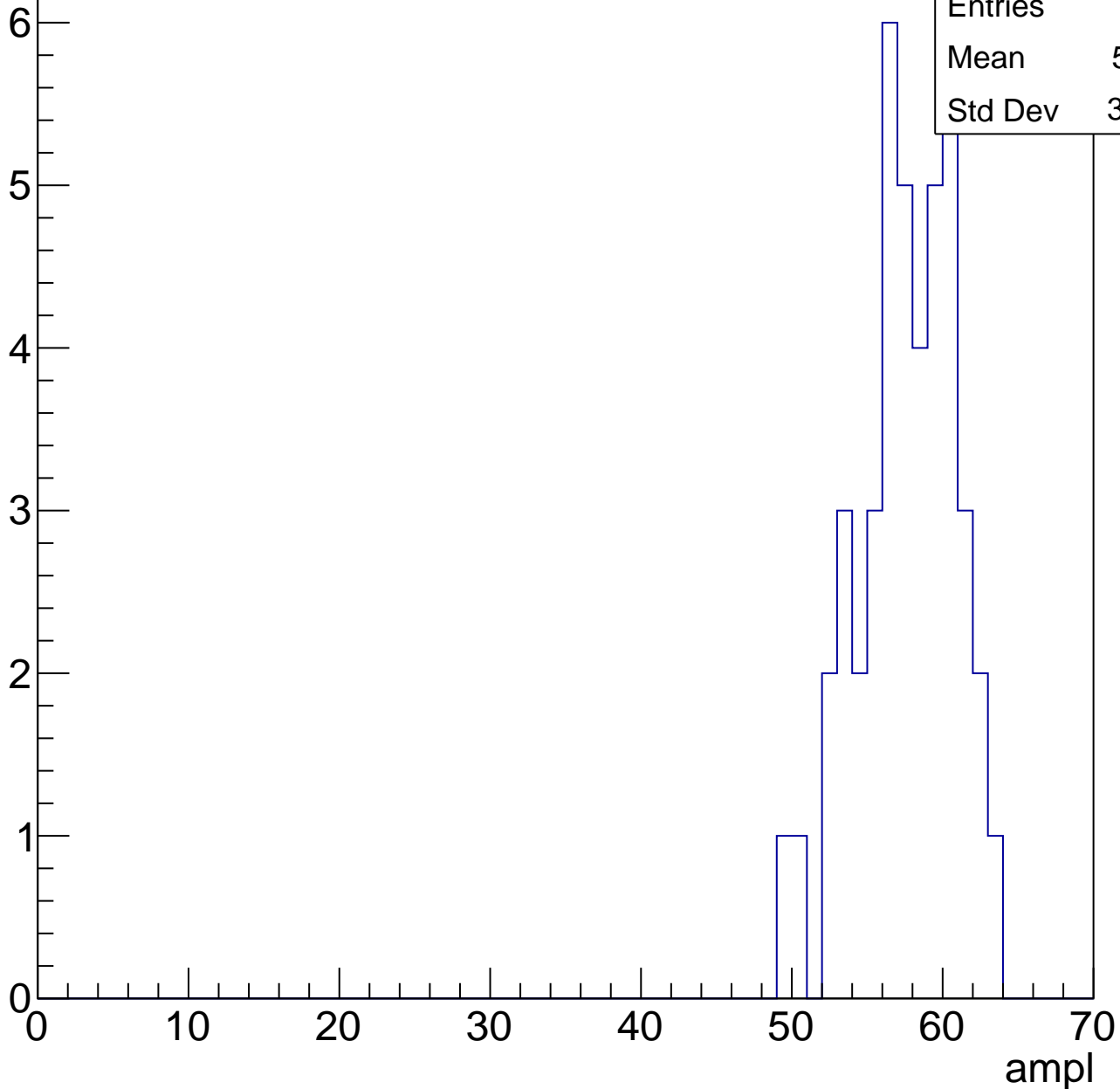


B1L102S, U22-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	57.11
Std Dev	3.228

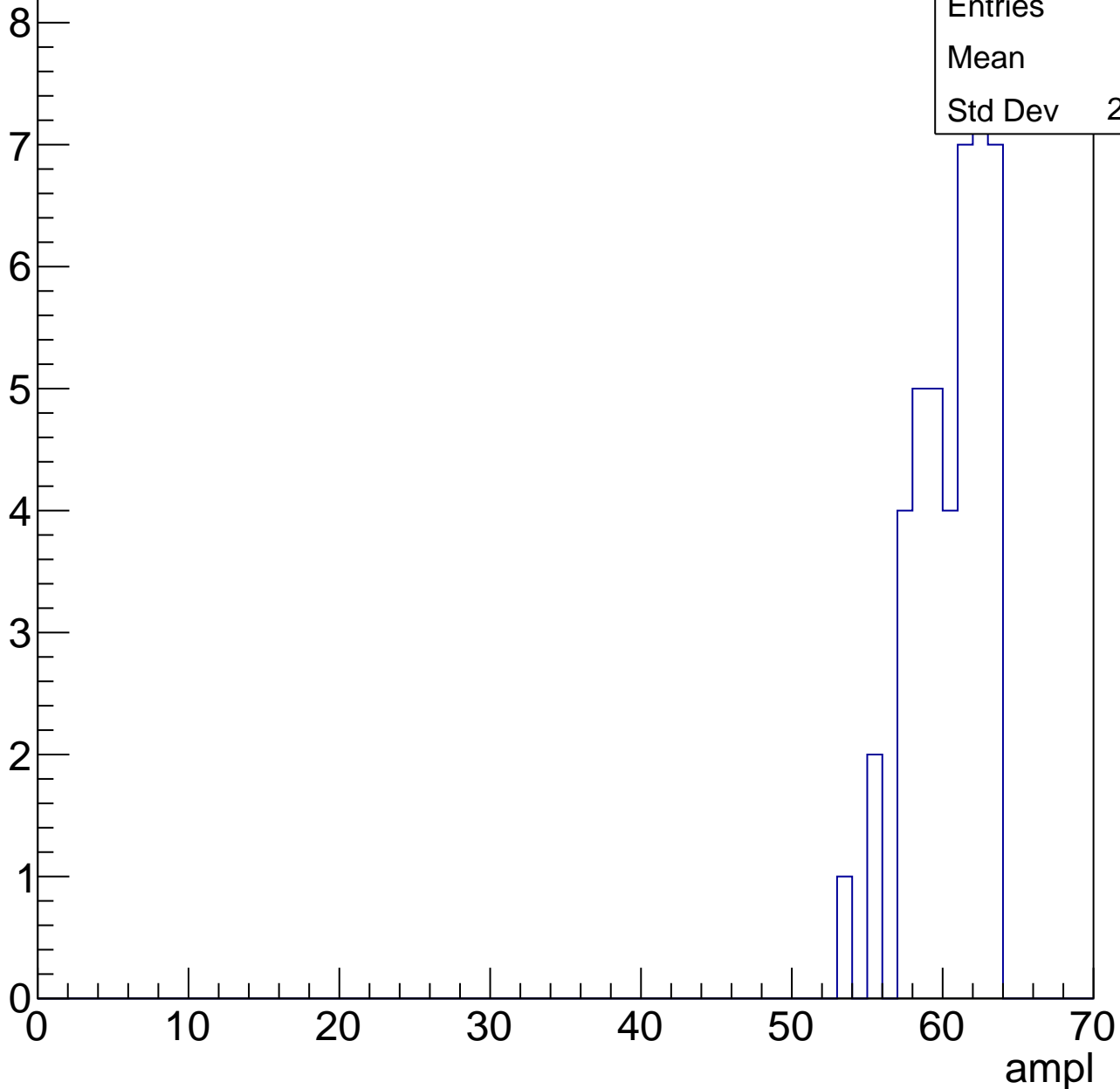


B1L102S, U22-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	60
Std Dev	2.468



B1L102S, U22-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L102S, U22-ch48, adc0

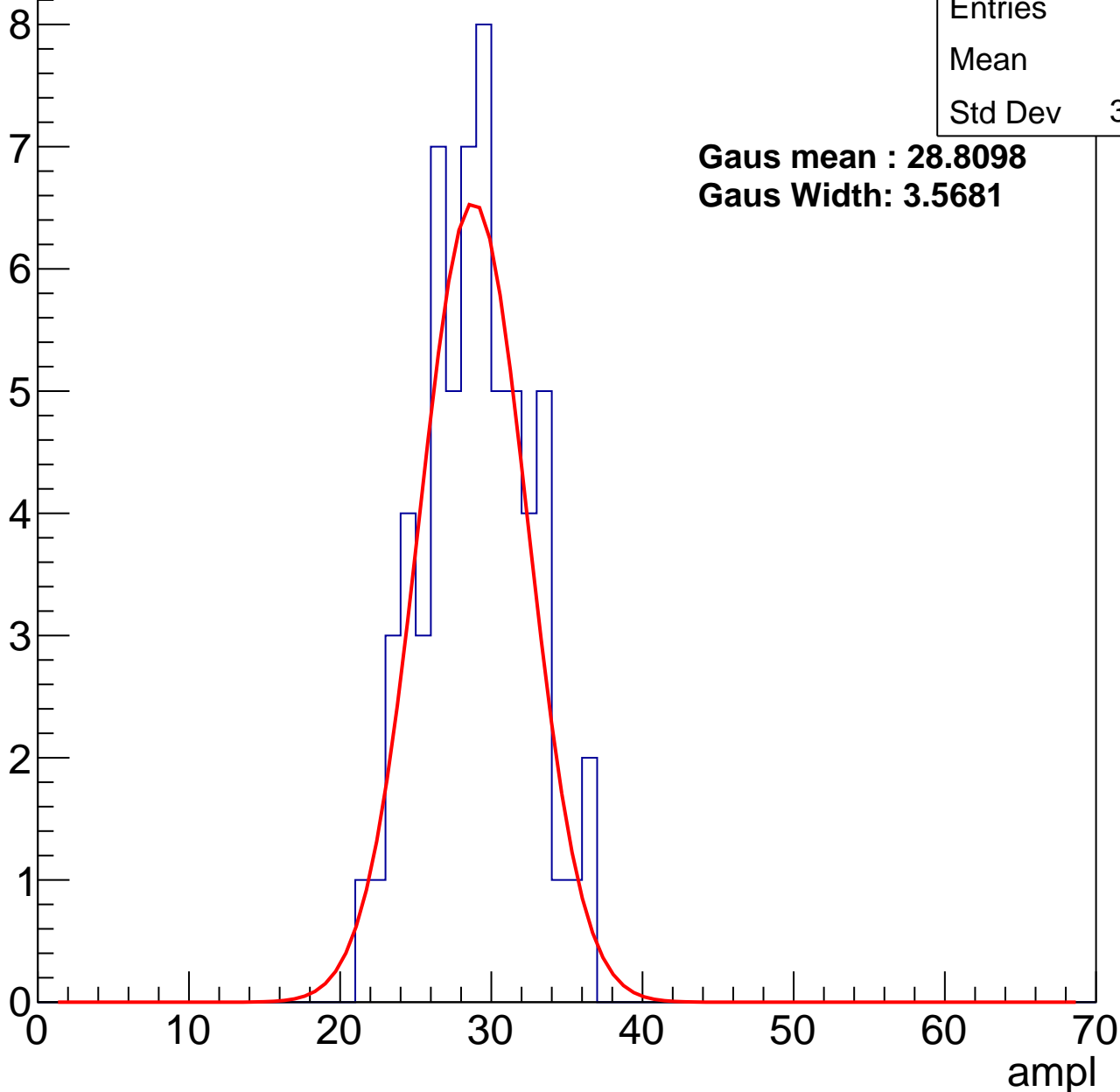
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.5
Std Dev	3.463

Gaus mean : 28.8098

Gaus Width: 3.5681



B1L102S, U22-ch48, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	34.93
Std Dev	3.241

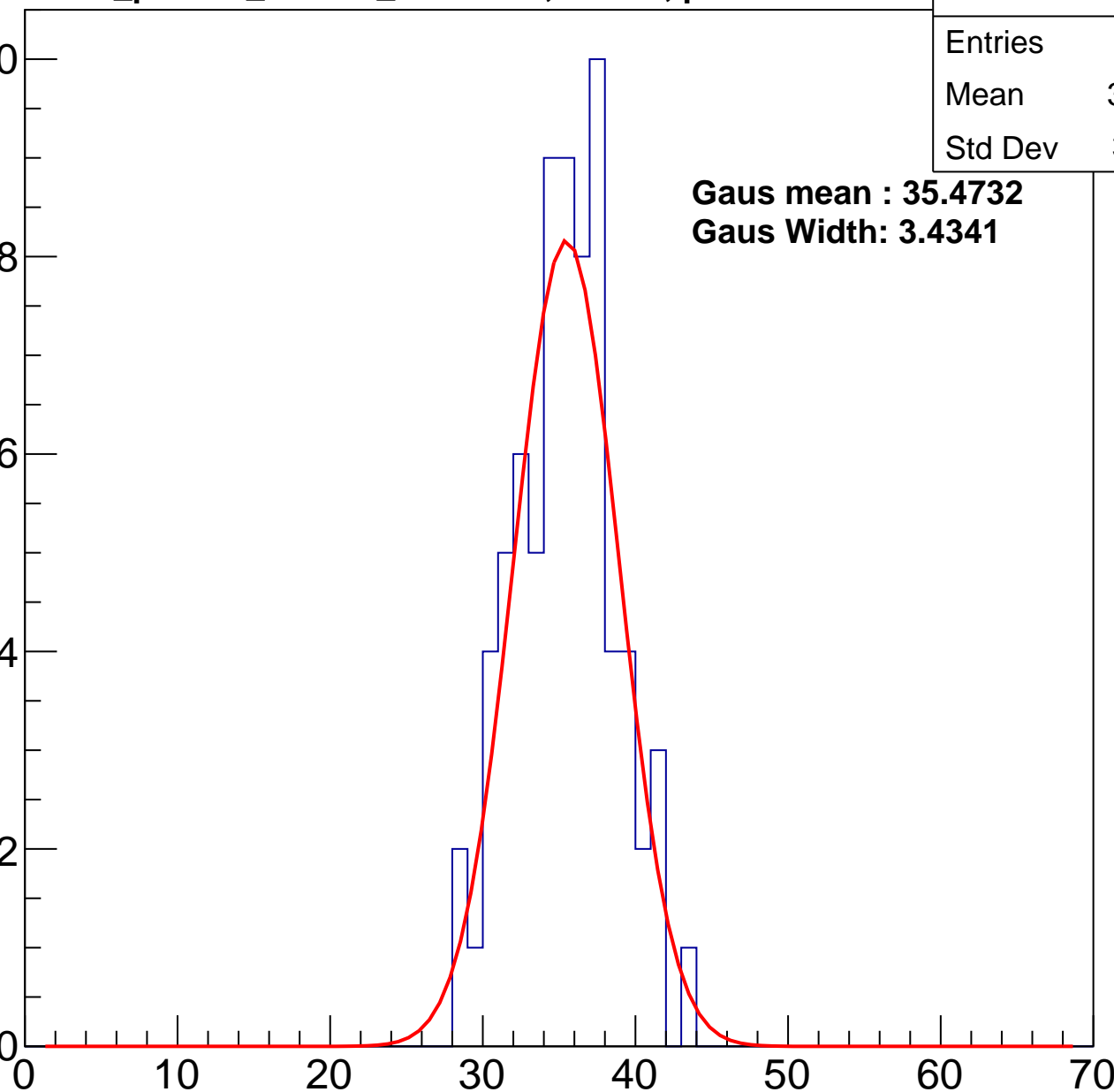
Gaus mean : 35.4732

Gaus Width: 3.4341

Entry

10
8
6
4
2
0

ampl



B1L102S, U22-ch48, adc2

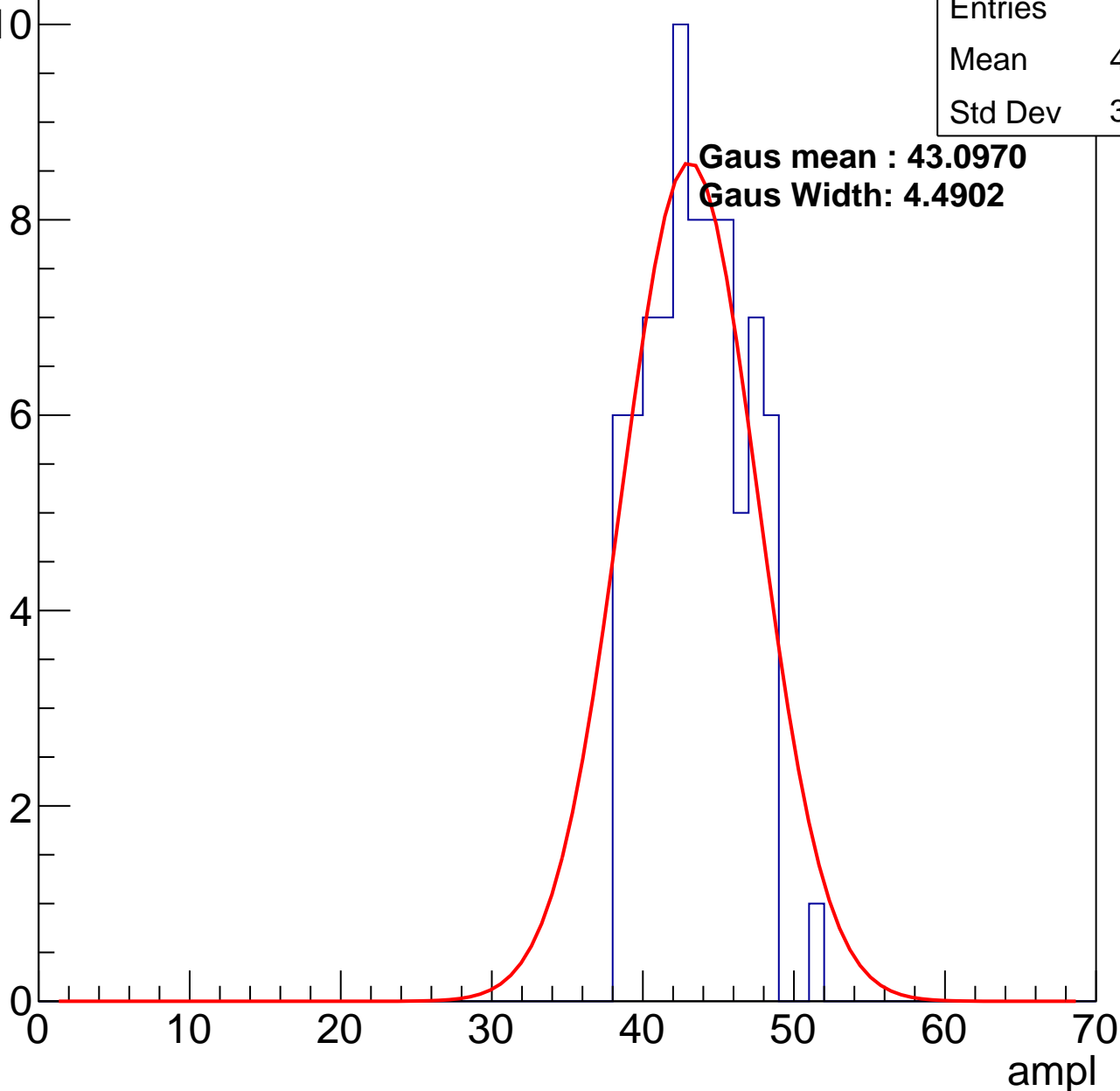
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	43.08
Std Dev	3.097

Gaus mean : 43.0970

Gaus Width: 4.4902

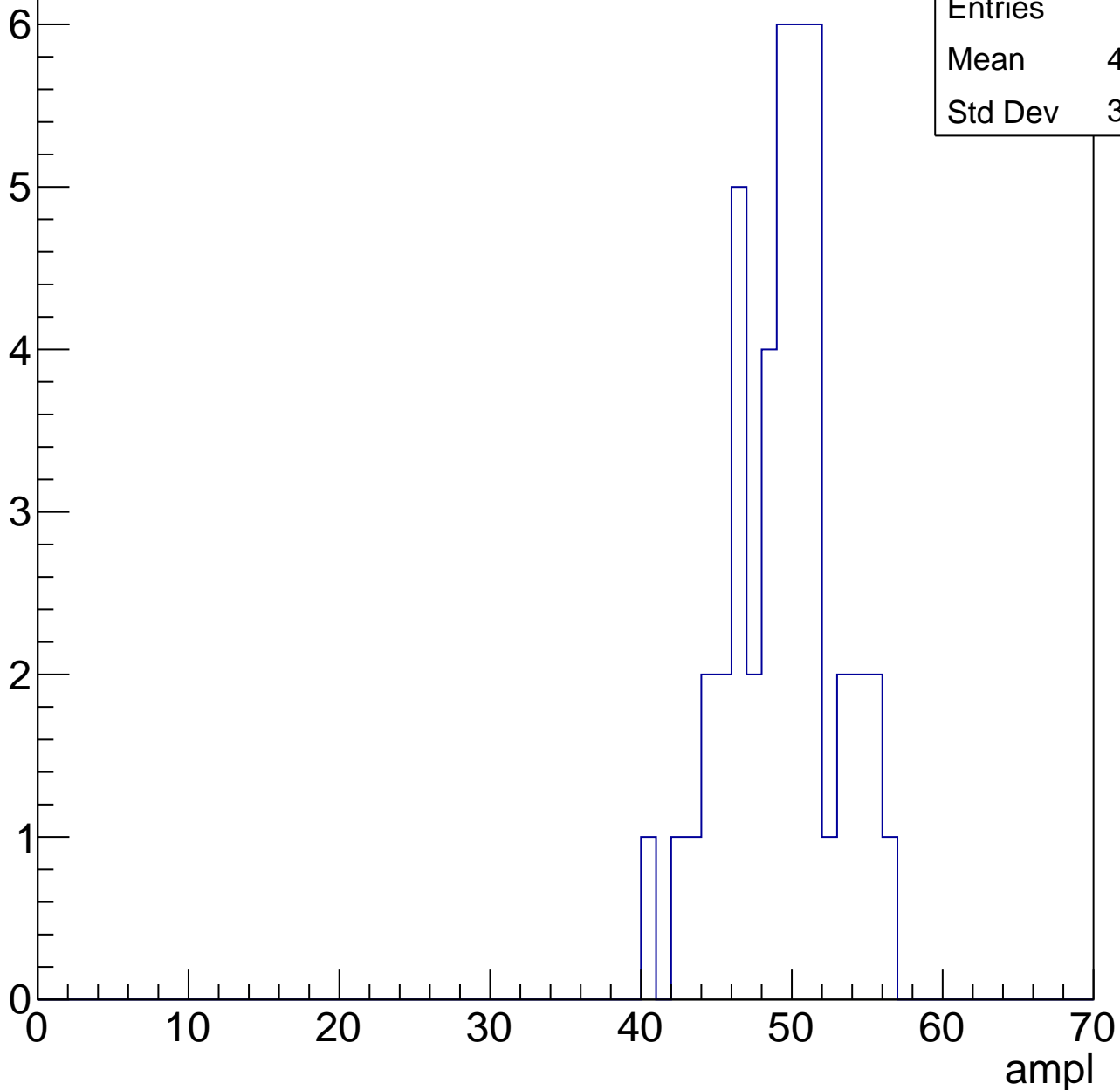


B1L102S, U22-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	48.89
Std Dev	3.524

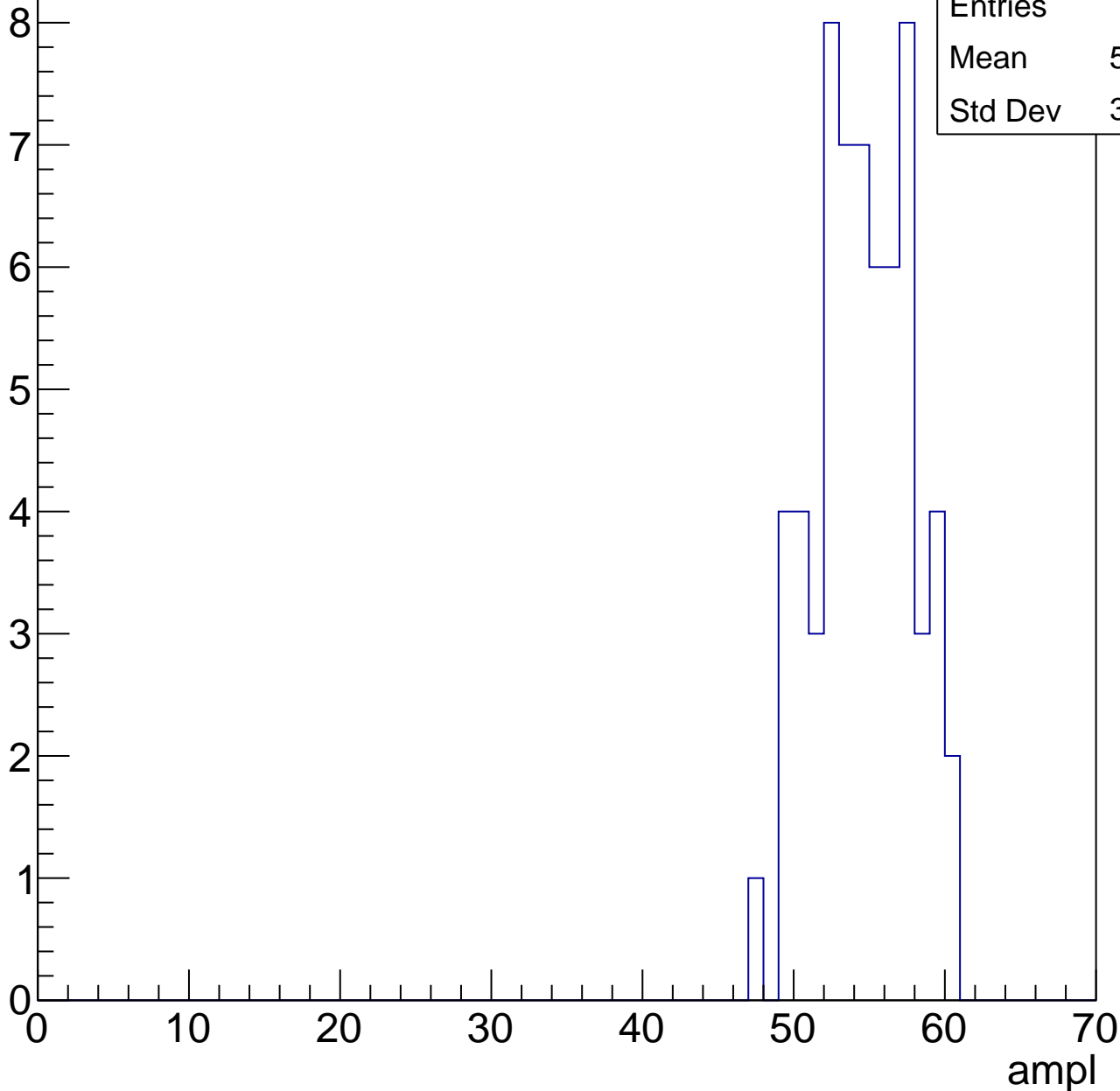


B1L102S, U22-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.17
Std Dev	3.084

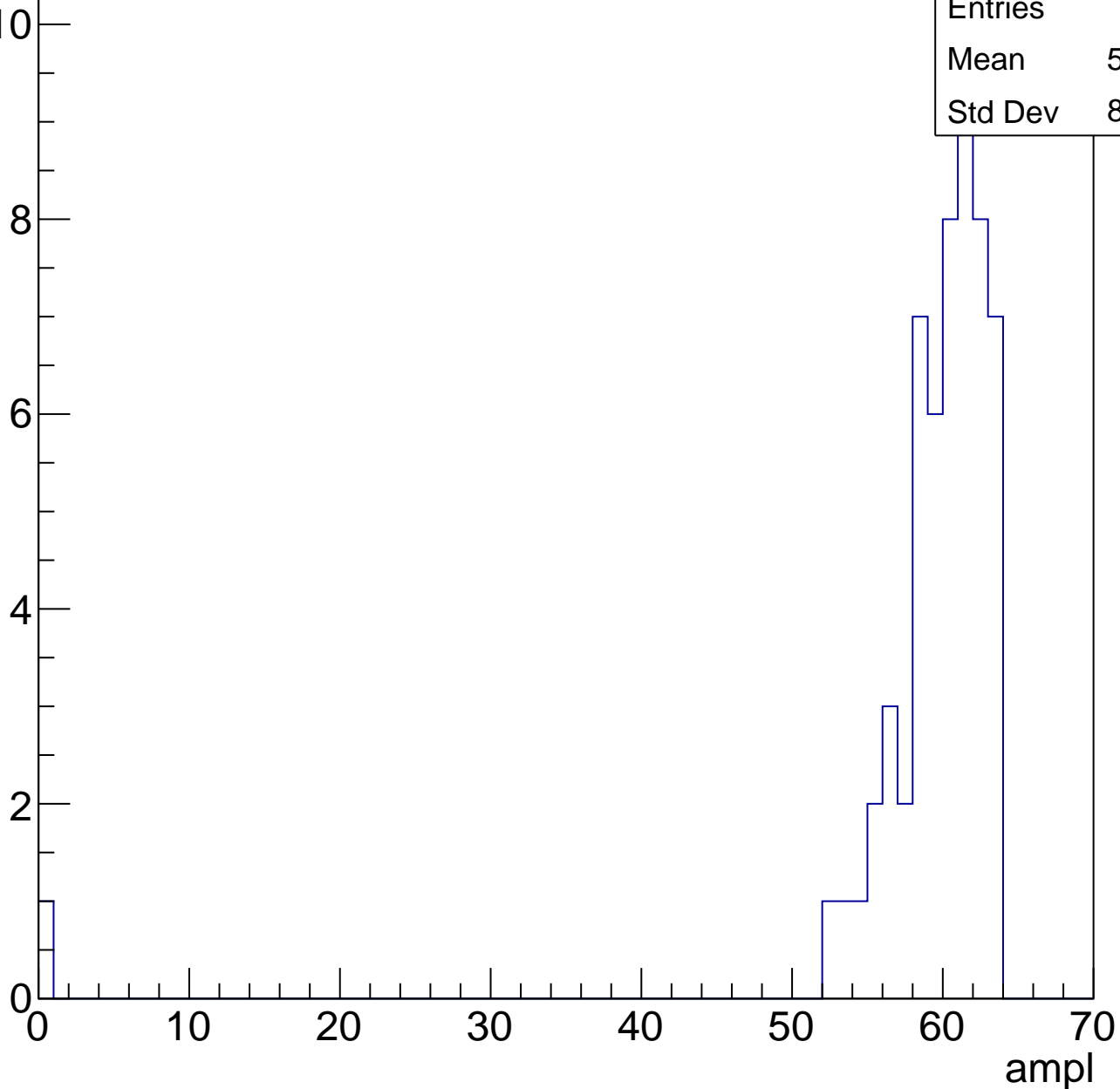


B1L102S, U22-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	58.56
Std Dev	8.257



B1L102S, U22-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch49, adc0

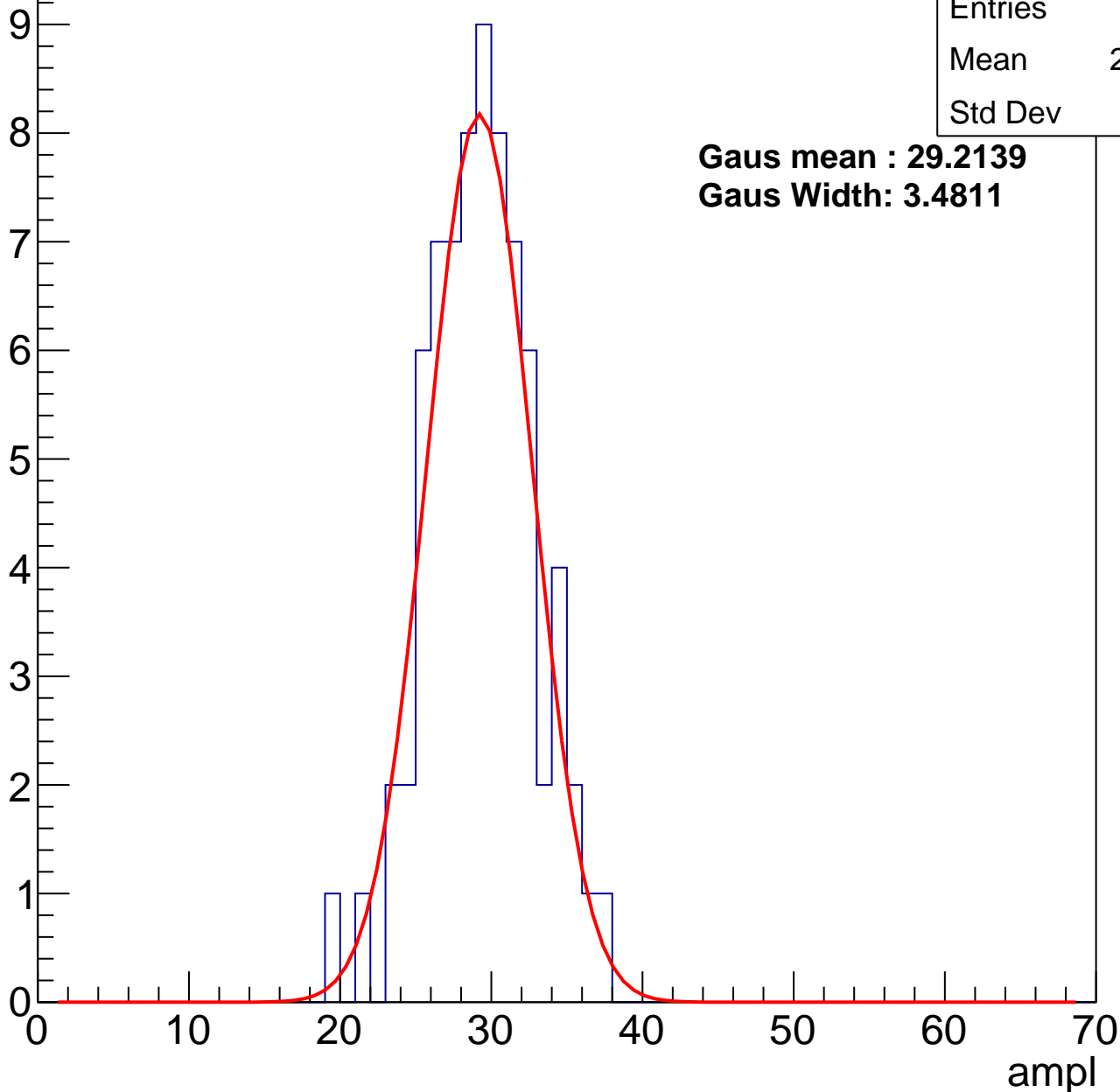
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.84
Std Dev	3.46

Gaus mean : 29.2139

Gaus Width: 3.4811



B1L102S, U22-ch49, adc1

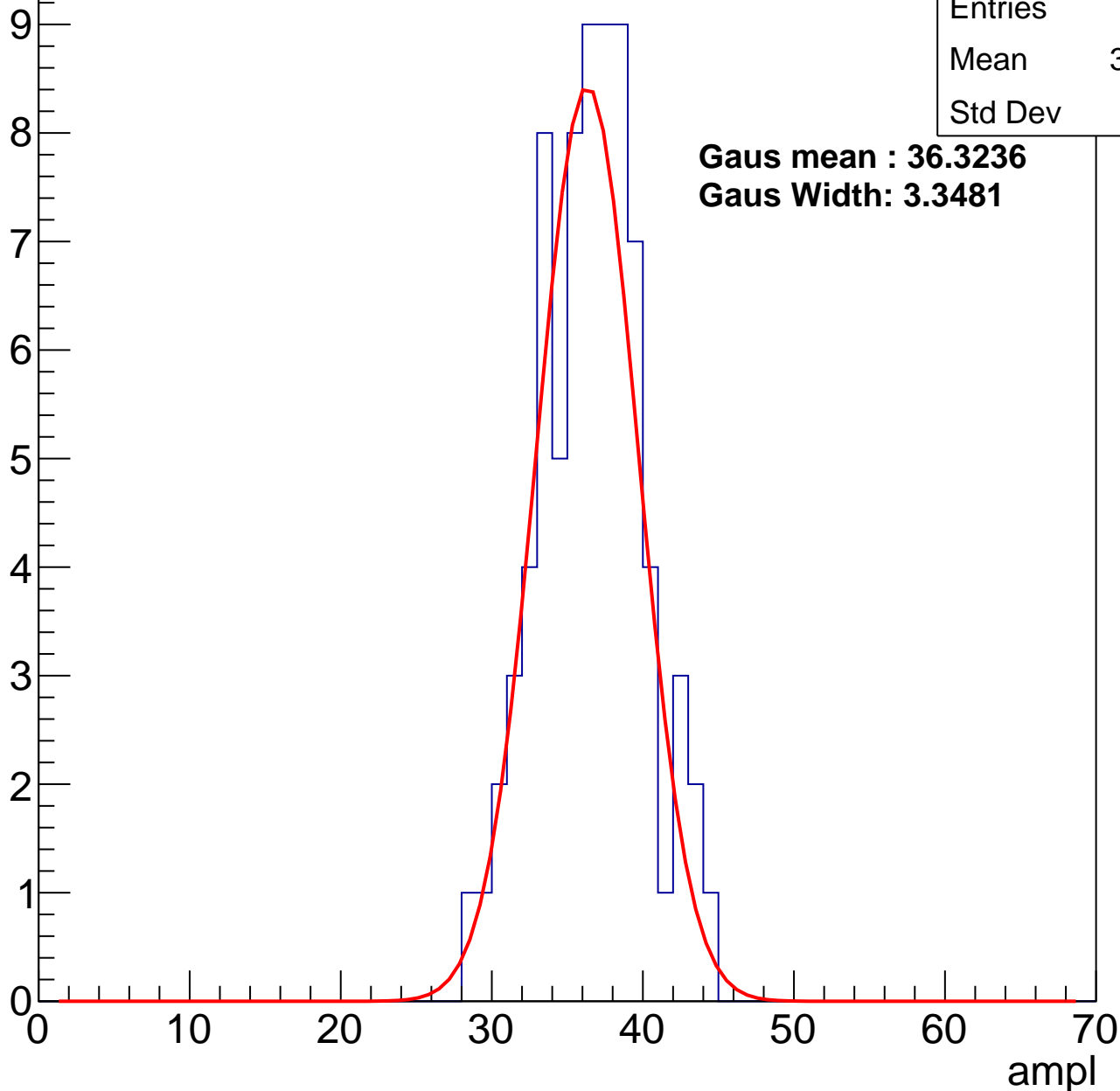
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.12
Std Dev	3.4

Gaus mean : 36.3236

Gaus Width: 3.3481



B1L102S, U22-ch49, adc2

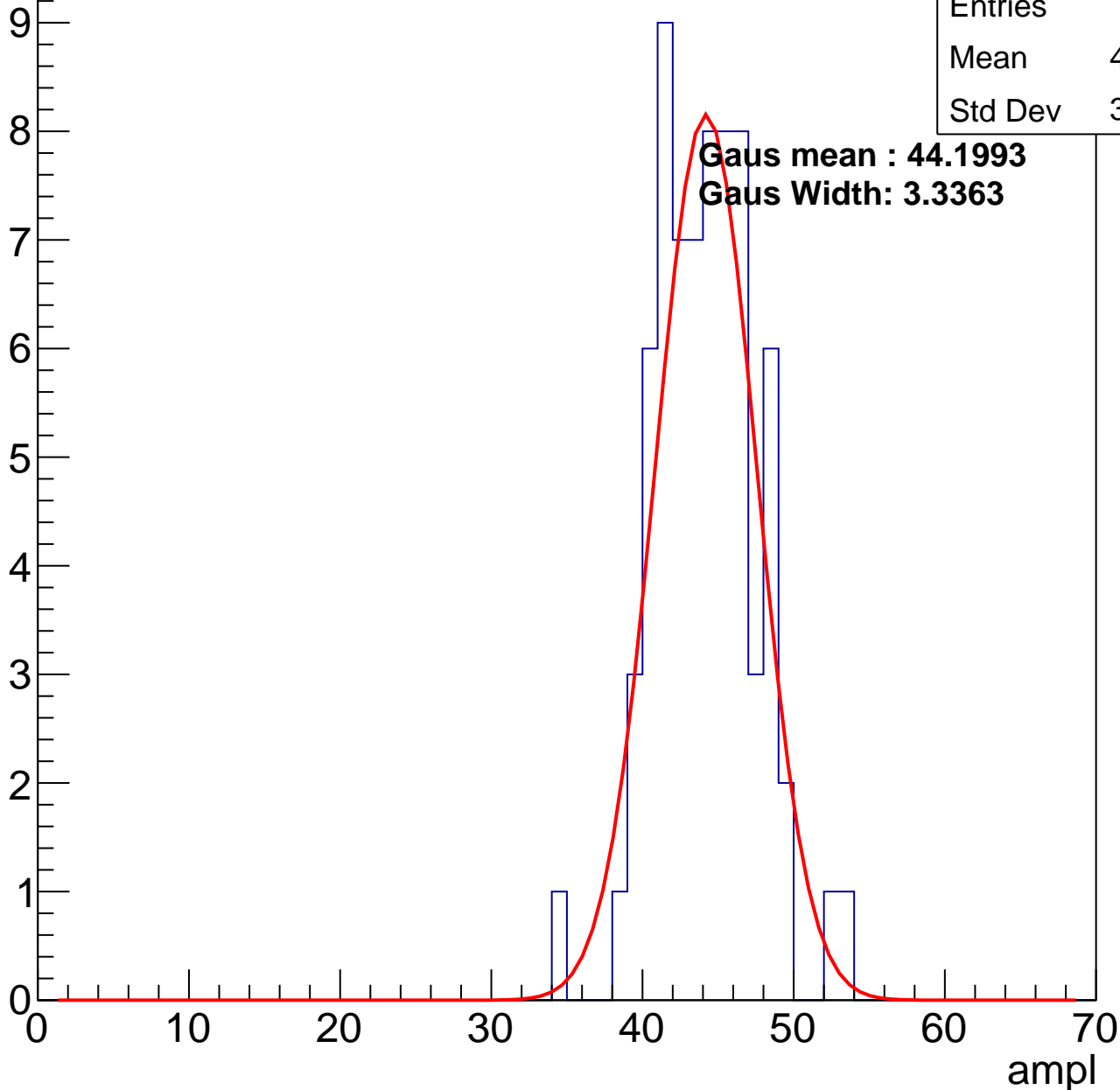
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.73
Std Dev	3.306

Gaus mean : 44.1993

Gaus Width: 3.3363

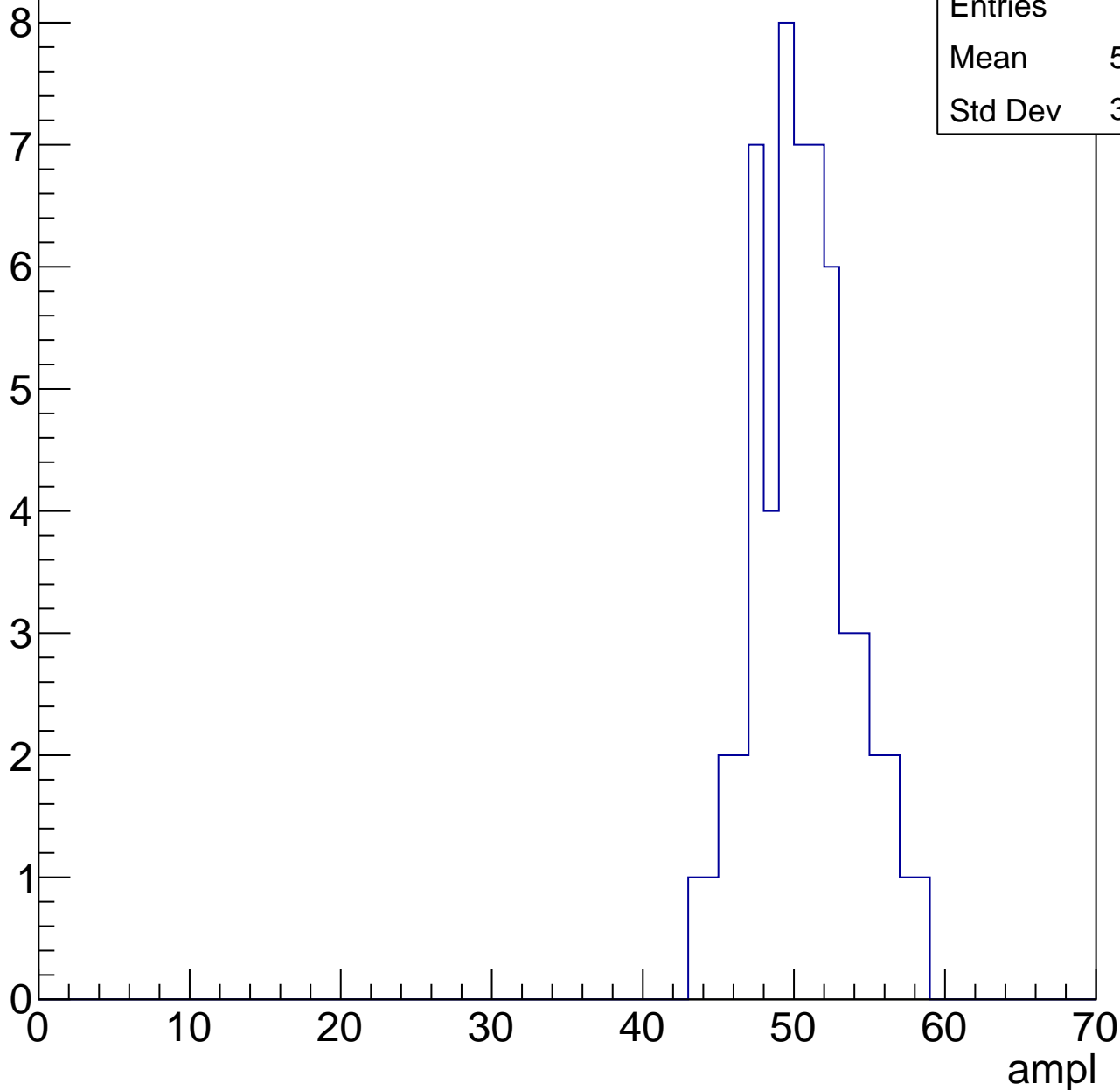


B1L102S, U22-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	50.16
Std Dev	3.227



B1L102S, U22-ch49, adc4

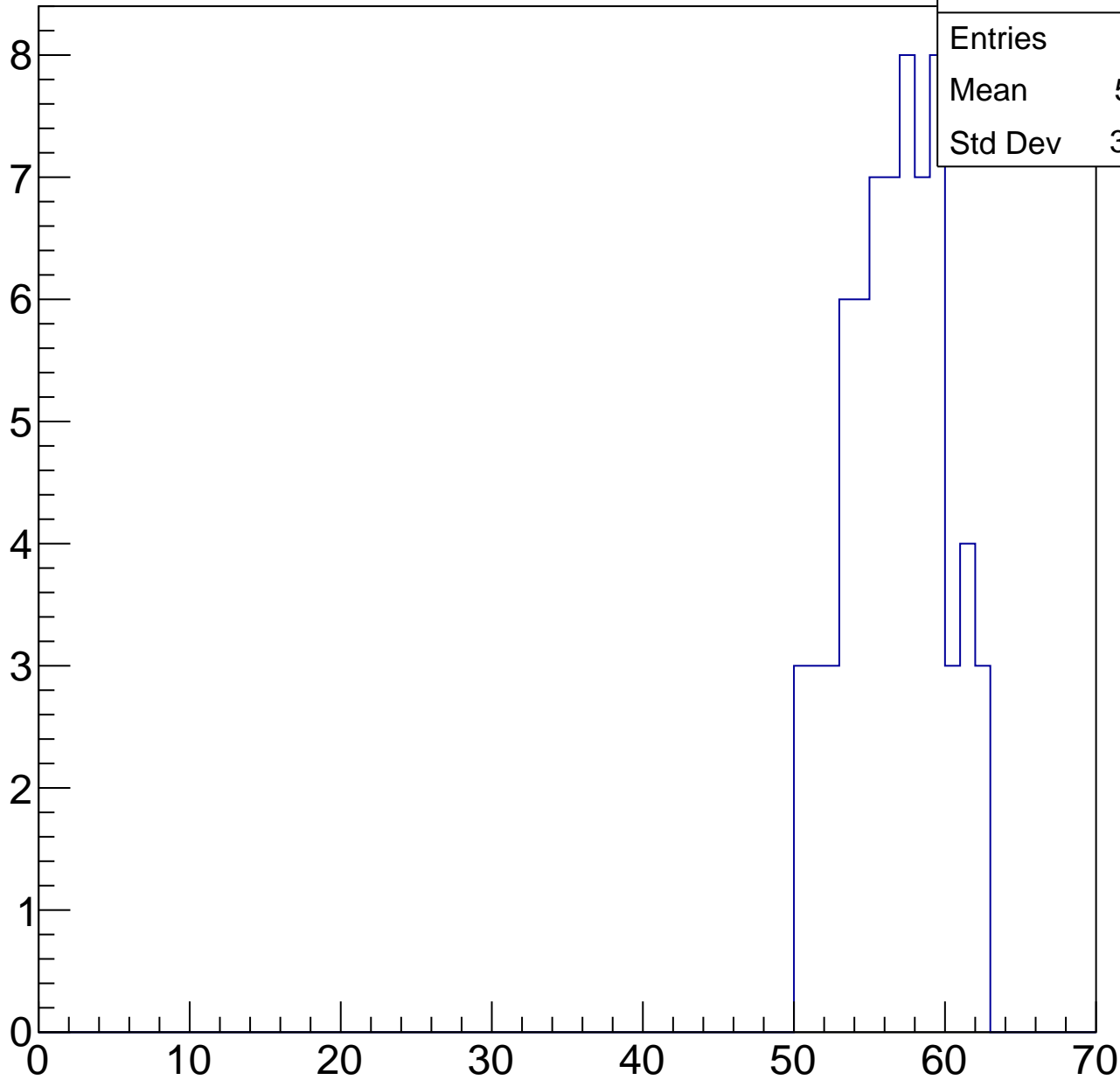
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	68
Mean	56.21
Std Dev	3.156

ampl

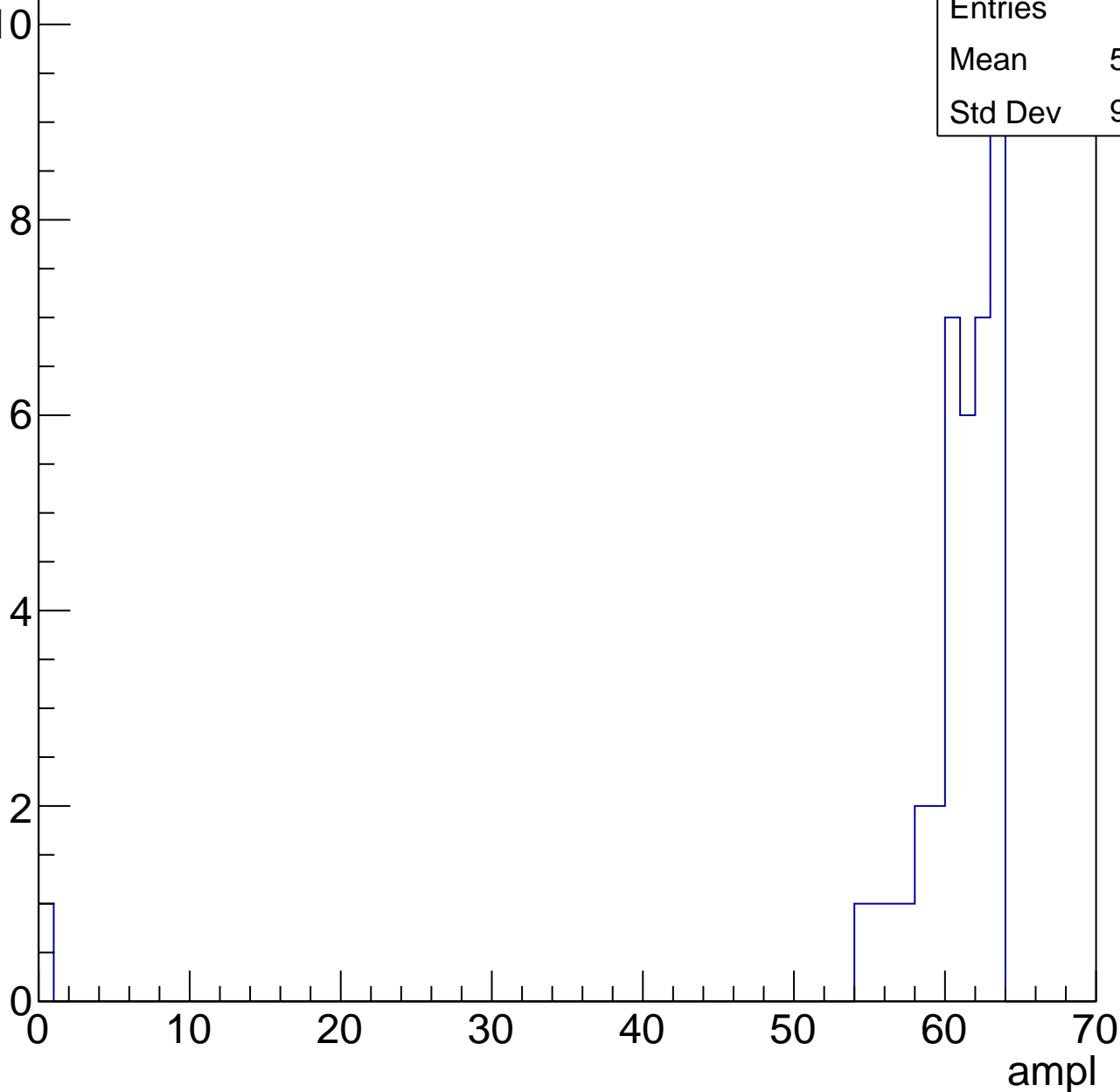


B1L102S, U22-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	59.13
Std Dev	9.858



B1L102S, U22-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch50, adc0

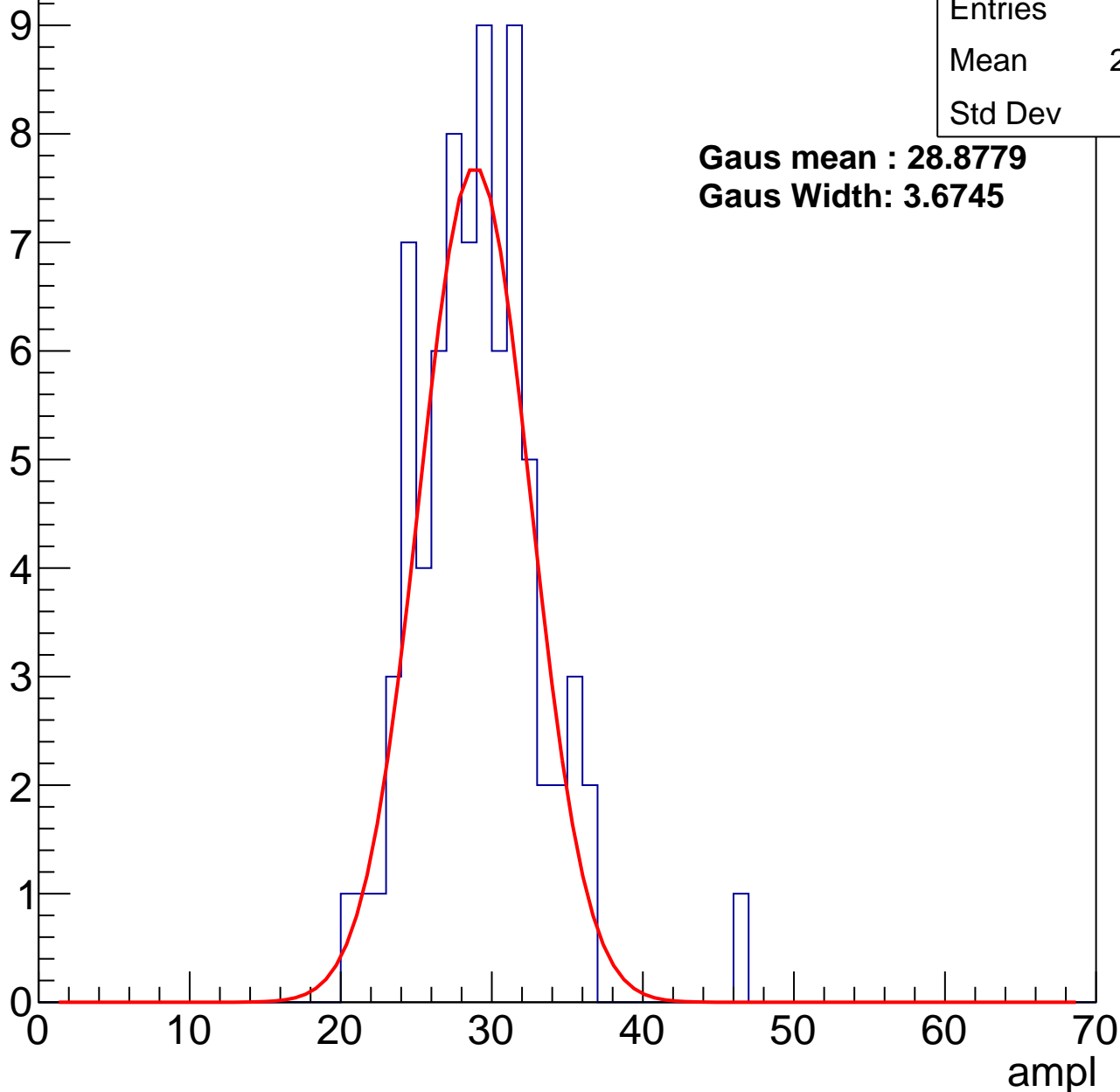
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.64
Std Dev	4.09

Gaus mean : 28.8779

Gaus Width: 3.6745



B1L102S, U22-ch50, adc1

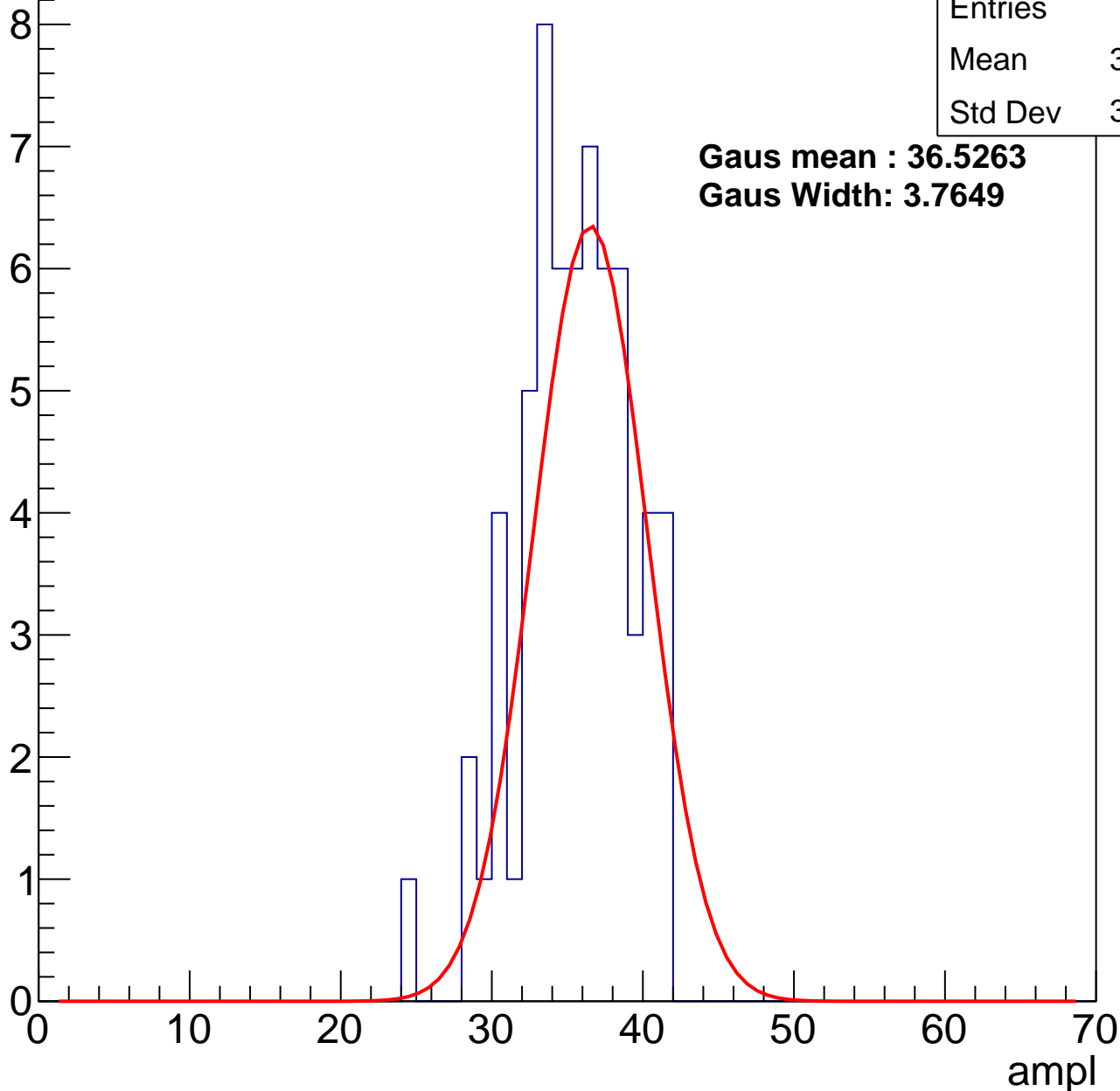
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.02
Std Dev	3.603

Gaus mean : 36.5263

Gaus Width: 3.7649



B1L102S, U22-ch50, adc2

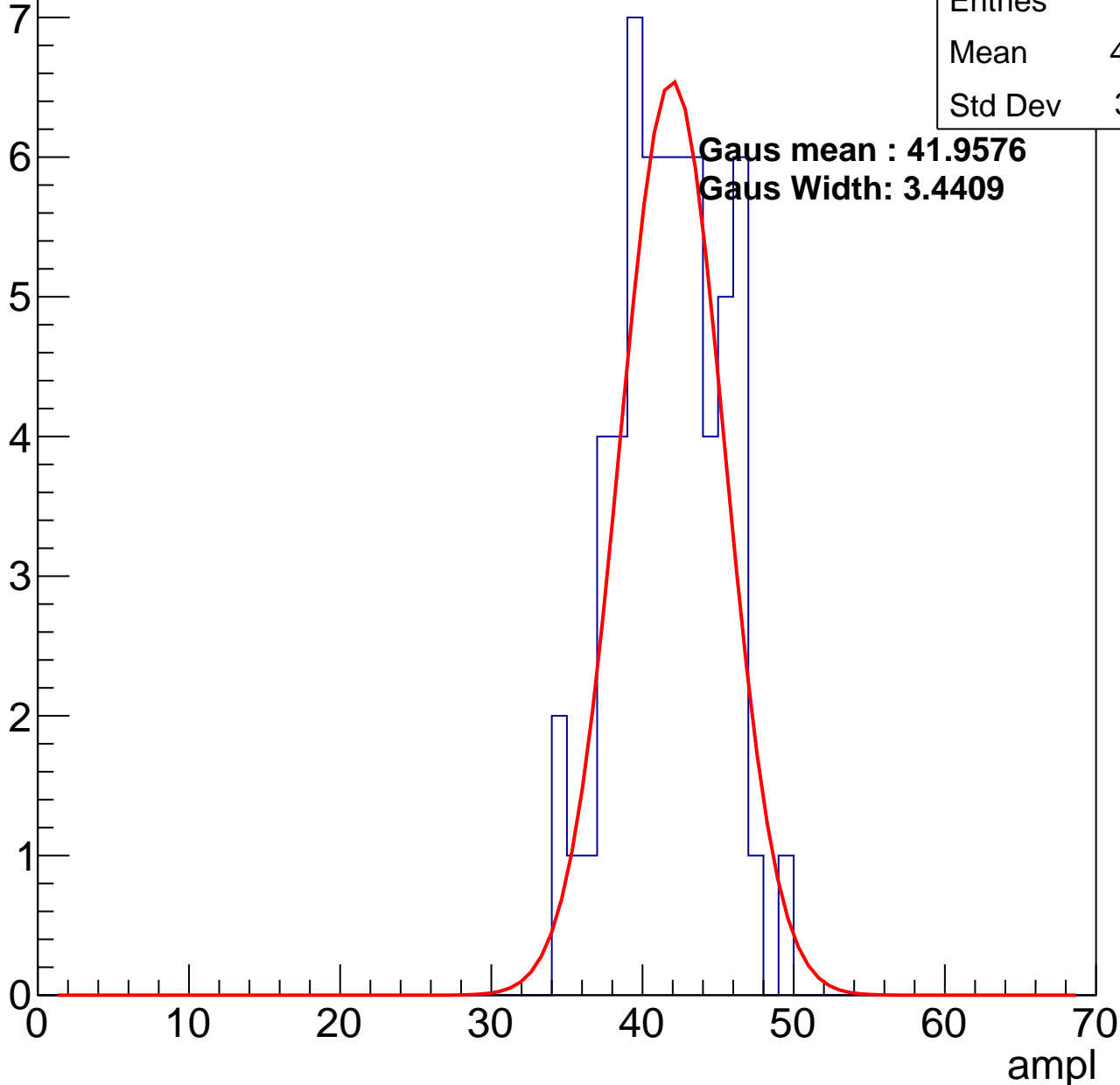
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.35
Std Dev	3.371

Gaus mean : 41.9576

Gaus Width: 3.4409

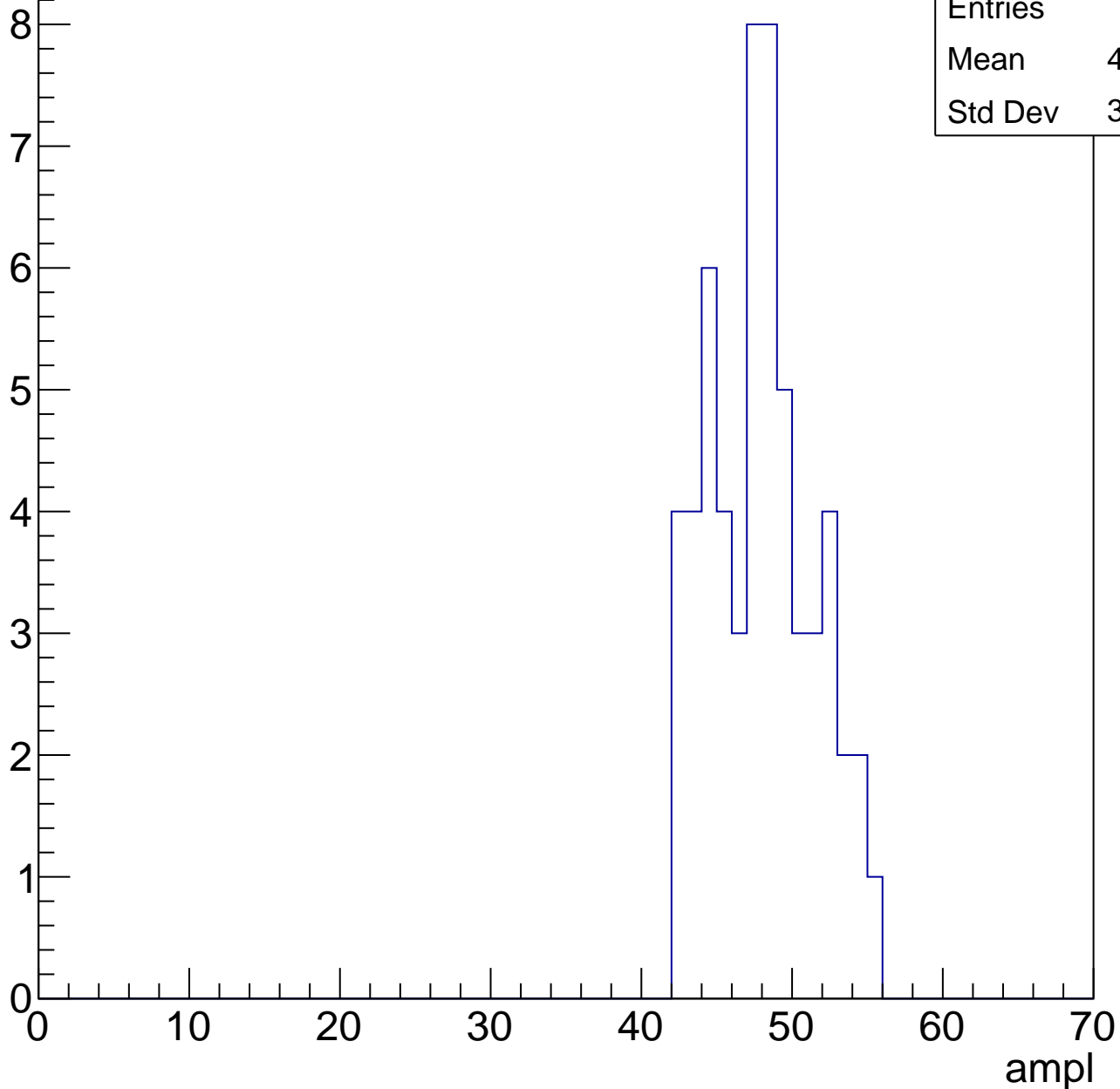


B1L102S, U22-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	47.49
Std Dev	3.403

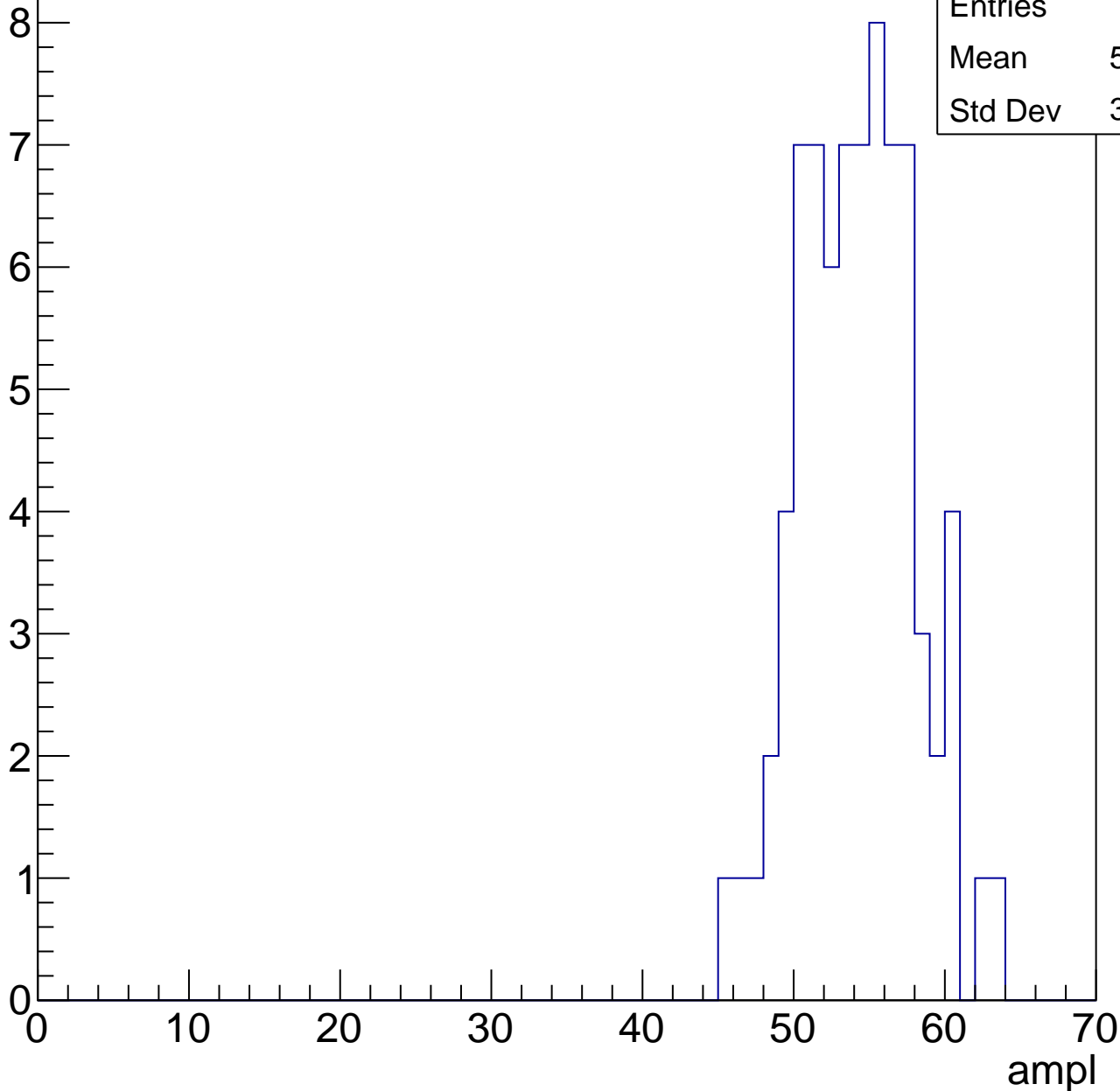


B1L102S, U22-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	53.76
Std Dev	3.724



B1L102S, U22-ch50, adc5

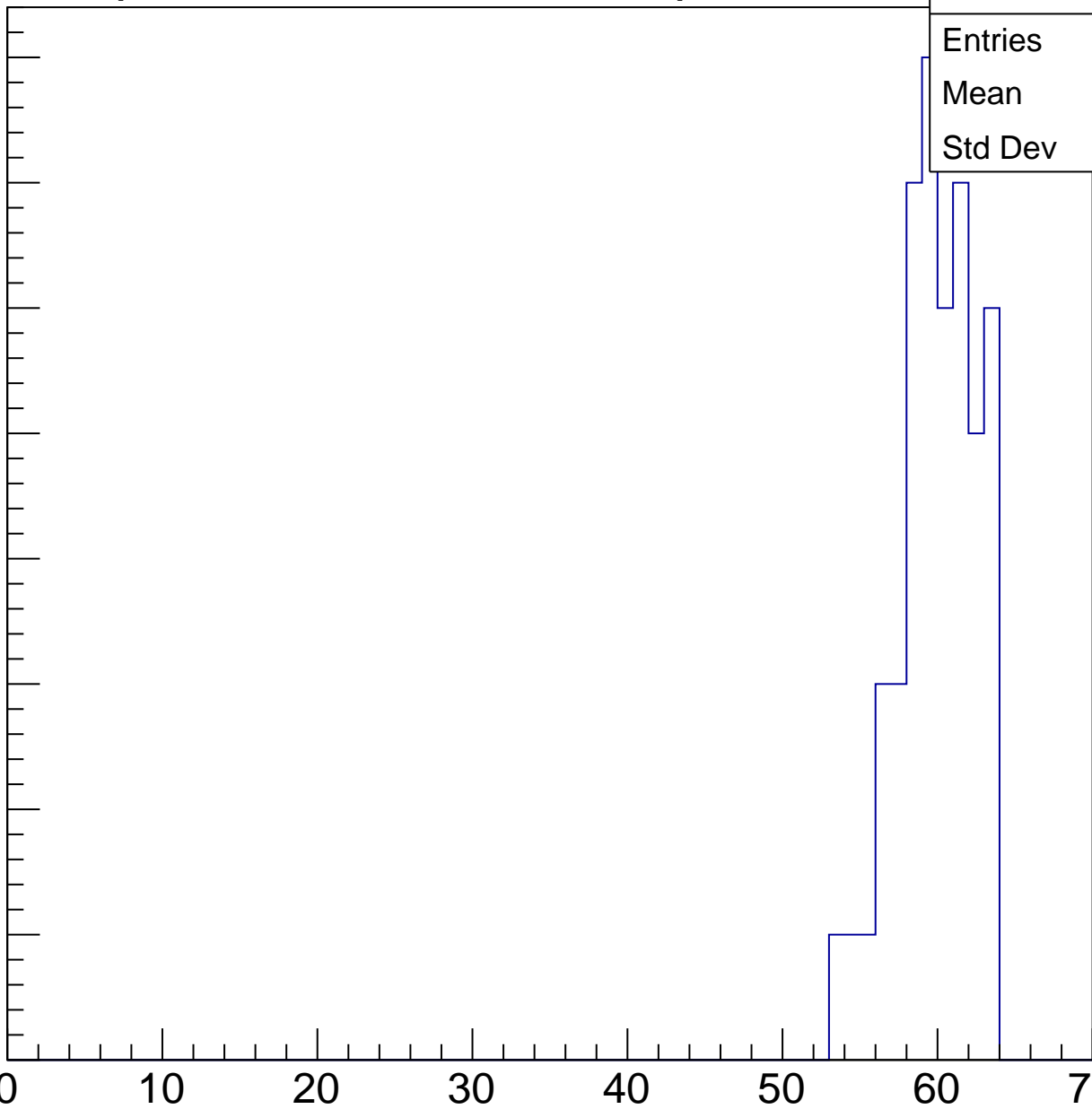
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.46
Std Dev	2.449

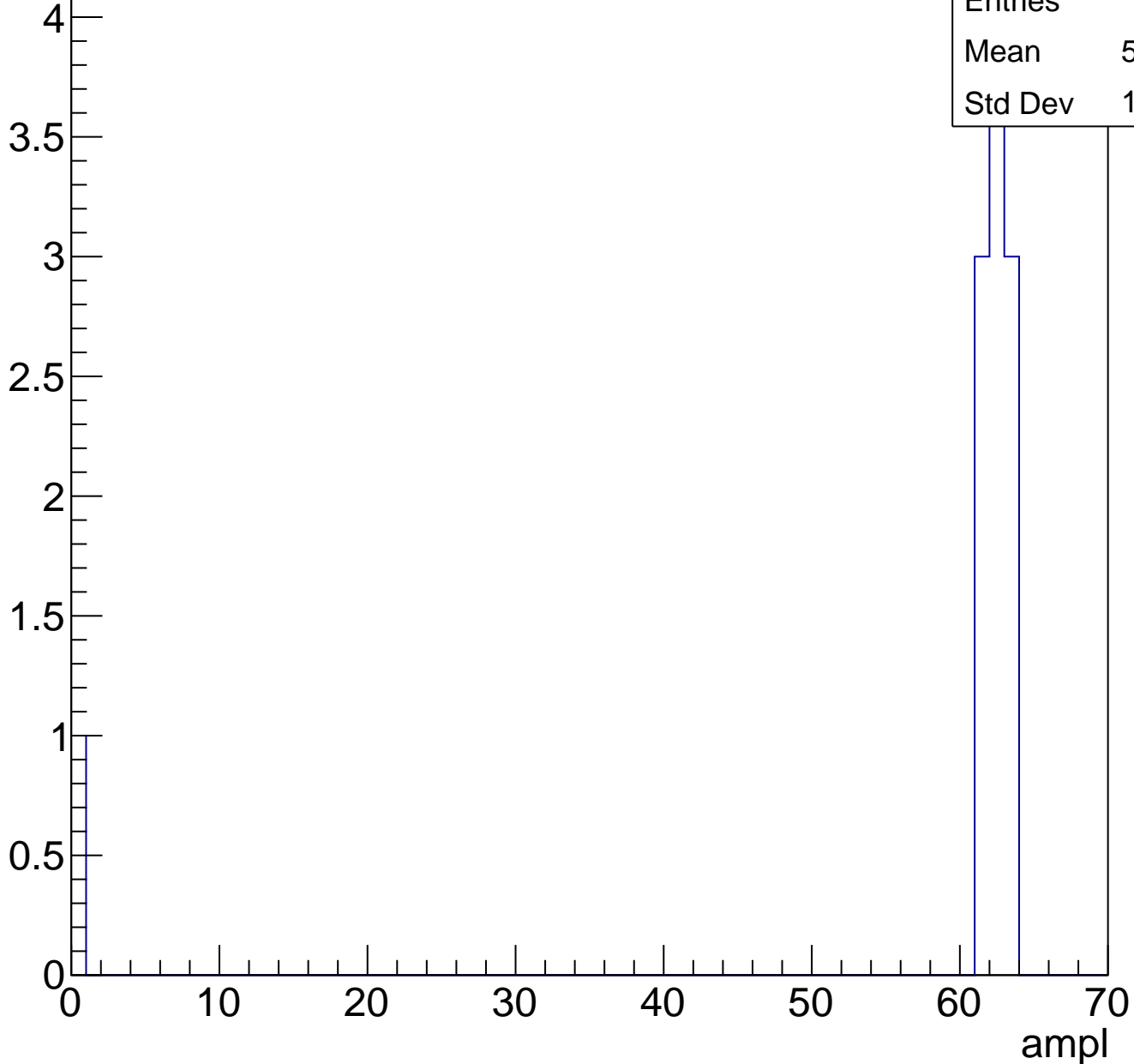
ampl



B1L102S, U22-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch51, adc0

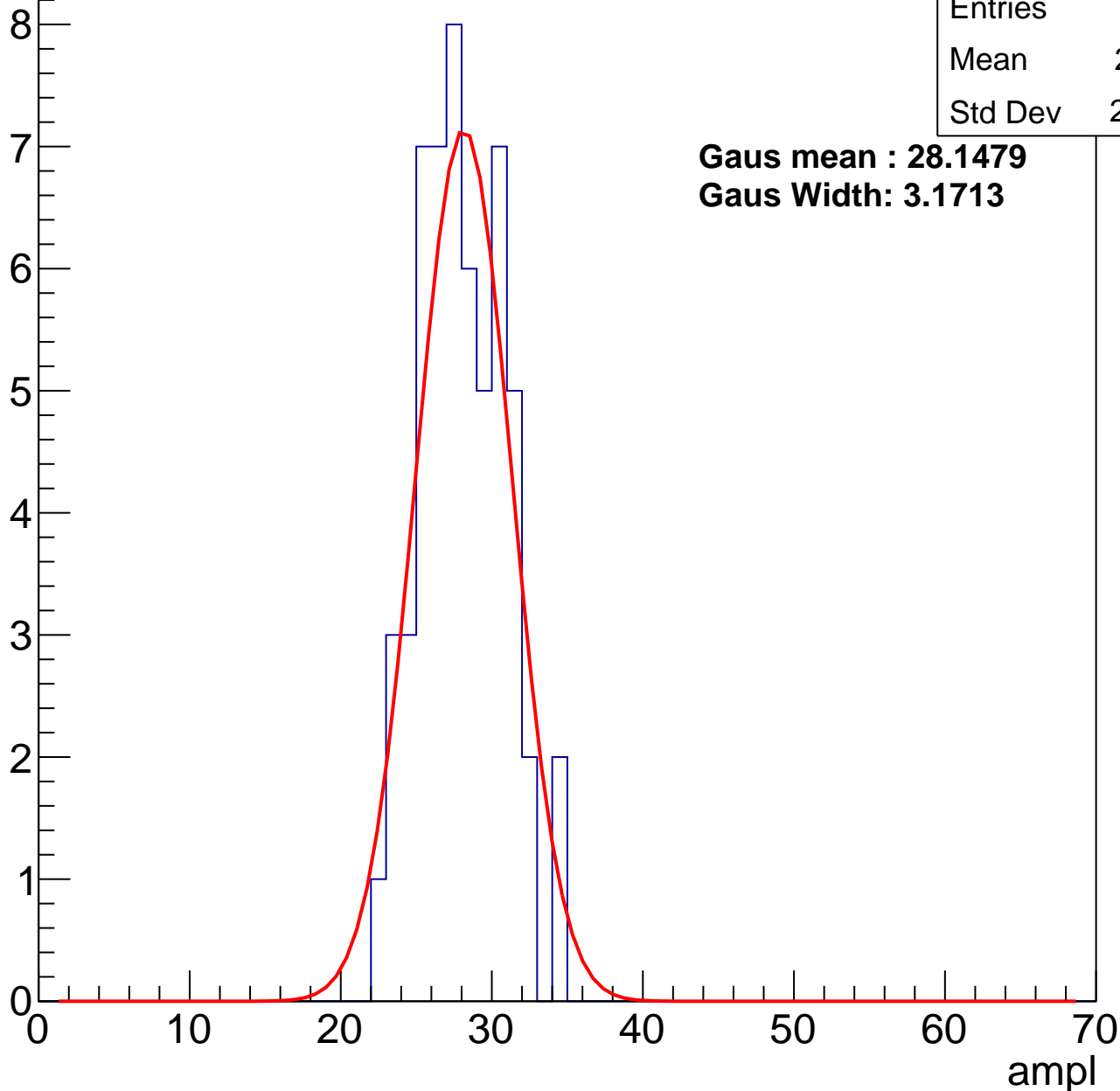
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	27.61
Std Dev	2.775

Gaus mean : 28.1479

Gaus Width: 3.1713



B1L102S, U22-ch51, adc1

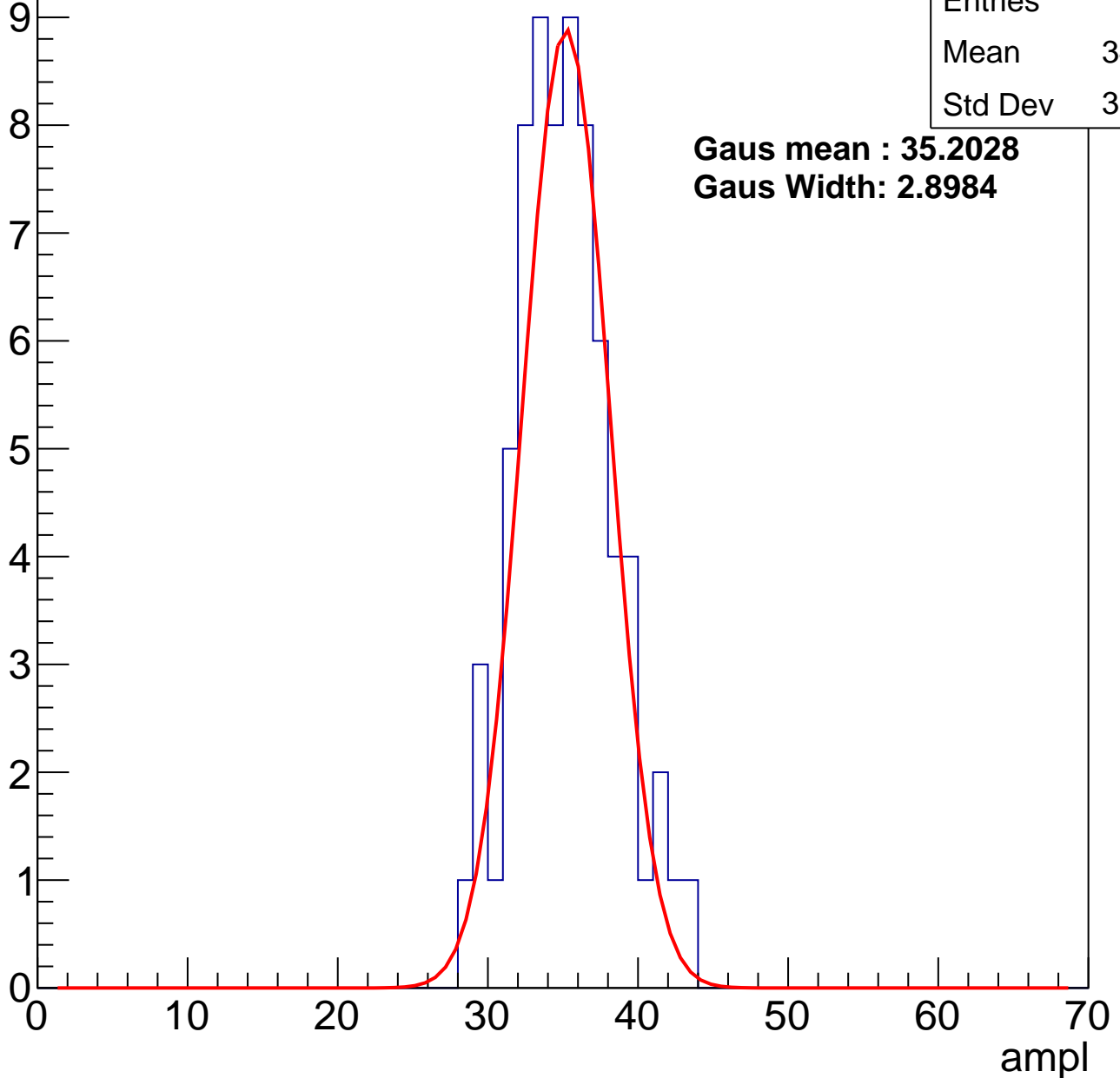
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.72
Std Dev	3.172

Gaus mean : 35.2028

Gaus Width: 2.8984



B1L102S, U22-ch51, adc2

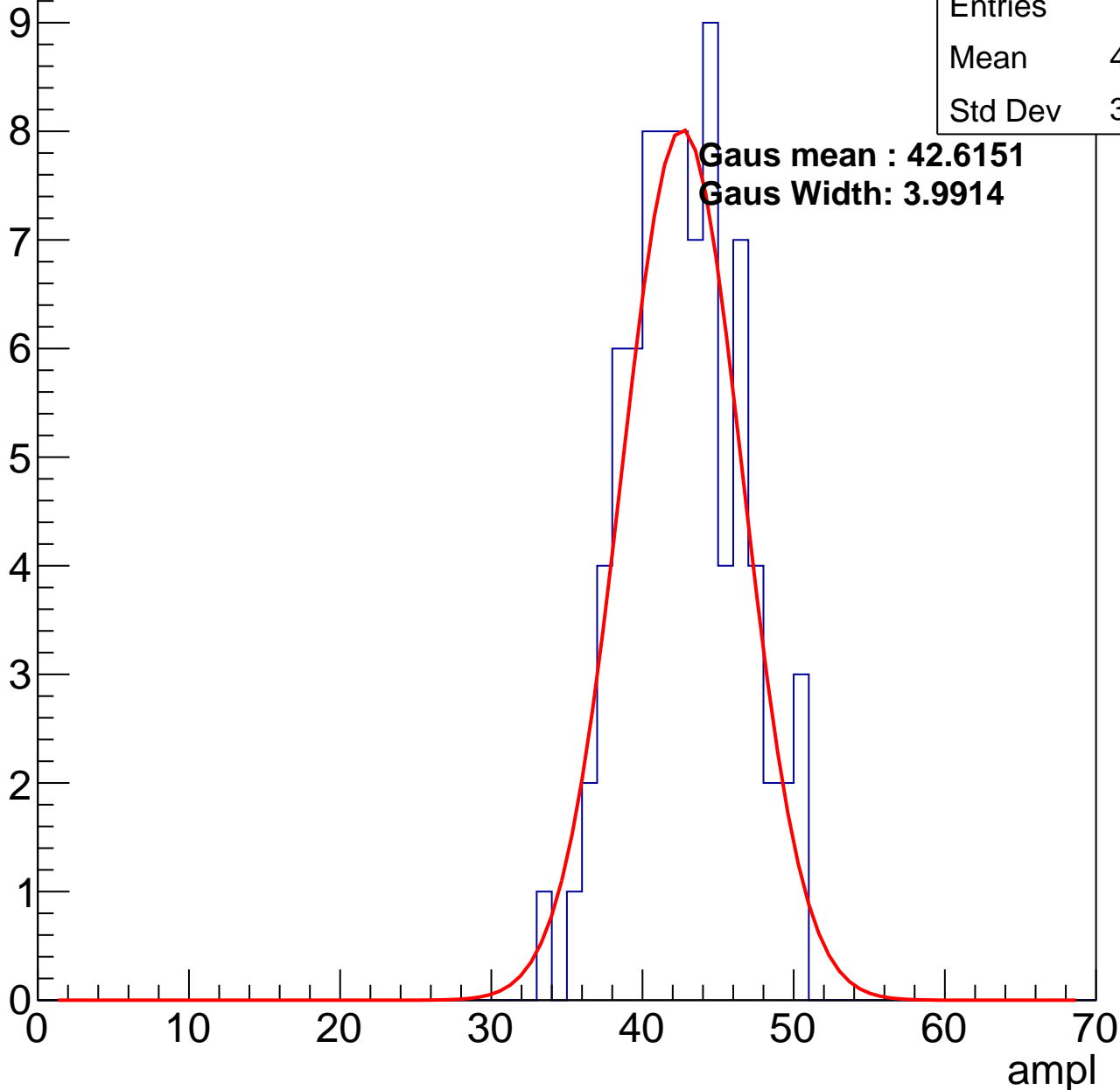
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	42.26
Std Dev	3.728

Gaus mean : 42.6151

Gaus Width: 3.9914

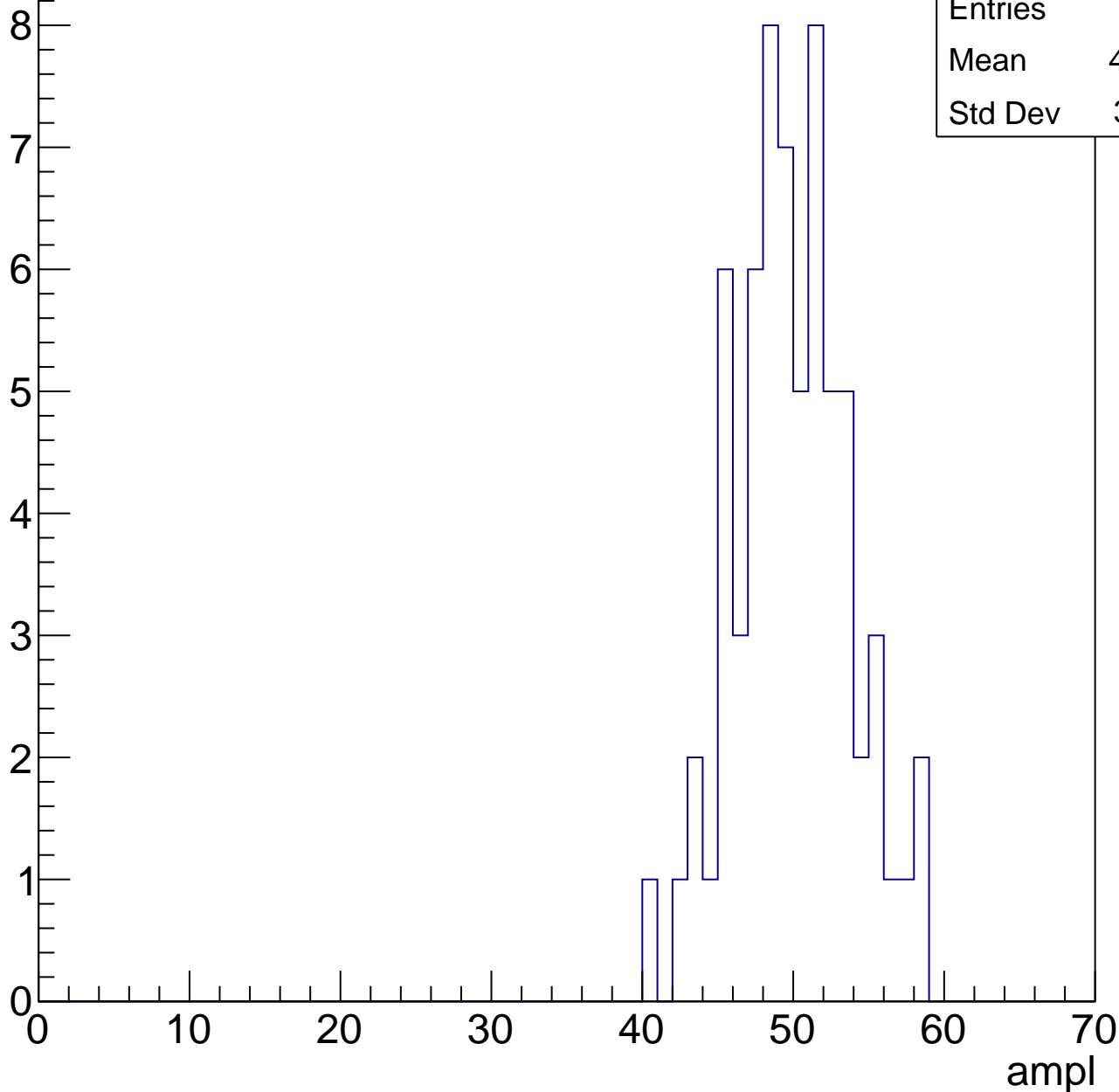


B1L102S, U22-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.46
Std Dev	3.791



B1L102S, U22-ch51, adc4

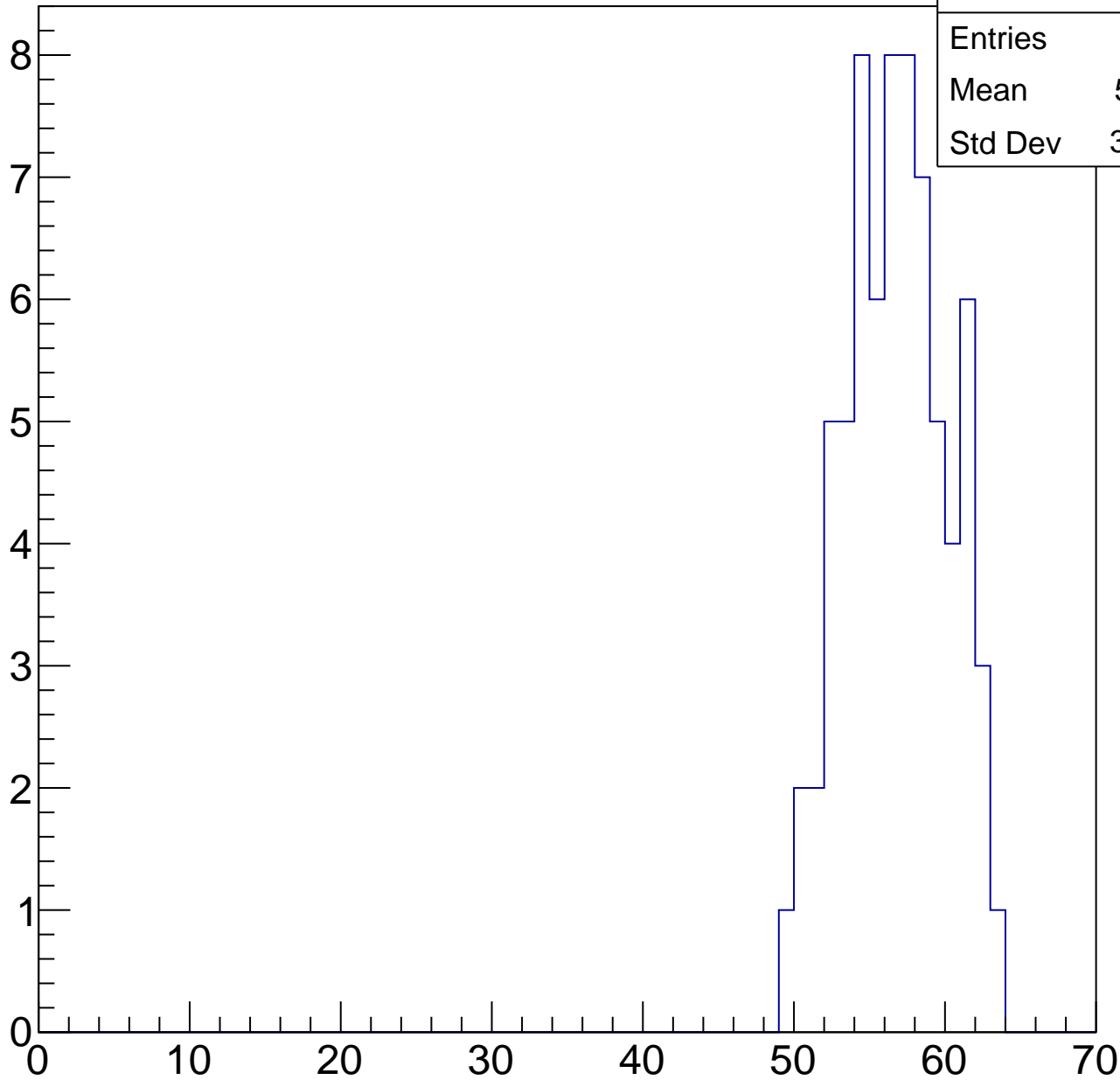
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	71
Mean	56.31
Std Dev	3.313

ampl

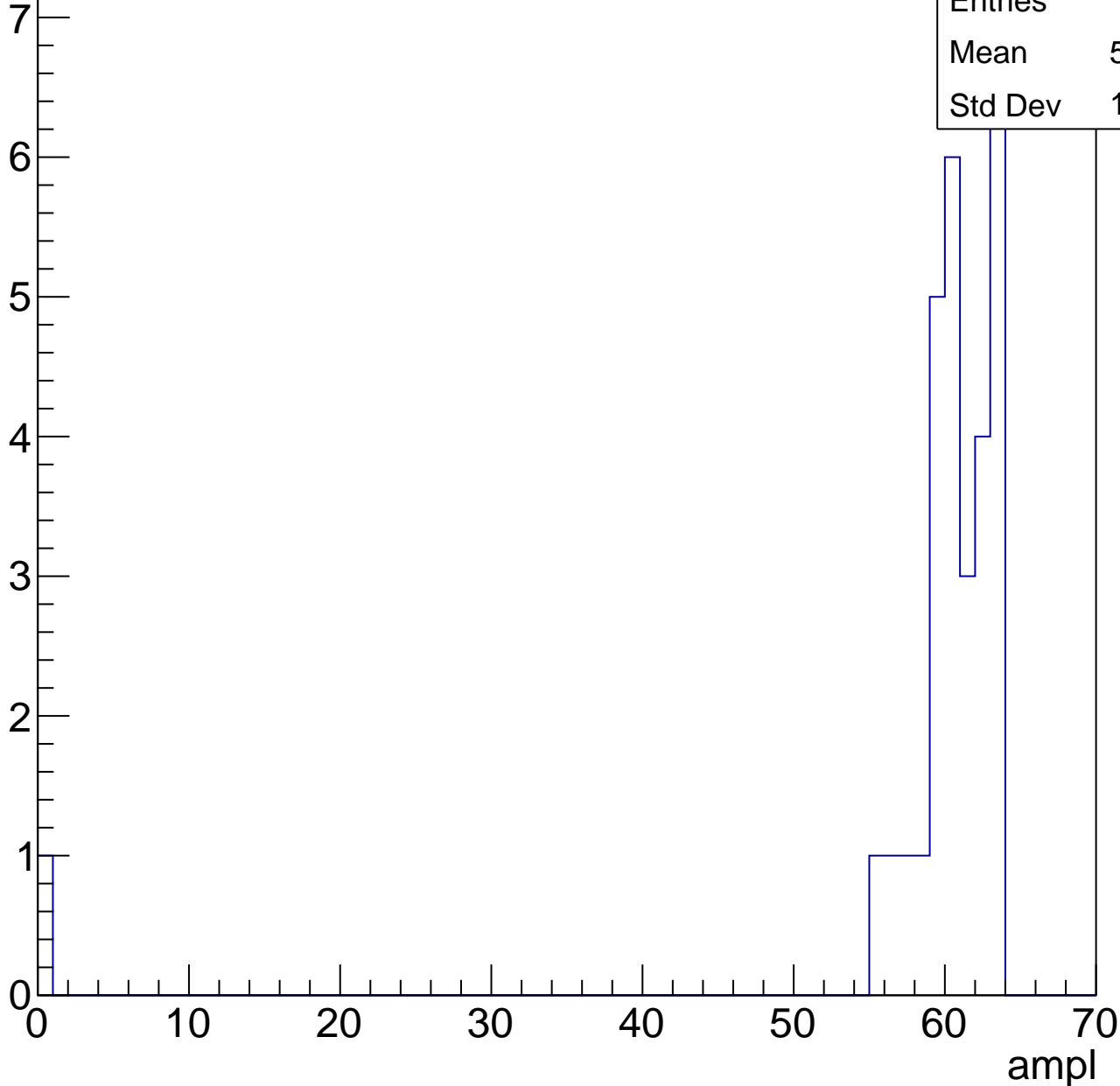


B1L102S, U22-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

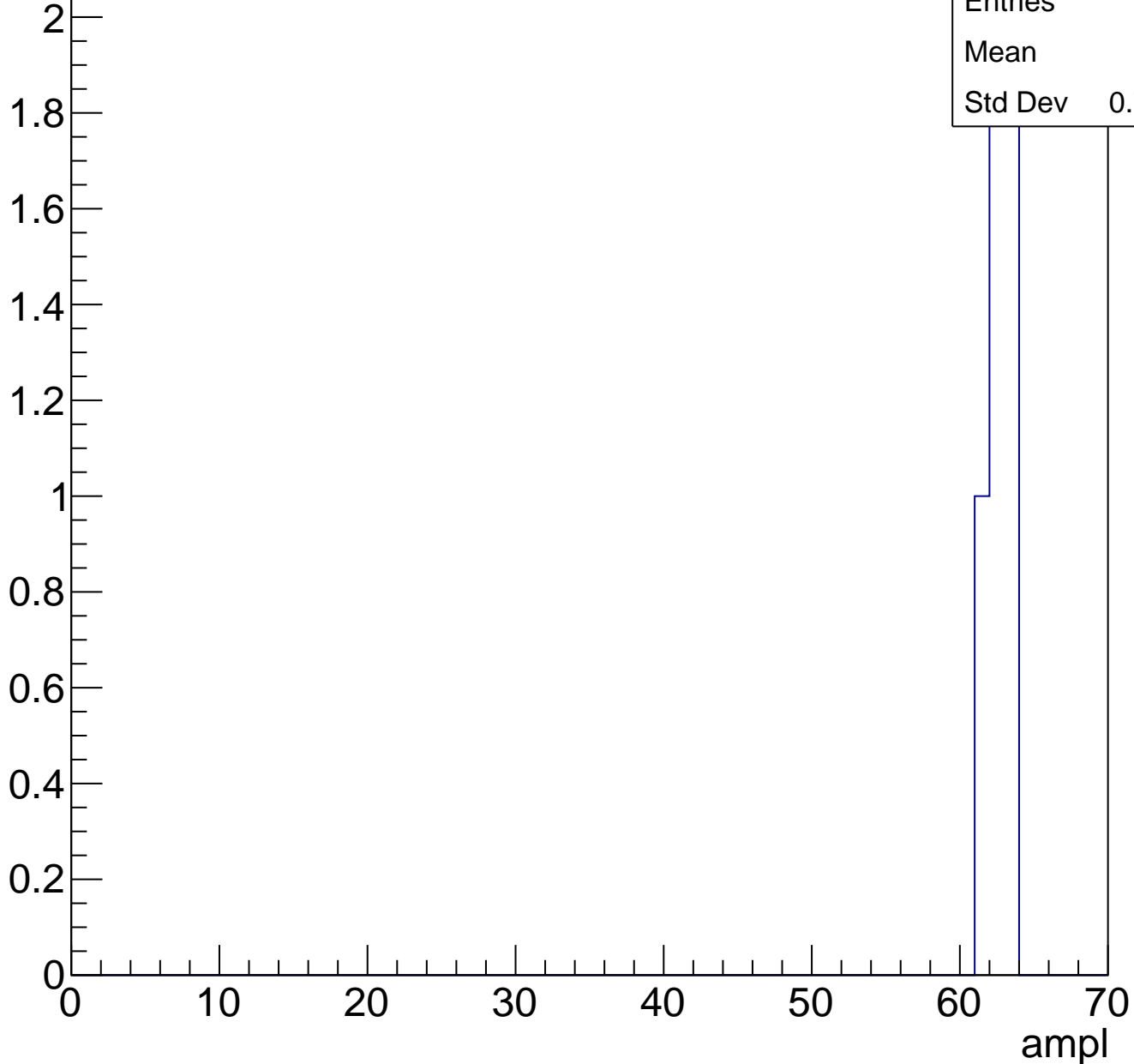
Entries	30
Mean	58.43
Std Dev	11.06



B1L102S, U22-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

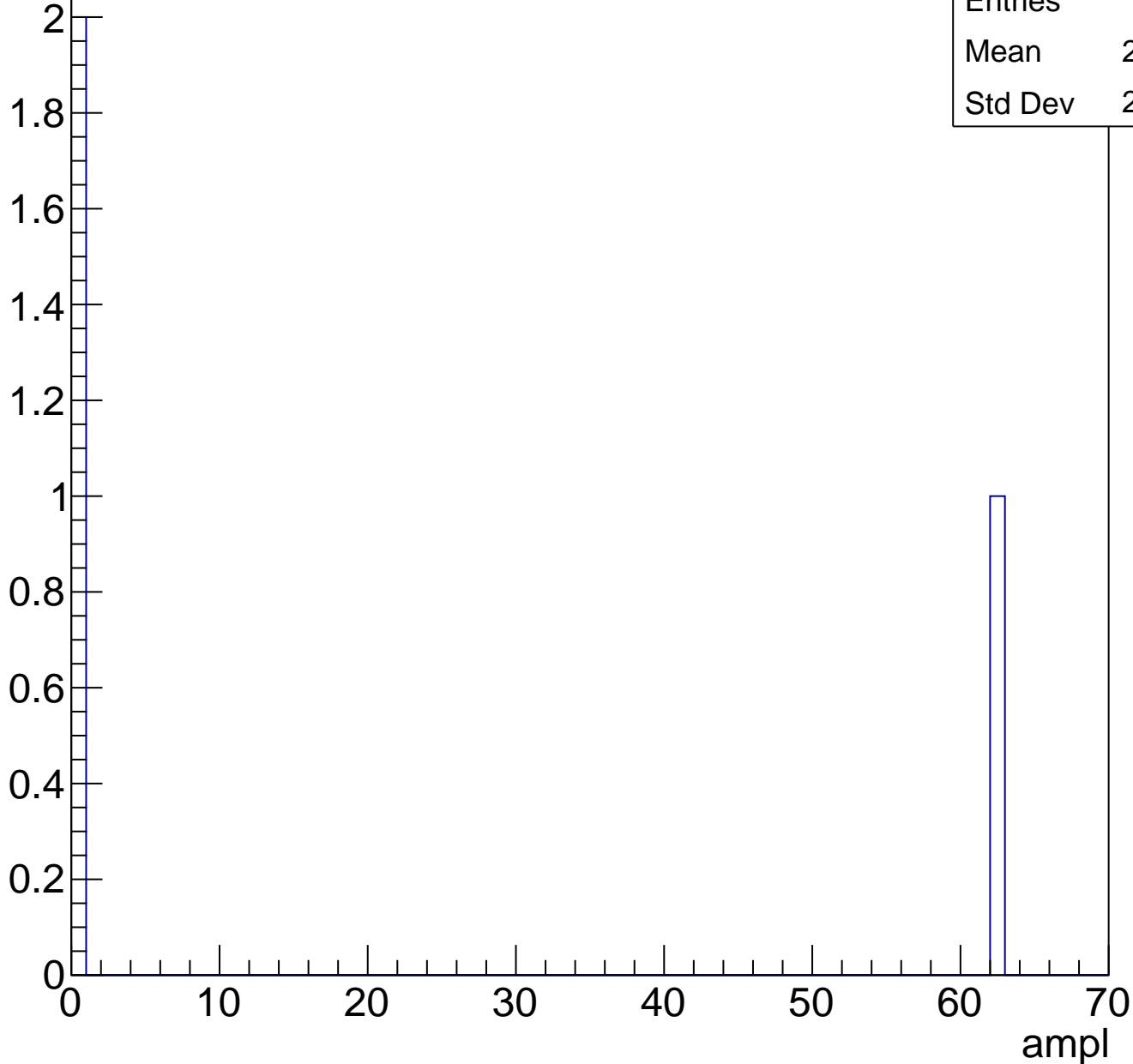
Entry



B1L102S, U22-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch52, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	29.89
Std Dev	3.793

Gaus mean : 30.2195

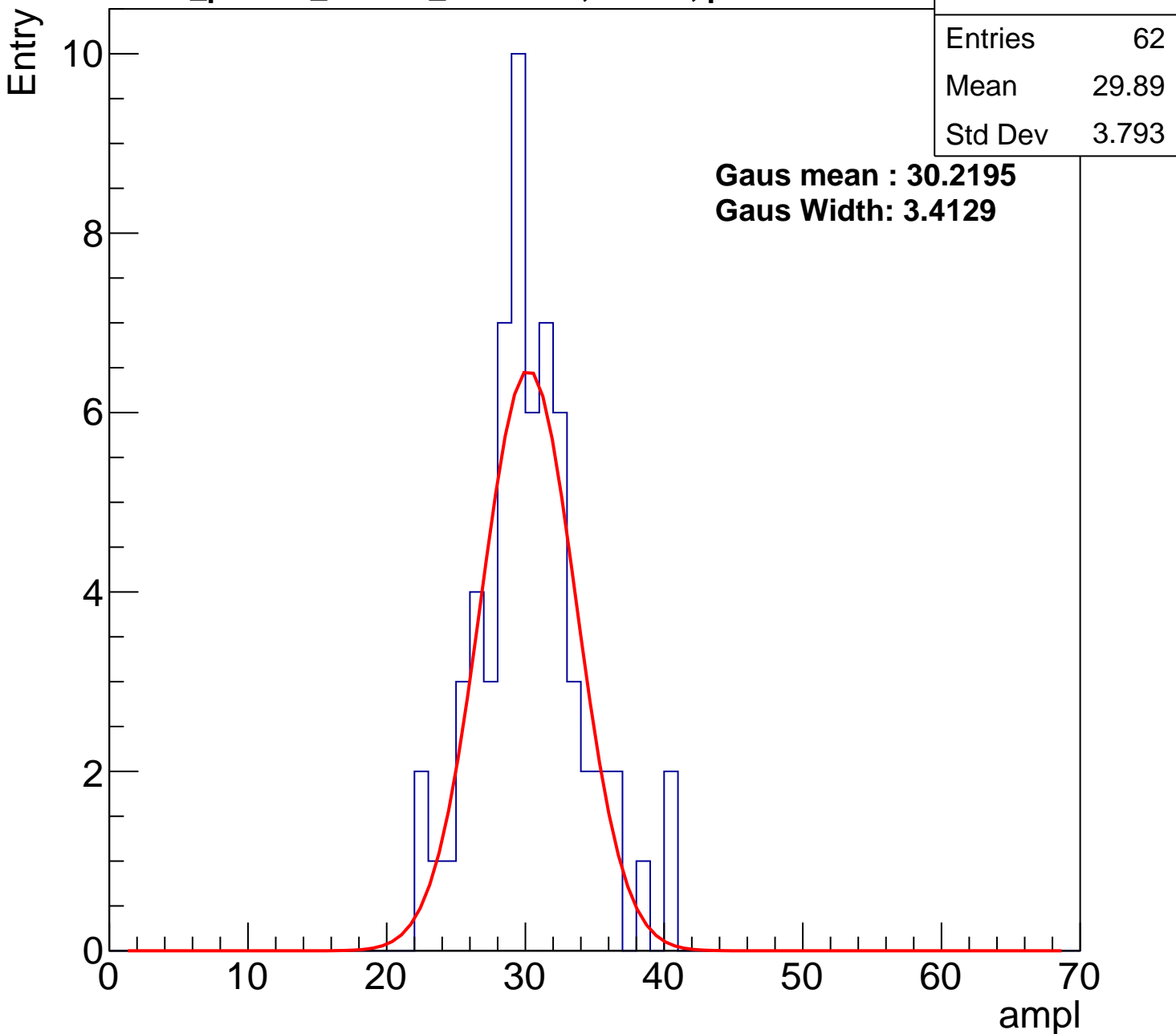
Gaus Width: 3.4129

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



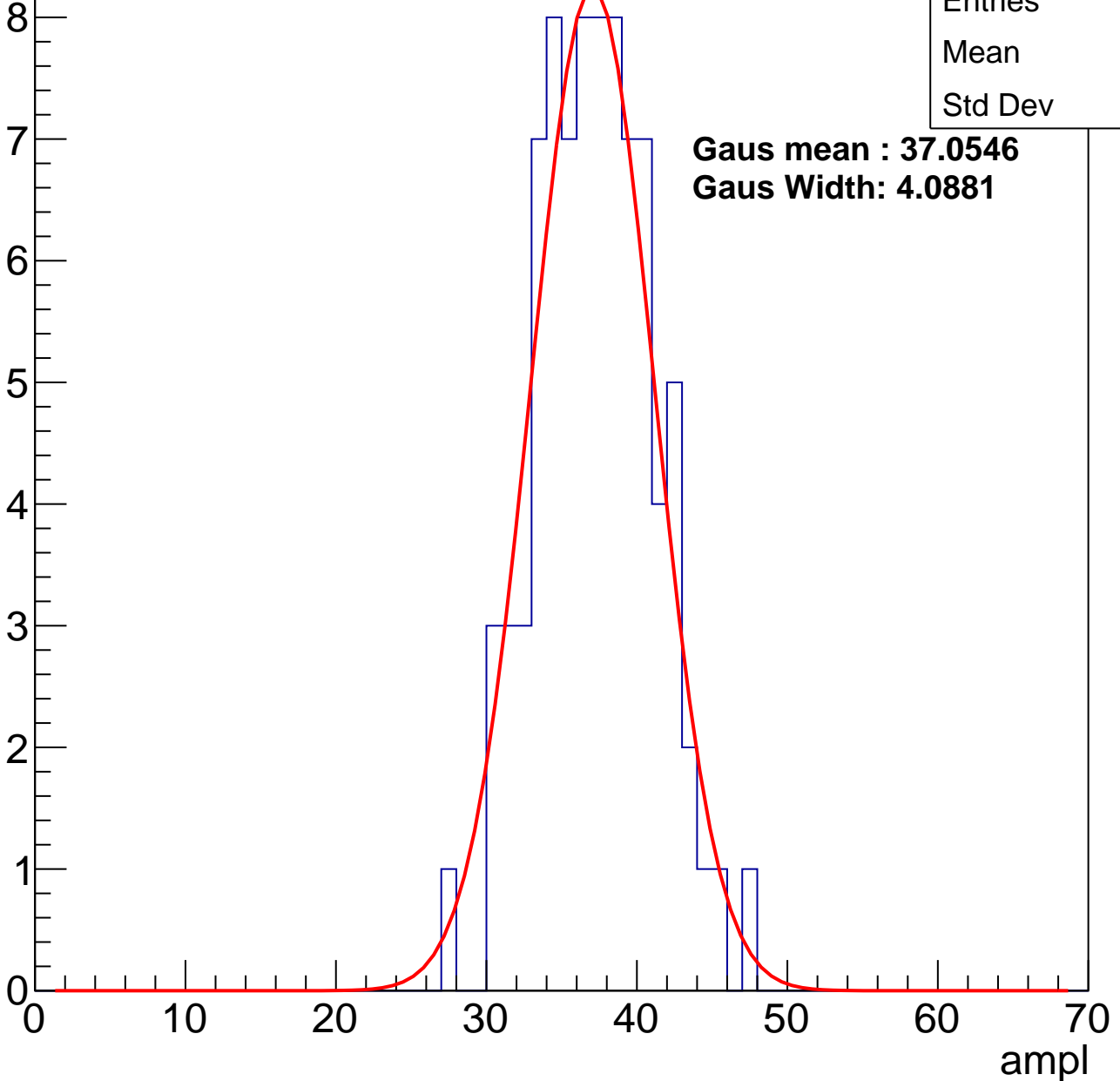
B1L102S, U22-ch52, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	36.8
Std Dev	3.81

Gaus mean : 37.0546
Gaus Width: 4.0881



B1L102S, U22-ch52, adc2

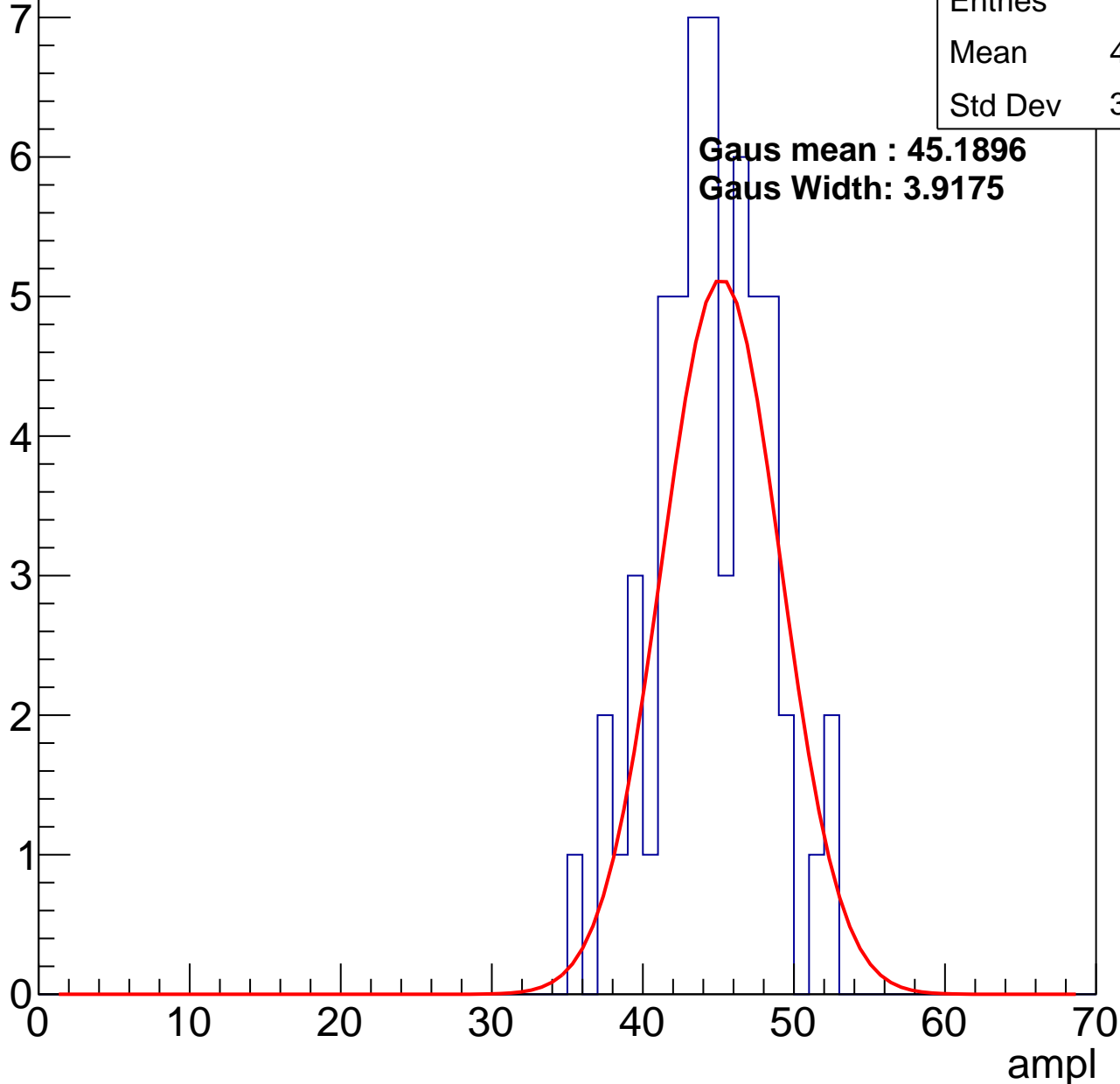
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	44.05
Std Dev	3.676

Gaus mean : 45.1896

Gaus Width: 3.9175

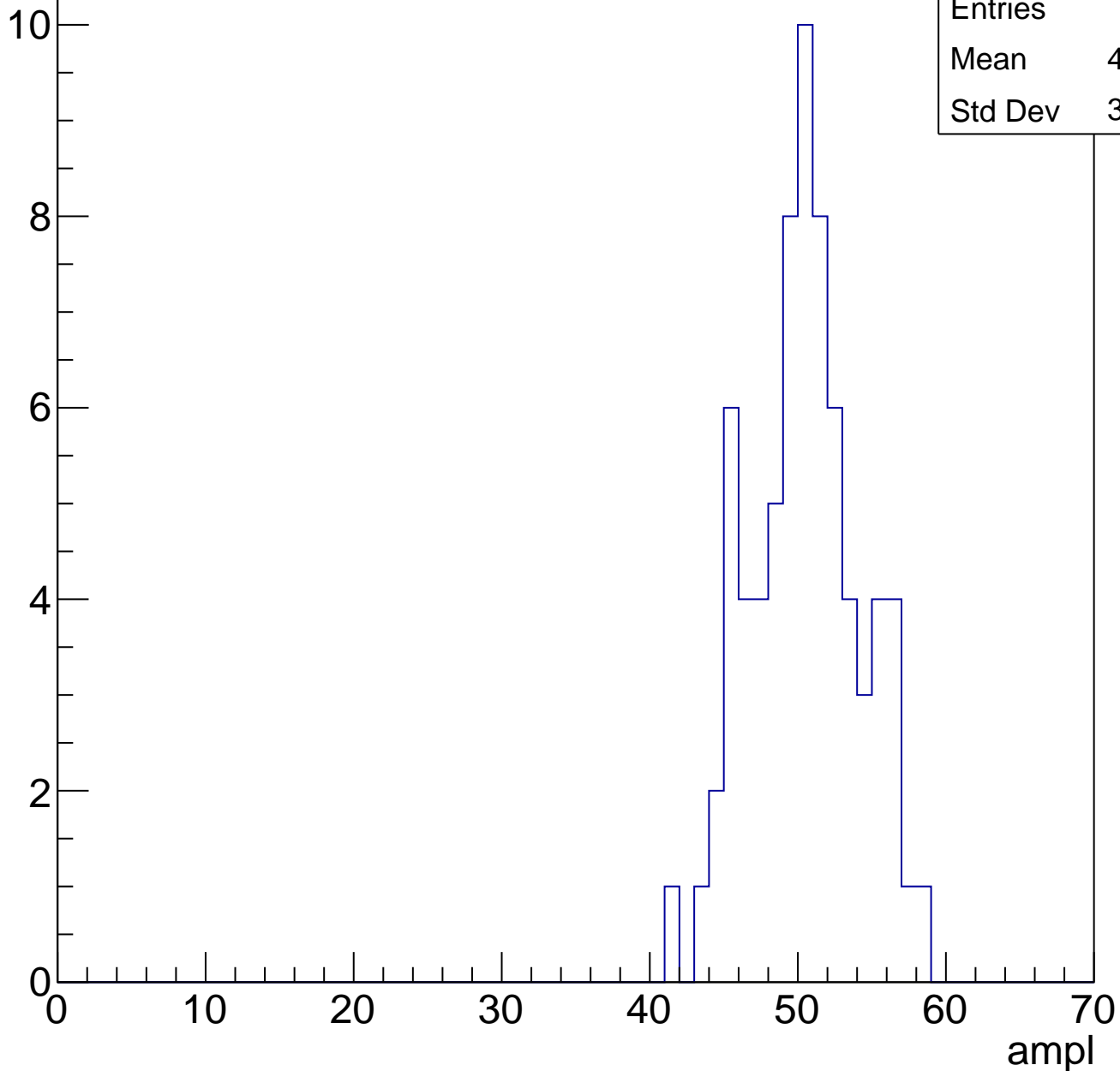


B1L102S, U22-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	49.99
Std Dev	3.638

Entry

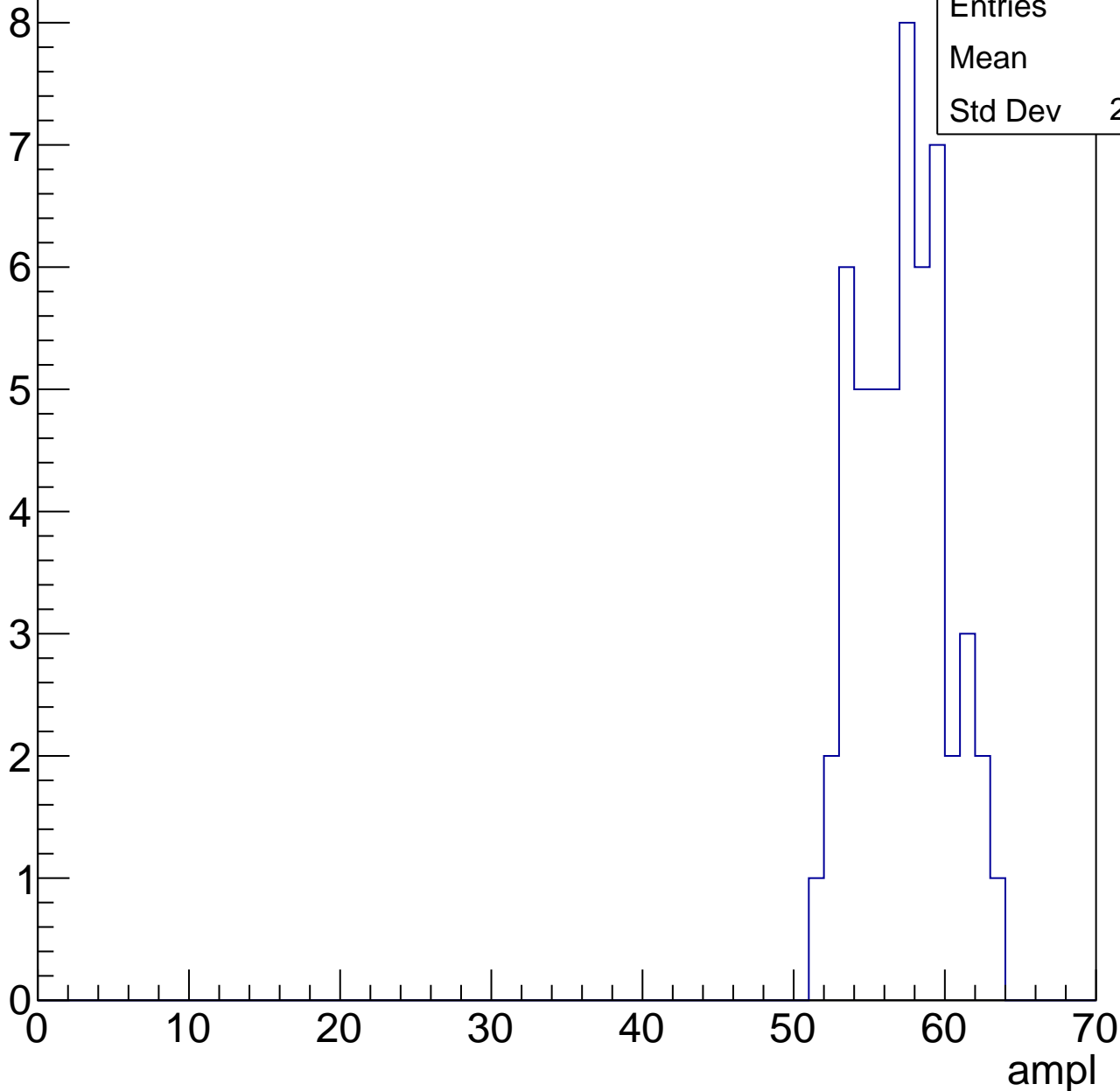


B1L102S, U22-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.7
Std Dev	2.859

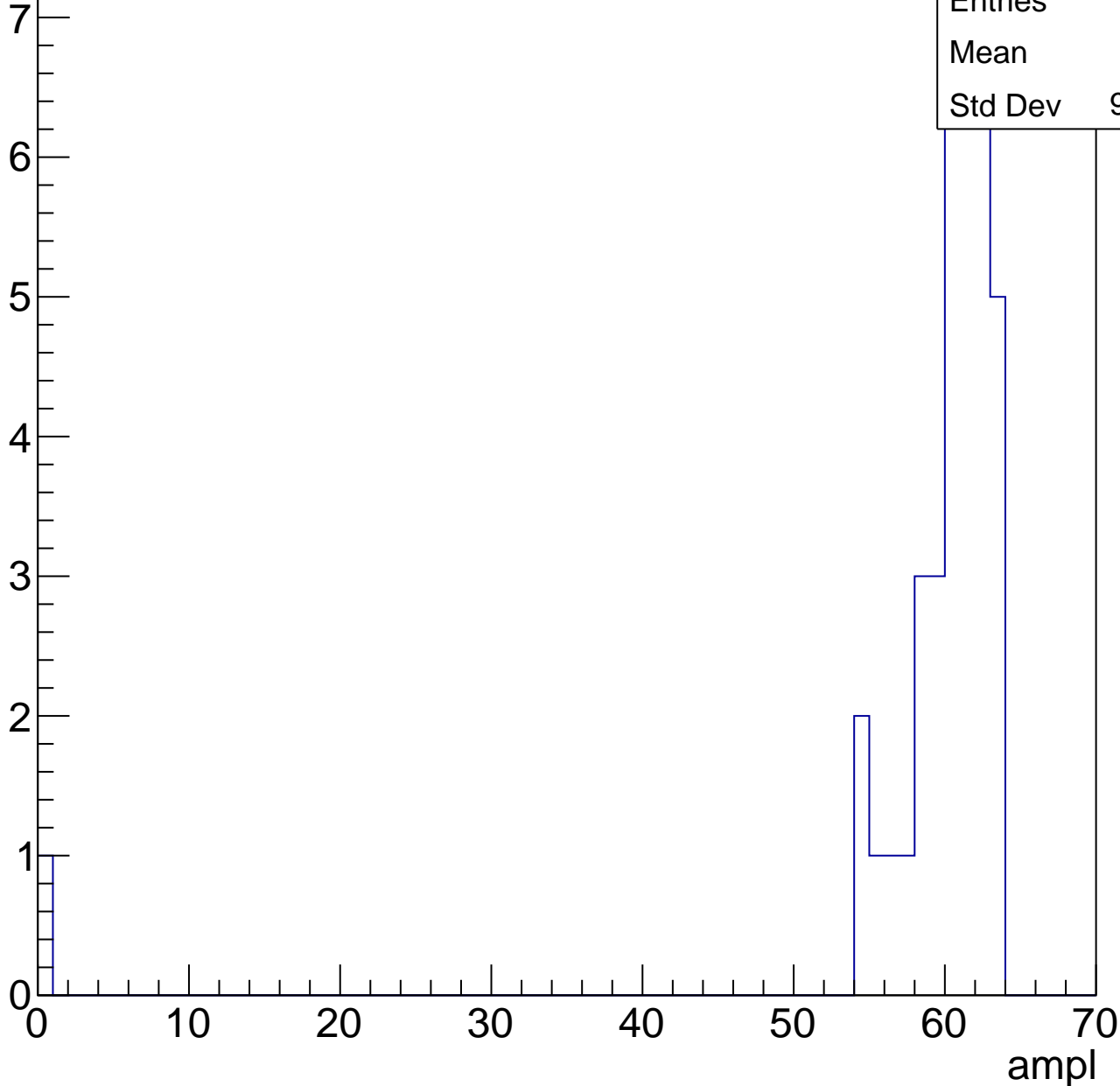


B1L102S, U22-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

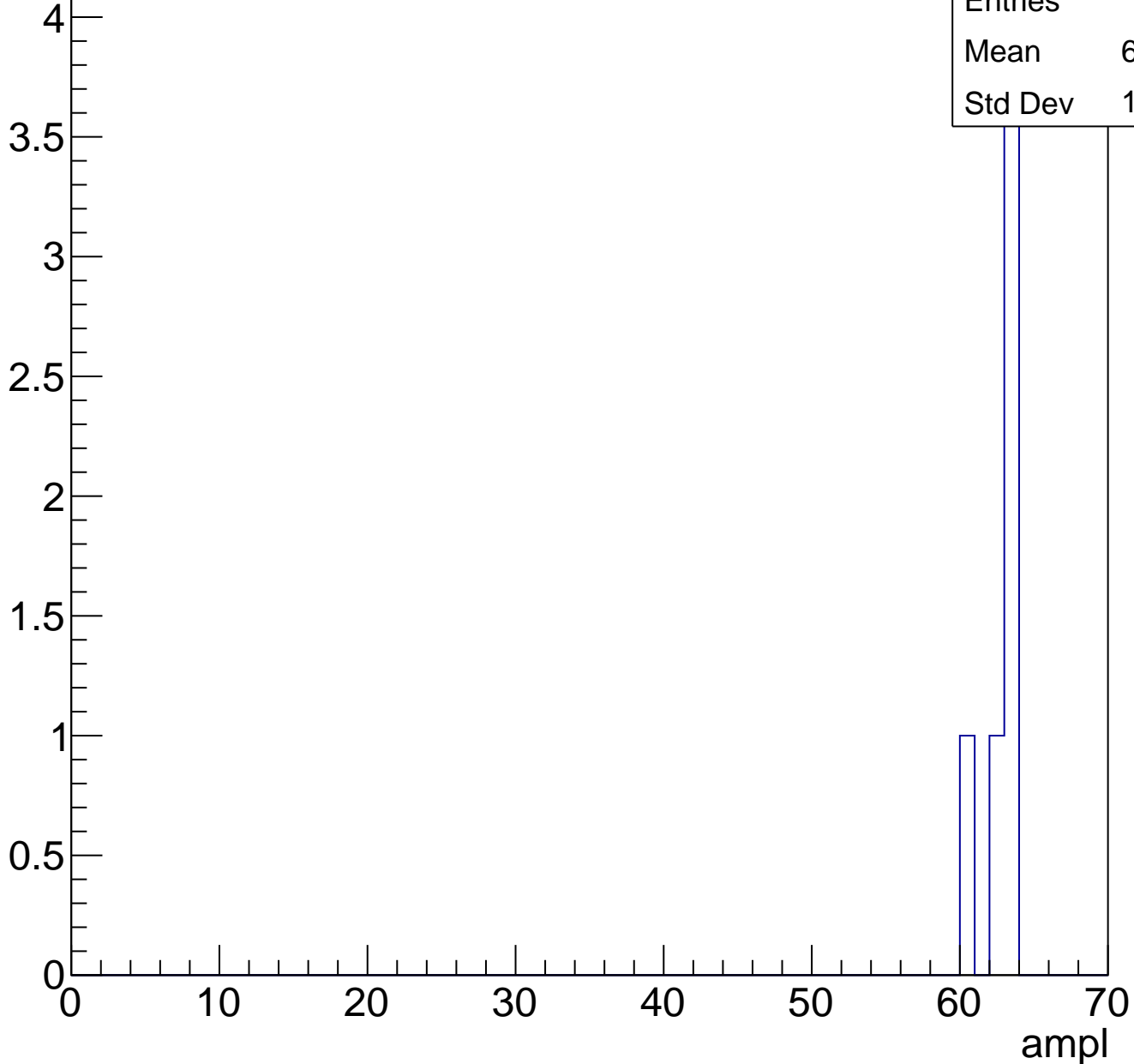
Entries	38
Mean	58.5
Std Dev	9.909



B1L102S, U22-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch53, adc0

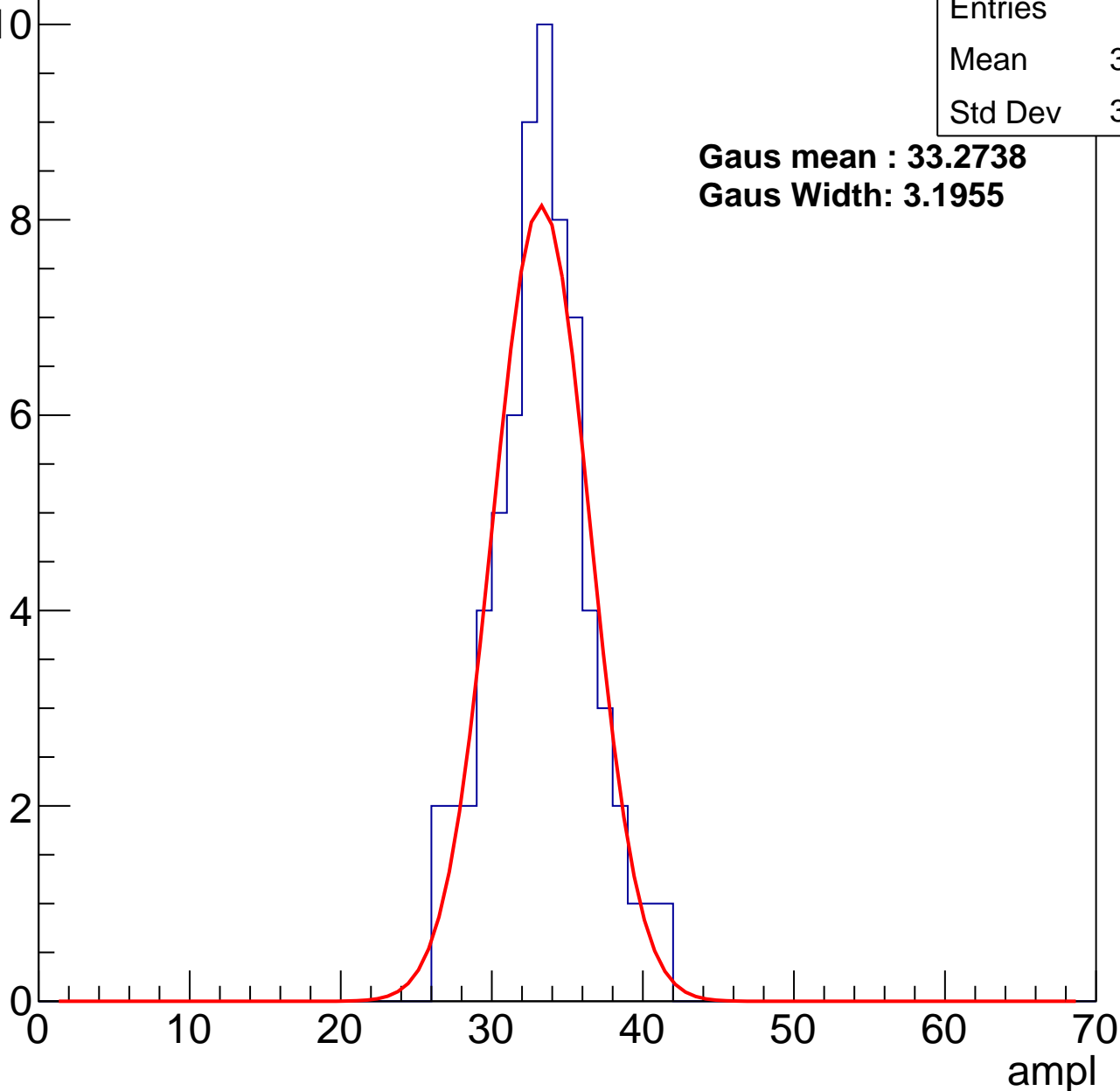
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	32.84
Std Dev	3.184

Gaus mean : 33.2738

Gaus Width: 3.1955



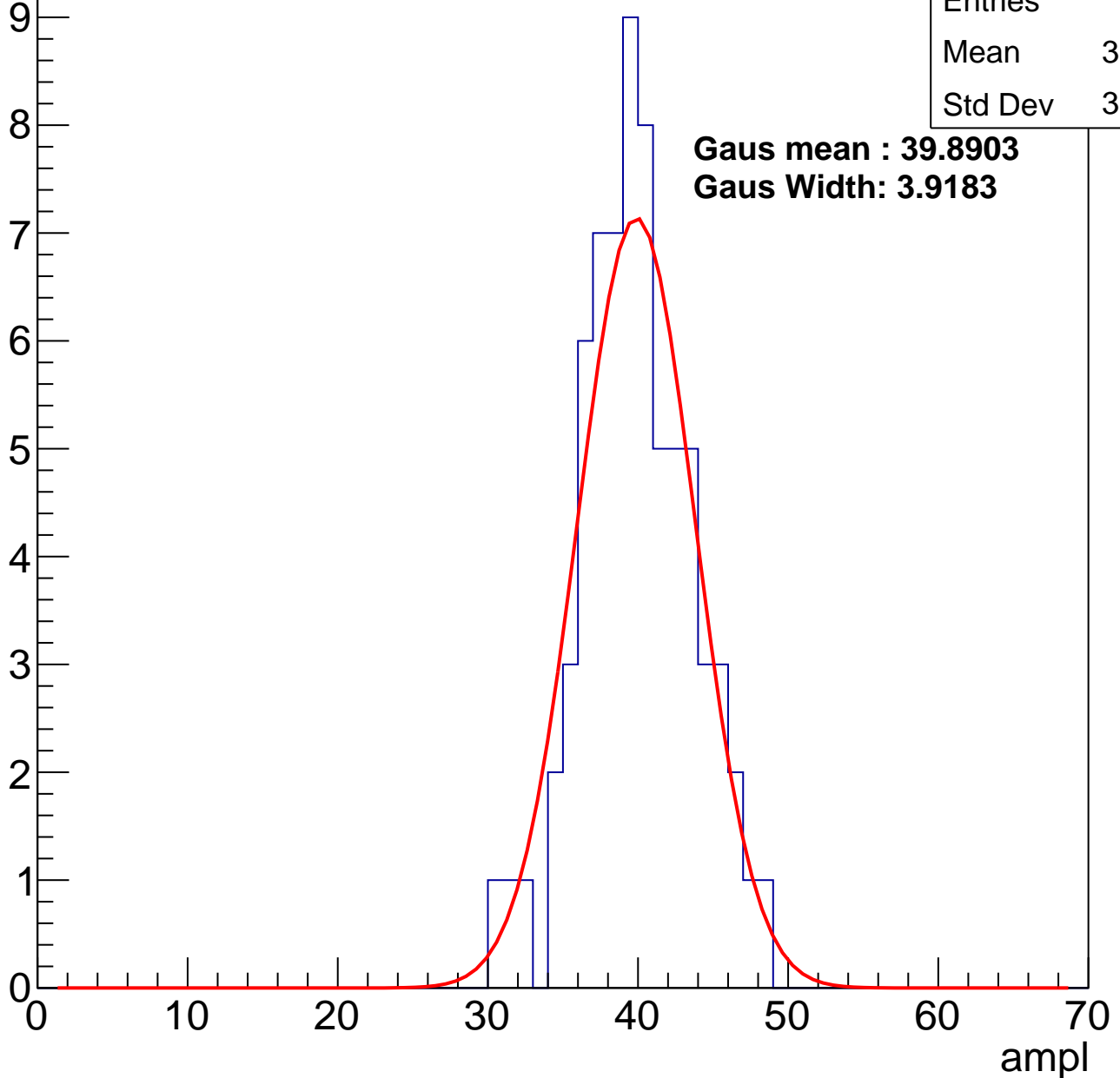
B1L102S, U22-ch53, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	39.46
Std Dev	3.675

Gaus mean : 39.8903
Gaus Width: 3.9183



B1L102S, U22-ch53, adc2

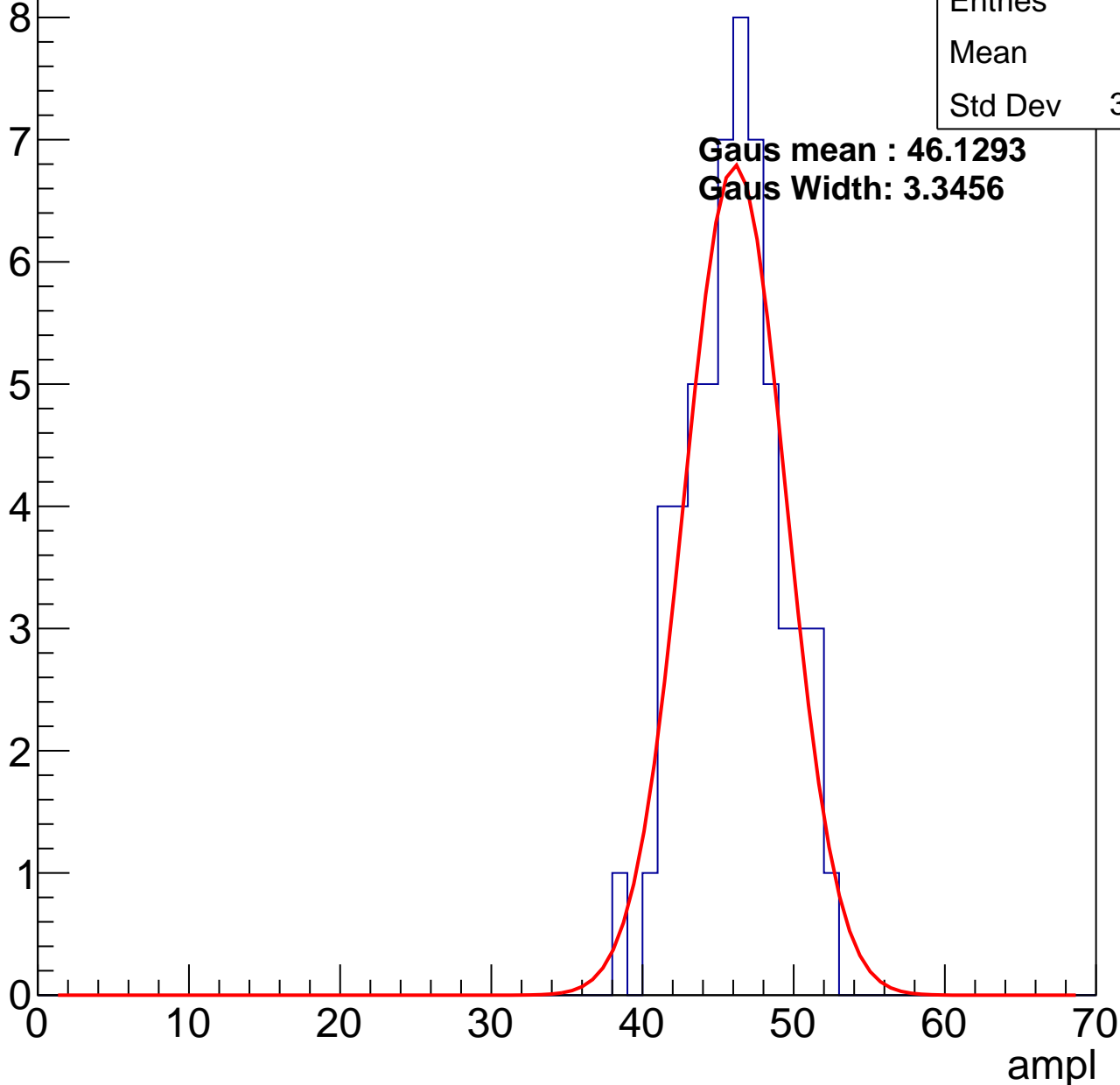
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	45.6
Std Dev	3.077

Gaus mean : 46.1293

Gaus Width: 3.3456

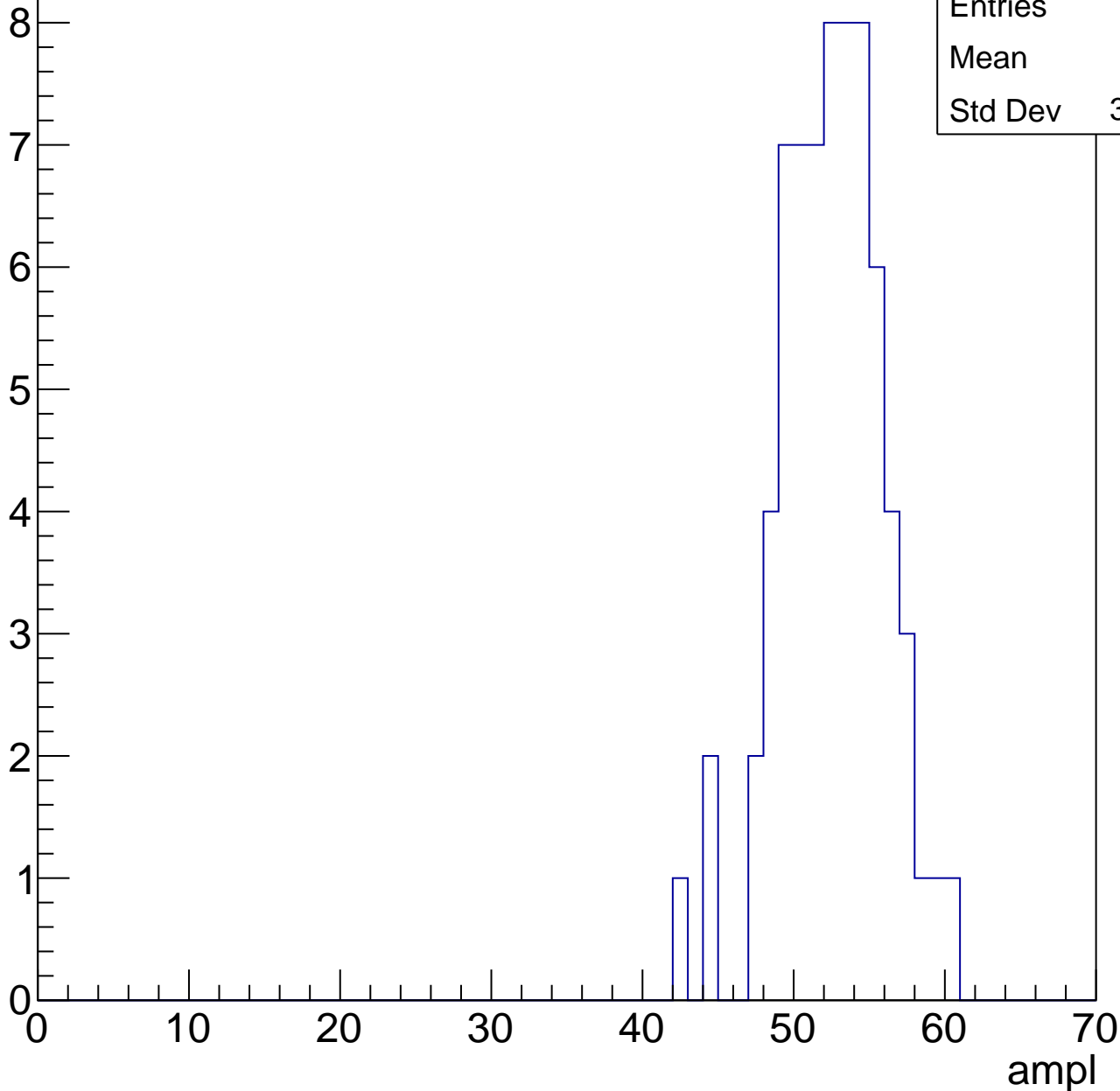


B1L102S, U22-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	52
Std Dev	3.427

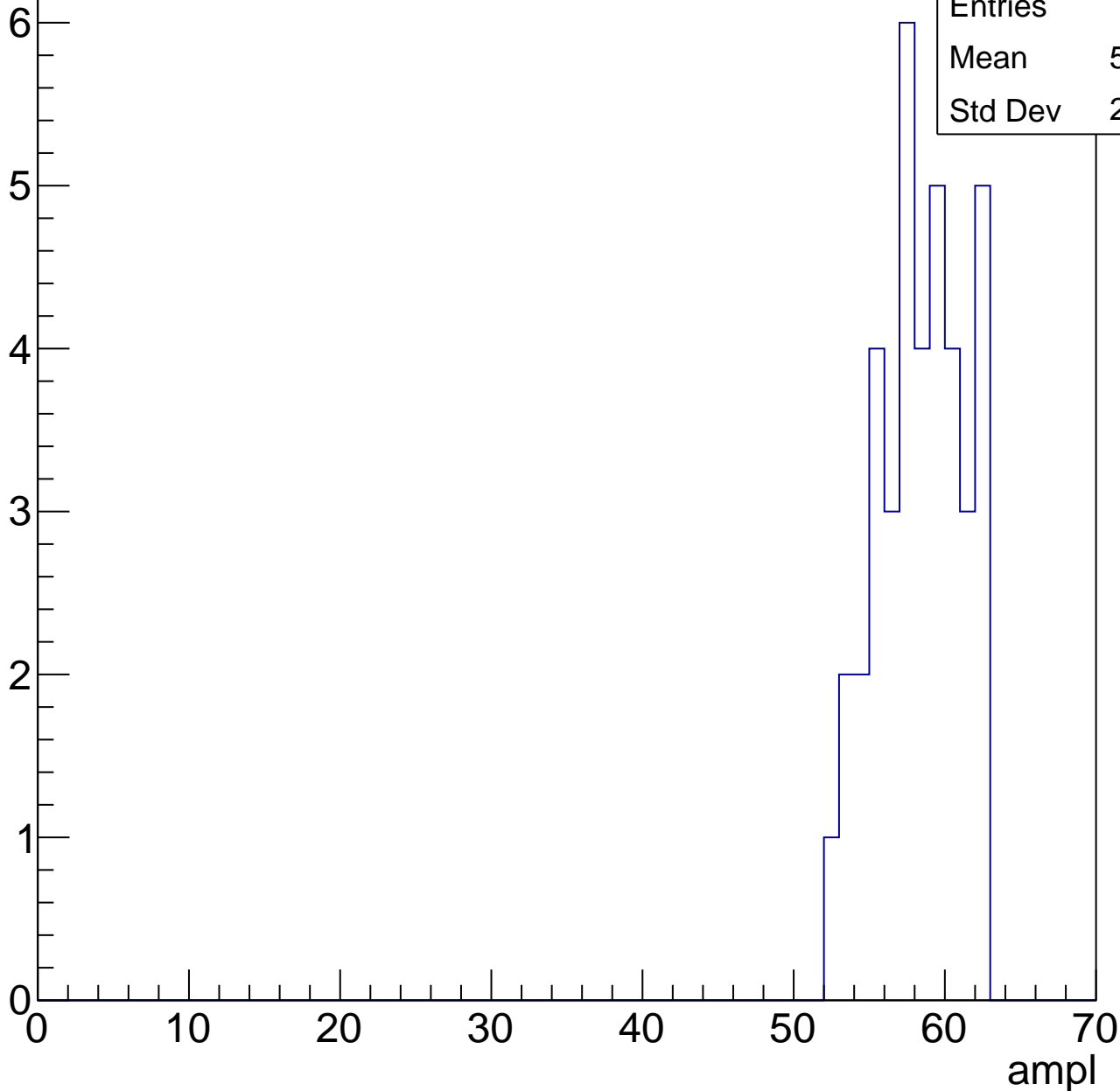


B1L102S, U22-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	57.85
Std Dev	2.769

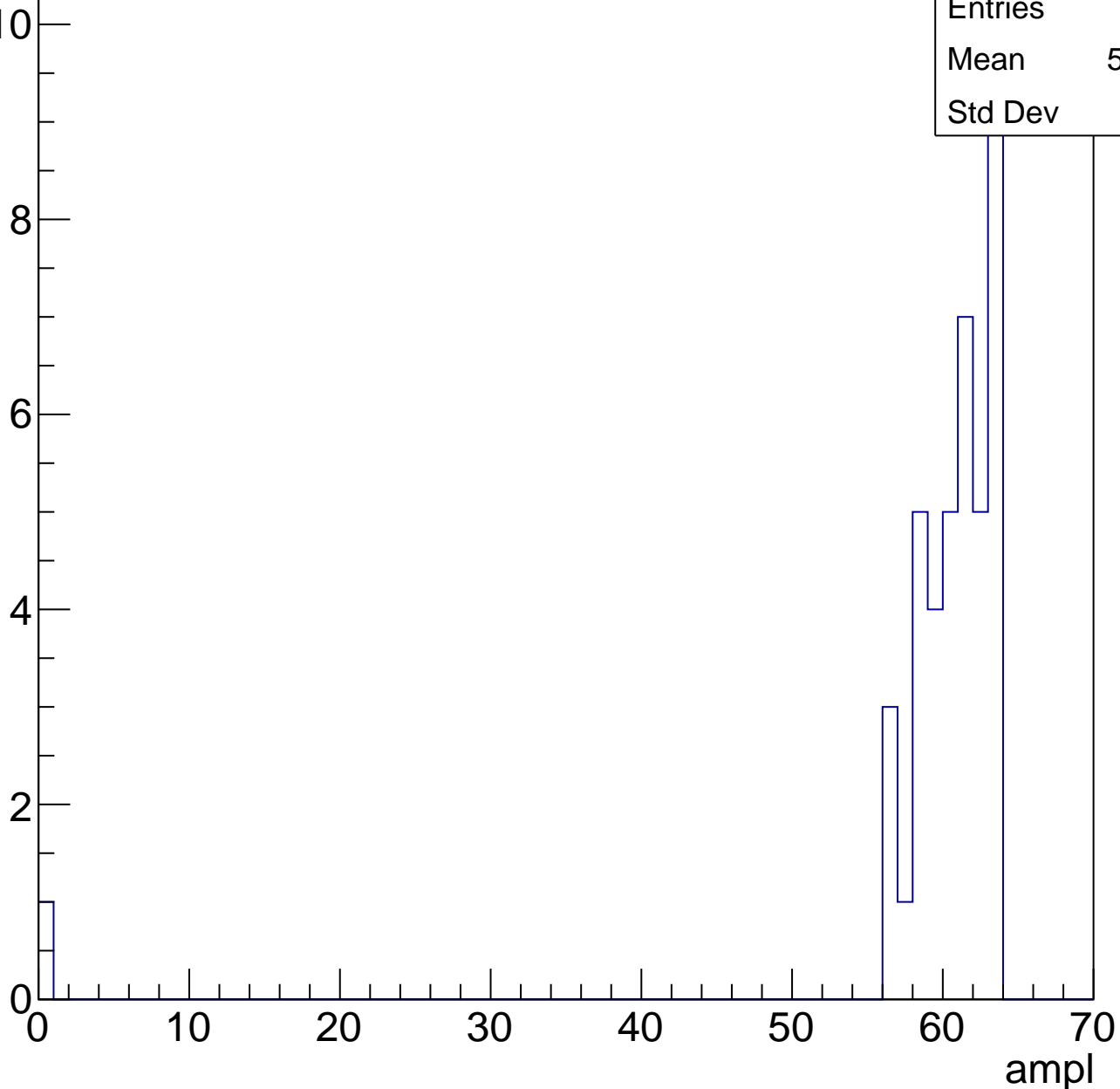


B1L102S, U22-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.98
Std Dev	9.57



B1L102S, U22-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U22-ch54, adc0

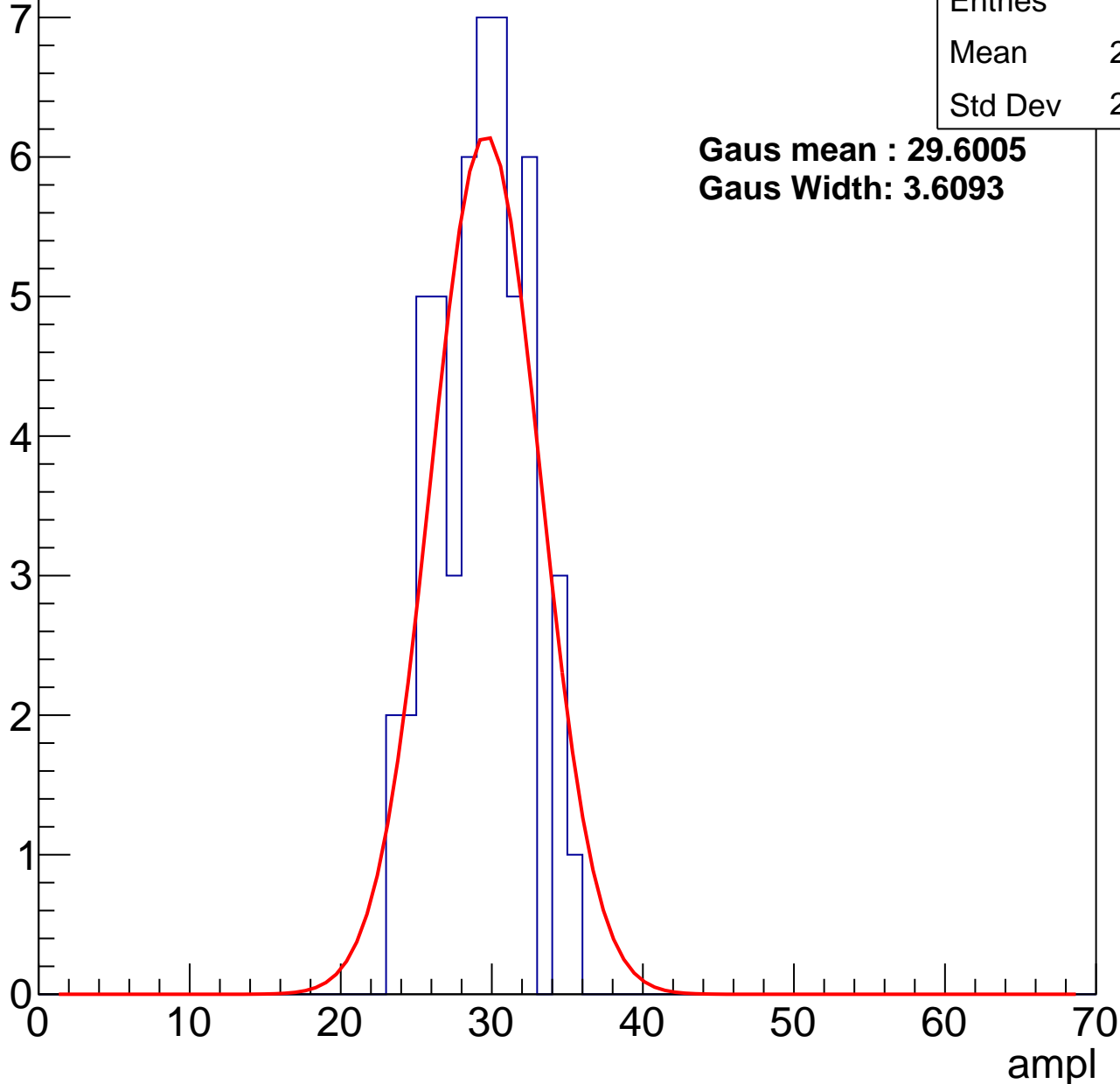
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	28.75
Std Dev	2.954

Gaus mean : 29.6005

Gaus Width: 3.6093



B1L102S, U22-ch54, adc1

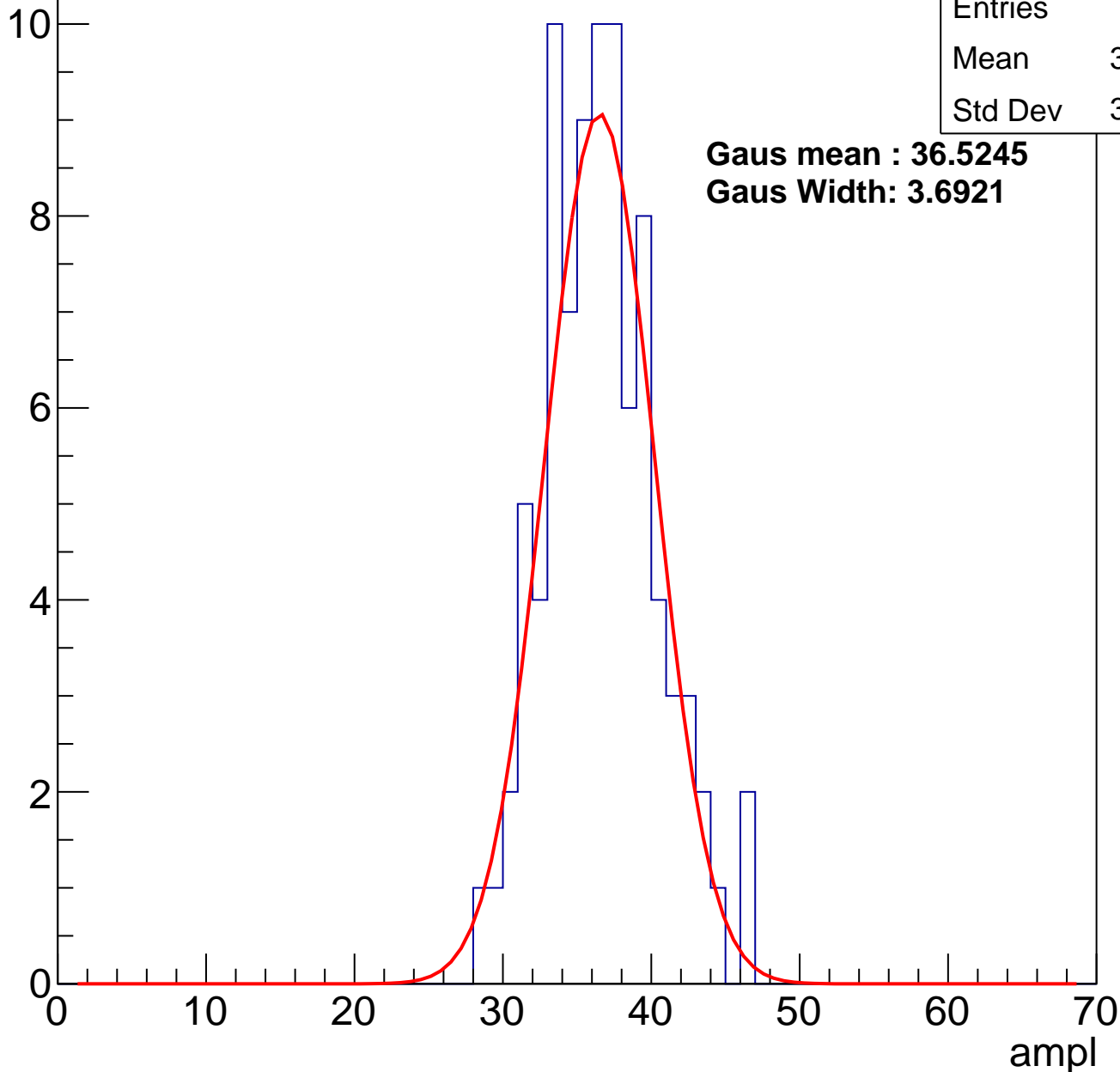
calib_packv5_042523_0143.root, FC#11, port A2

Entries	88
Mean	36.18
Std Dev	3.713

Gaus mean : 36.5245

Gaus Width: 3.6921

Entry

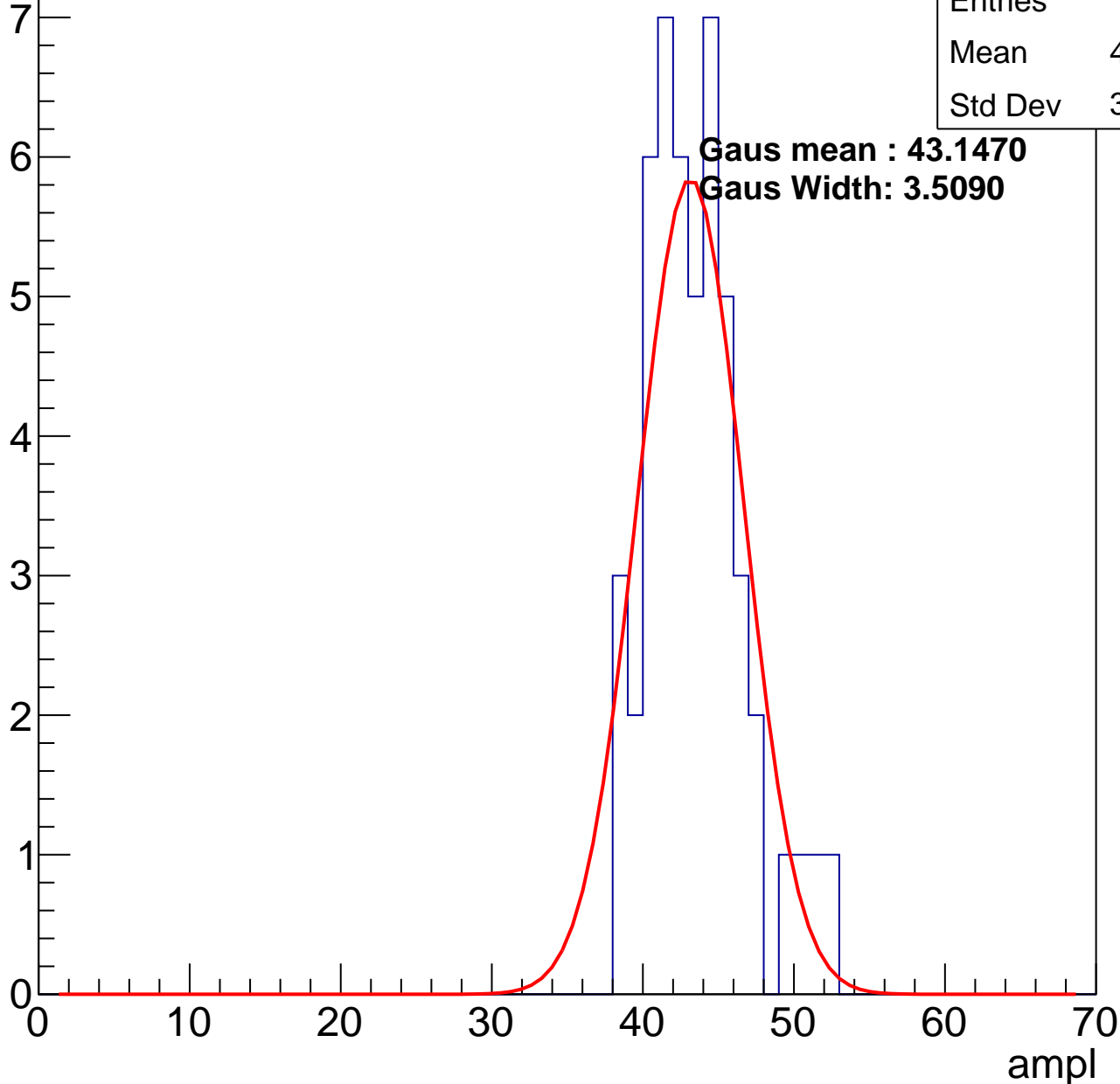


B1L102S, U22-ch54, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	43.06
Std Dev	3.196

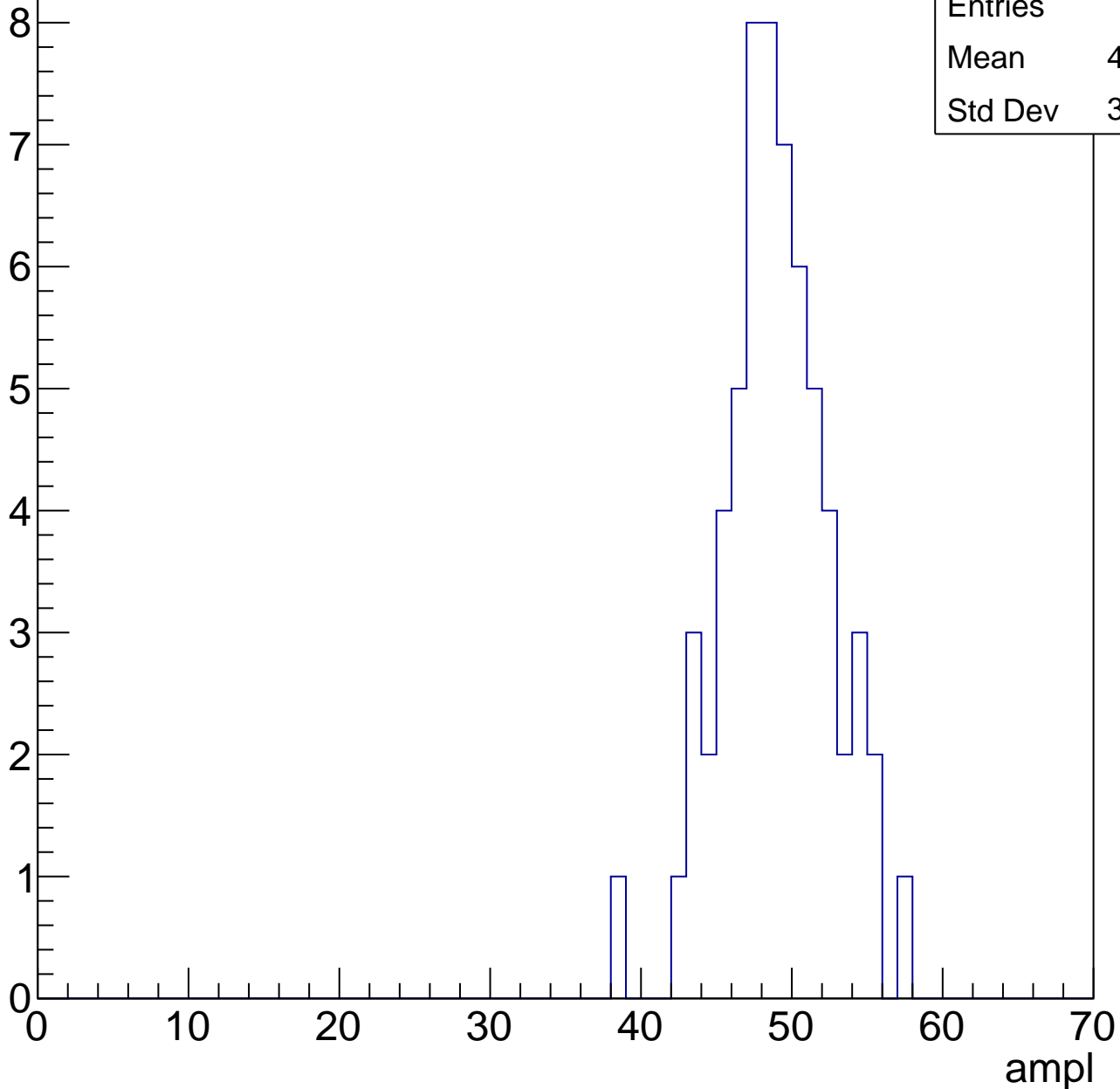


B1L102S, U22-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

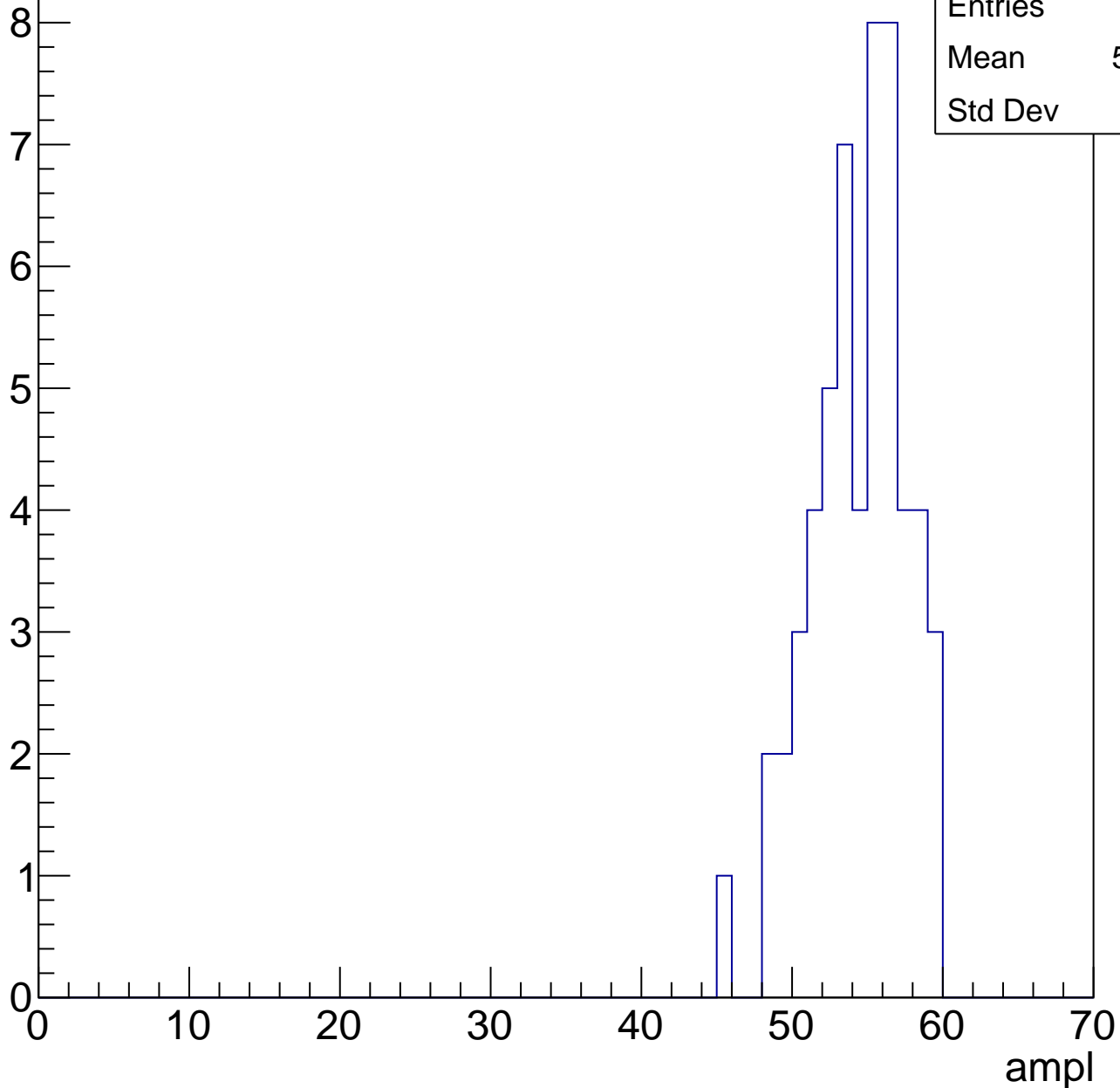
Entries	62
Mean	48.52
Std Dev	3.518



B1L102S, U22-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

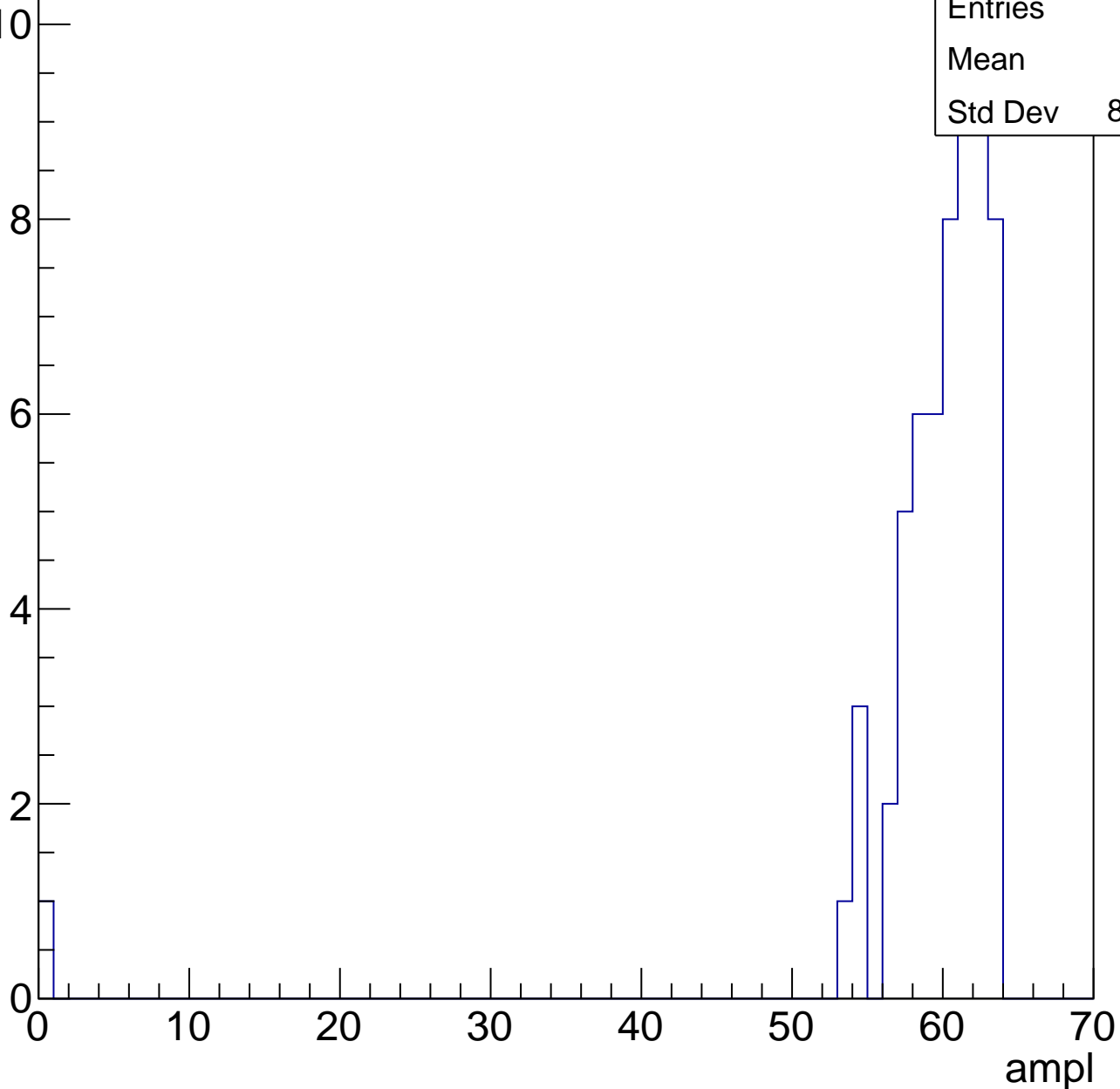


B1L102S, U22-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.8
Std Dev	8.062



B1L102S, U22-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

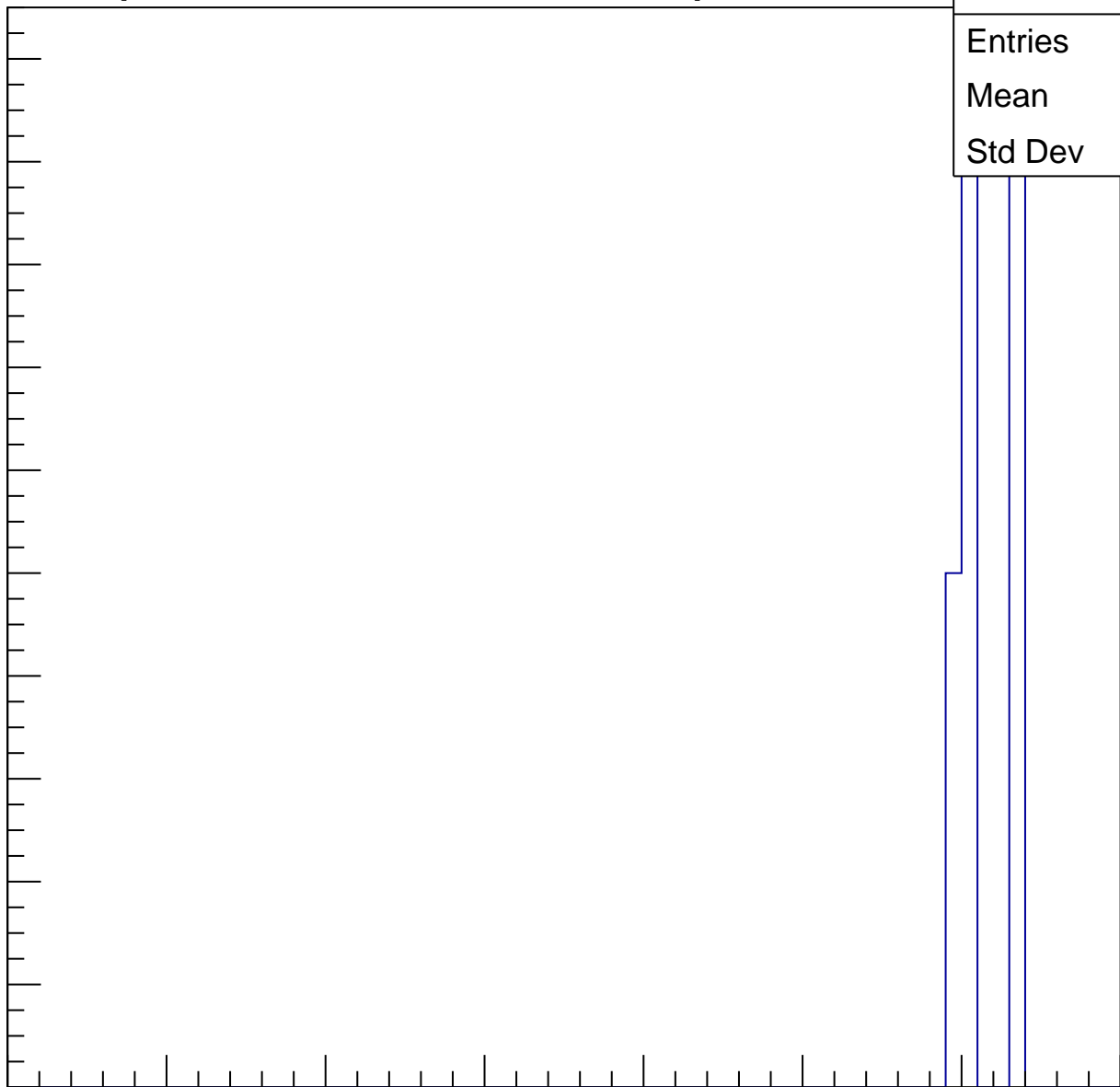
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61
Std Dev	1.673

0 10 20 30 40 50 60 70

ampl



B1L102S, U22-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch55, adc0

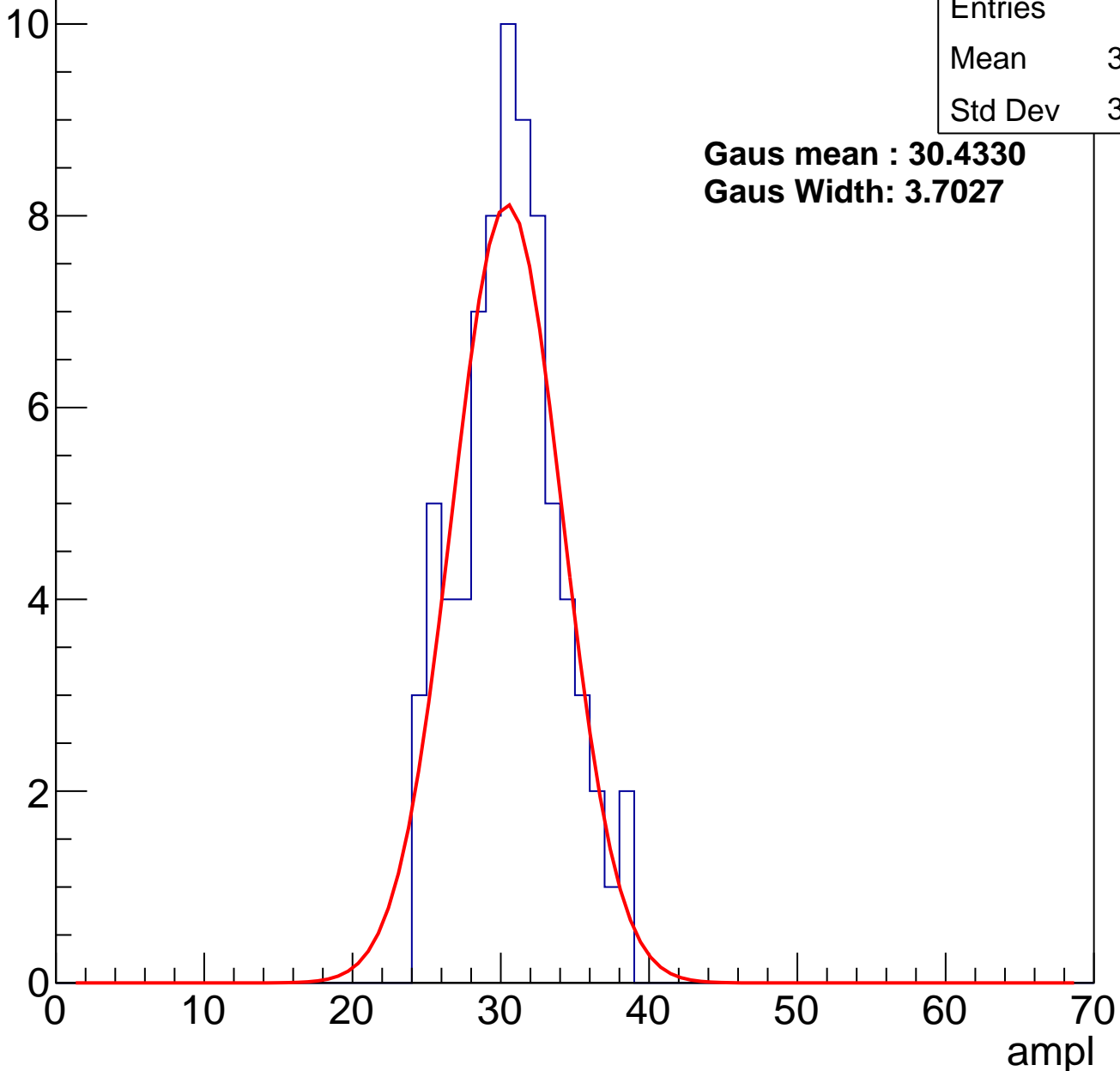
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	30.17
Std Dev	3.348

Gaus mean : 30.4330

Gaus Width: 3.7027

Entry



B1L102S, U22-ch55, adc1

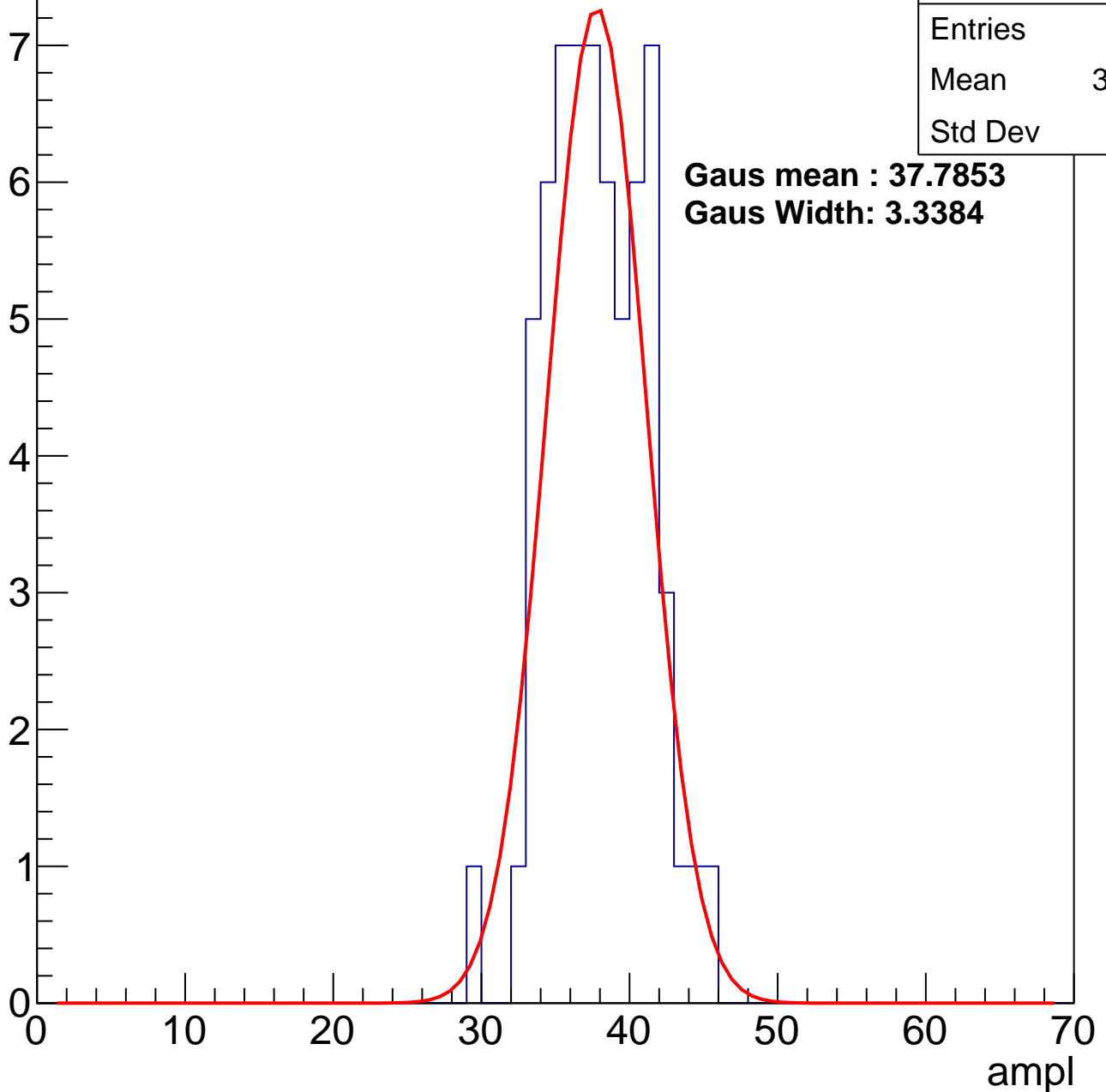
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.41
Std Dev	3.22

Gaus mean : 37.7853

Gaus Width: 3.3384



B1L102S, U22-ch55, adc2

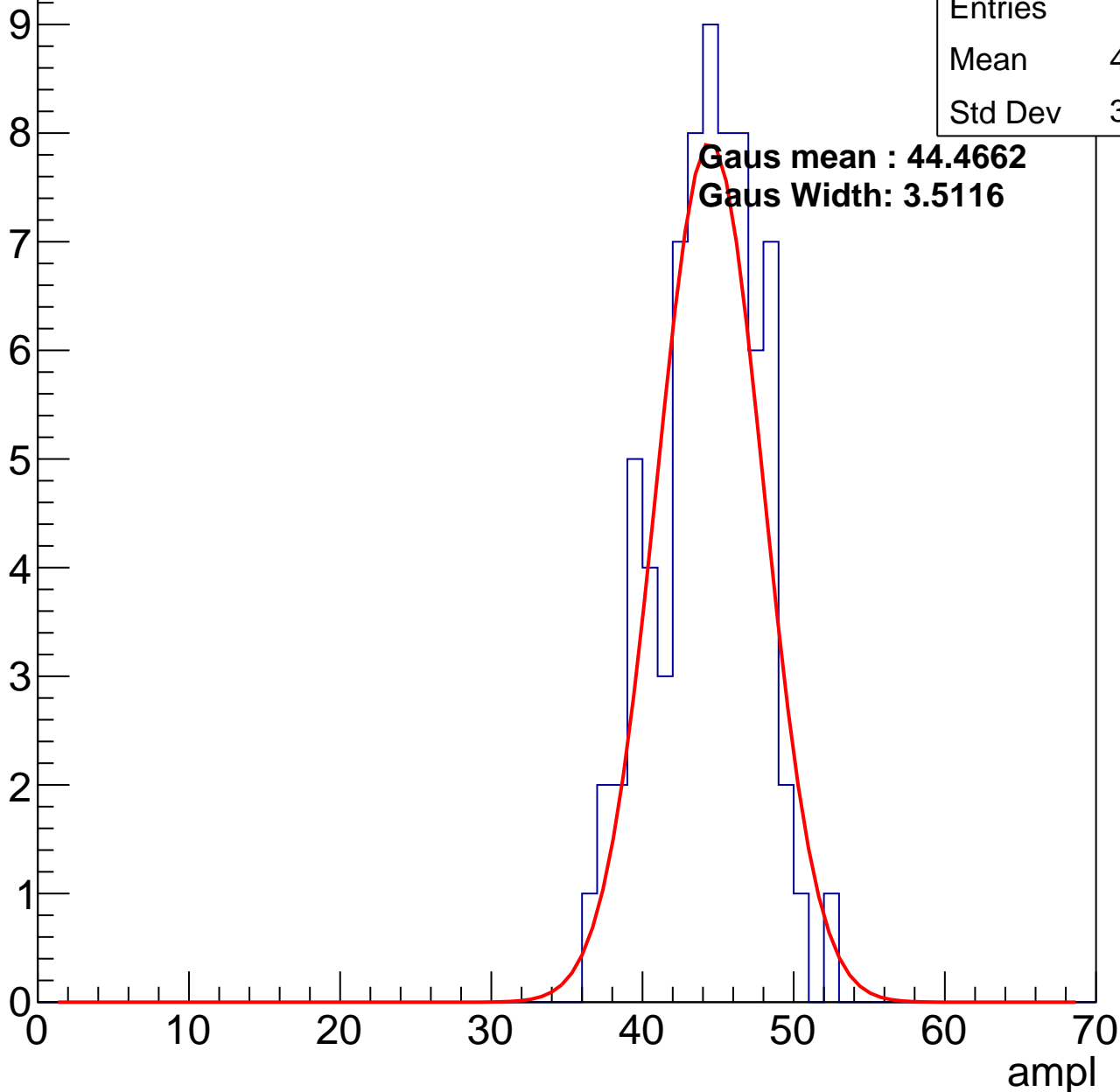
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	43.84
Std Dev	3.369

Gaus mean : 44.4662

Gaus Width: 3.5116

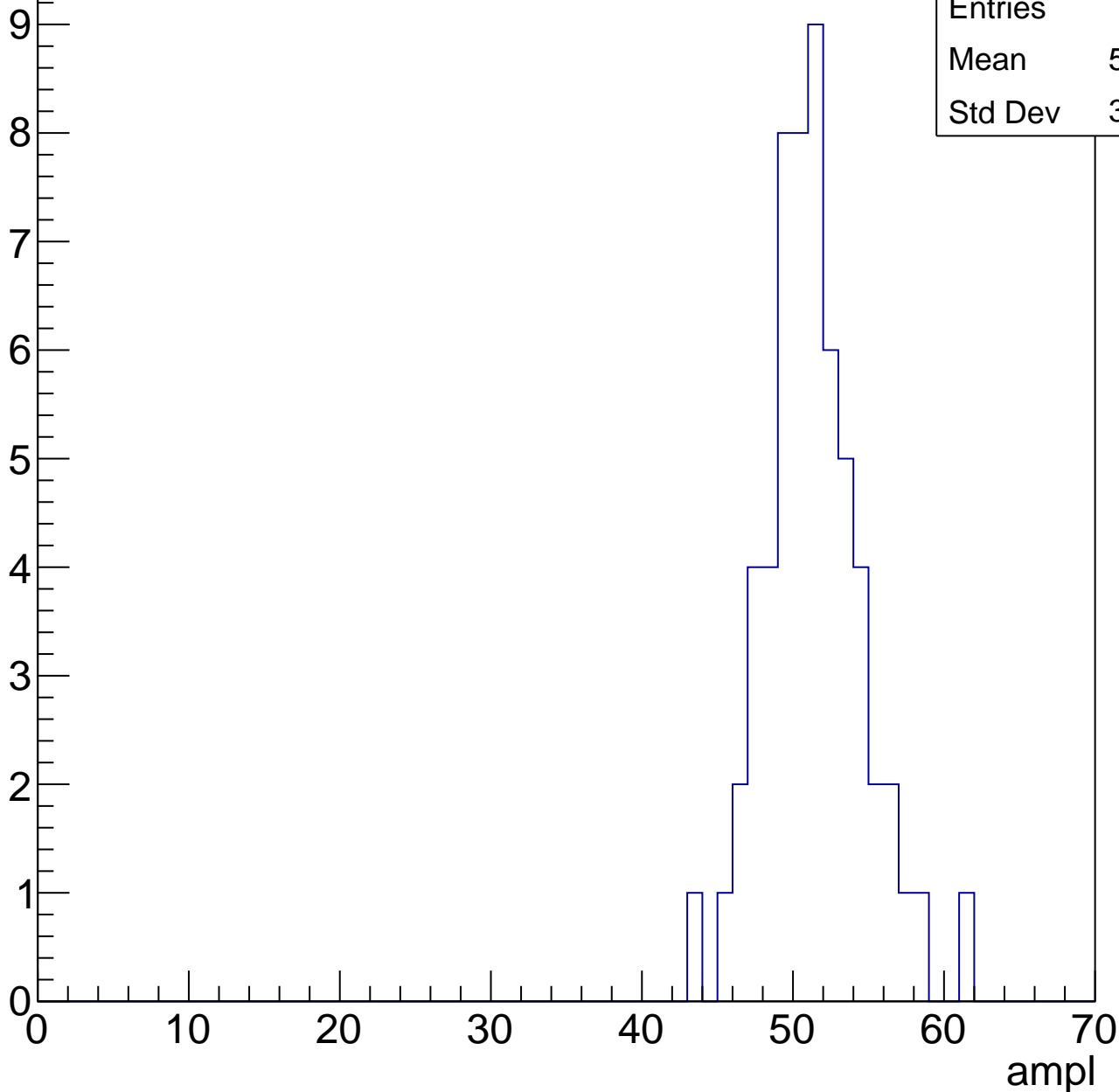


B1L102S, U22-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	50.88
Std Dev	3.237

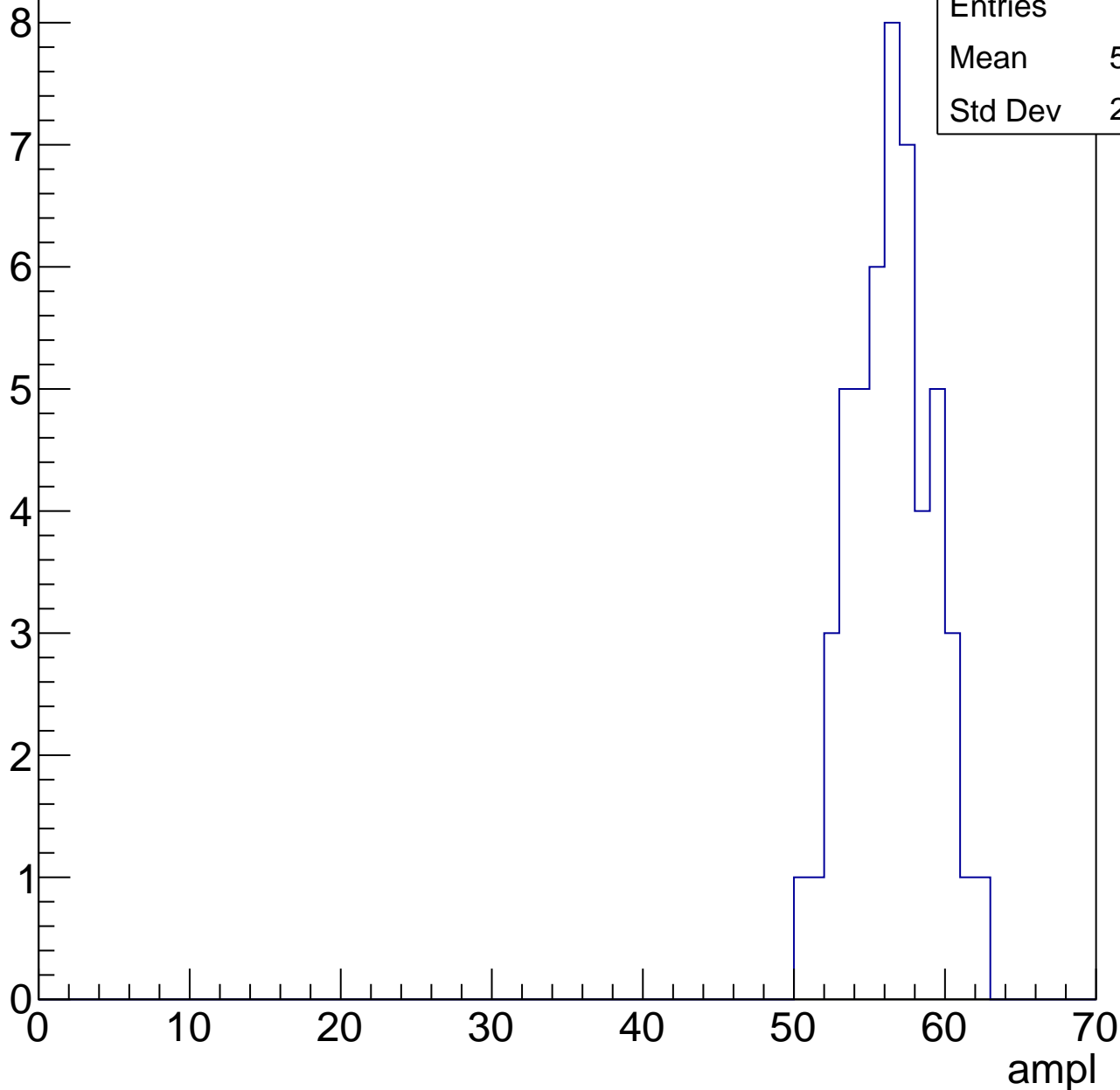


B1L102S, U22-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.98
Std Dev	2.672

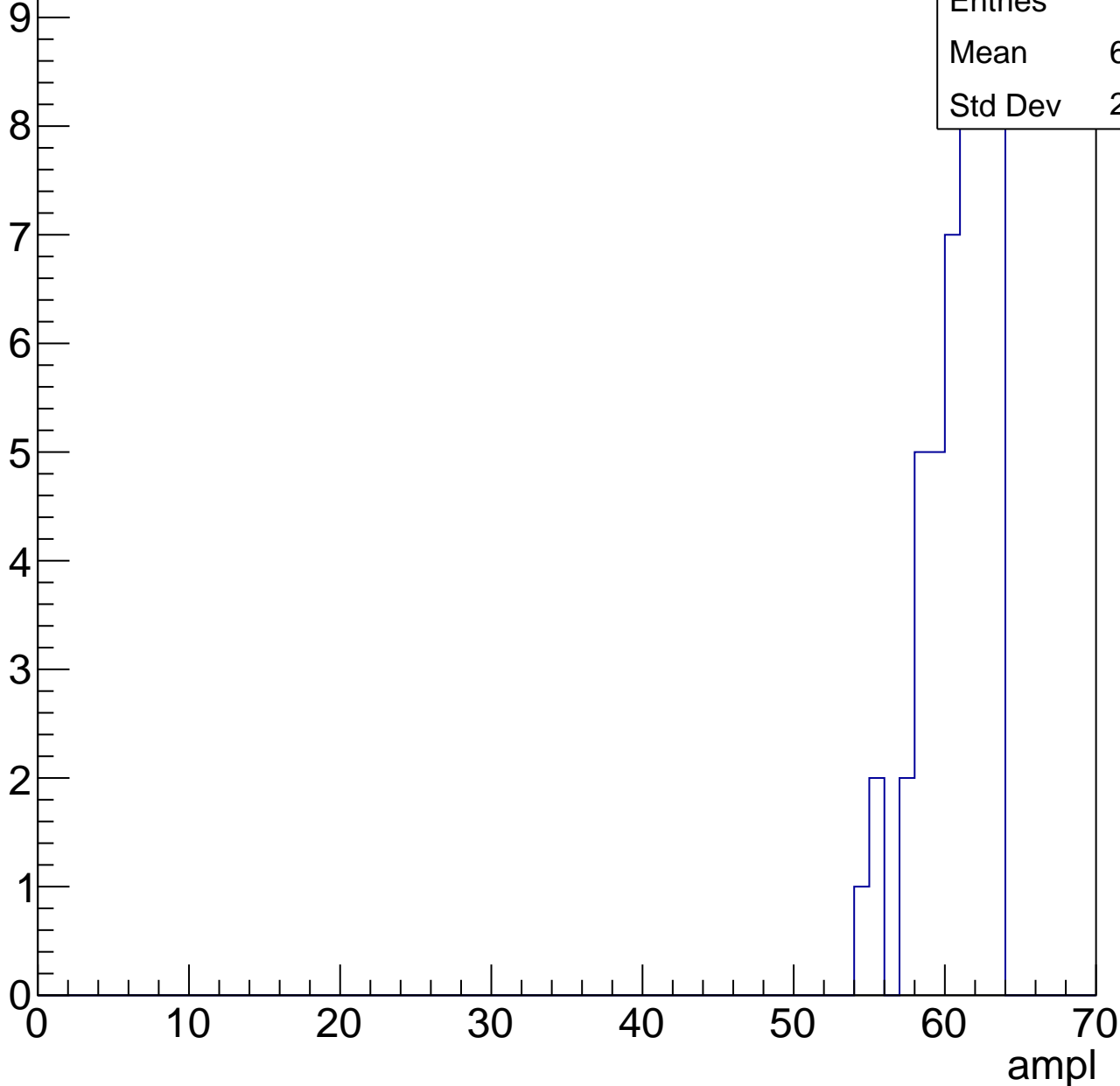


B1L102S, U22-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	60.33
Std Dev	2.276



B1L102S, U22-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L102S, U22-ch56, adc0

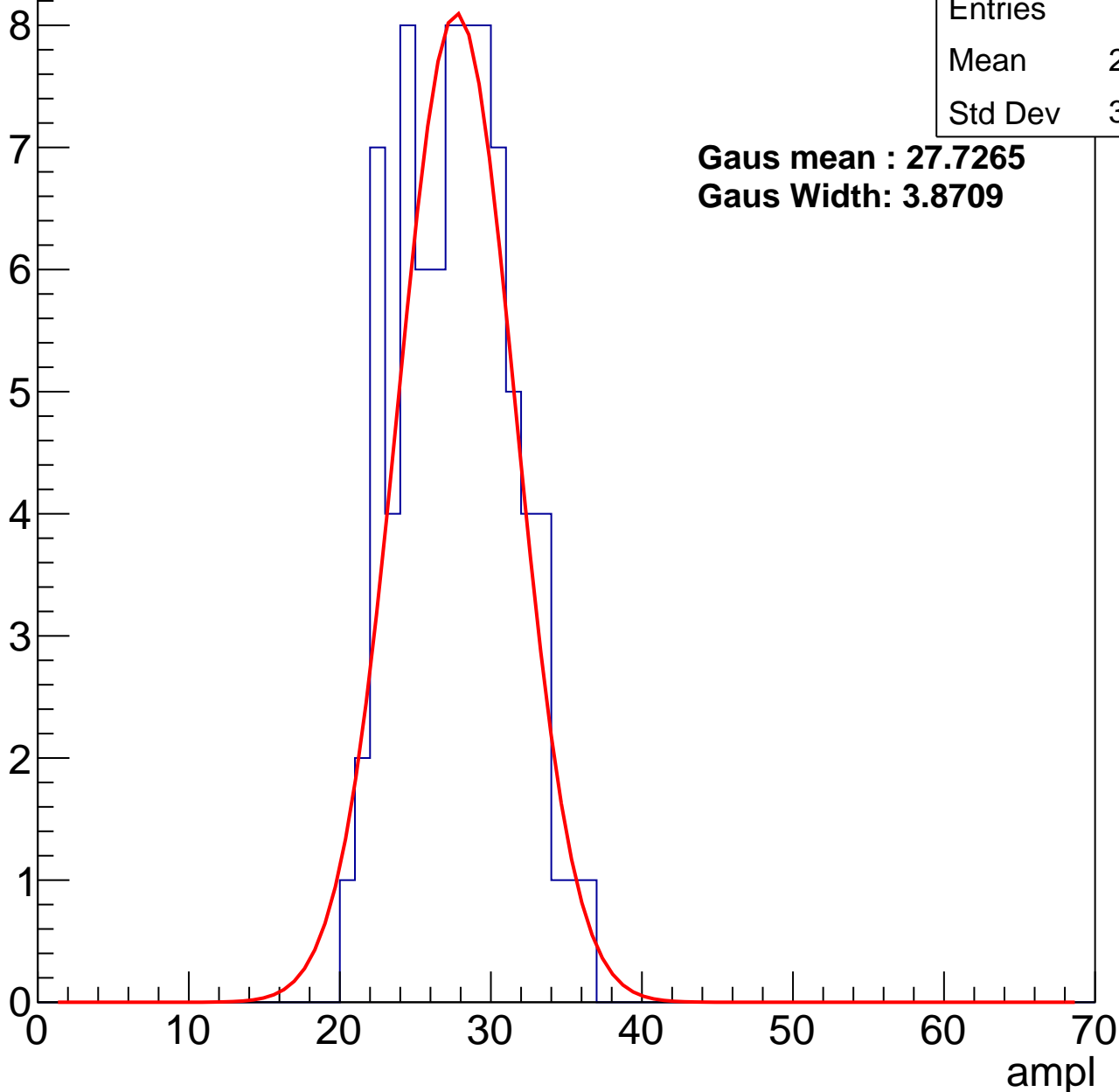
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.26
Std Dev	3.647

Gaus mean : 27.7265

Gaus Width: 3.8709



B1L102S, U22-ch56, adc1

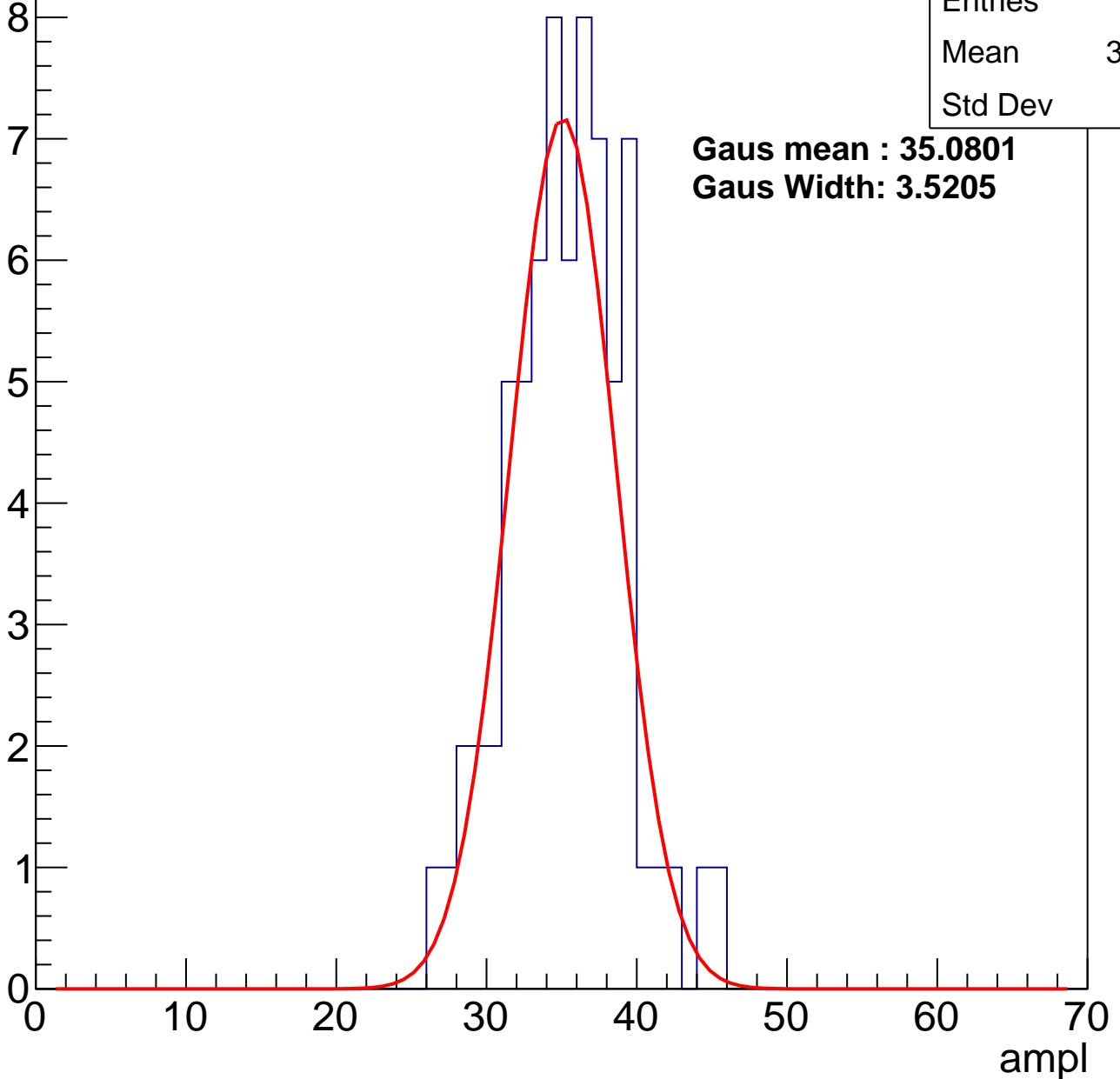
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.91
Std Dev	3.79

Gaus mean : 35.0801

Gaus Width: 3.5205



B1L102S, U22-ch56, adc2

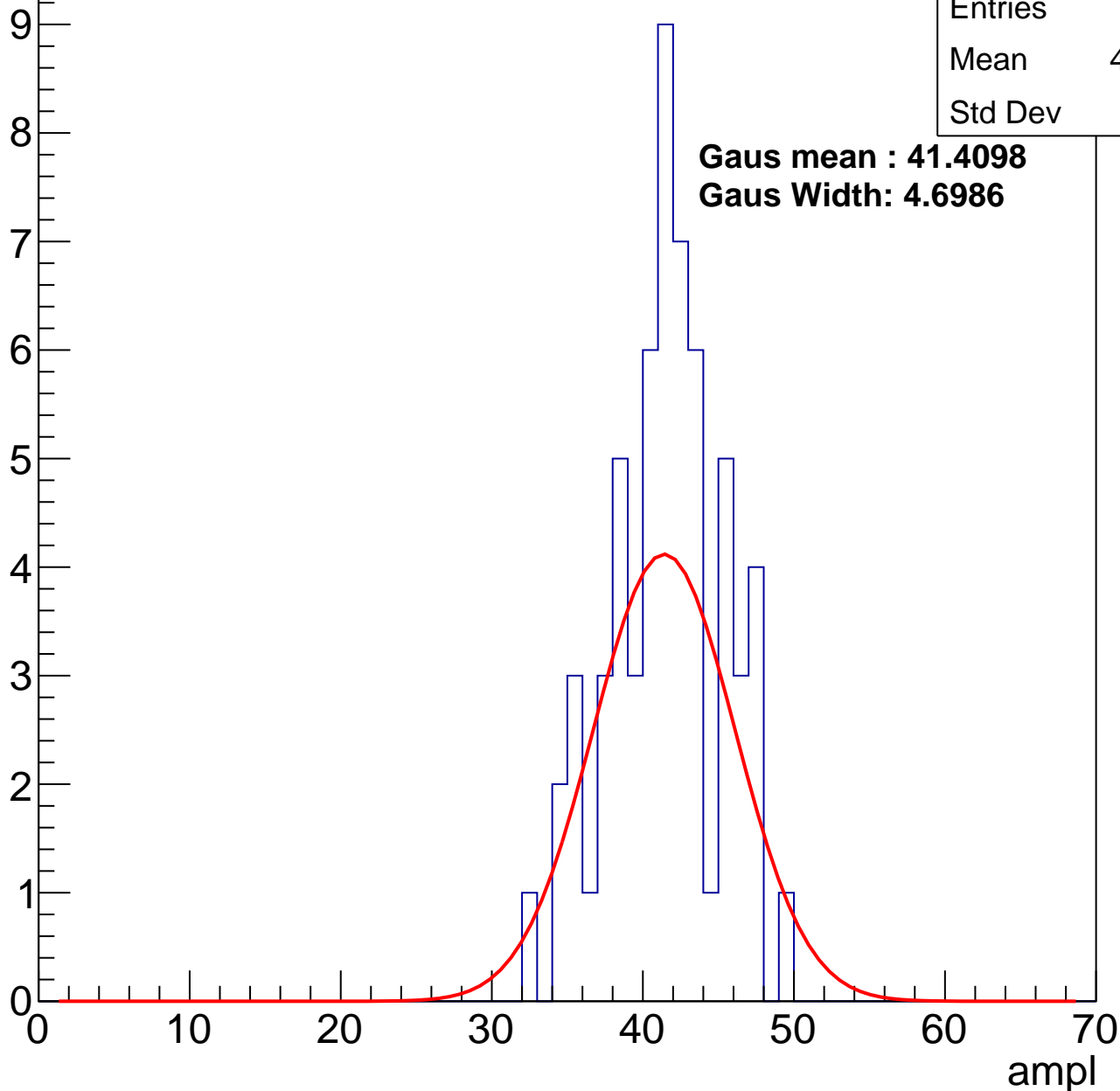
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.07
Std Dev	3.71

Gaus mean : 41.4098

Gaus Width: 4.6986

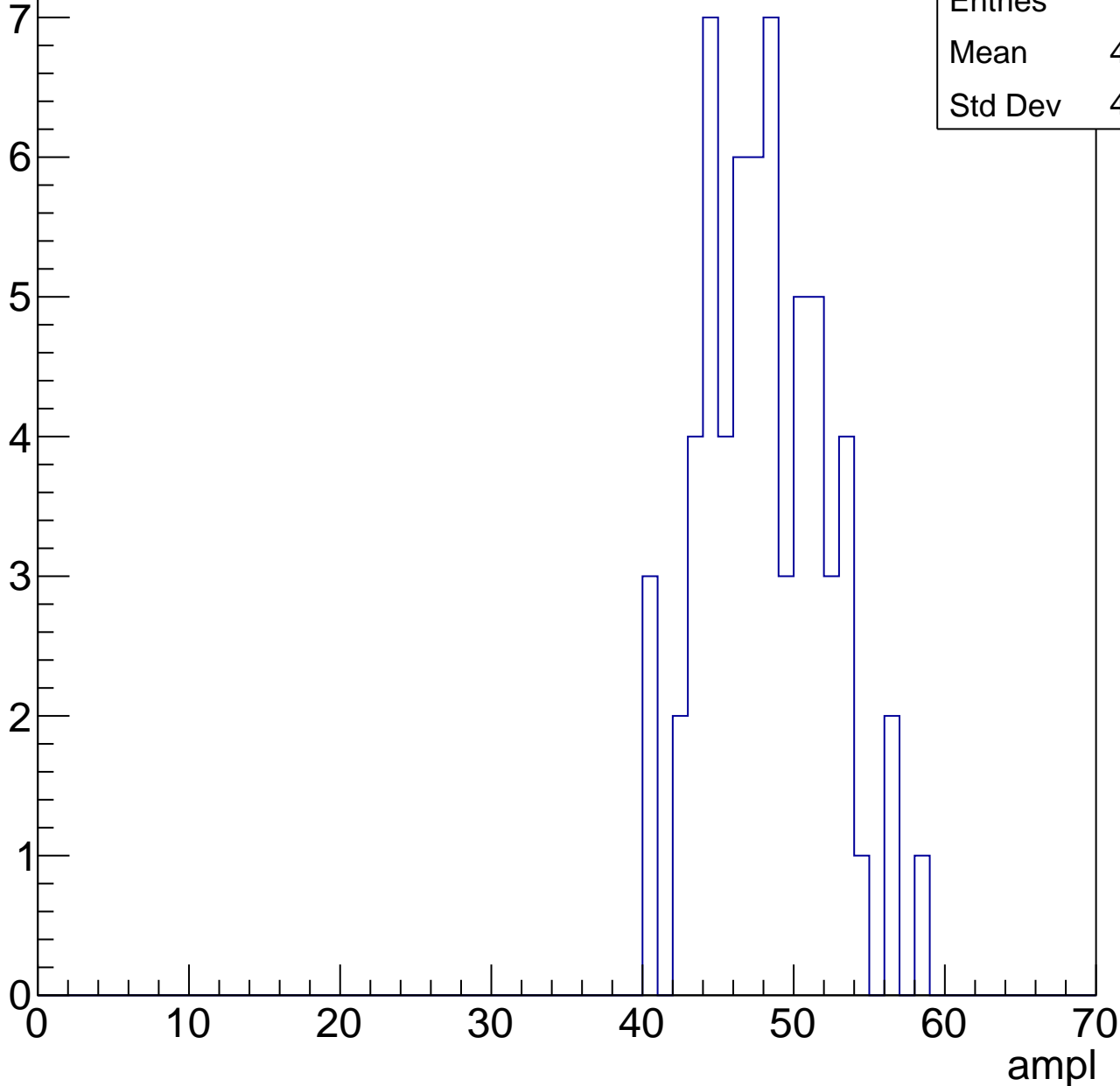


B1L102S, U22-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.65
Std Dev	4.013

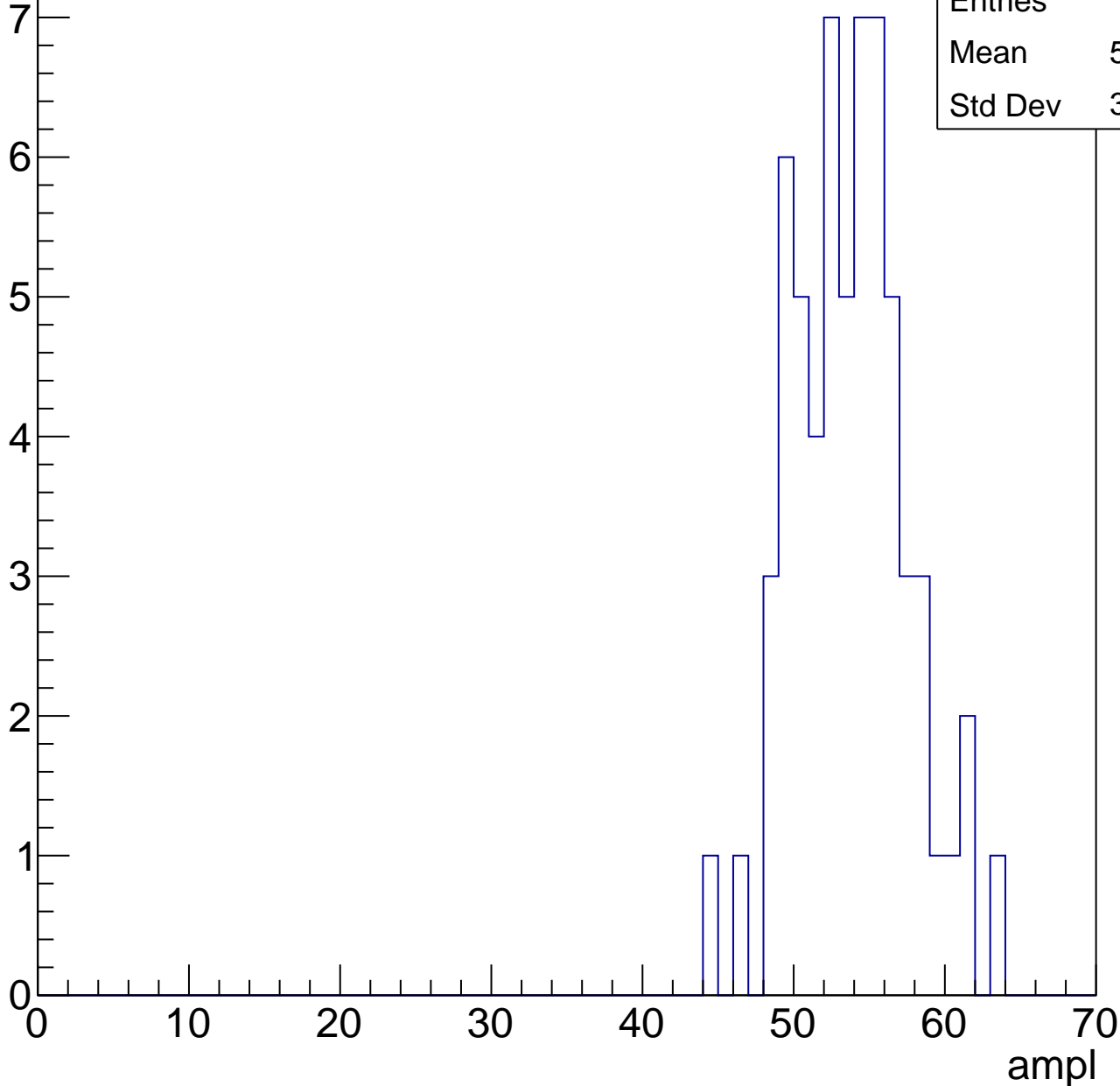


B1L102S, U22-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	53.27
Std Dev	3.768

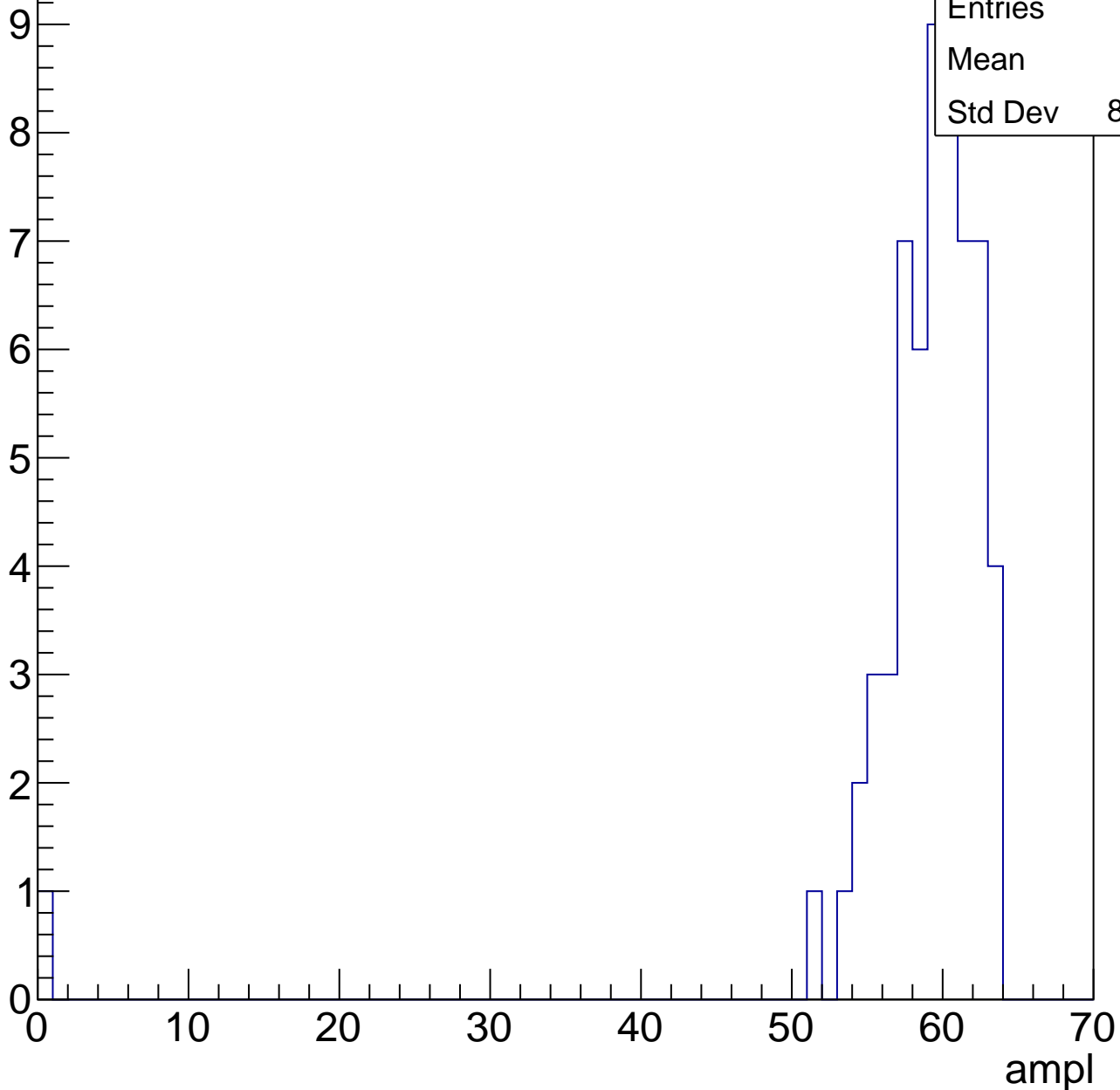


B1L102S, U22-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.9
Std Dev	8.058



B1L102S, U22-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch57, adc0

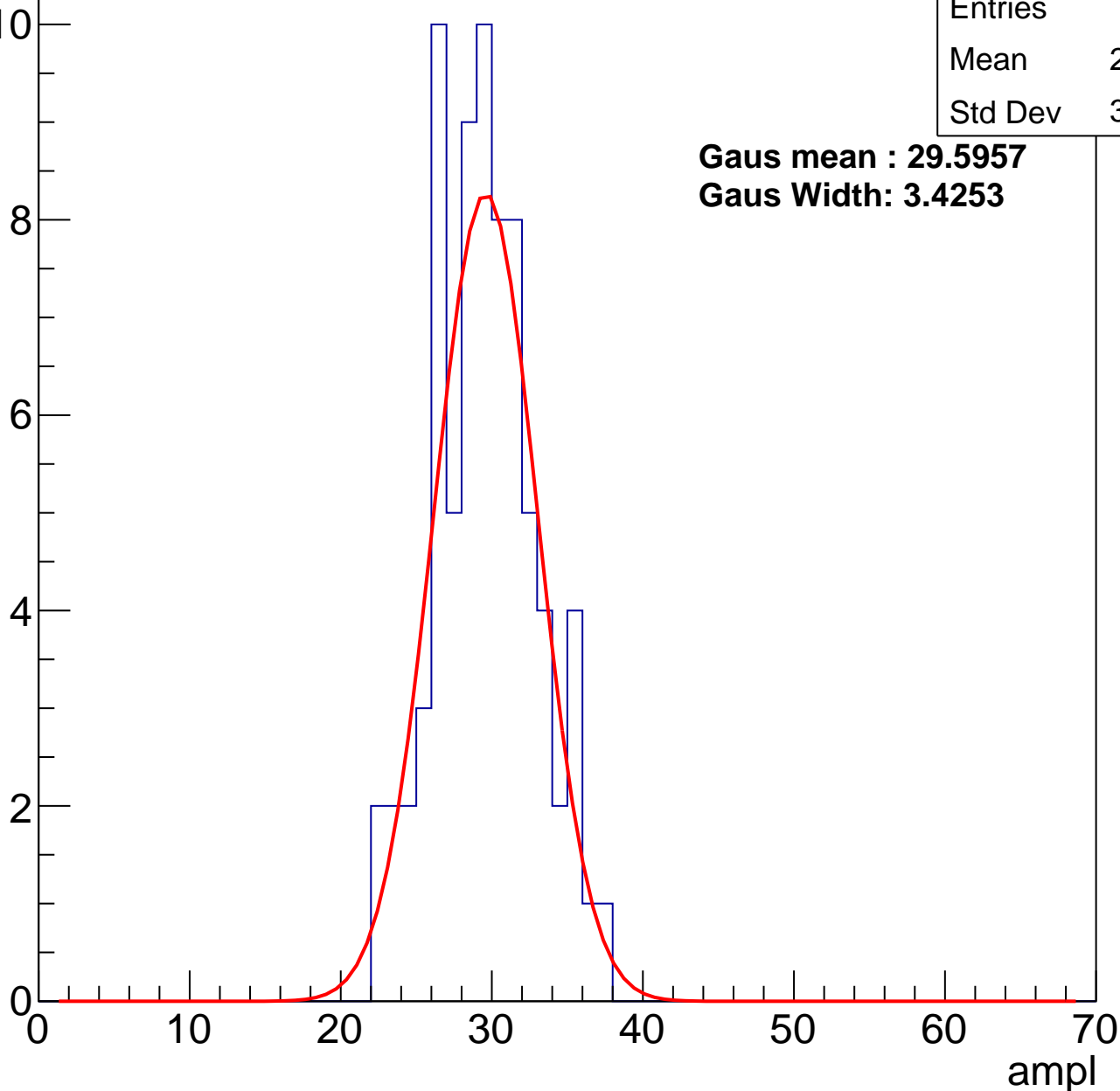
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.09
Std Dev	3.329

Gaus mean : 29.5957

Gaus Width: 3.4253



B1L102S, U22-ch57, adc1

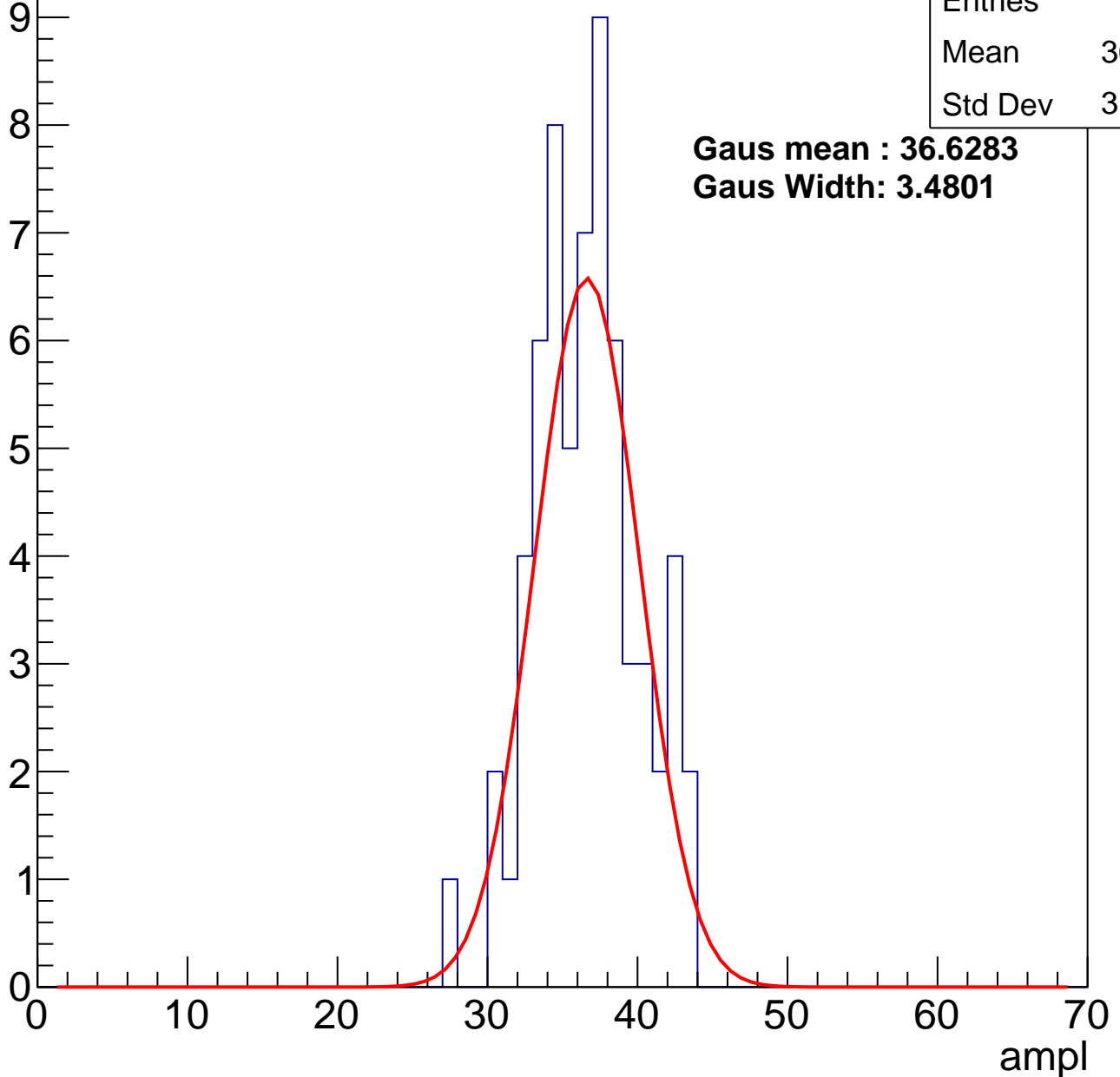
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.14
Std Dev	3.408

Gaus mean : 36.6283

Gaus Width: 3.4801



B1L102S, U22-ch57, adc2

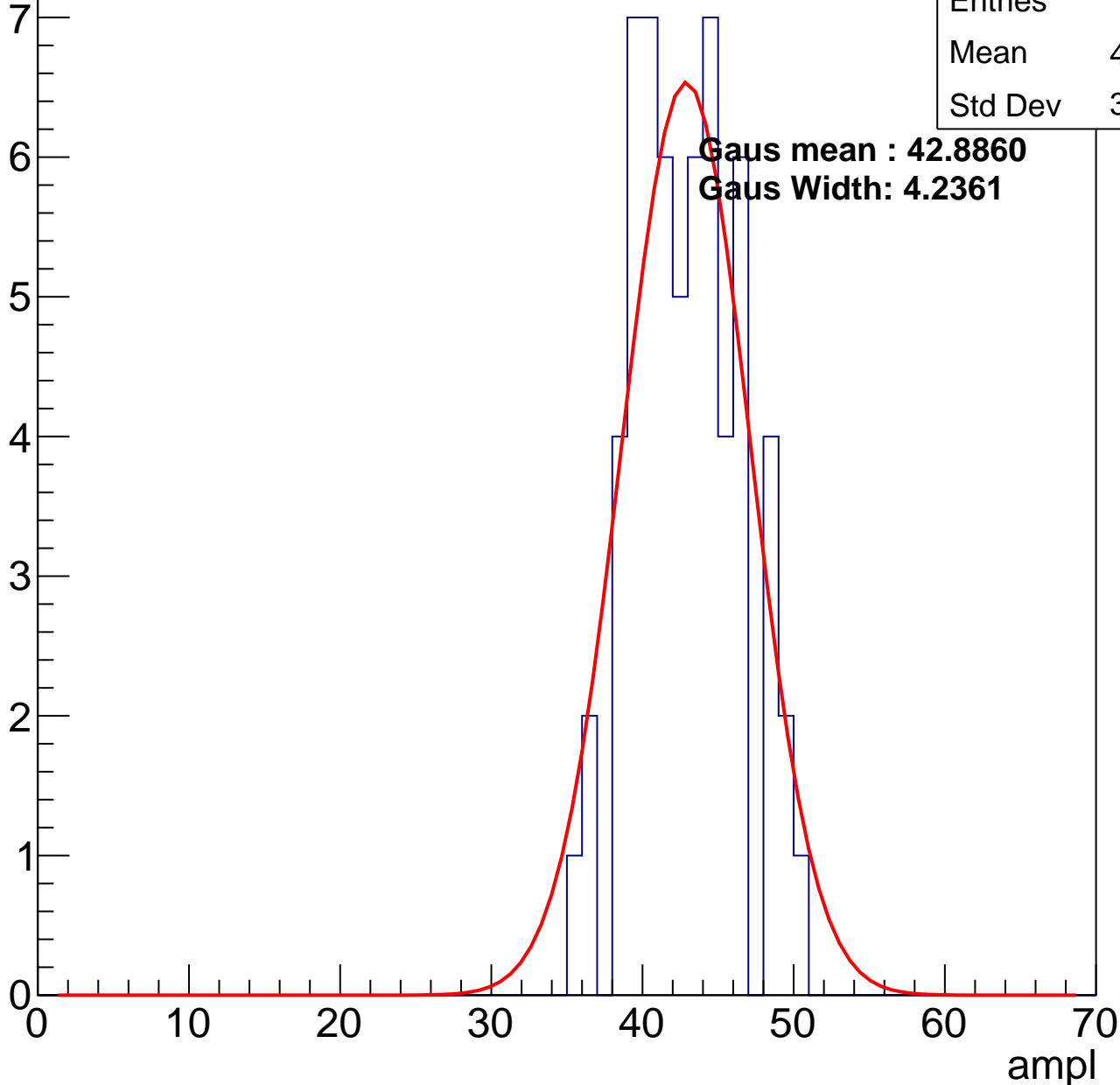
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.42
Std Dev	3.462

Gaus mean : 42.8860

Gaus Width: 4.2361

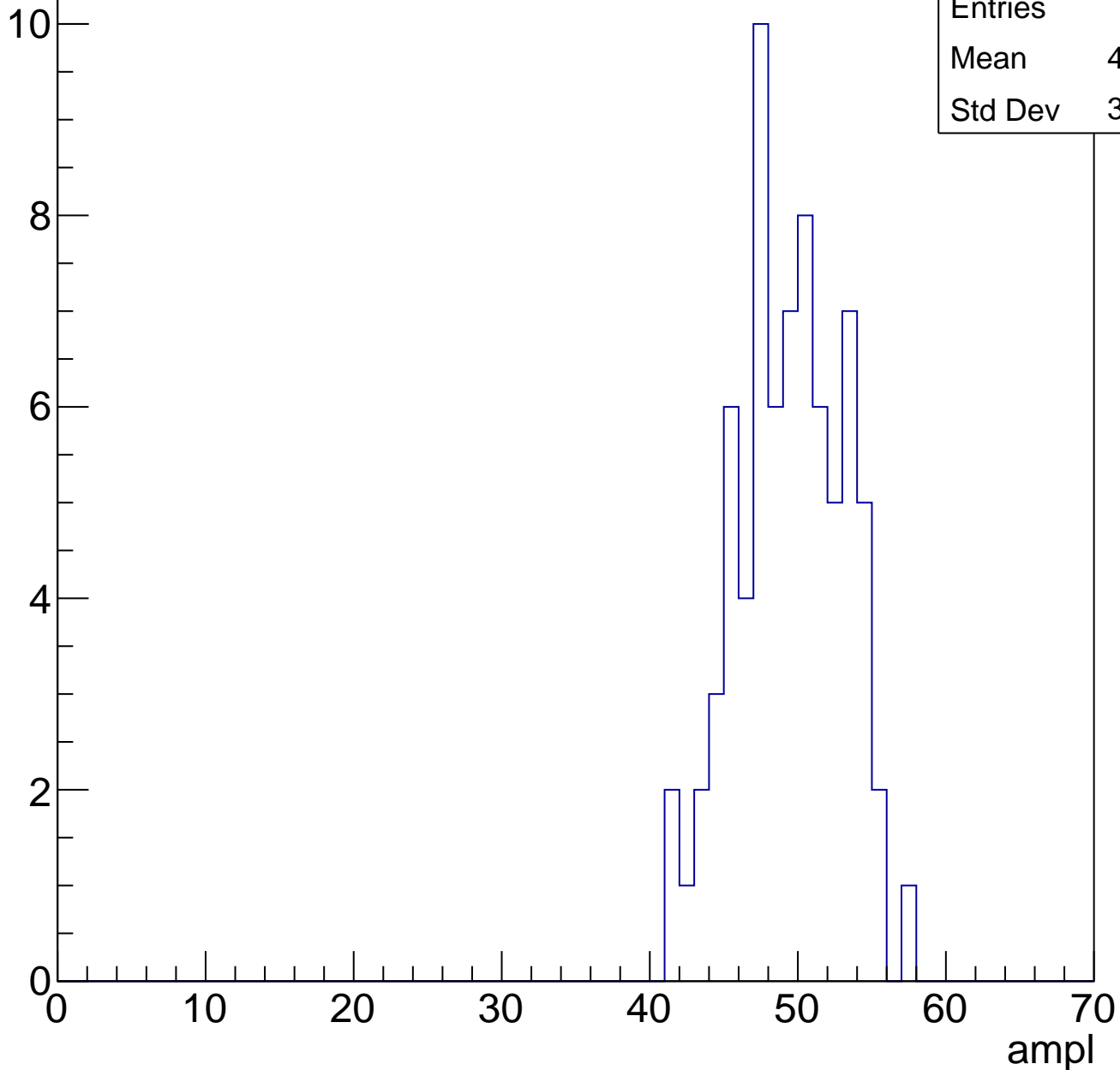


B1L102S, U22-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	48.95
Std Dev	3.562

Entry

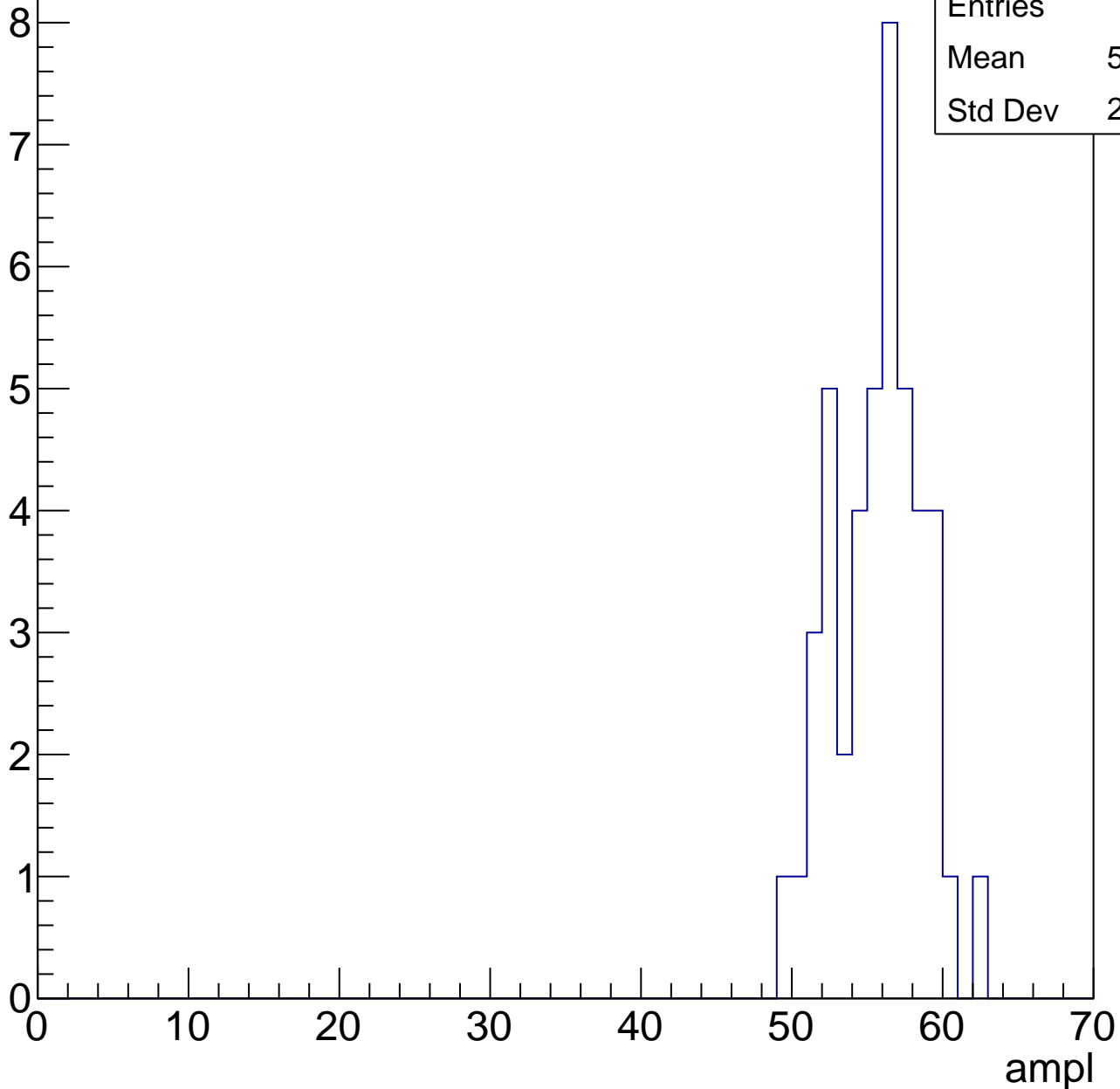


B1L102S, U22-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	55.27
Std Dev	2.879

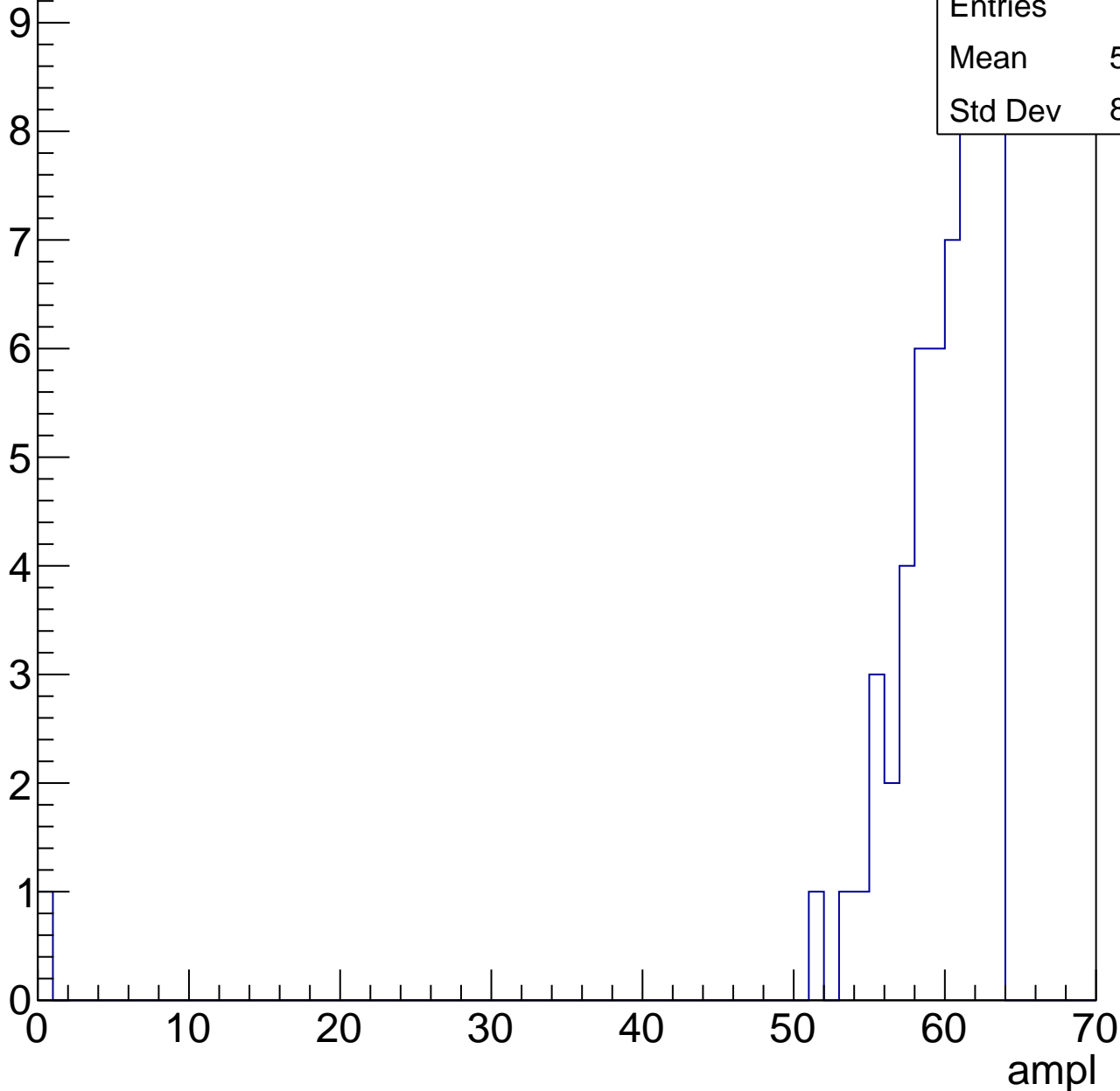


B1L102S, U22-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

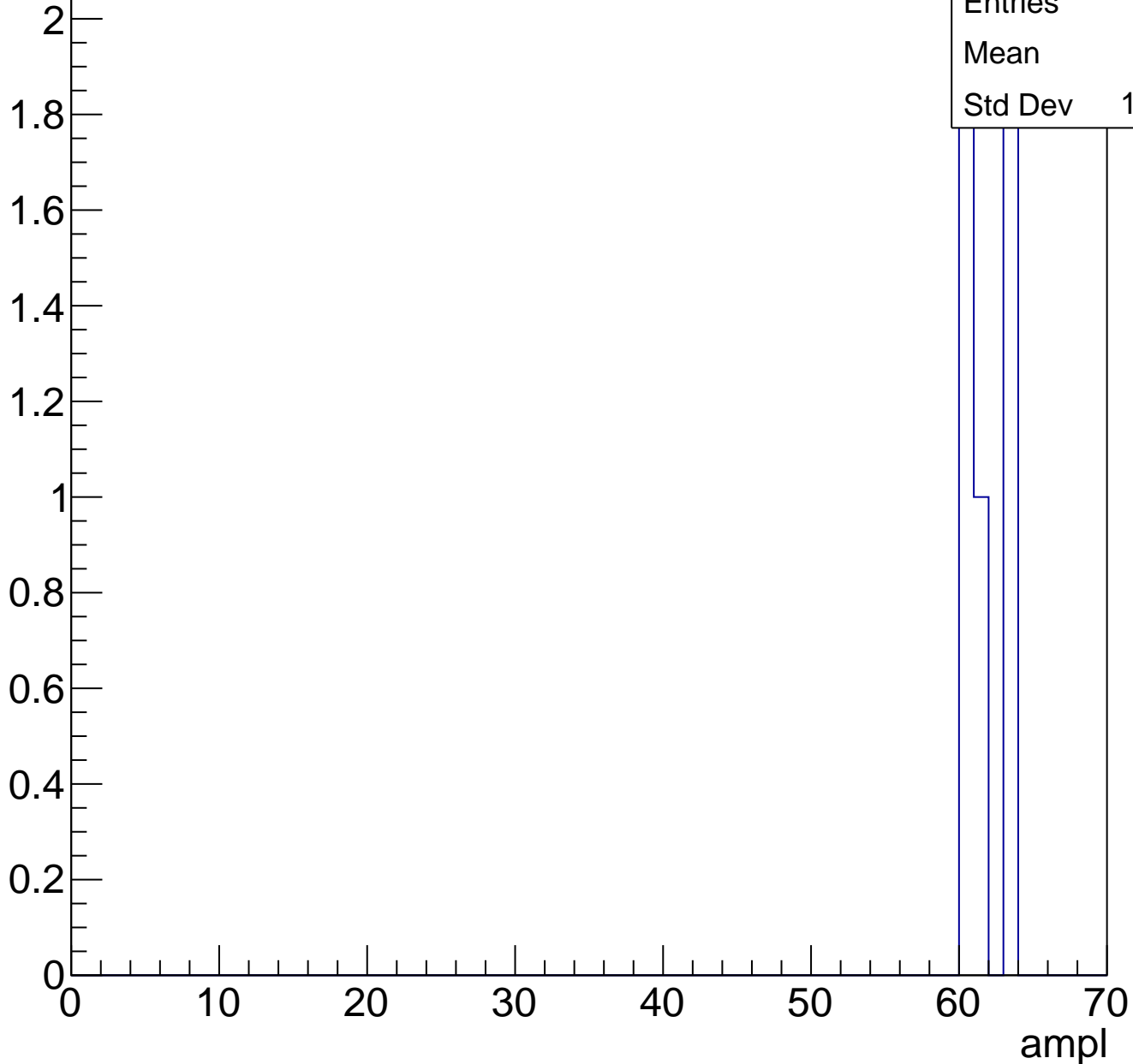
Entries	58
Mean	58.55
Std Dev	8.236



B1L102S, U22-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch58, adc0

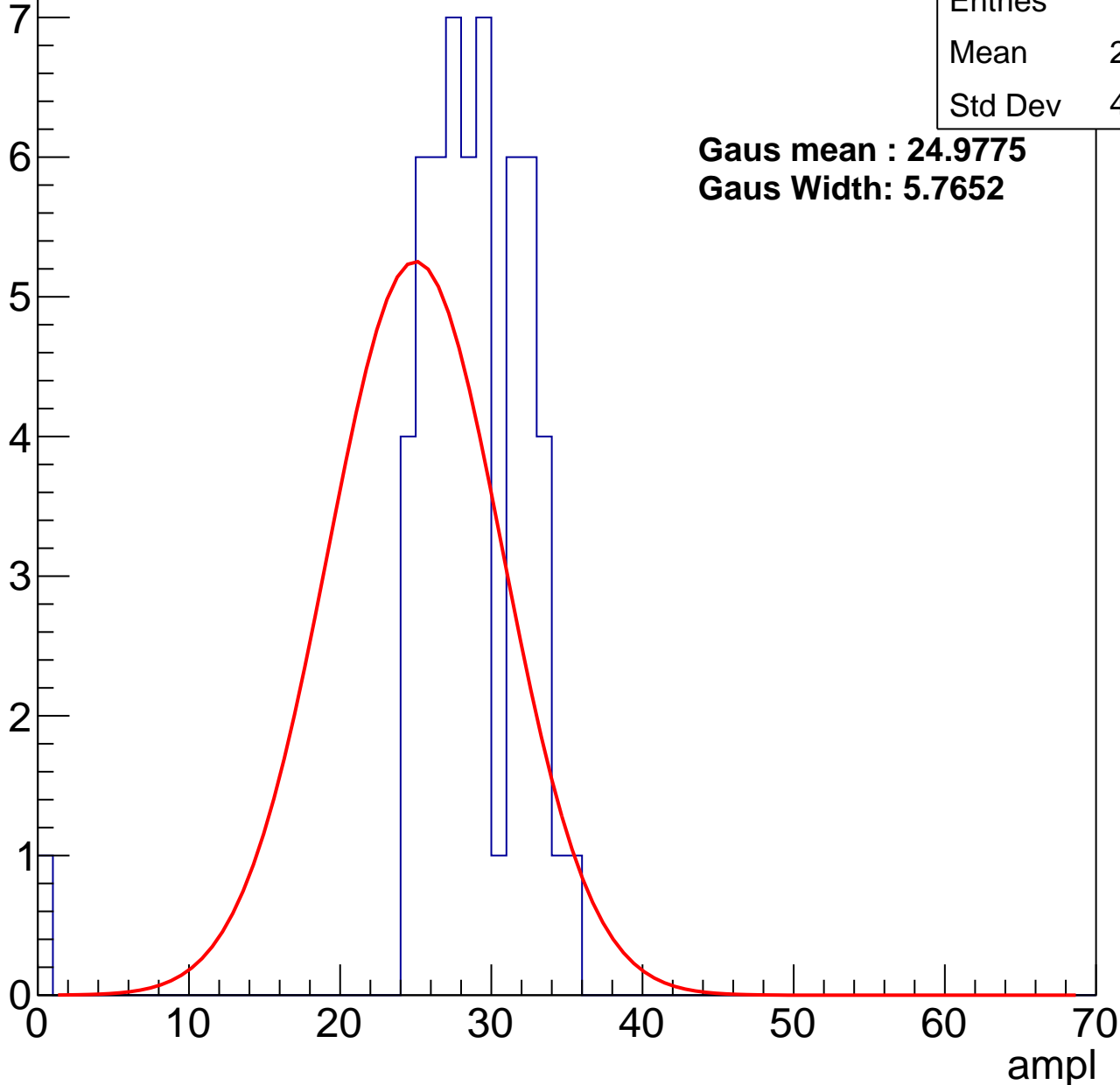
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.05
Std Dev	4.779

Gaus mean : 24.9775

Gaus Width: 5.7652



B1L102S, U22-ch58, adc1

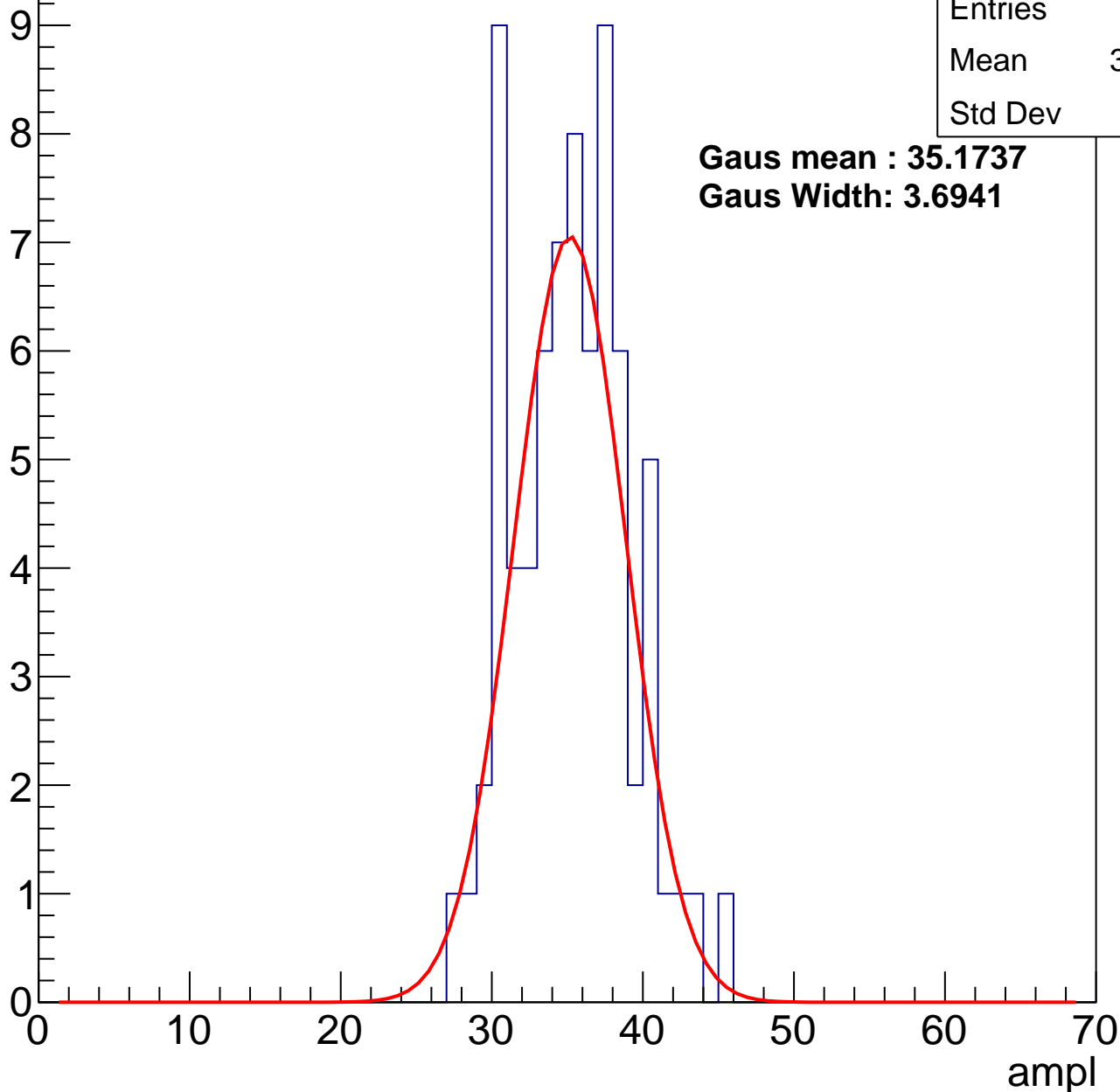
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.82
Std Dev	3.75

Gaus mean : 35.1737

Gaus Width: 3.6941



B1L102S, U22-ch58, adc2

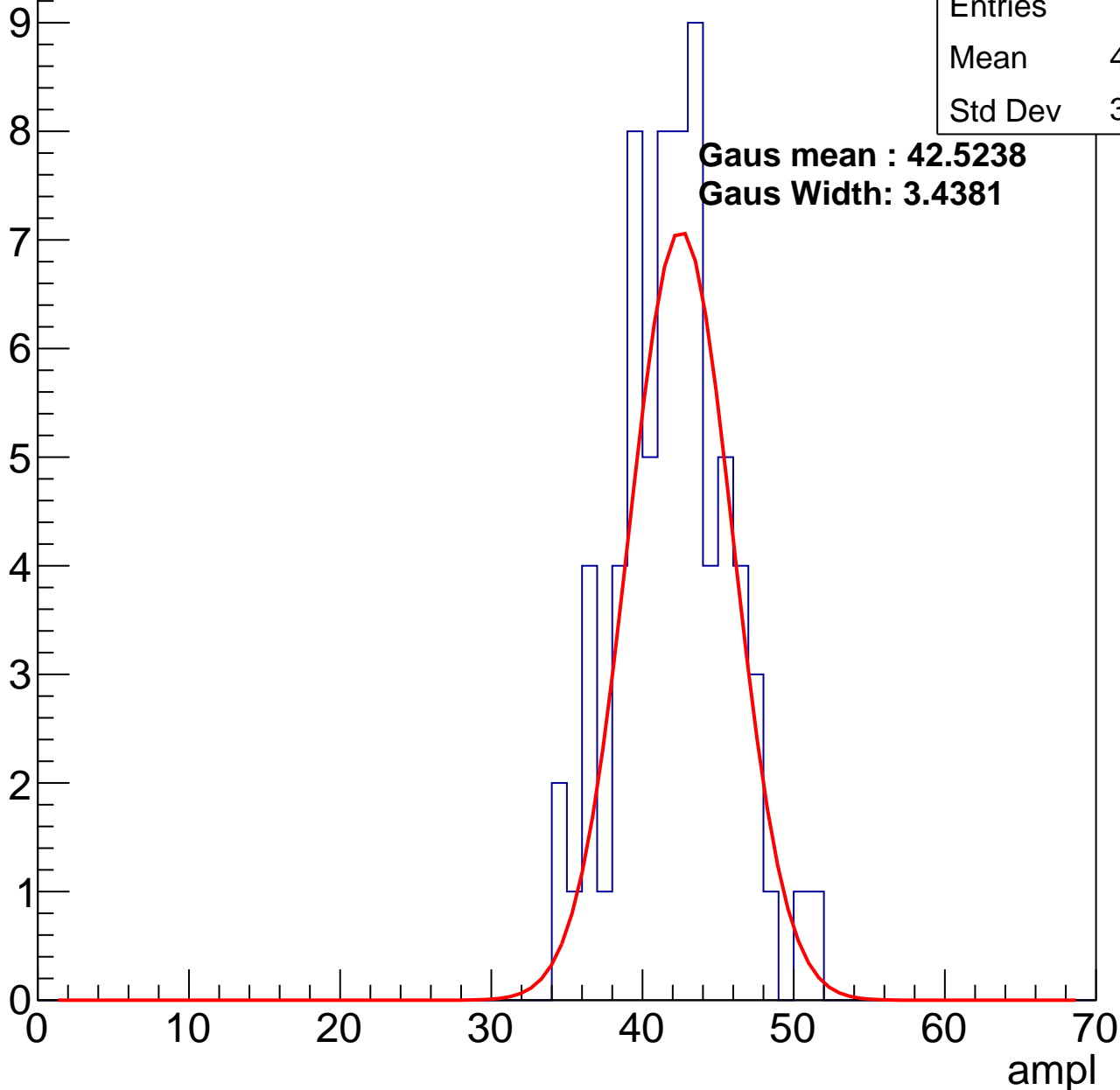
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.65
Std Dev	3.595

Gaus mean : 42.5238

Gaus Width: 3.4381

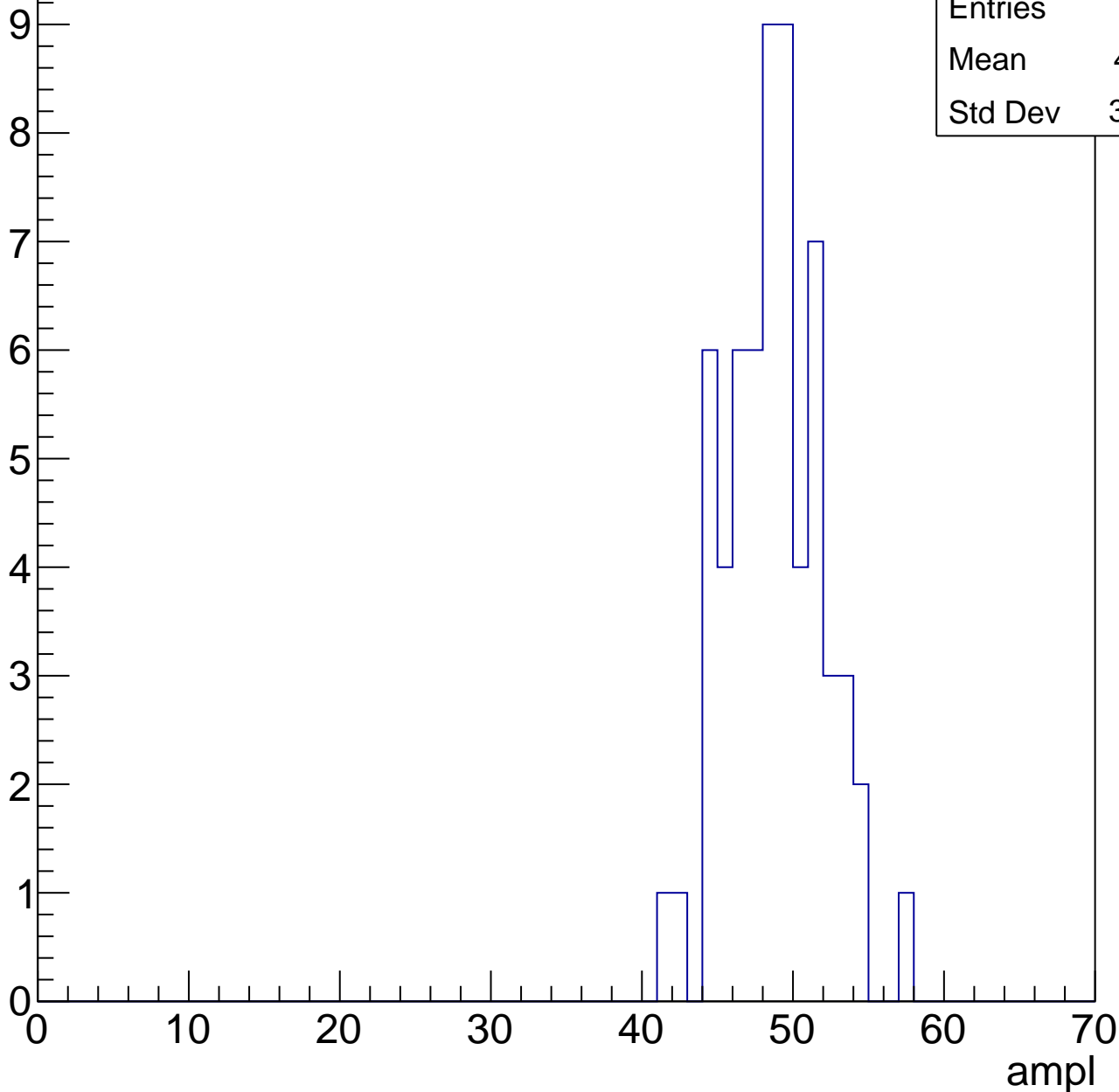


B1L102S, U22-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.31
Std Dev	3.124

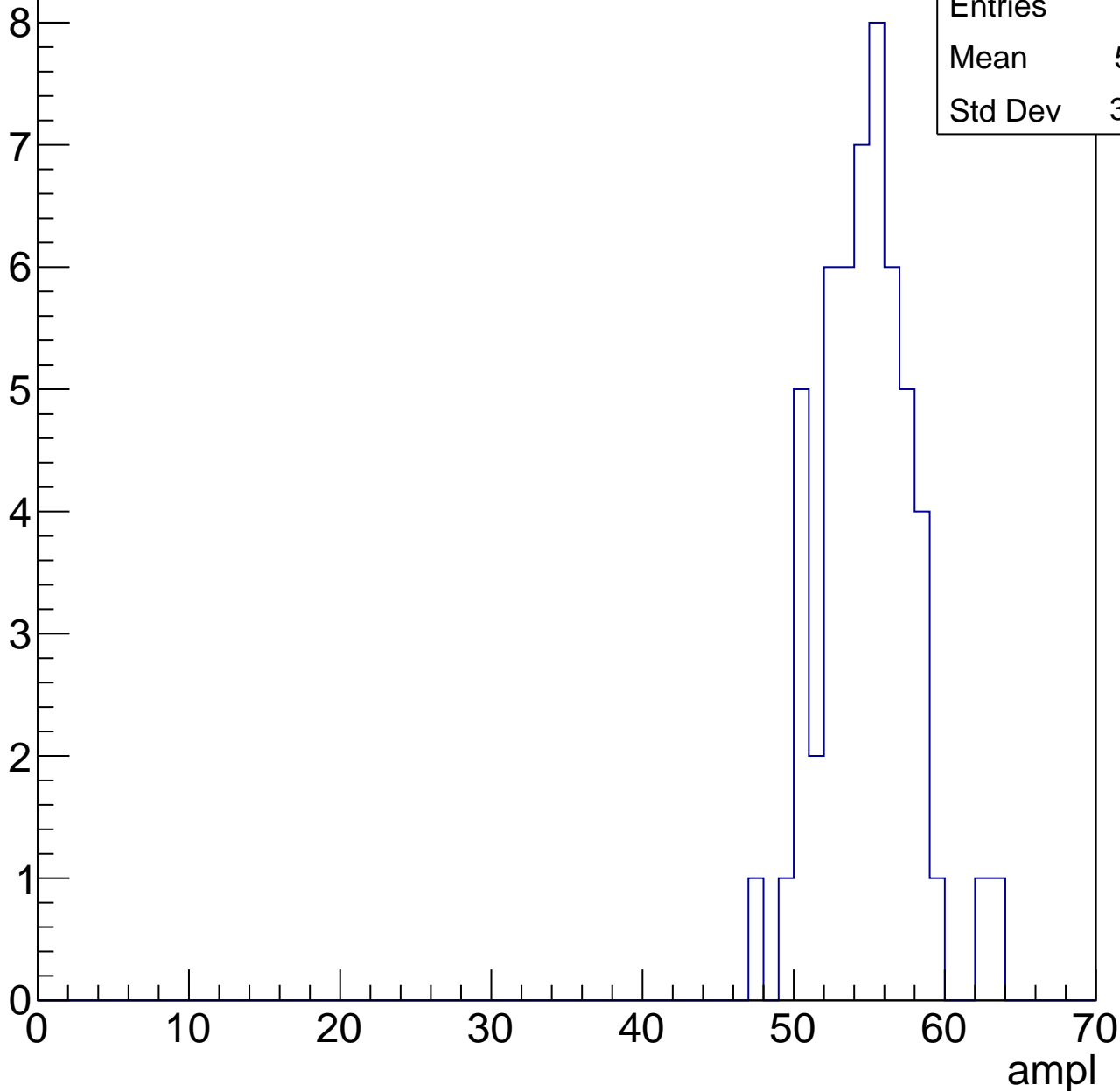


B1L102S, U22-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.31
Std Dev	3.066

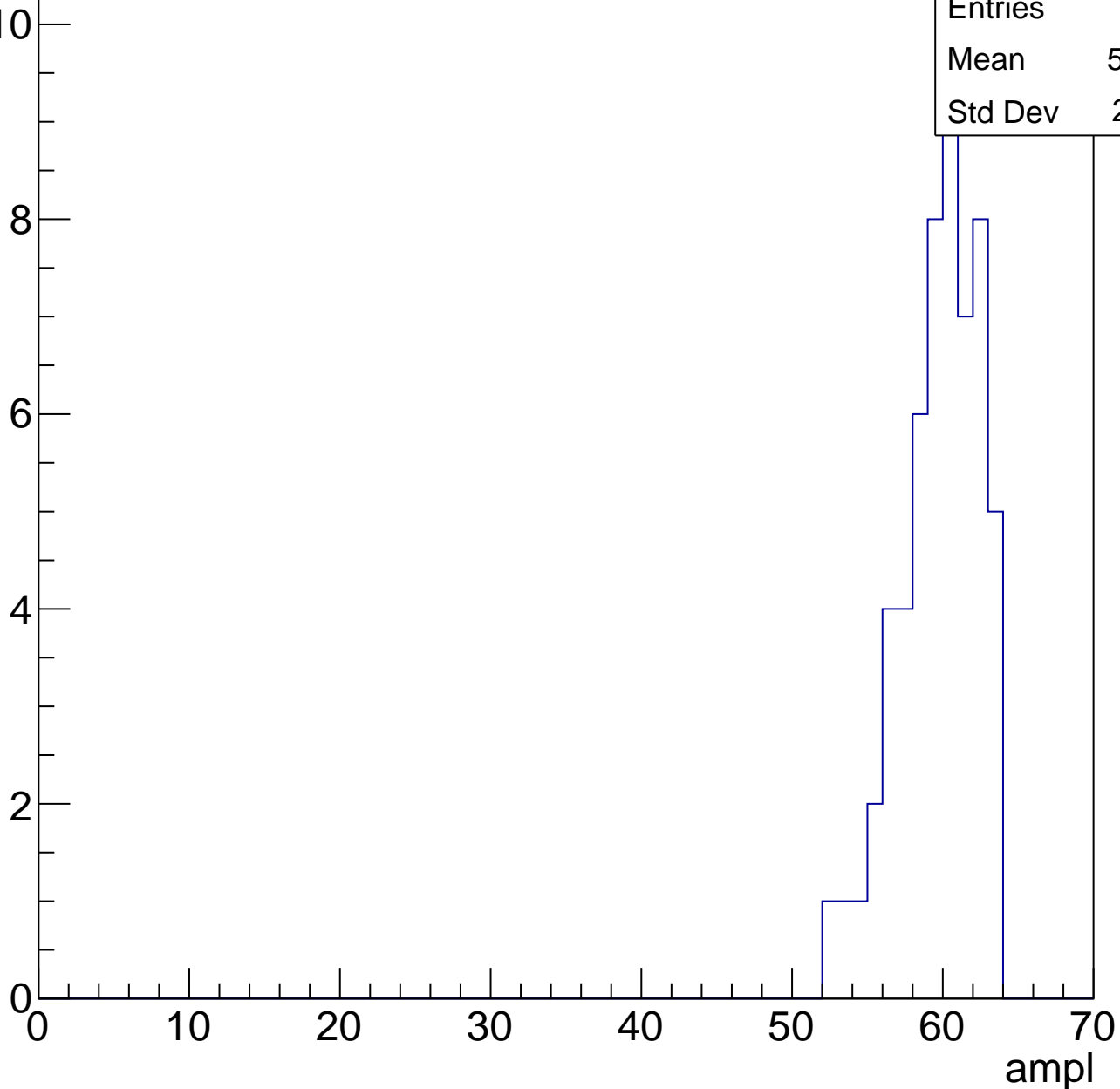


B1L102S, U22-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

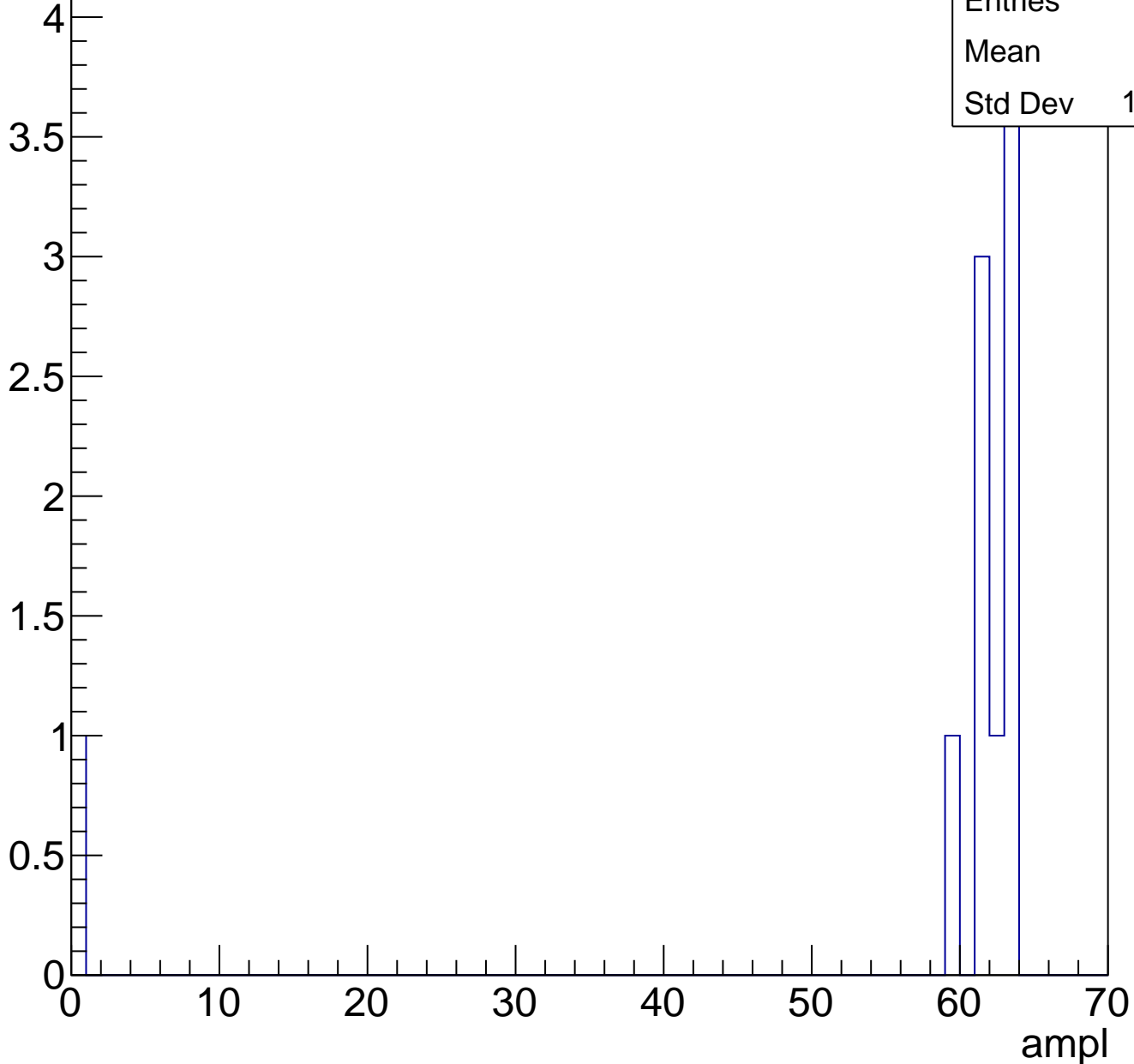
Entries	57
Mean	59.28
Std Dev	2.601



B1L102S, U22-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

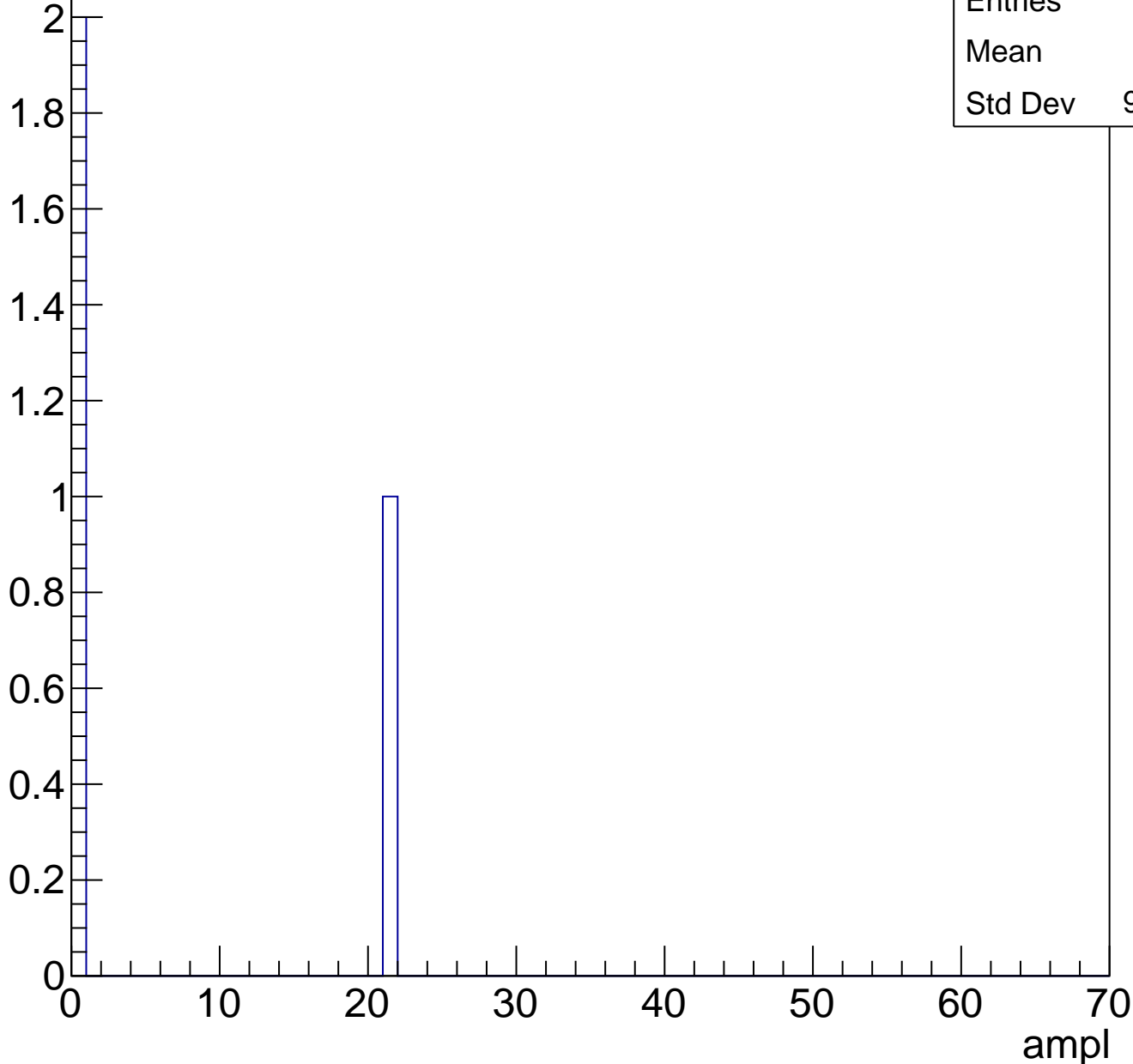
Entry



B1L102S, U22-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L102S, U22-ch59, adc0

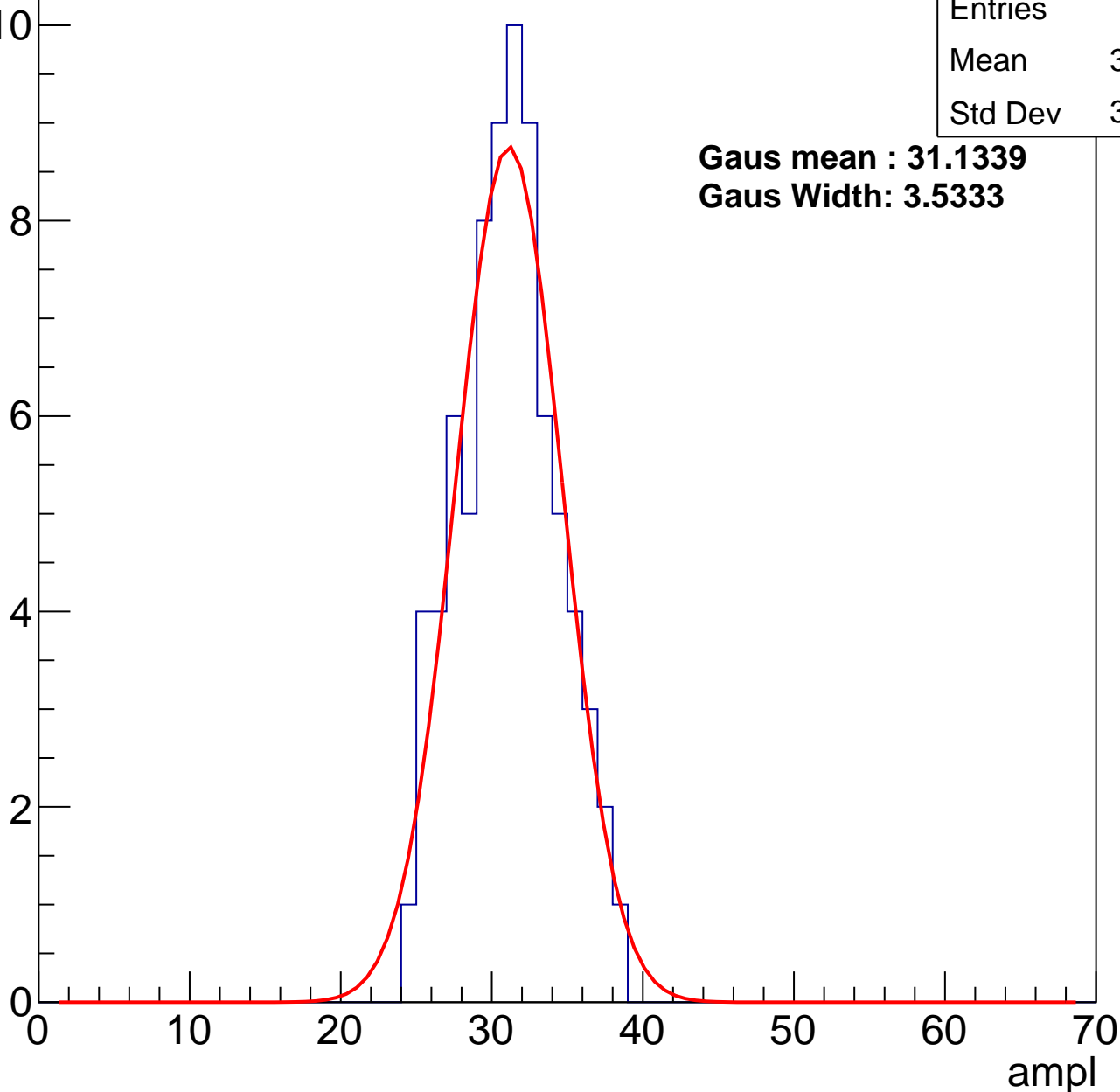
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	30.62
Std Dev	3.227

Gaus mean : 31.1339

Gaus Width: 3.5333



B1L102S, U22-ch59, adc1

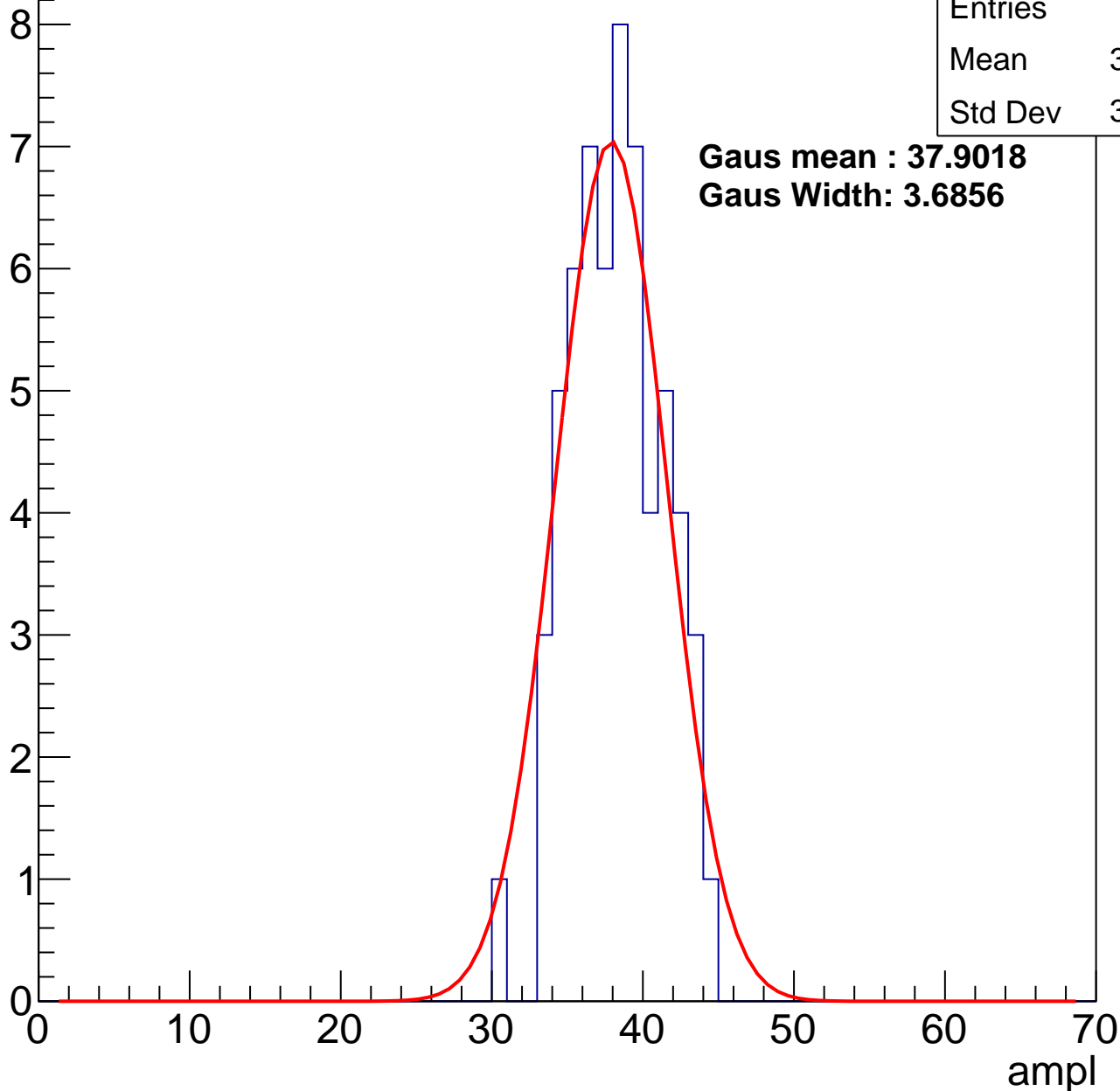
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	37.77
Std Dev	3.019

Gaus mean : 37.9018

Gaus Width: 3.6856



B1L102S, U22-ch59, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	44.7
Std Dev	3.709

Gaus mean : 44.7446

Gaus Width: 3.5302

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

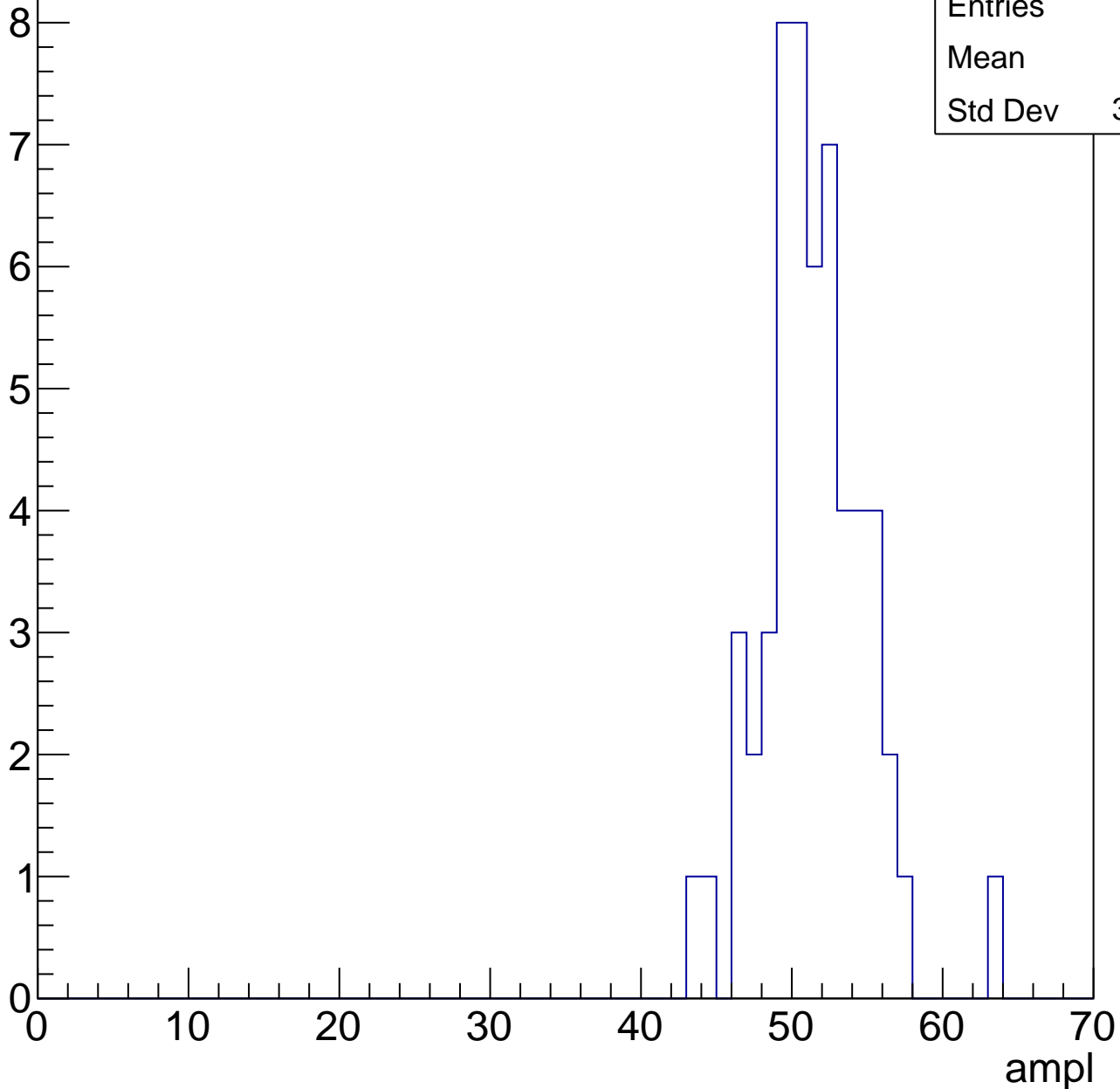
70

B1L102S, U22-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	51
Std Dev	3.411

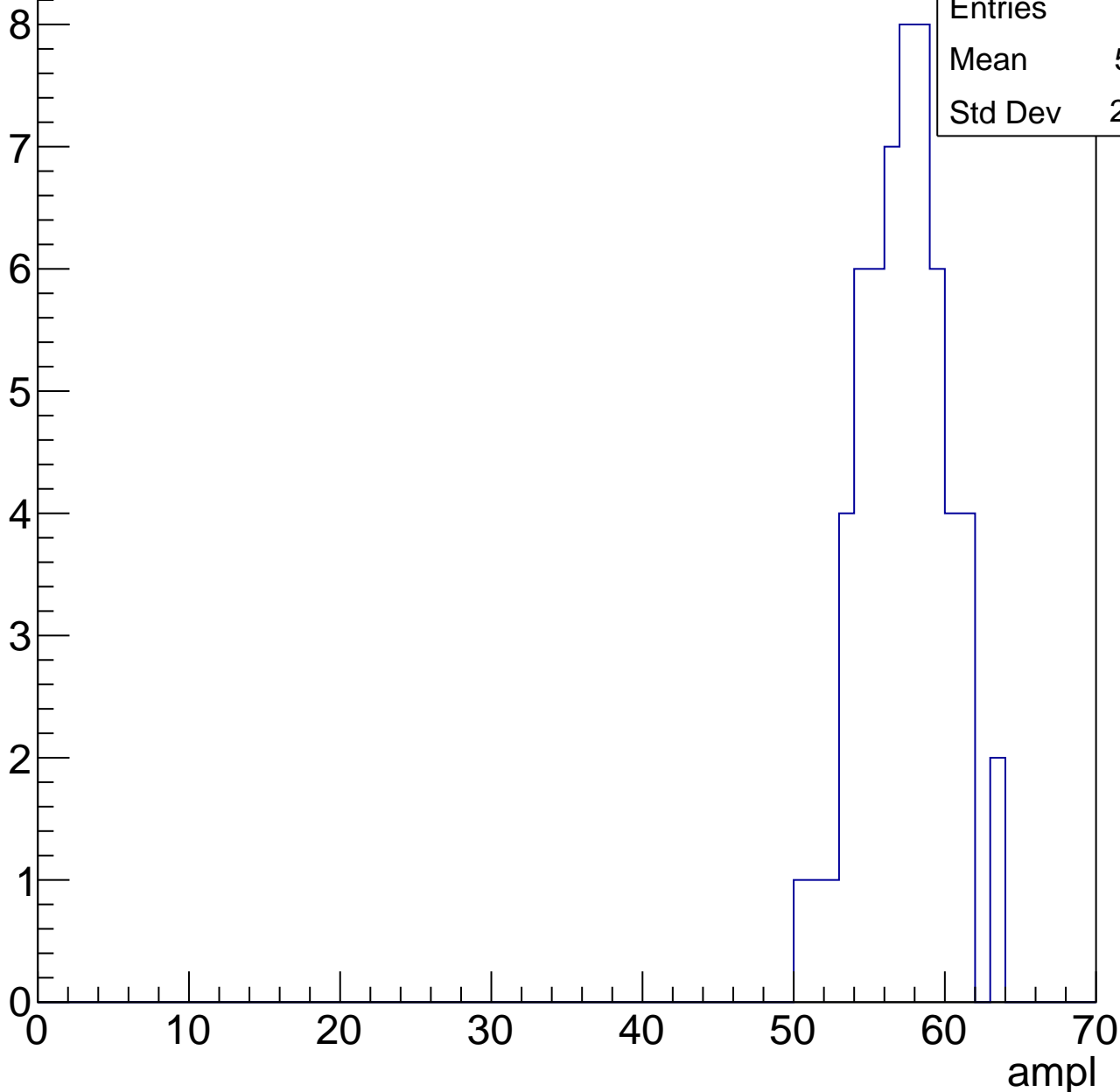


B1L102S, U22-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.81
Std Dev	2.819

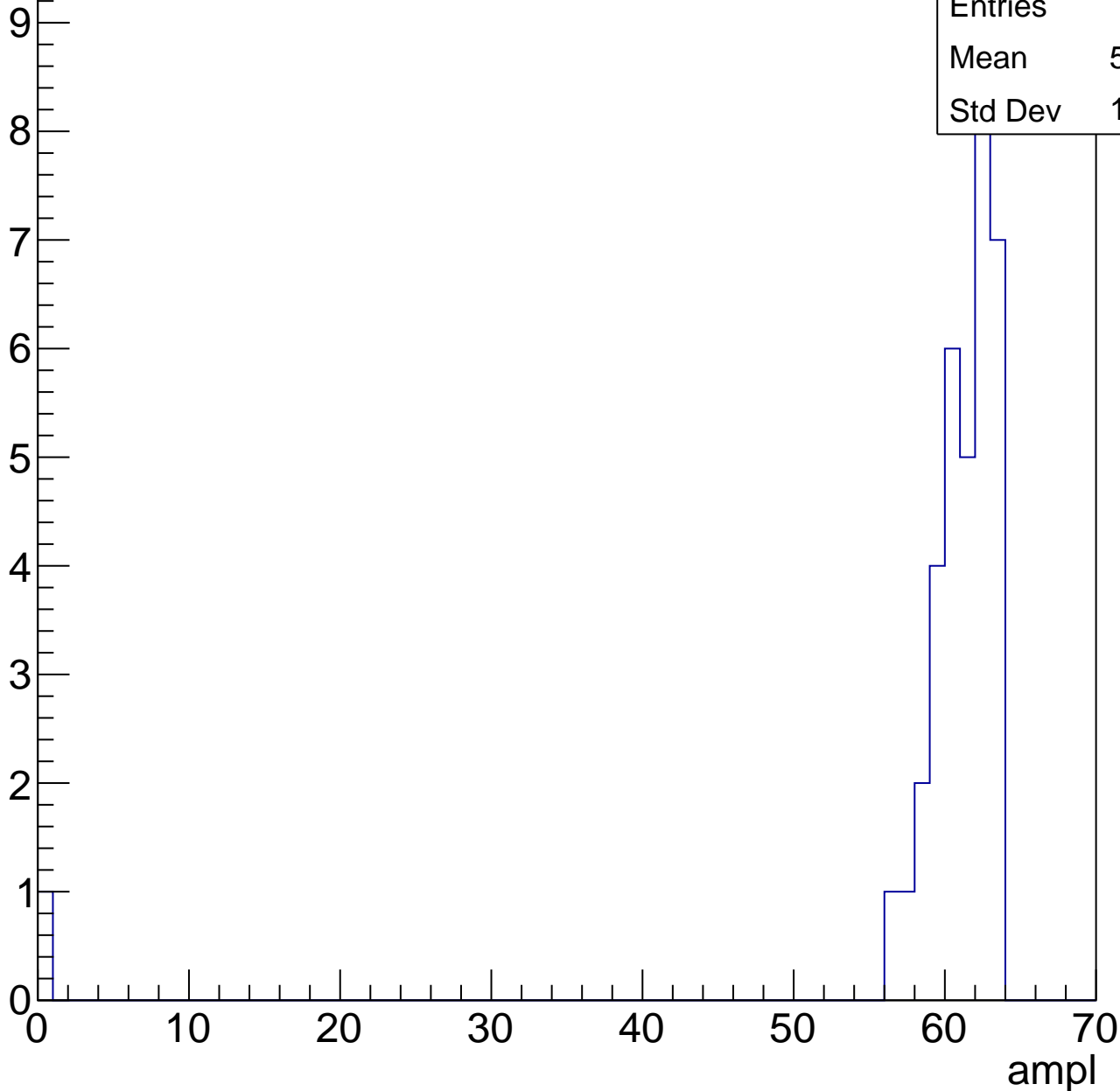


B1L102S, U22-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	59.14
Std Dev	10.16



B1L102S, U22-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch60, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	31.2
Std Dev	3.373

Gaus mean : 31.6016

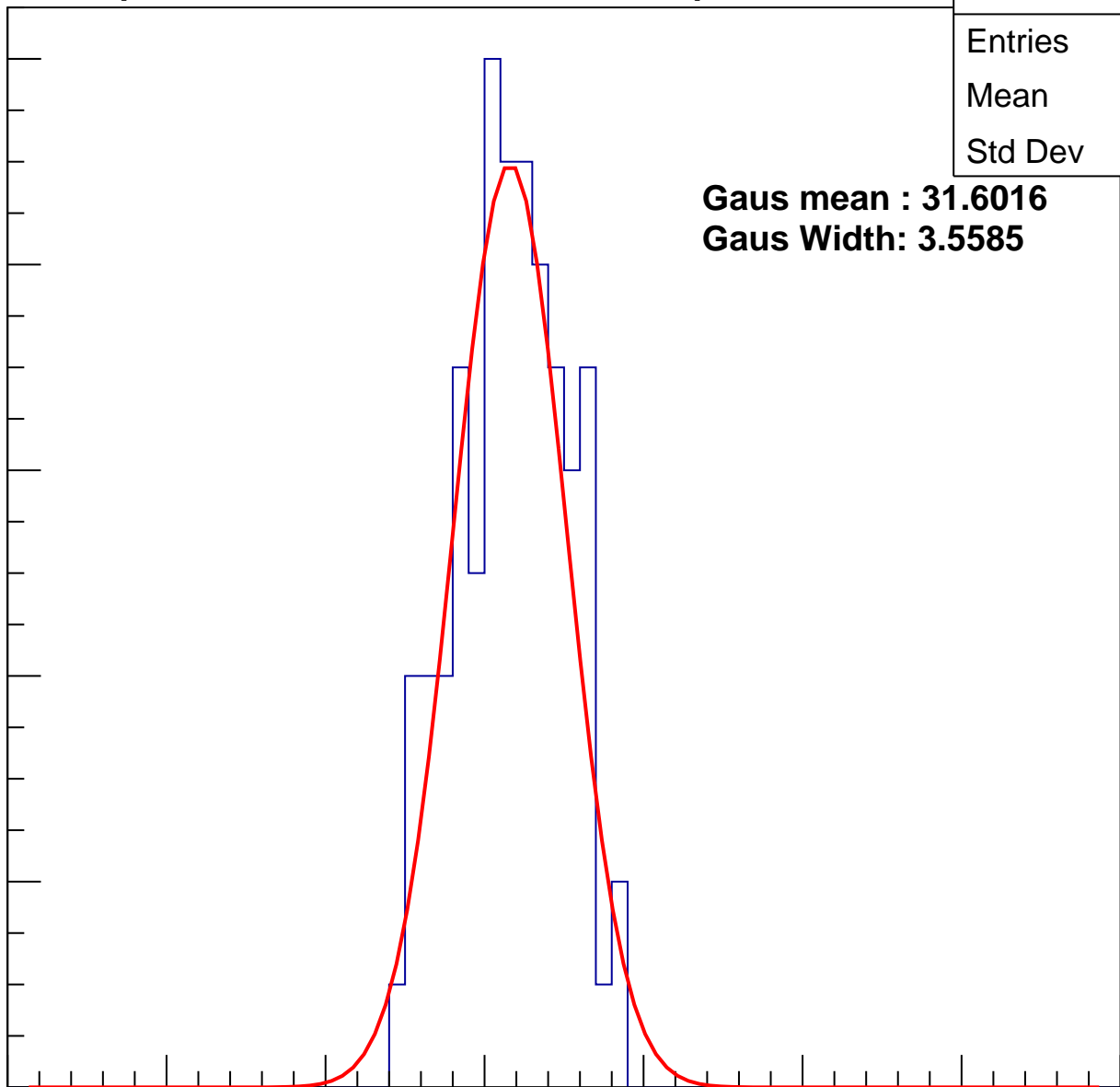
Gaus Width: 3.5585

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch60, adc1

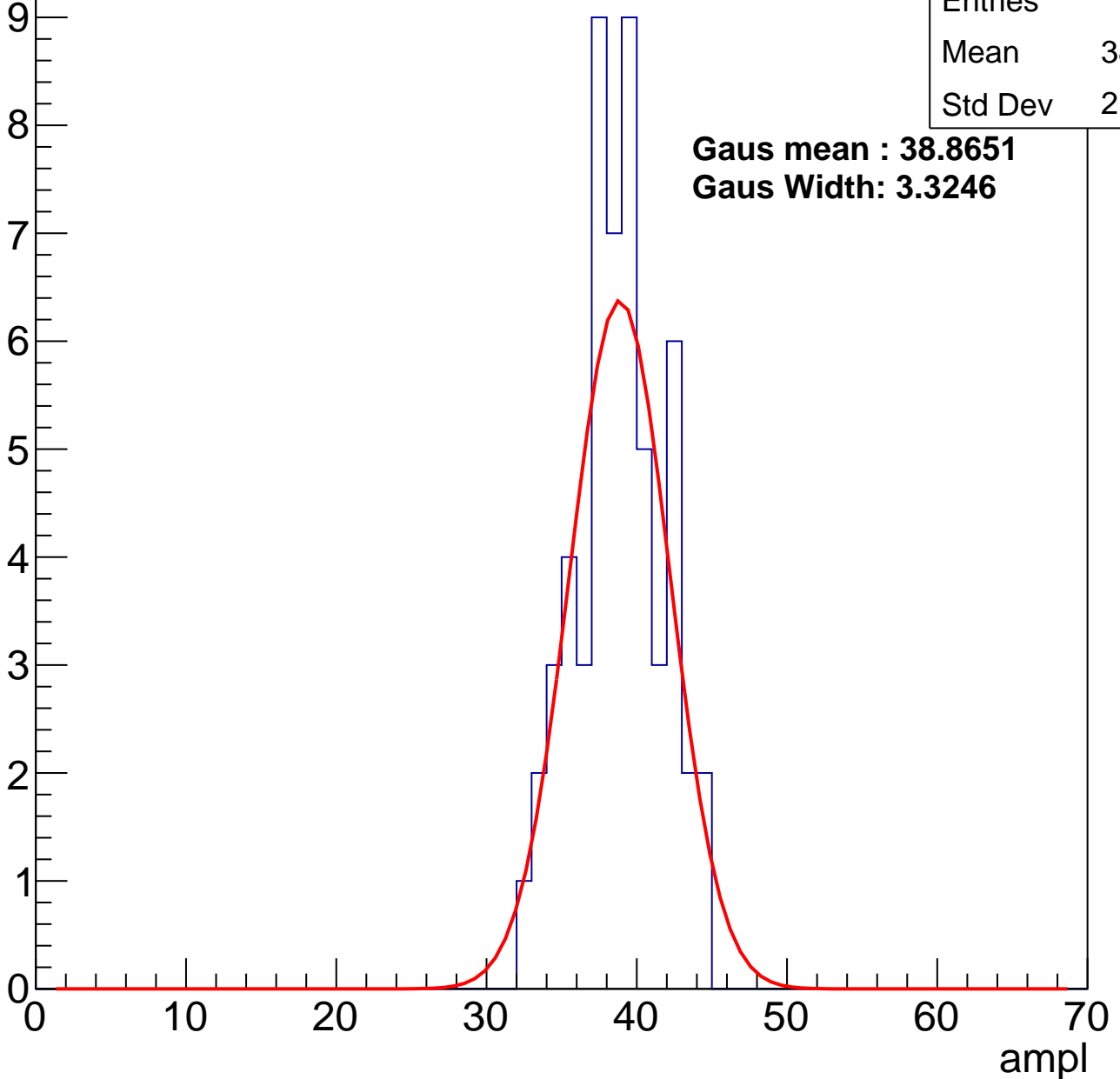
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	38.34
Std Dev	2.862

Gaus mean : 38.8651

Gaus Width: 3.3246



B1L102S, U22-ch60, adc2

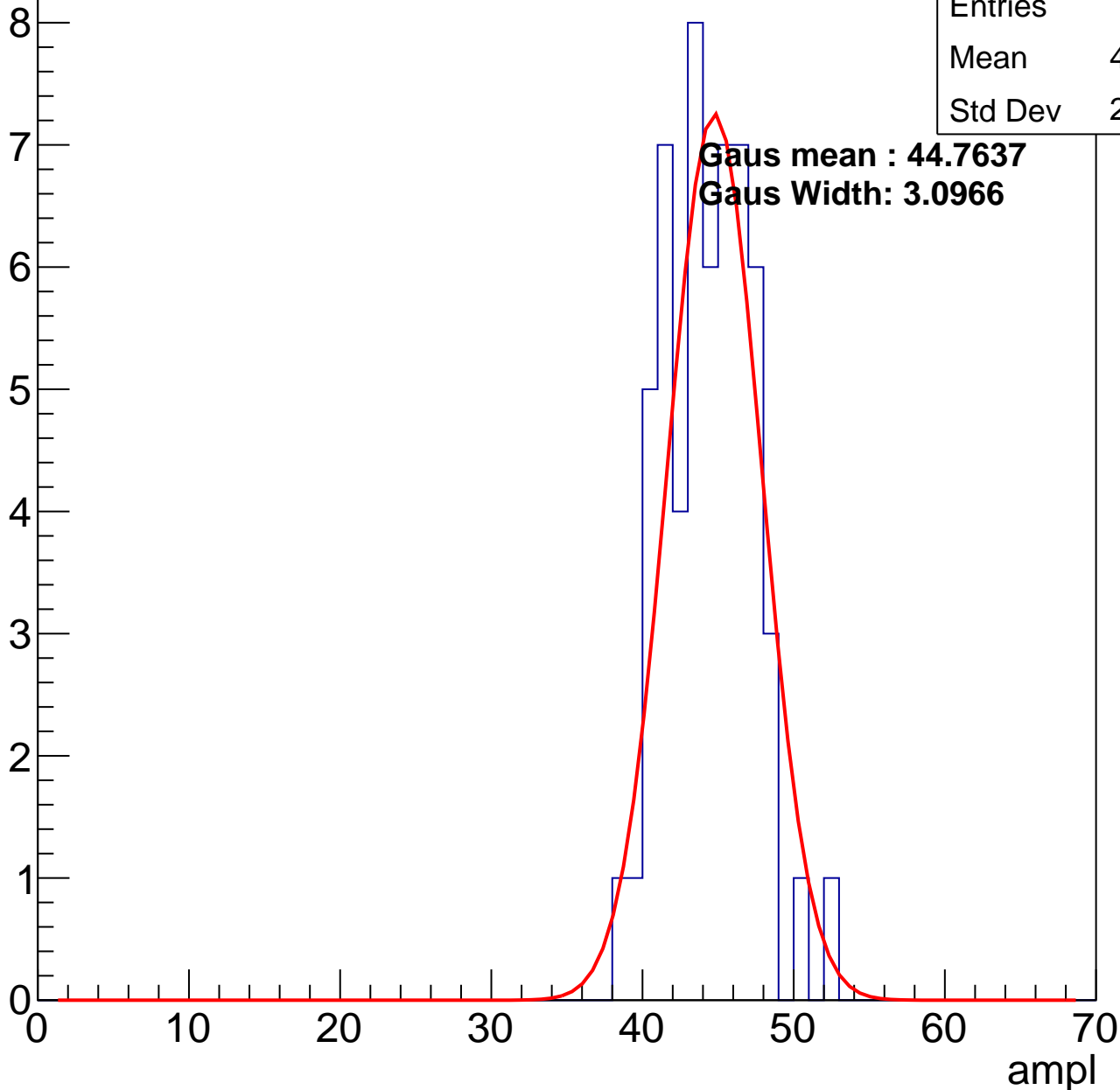
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.95
Std Dev	2.856

Gaus mean : 44.7637

Gaus Width: 3.0966

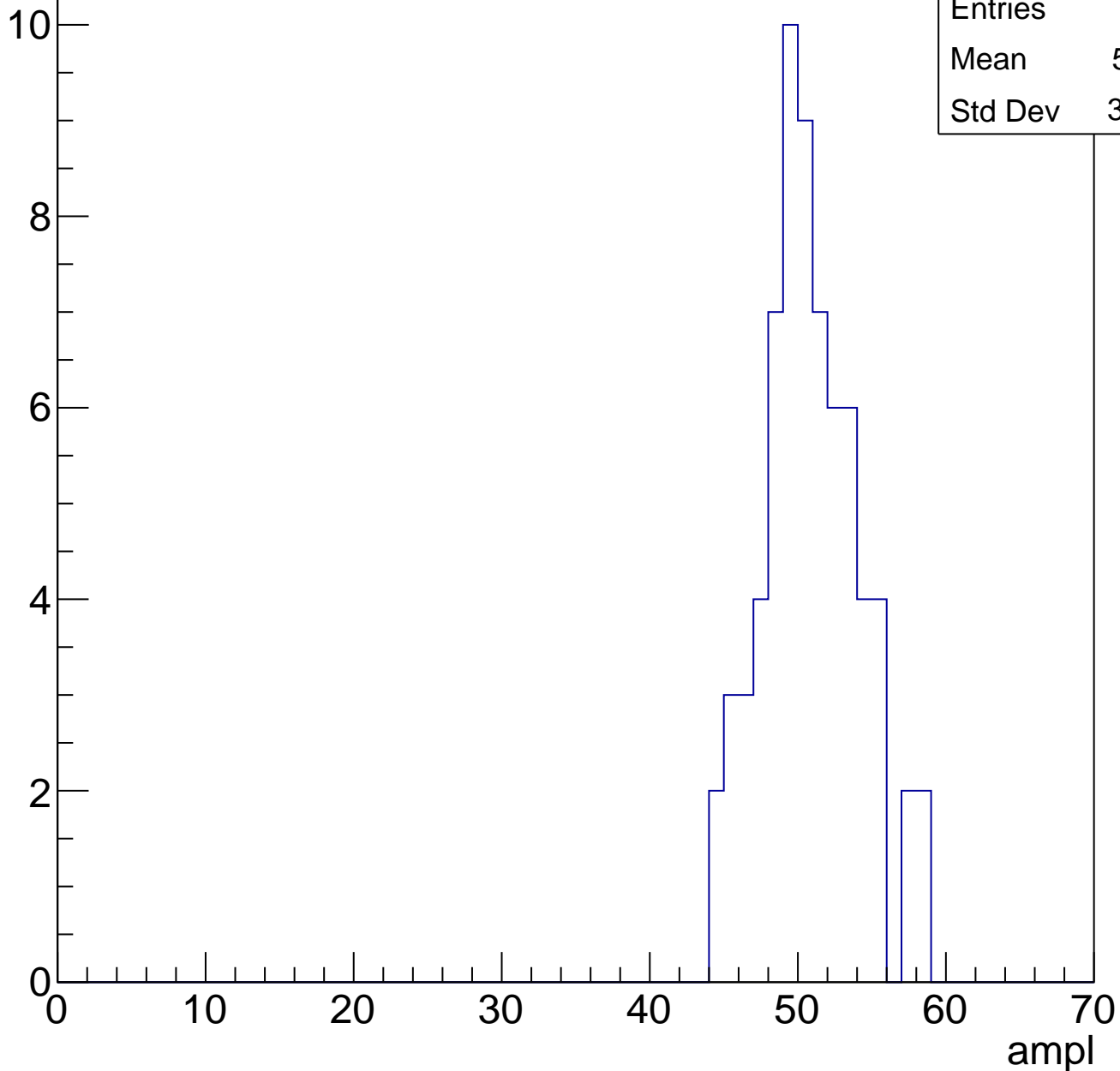


B1L102S, U22-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	50.41
Std Dev	3.259

Entry



B1L102S, U22-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

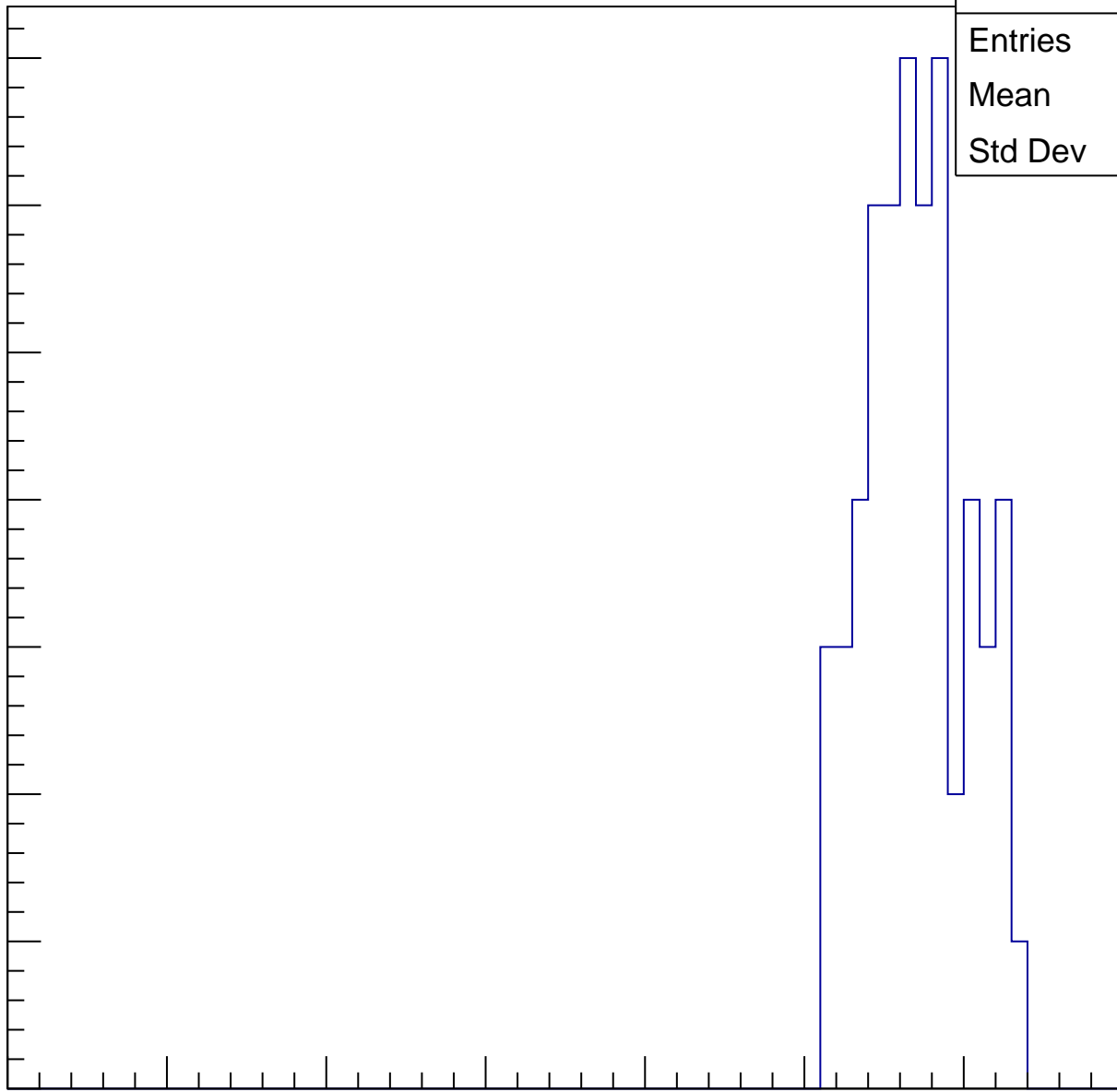
Entry

7
6
5
4
3
2
1
0

Entries	56
Mean	56.55
Std Dev	3.151

ampl

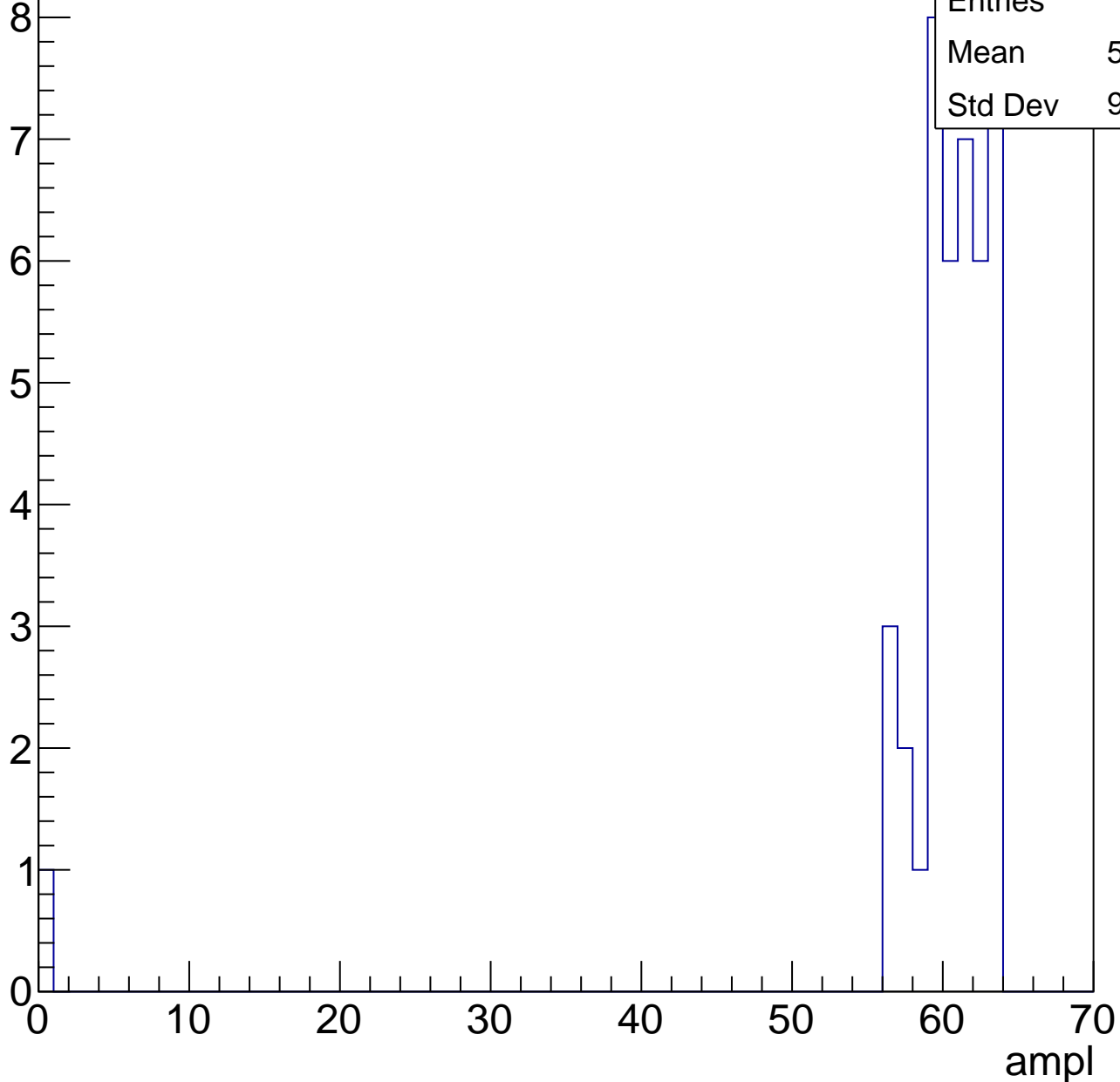
0 10 20 30 40 50 60 70



B1L102S, U22-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	42
Mean	58.93
Std Dev	9.427

B1L102S, U22-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch61, adc0

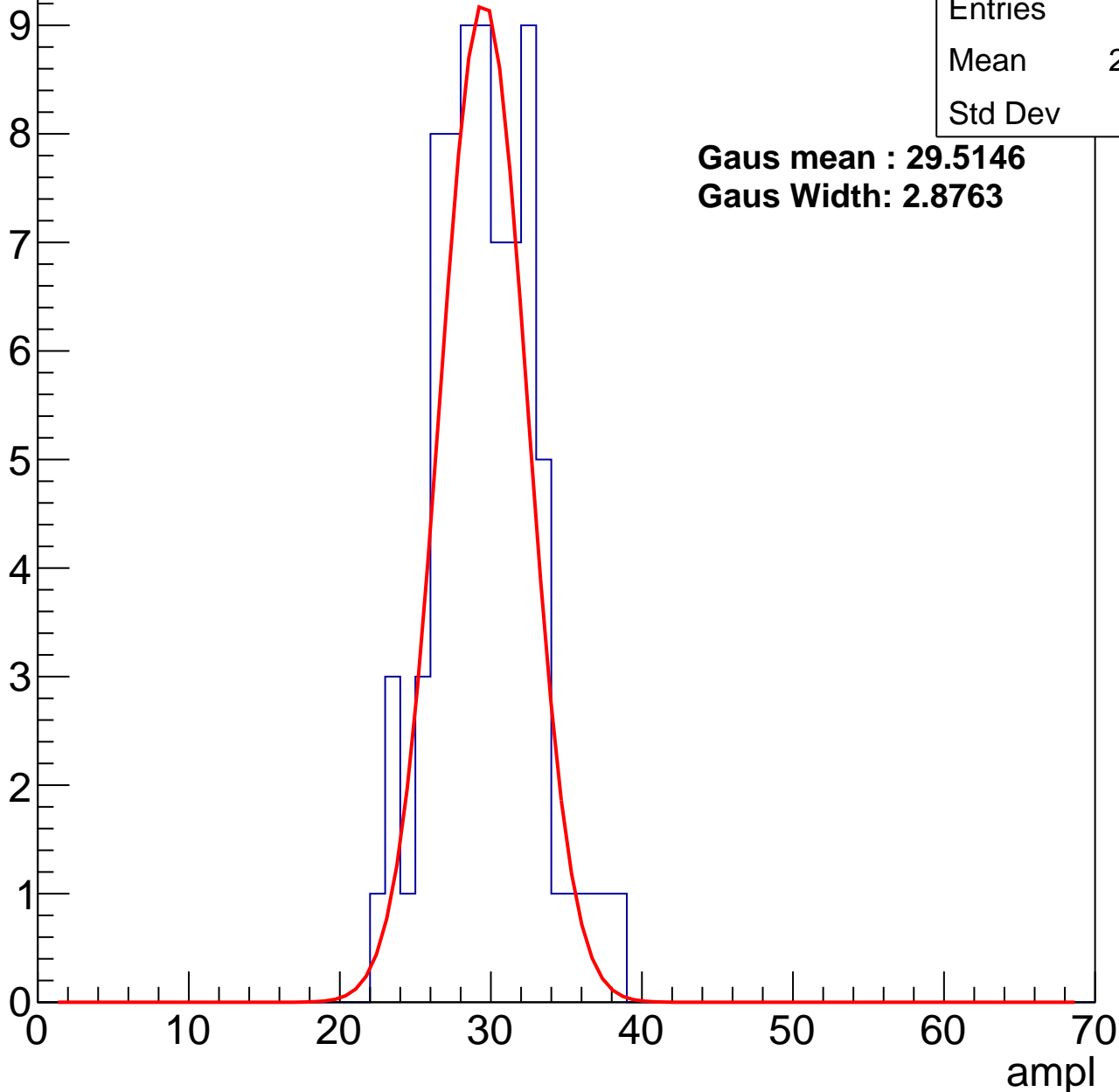
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.16
Std Dev	3.25

Gaus mean : 29.5146

Gaus Width: 2.8763



B1L102S, U22-ch61, adc1

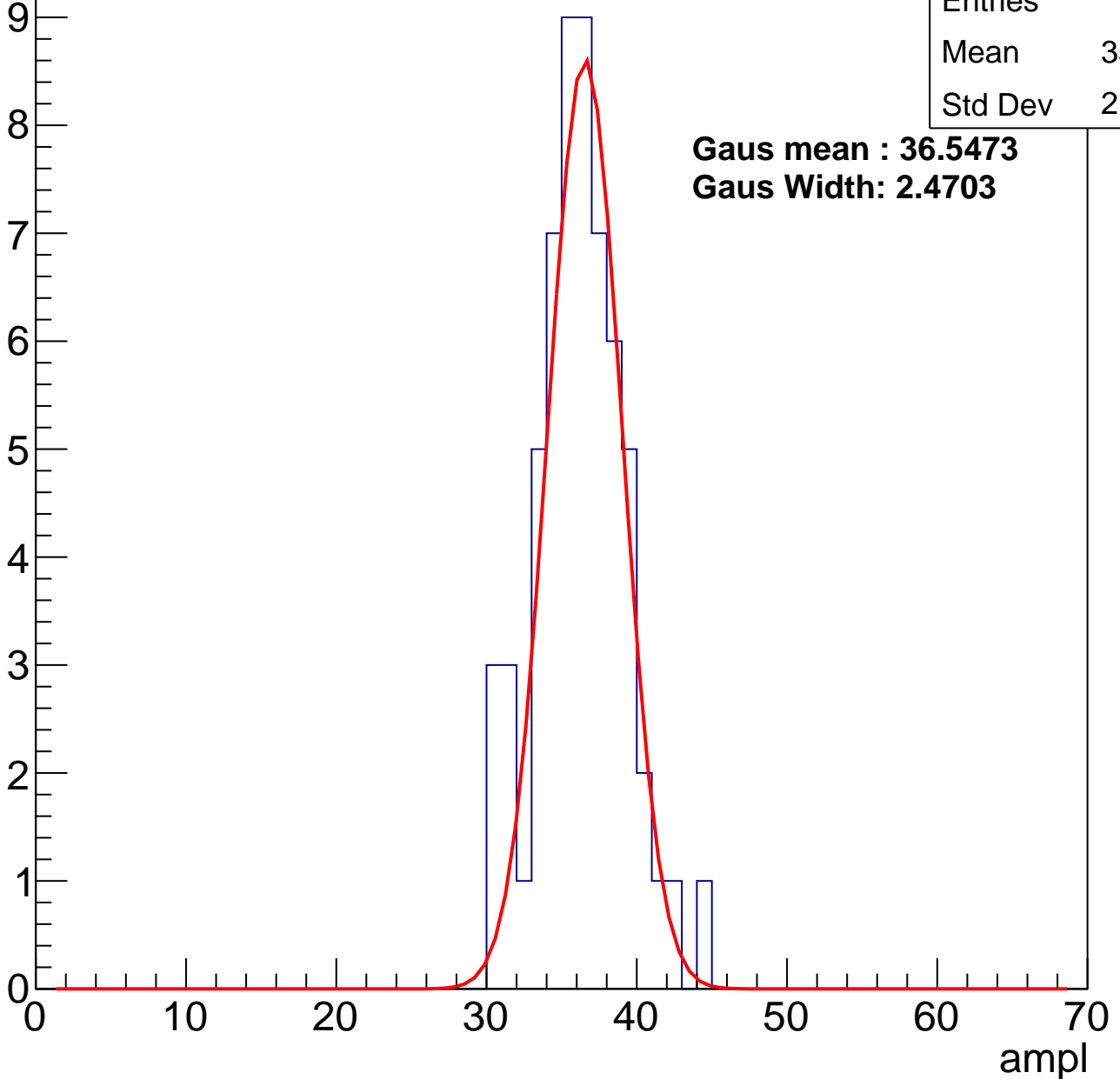
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.77
Std Dev	2.918

Gaus mean : 36.5473

Gaus Width: 2.4703



B1L102S, U22-ch61, adc2

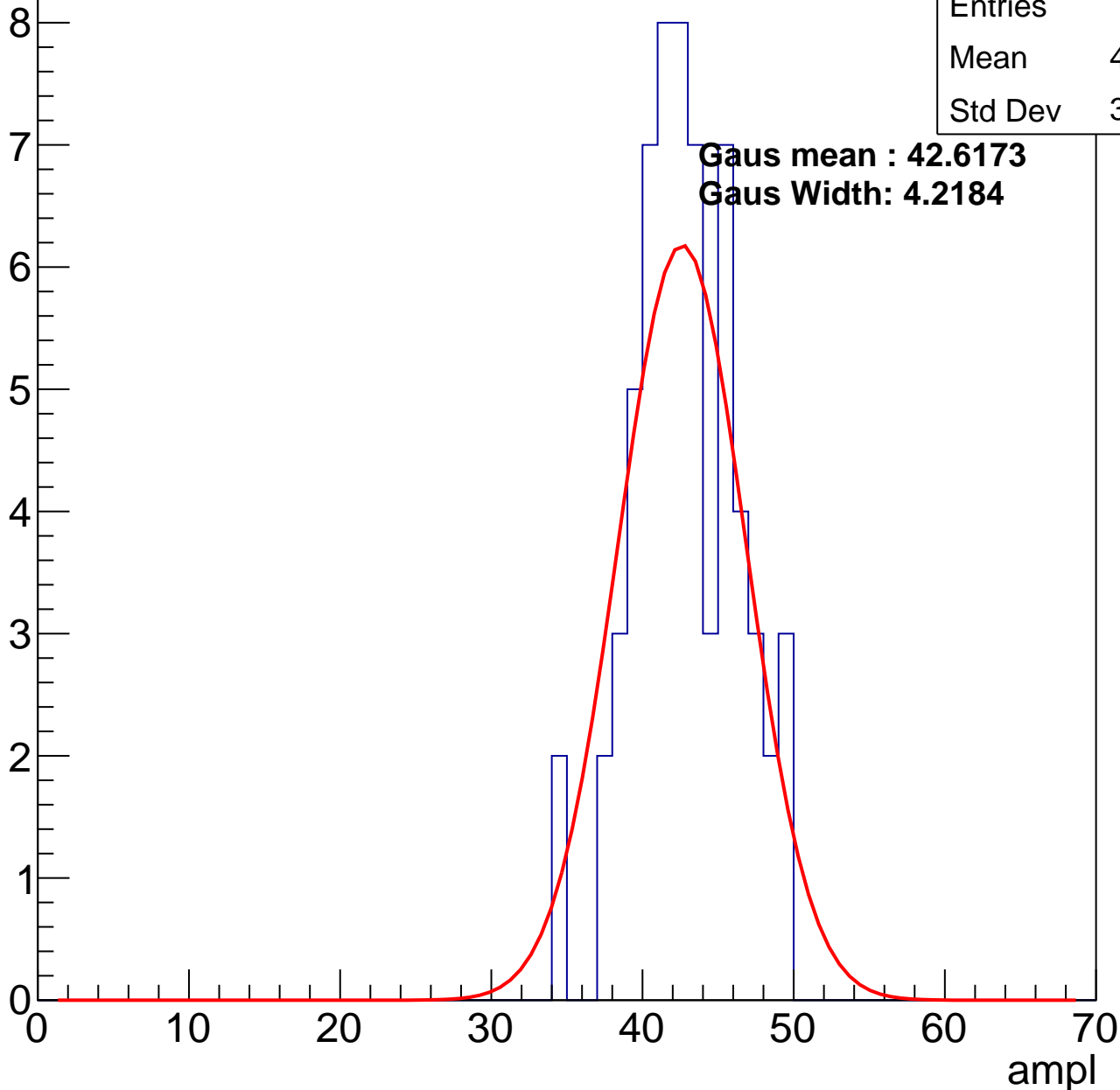
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.36
Std Dev	3.397

Gaus mean : 42.6173

Gaus Width: 4.2184

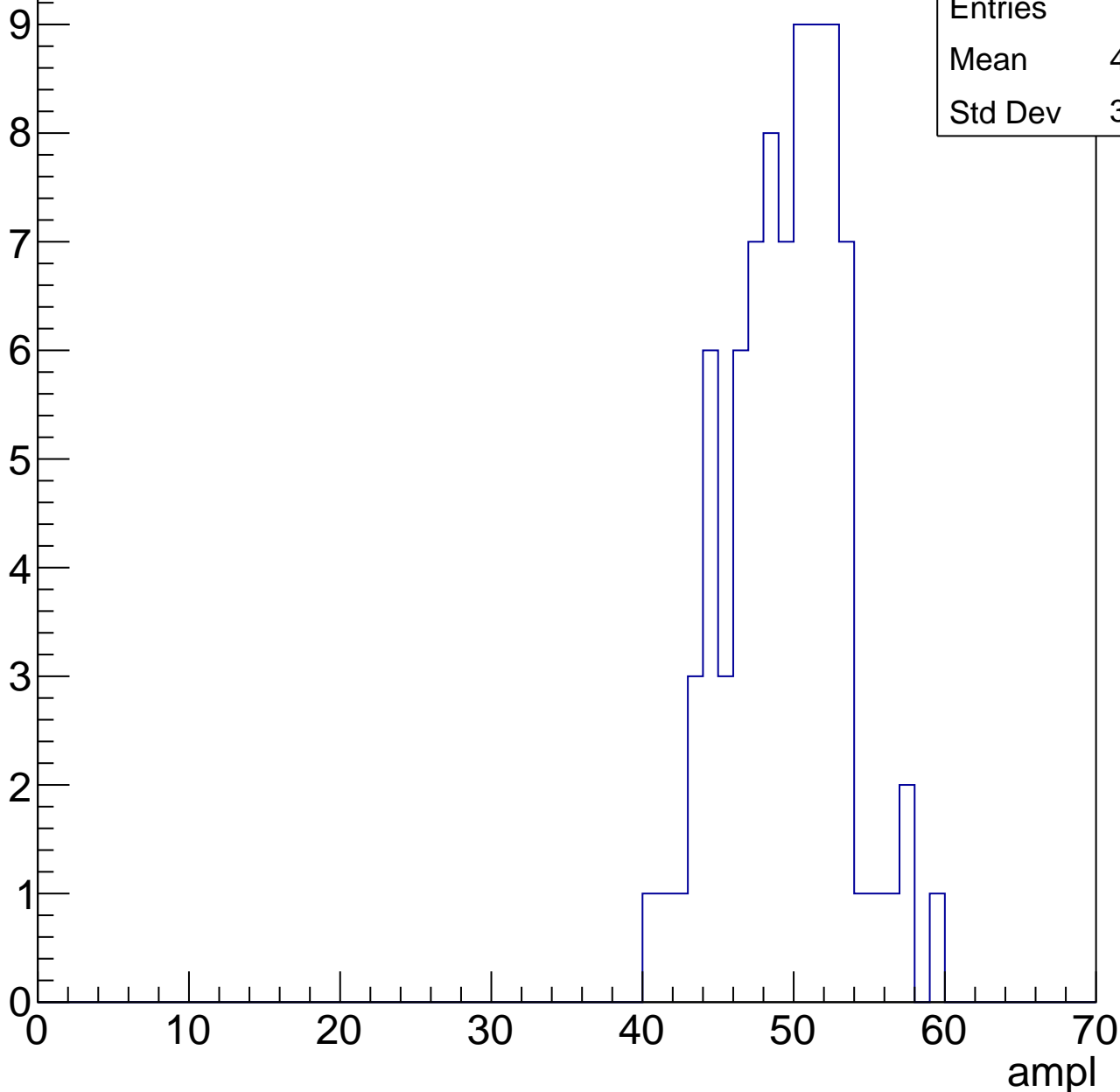


B1L102S, U22-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	49.02
Std Dev	3.748

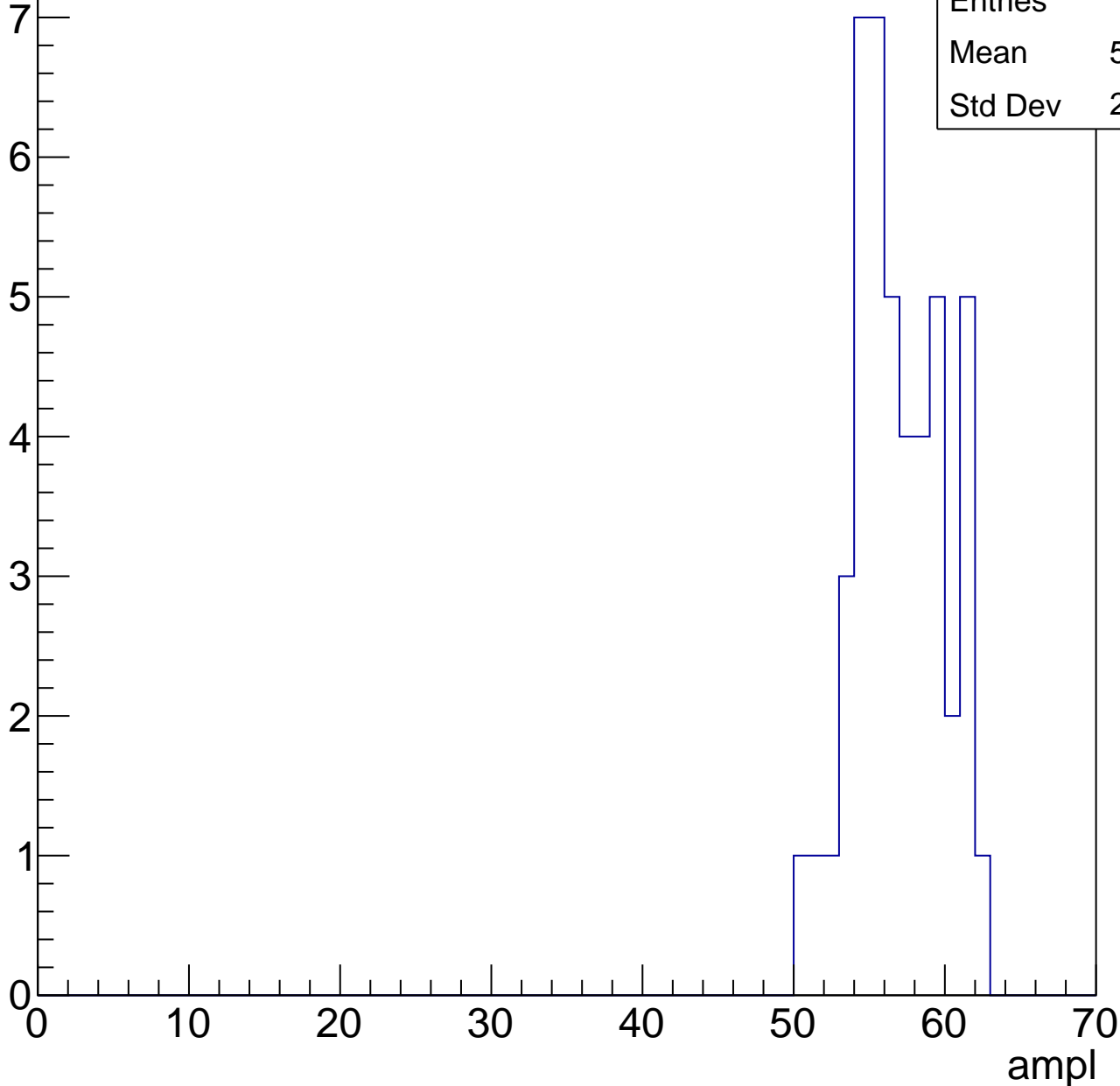


B1L102S, U22-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	56.46
Std Dev	2.902

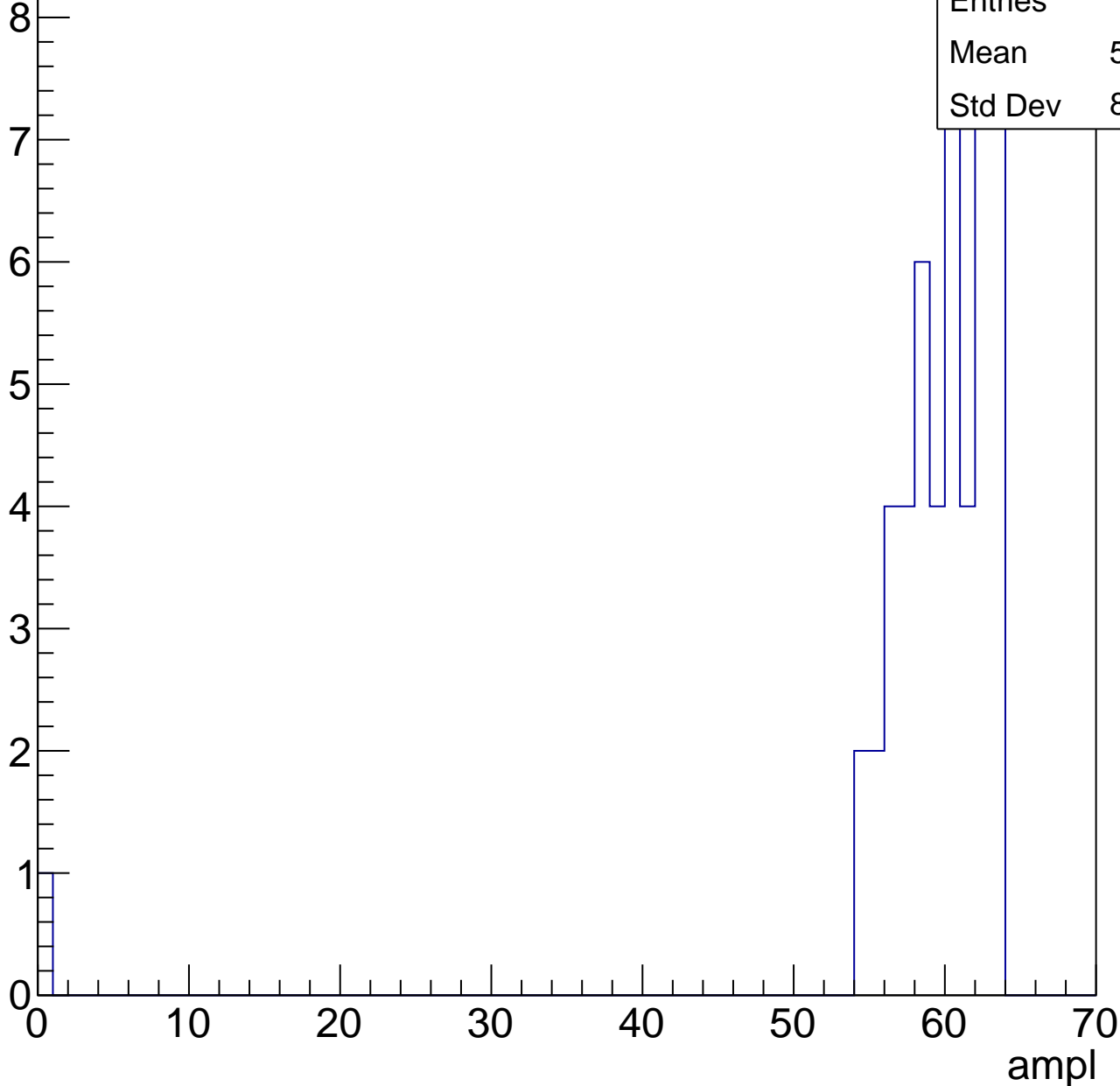


B1L102S, U22-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.39
Std Dev	8.662



B1L102S, U22-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L102S, U22-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch62, adc0

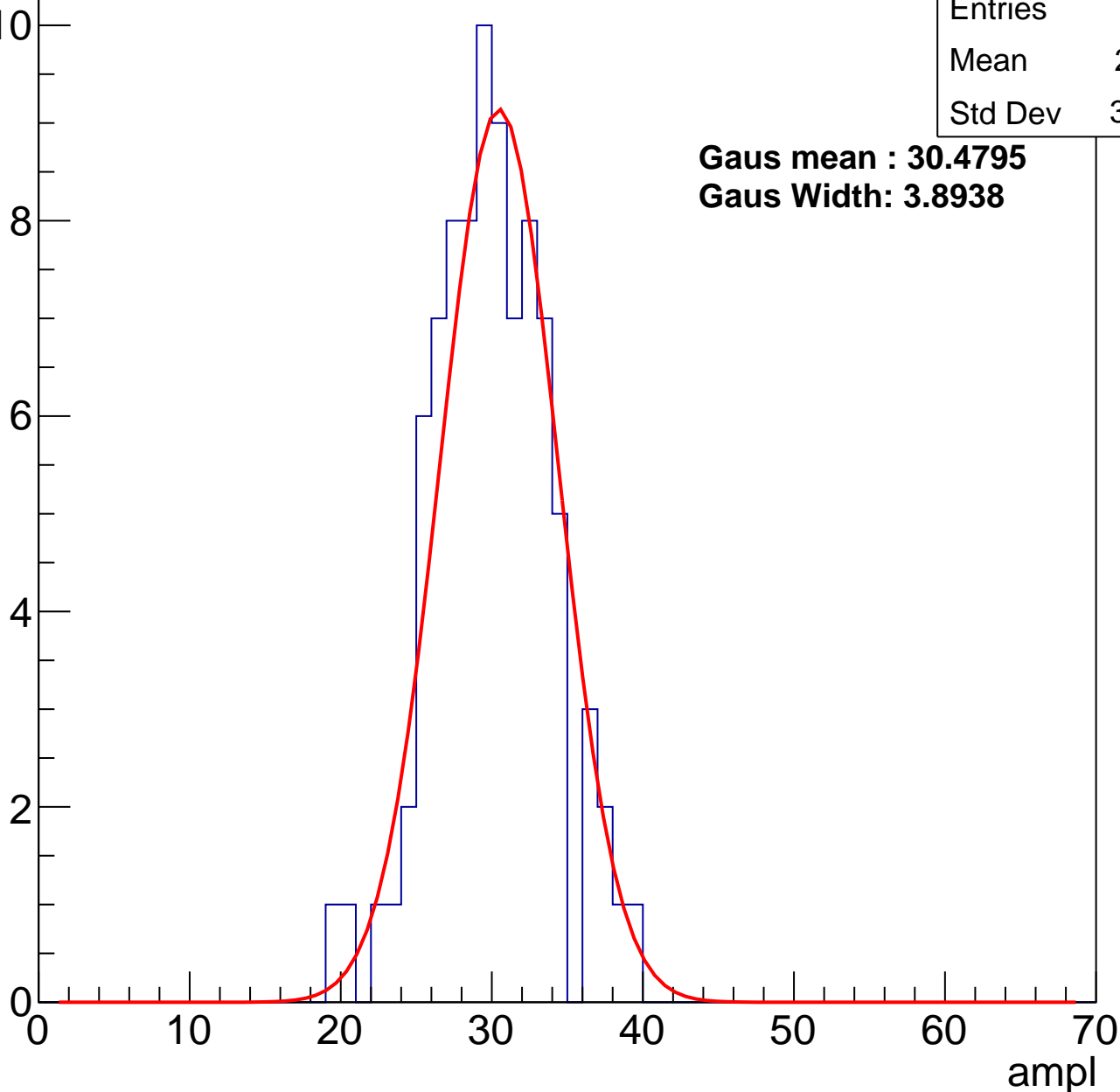
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	29.51
Std Dev	3.835

Gaus mean : 30.4795

Gaus Width: 3.8938



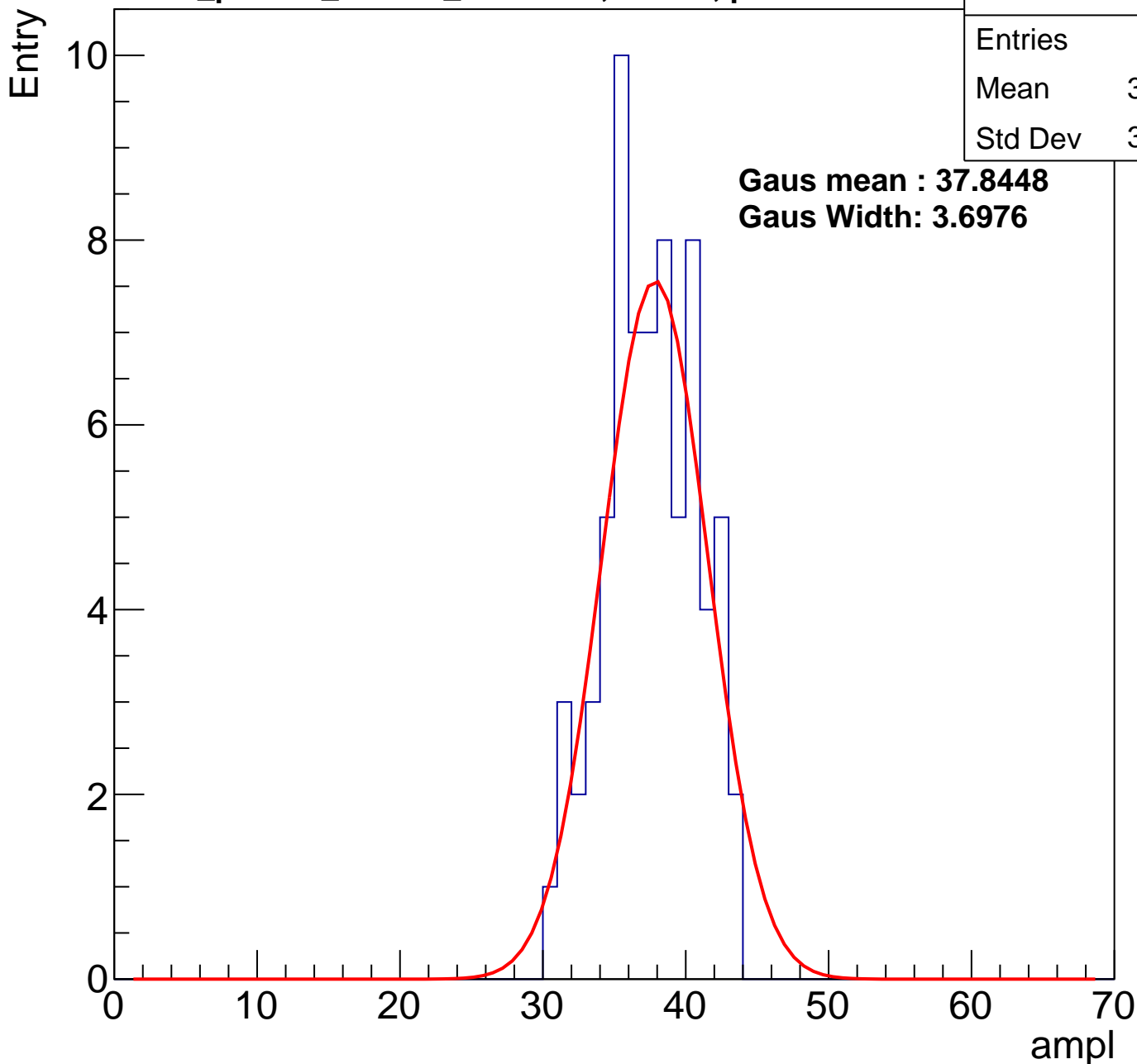
B1L102S, U22-ch62, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	37.09
Std Dev	3.179

Gaus mean : 37.8448

Gaus Width: 3.6976



B1L102S, U22-ch62, adc2

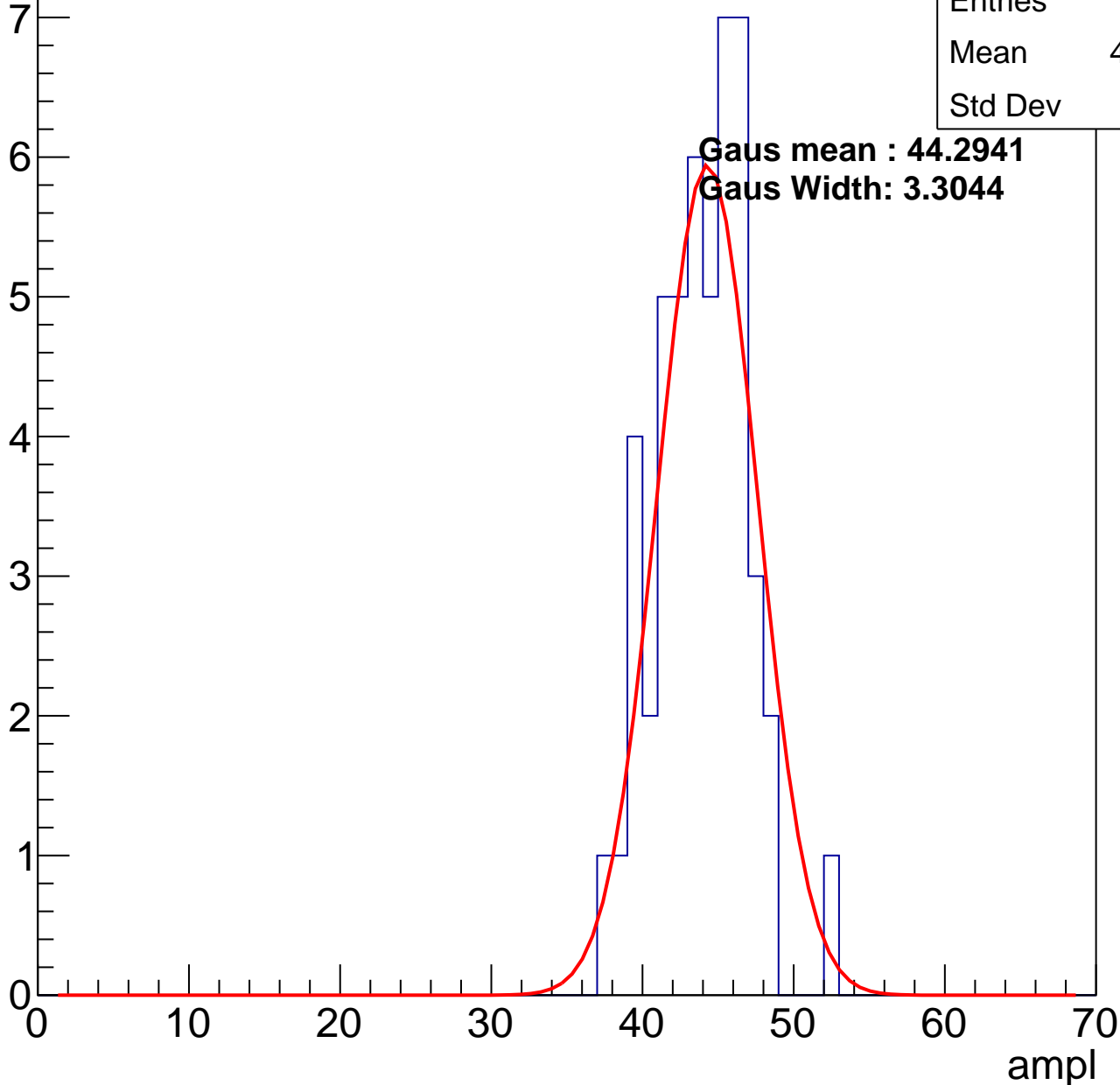
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	43.47
Std Dev	2.97

Gaus mean : 44.2941

Gaus Width: 3.3044

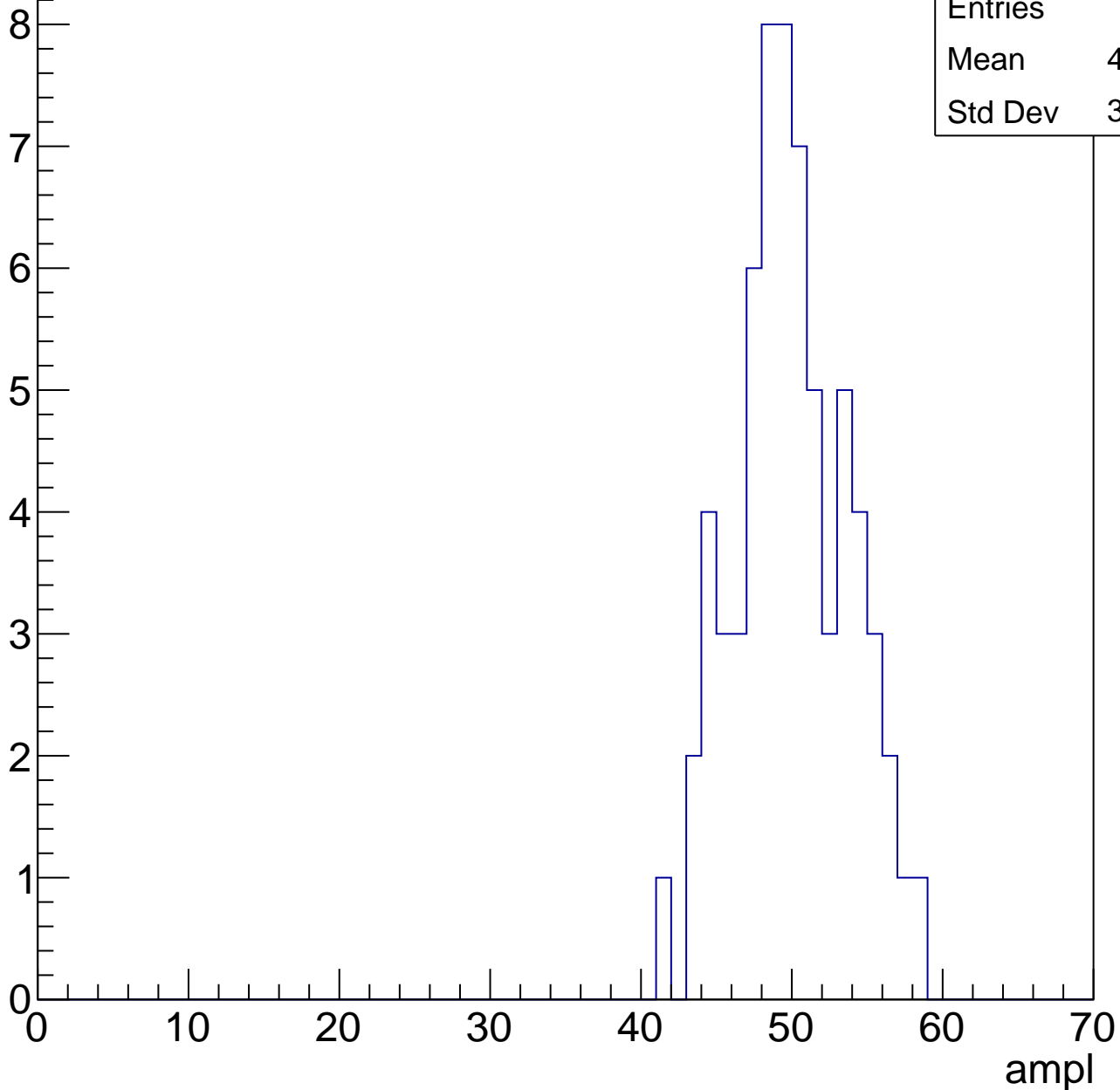


B1L102S, U22-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.52
Std Dev	3.714

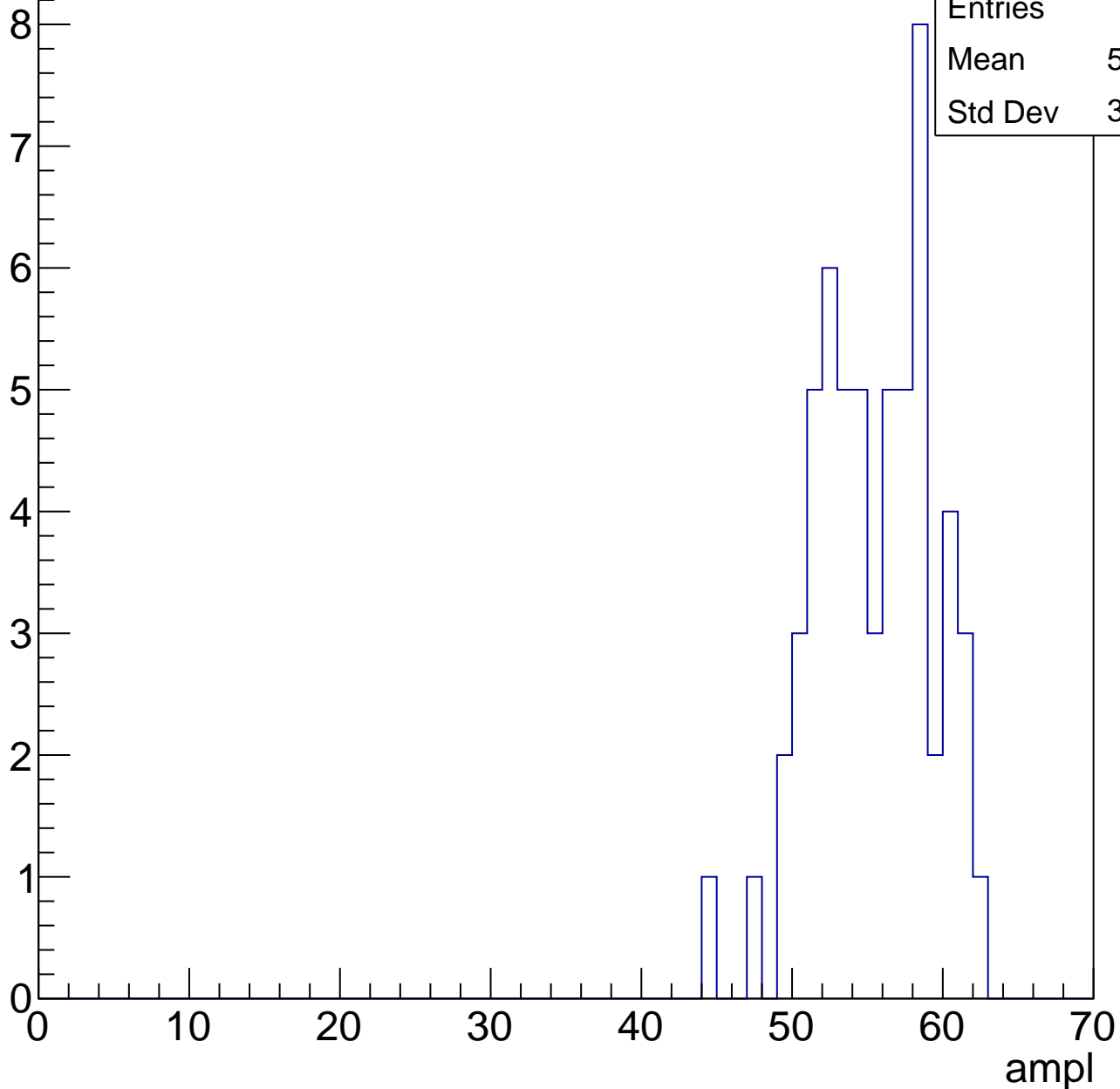


B1L102S, U22-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.88
Std Dev	3.854



B1L102S, U22-ch62, adc5

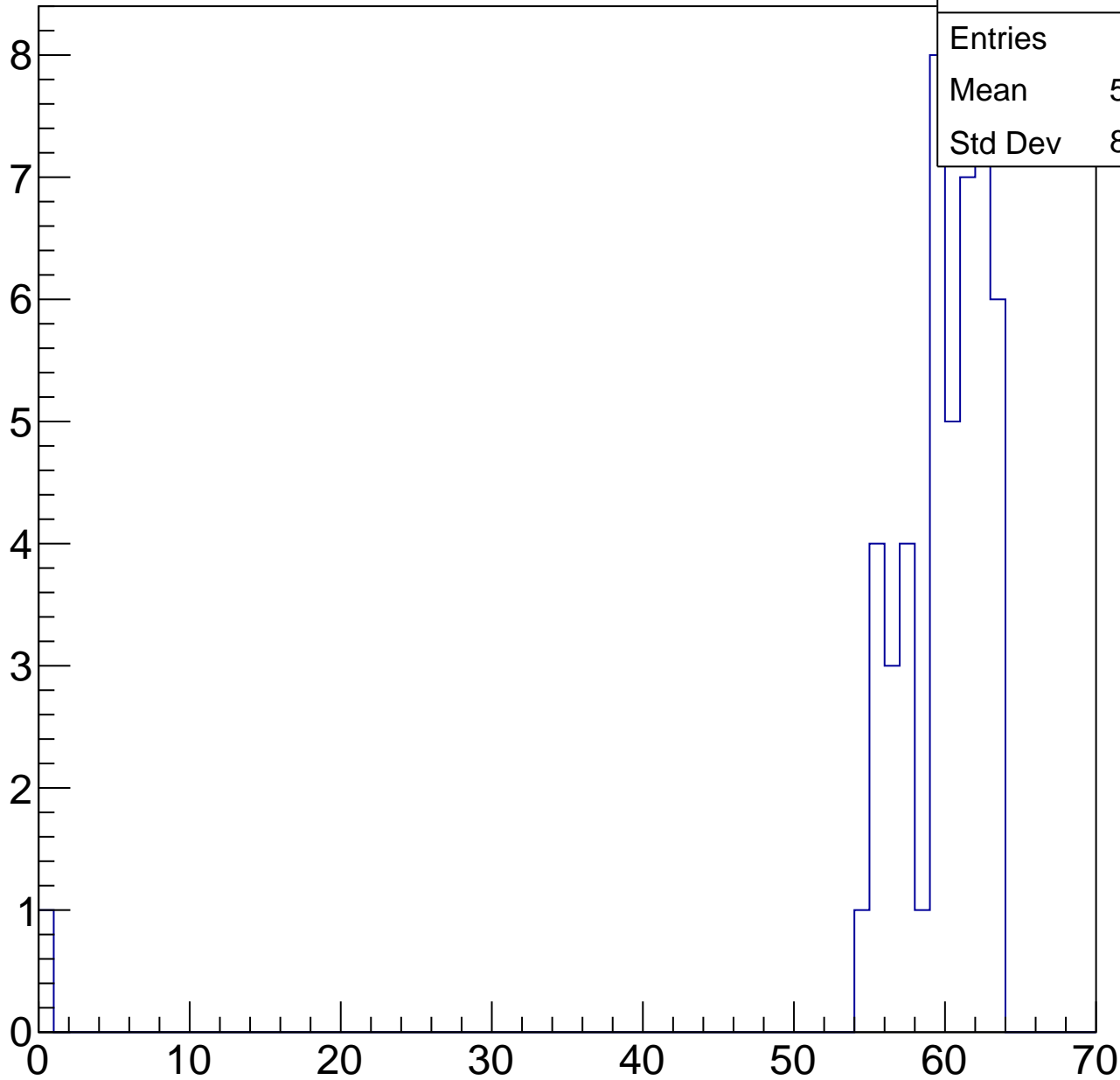
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.35
Std Dev	8.892

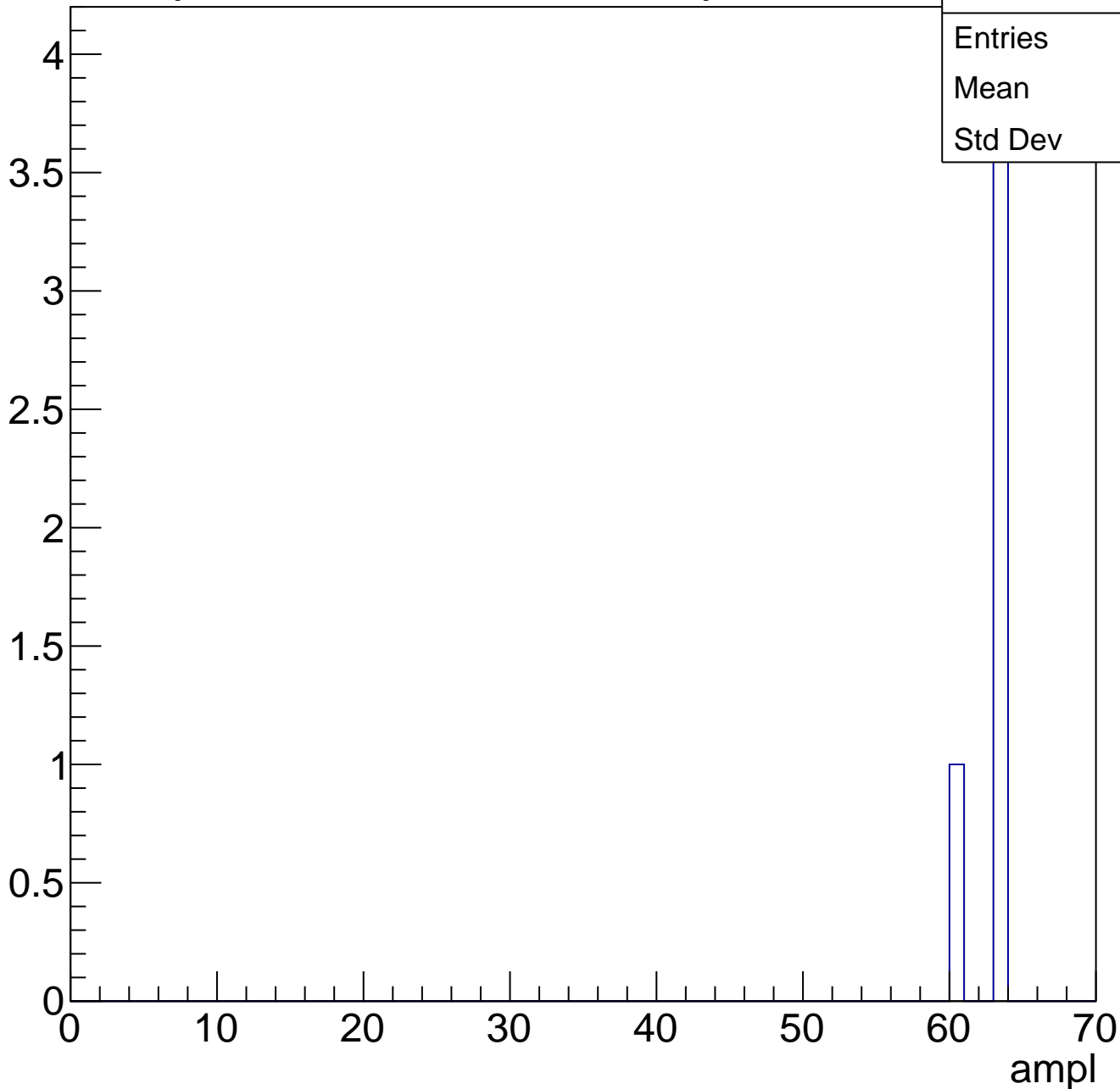
ampl



B1L102S, U22-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

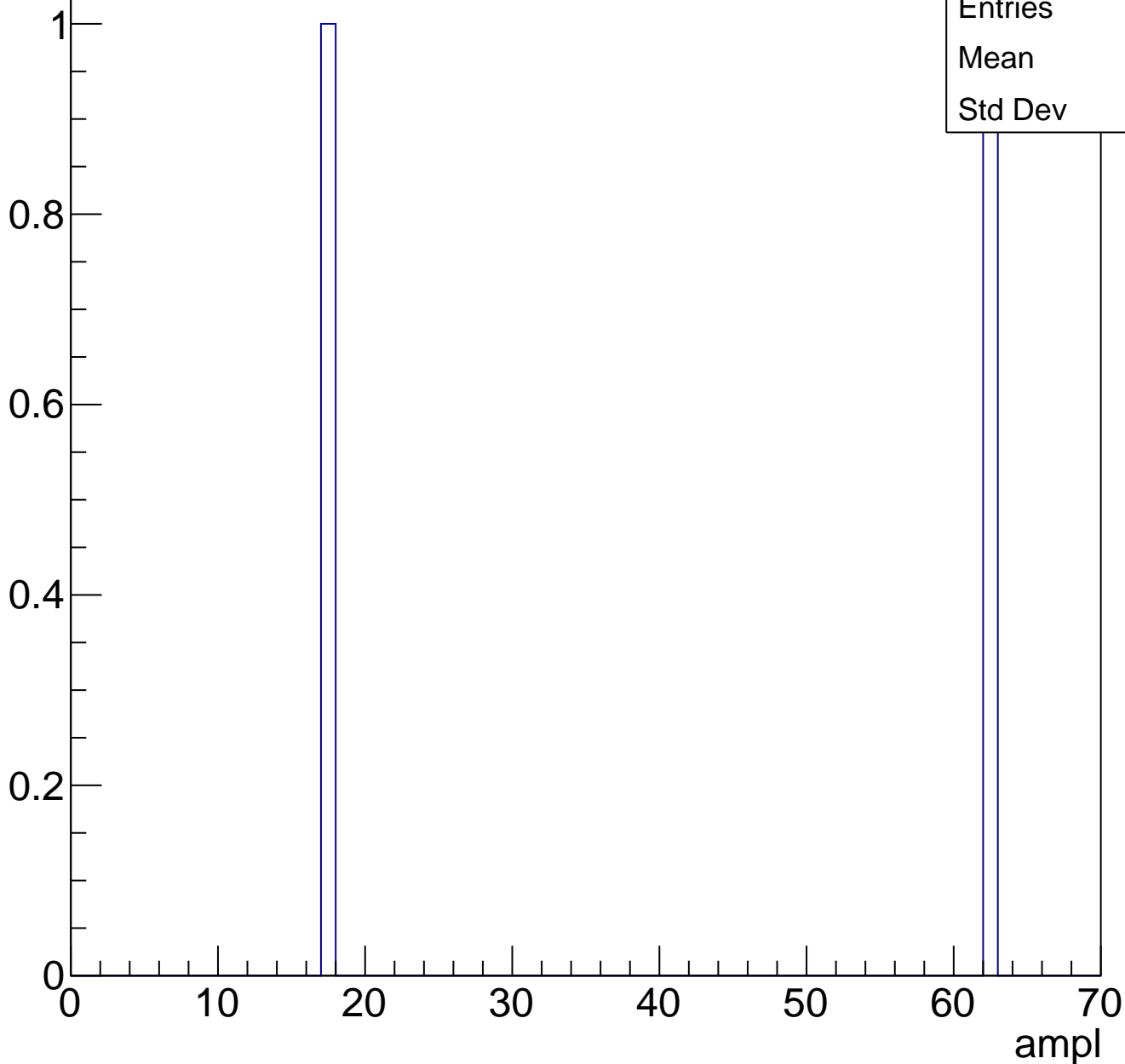
Entry



B1L102S, U22-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch63, adc0

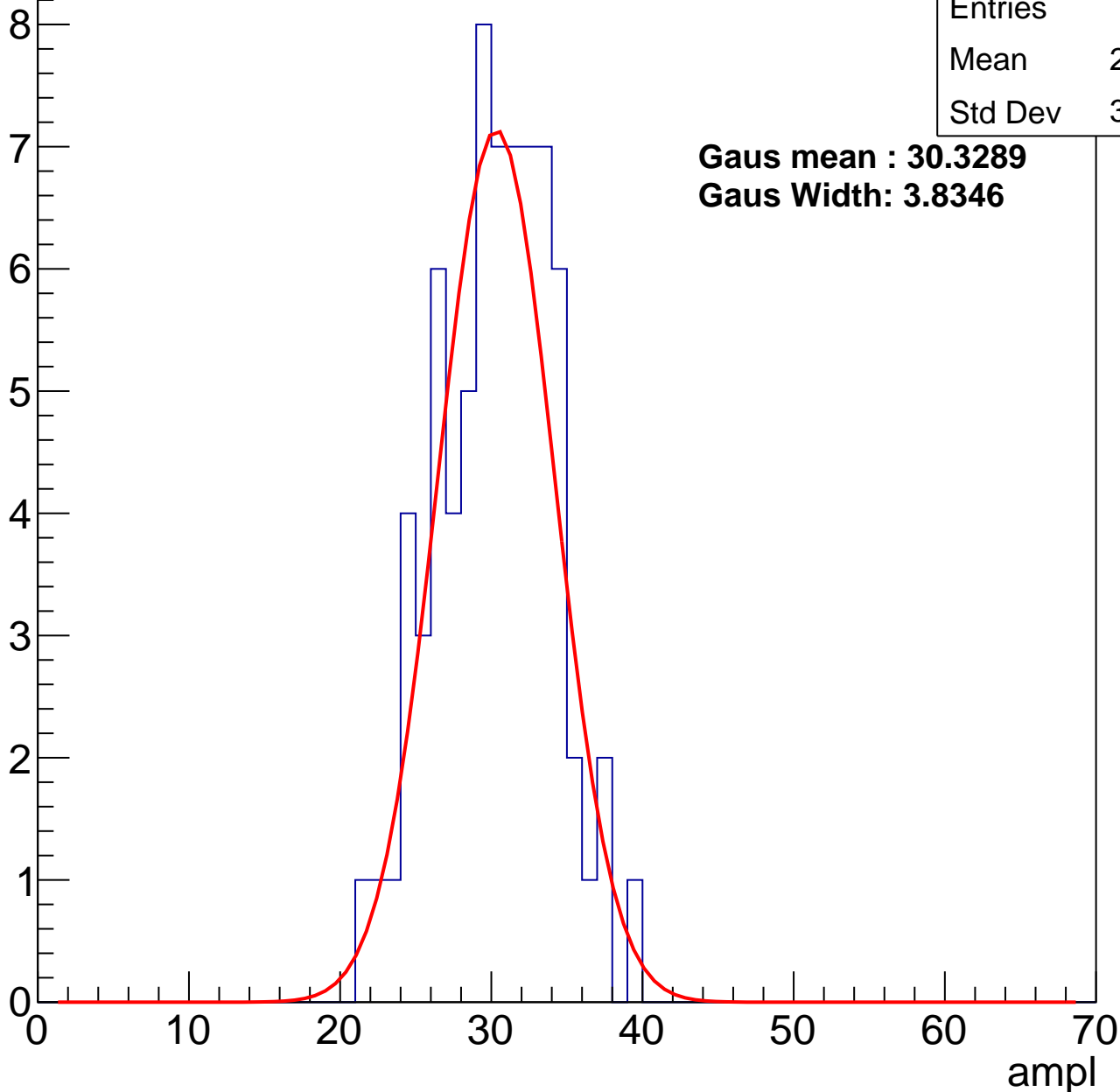
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29.84
Std Dev	3.756

Gaus mean : 30.3289

Gaus Width: 3.8346



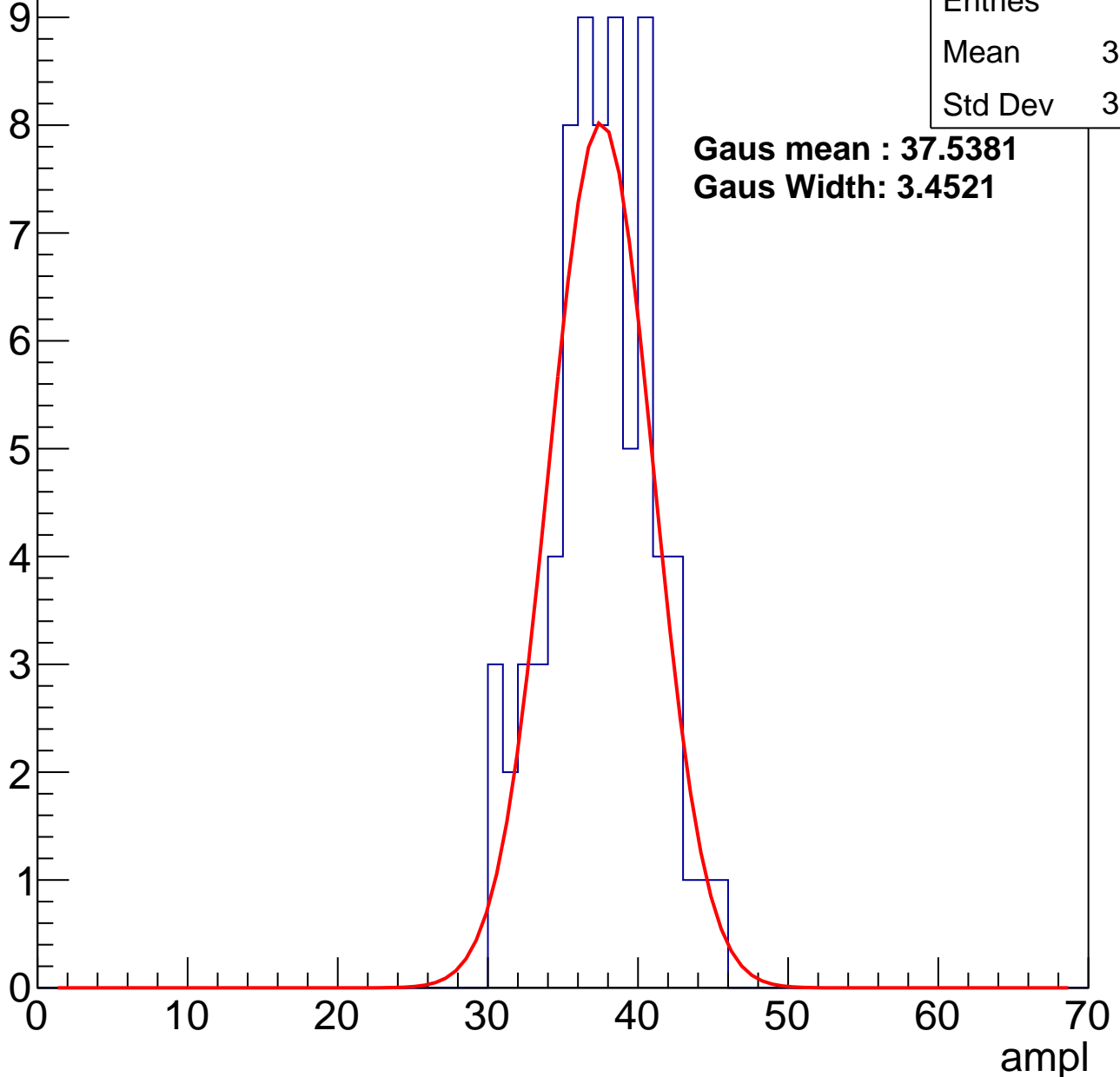
B1L102S, U22-ch63, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	37.08
Std Dev	3.372

Gaus mean : 37.5381
Gaus Width: 3.4521



B1L102S, U22-ch63, adc2

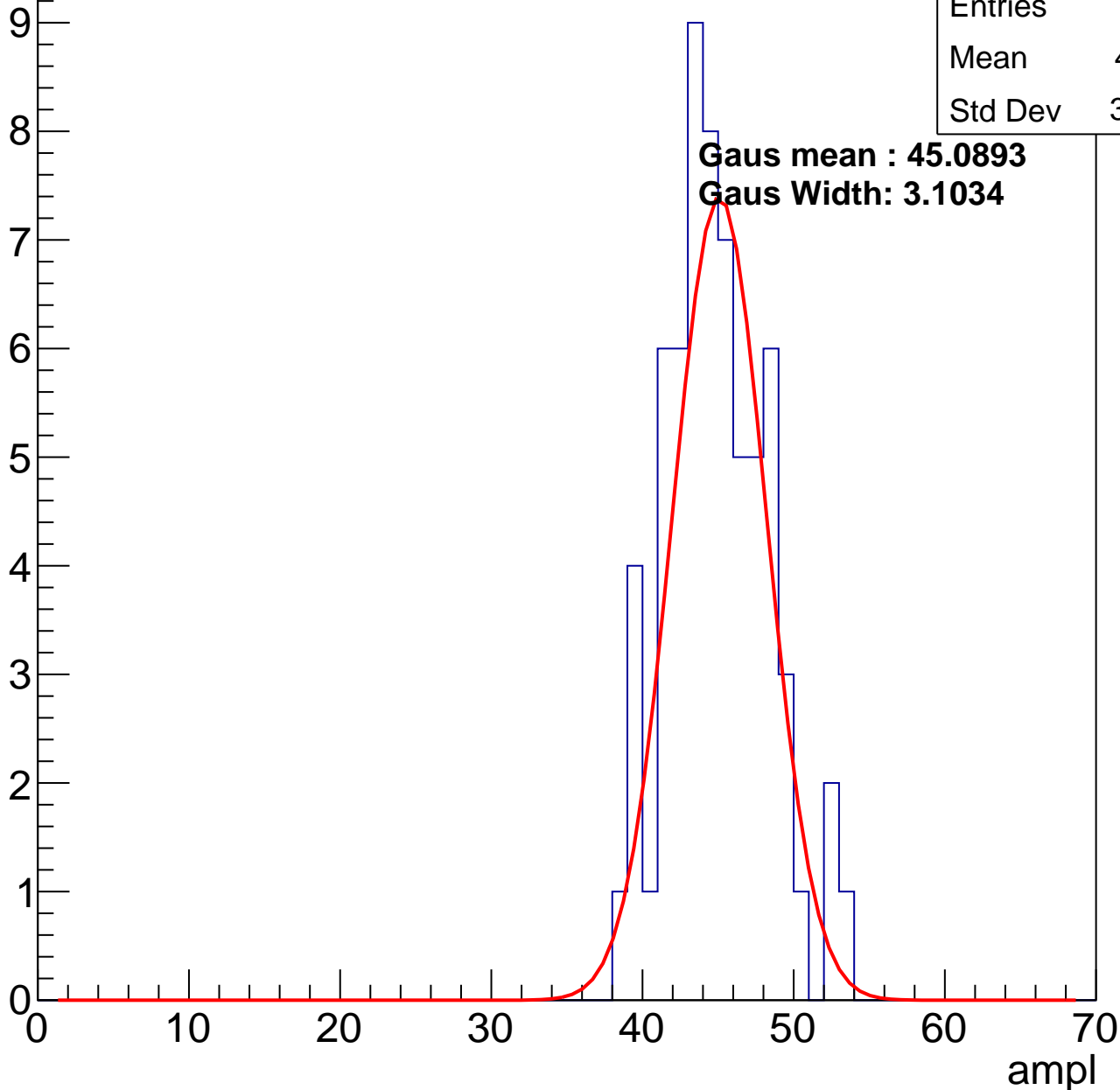
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.51
Std Dev	3.306

Gaus mean : 45.0893

Gaus Width: 3.1034

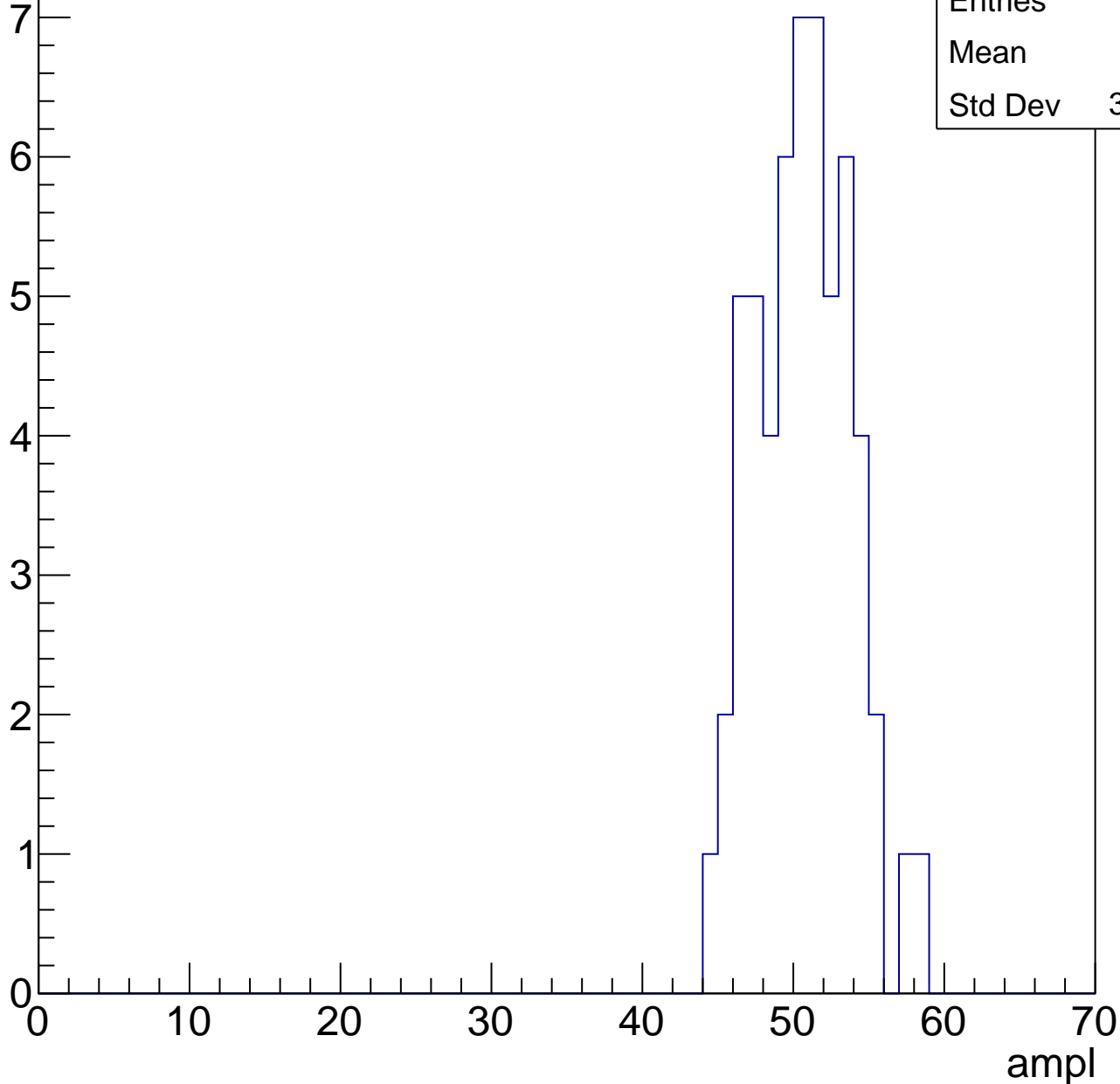


B1L102S, U22-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	50.2
Std Dev	3.102

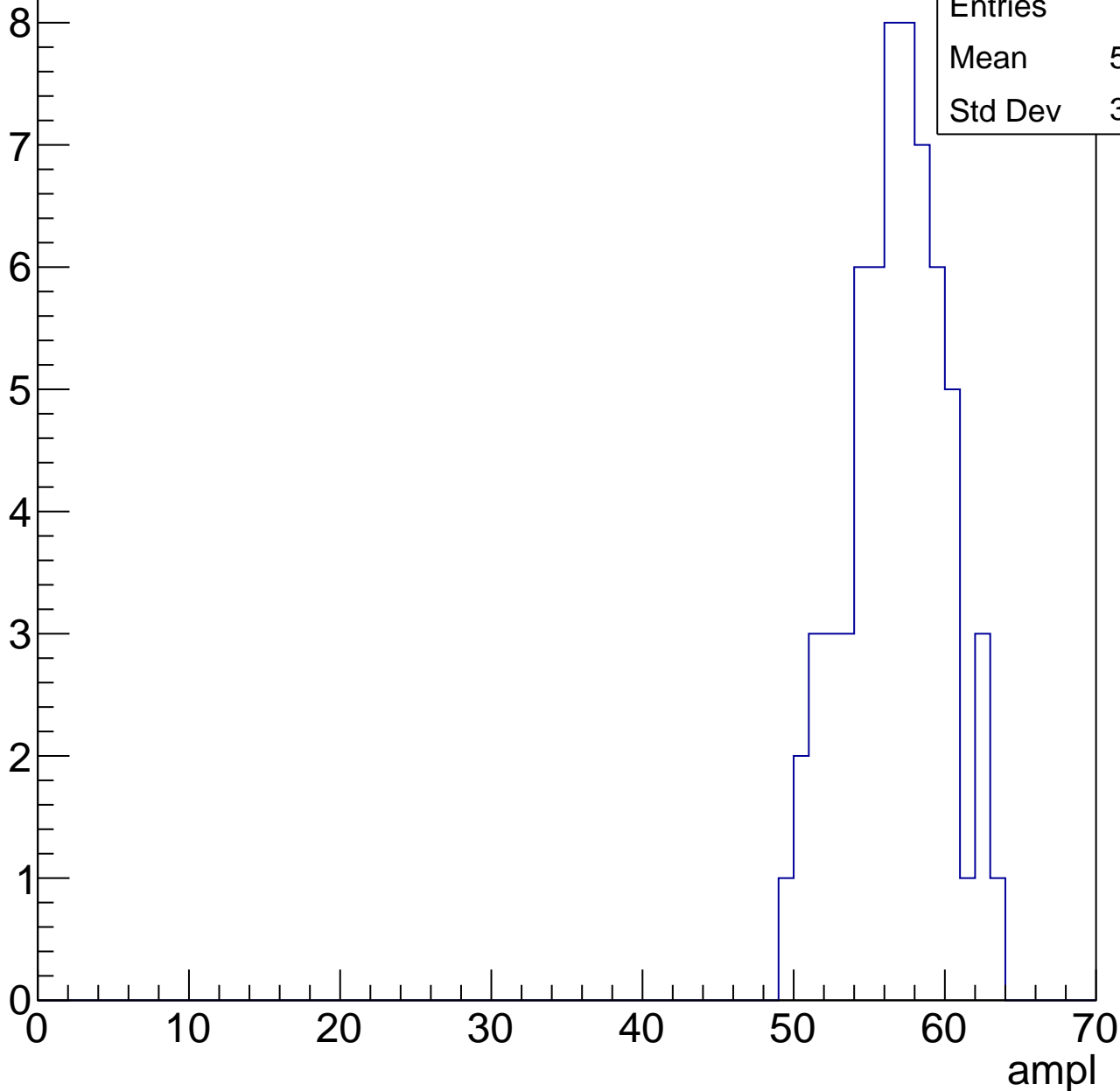


B1L102S, U22-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	56.27
Std Dev	3.208

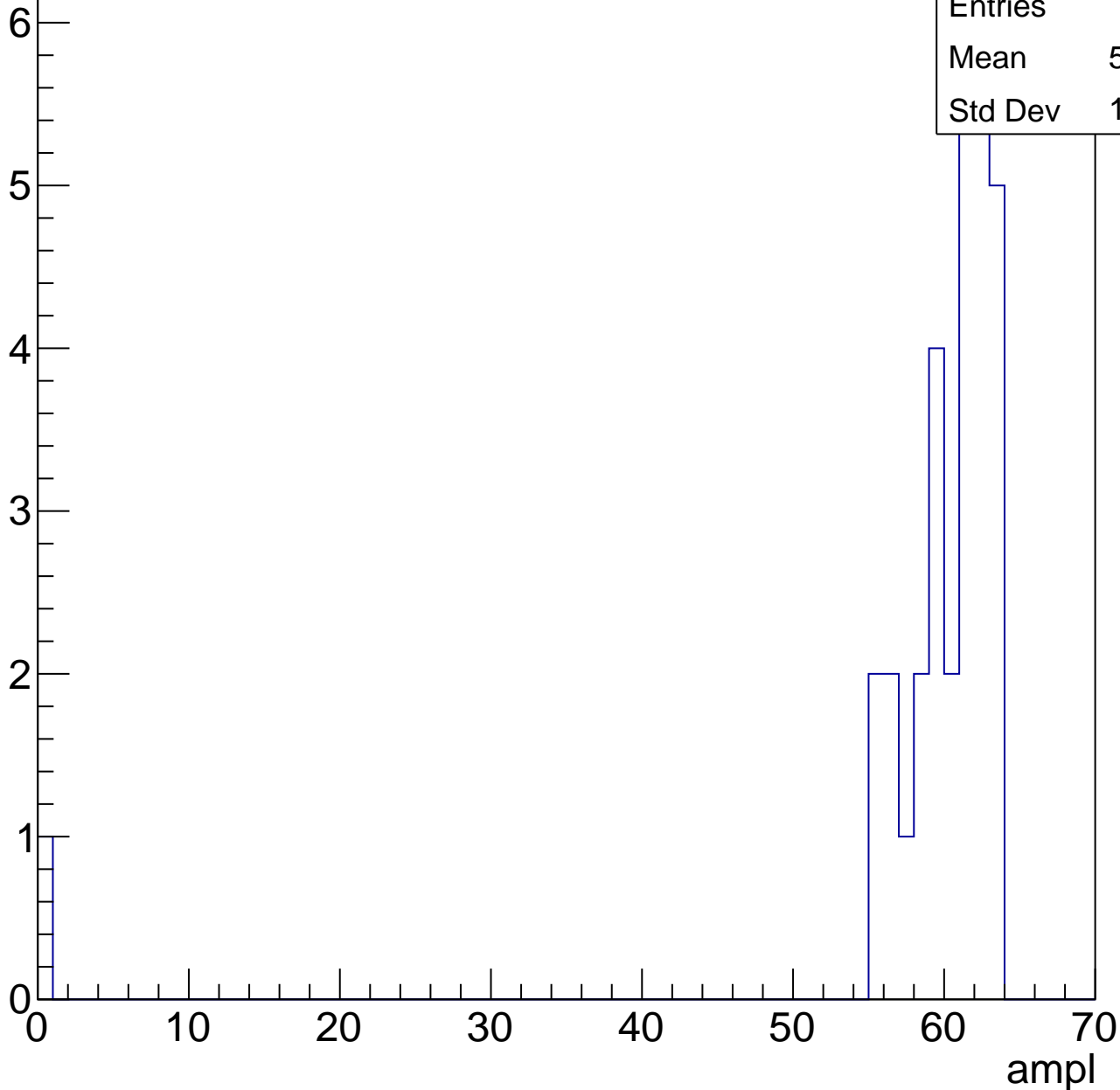


B1L102S, U22-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

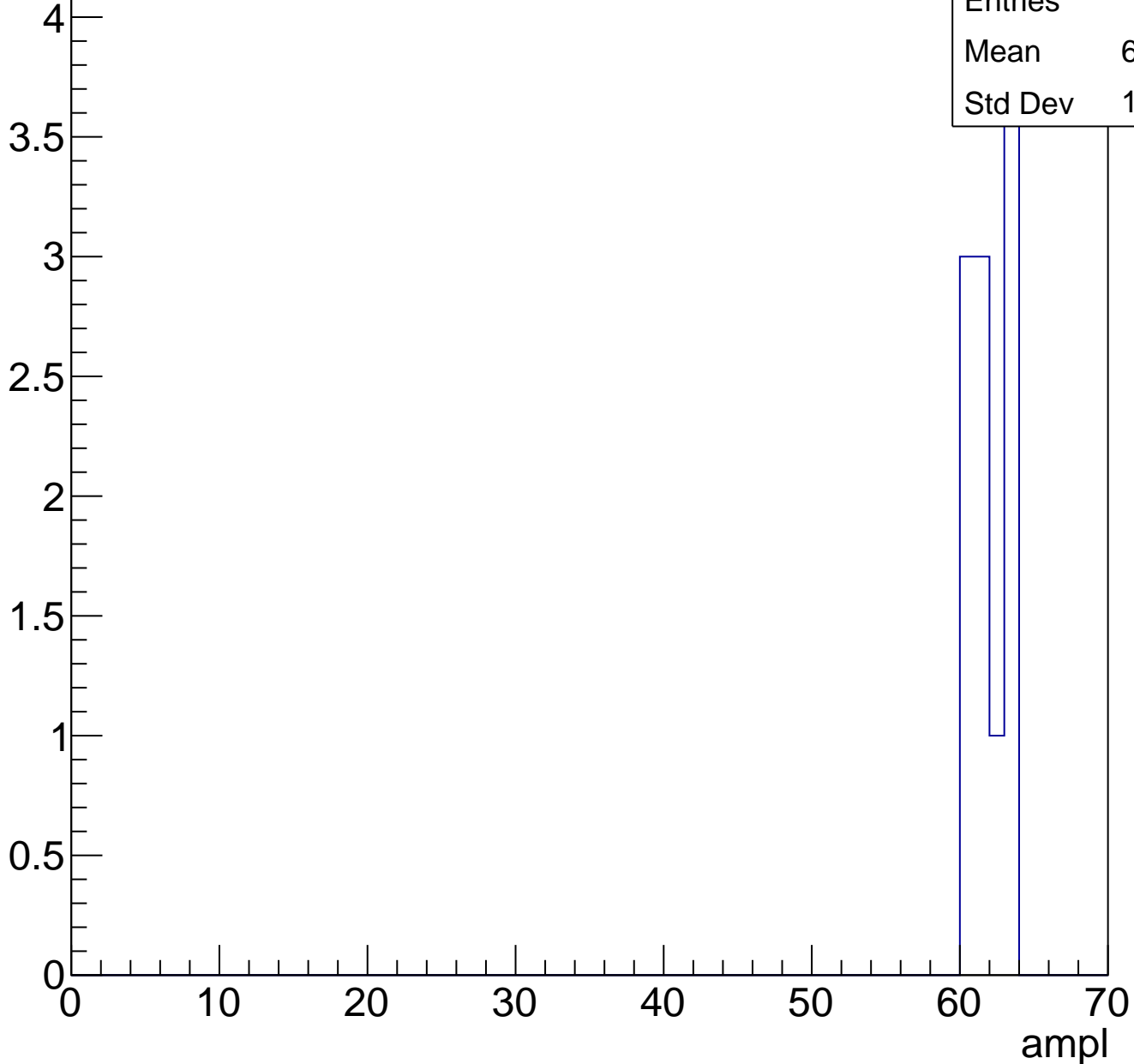
Entries	31
Mean	58.19
Std Dev	10.89



B1L102S, U22-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch64, adc0

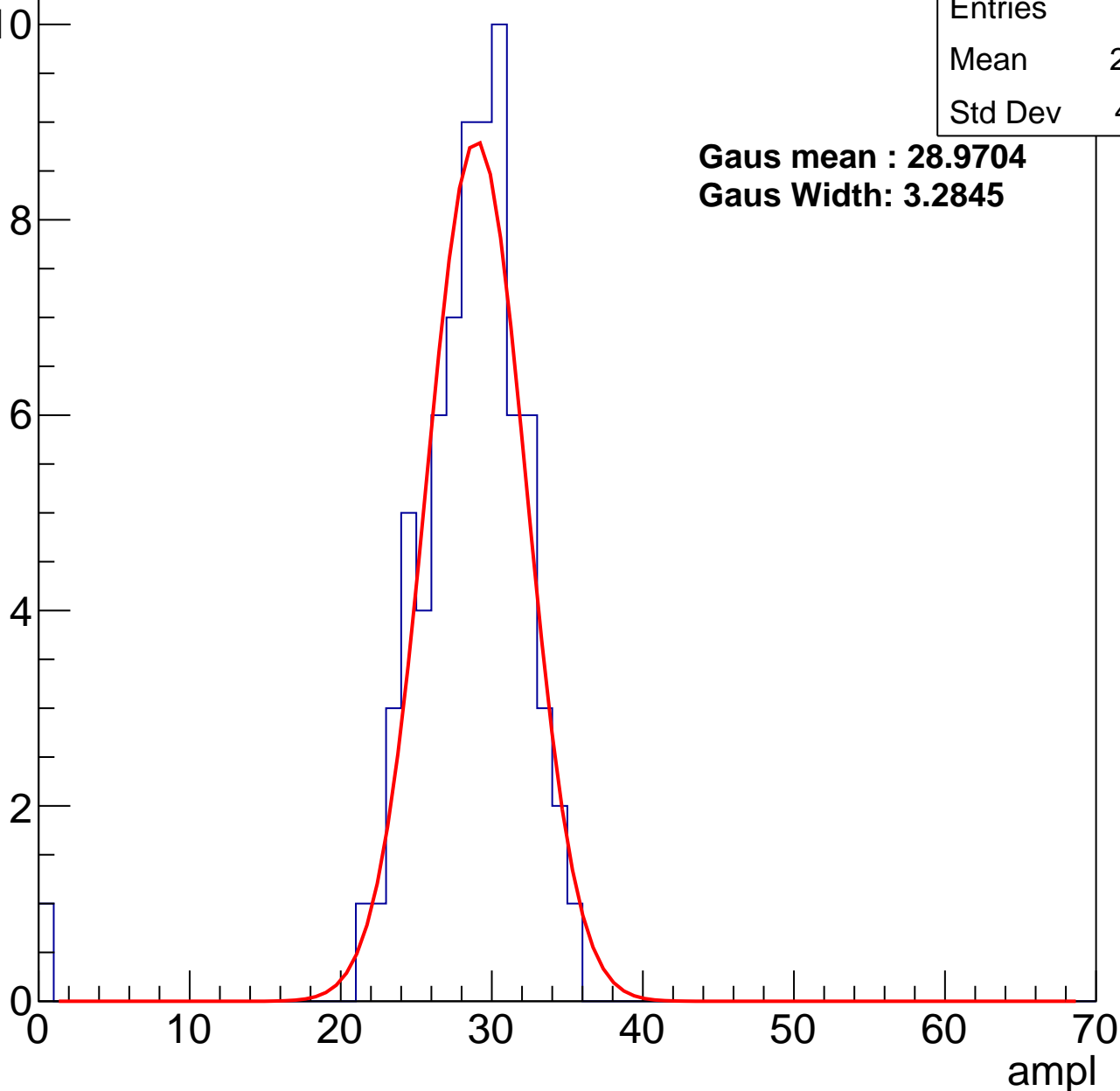
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	27.97
Std Dev	4.481

Gaus mean : 28.9704

Gaus Width: 3.2845



B1L102S, U22-ch64, adc1

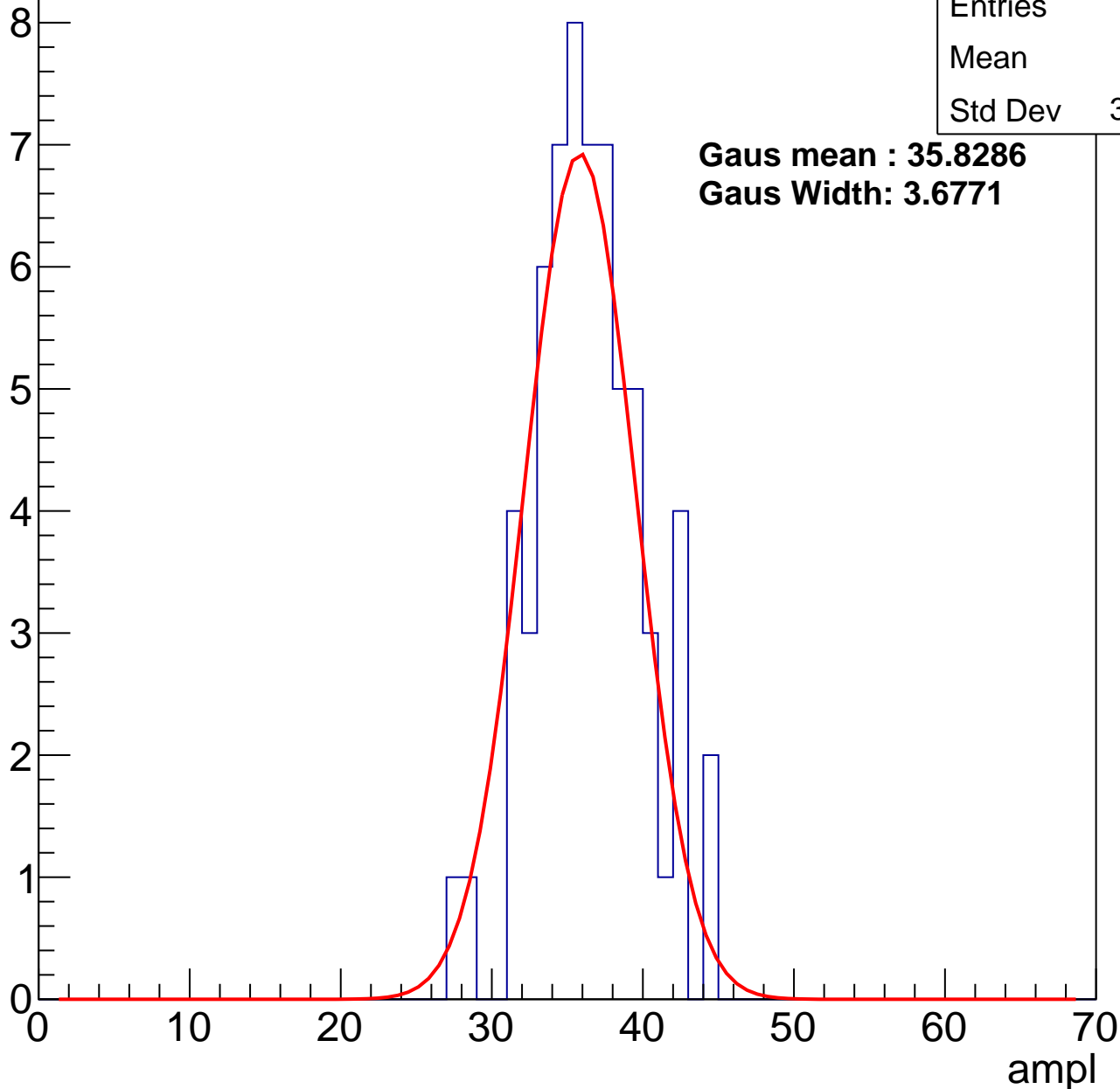
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36
Std Dev	3.536

Gaus mean : 35.8286

Gaus Width: 3.6771



B1L102S, U22-ch64, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.72
Std Dev	3.64

Gaus mean : 42.3234

Gaus Width: 4.0591

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

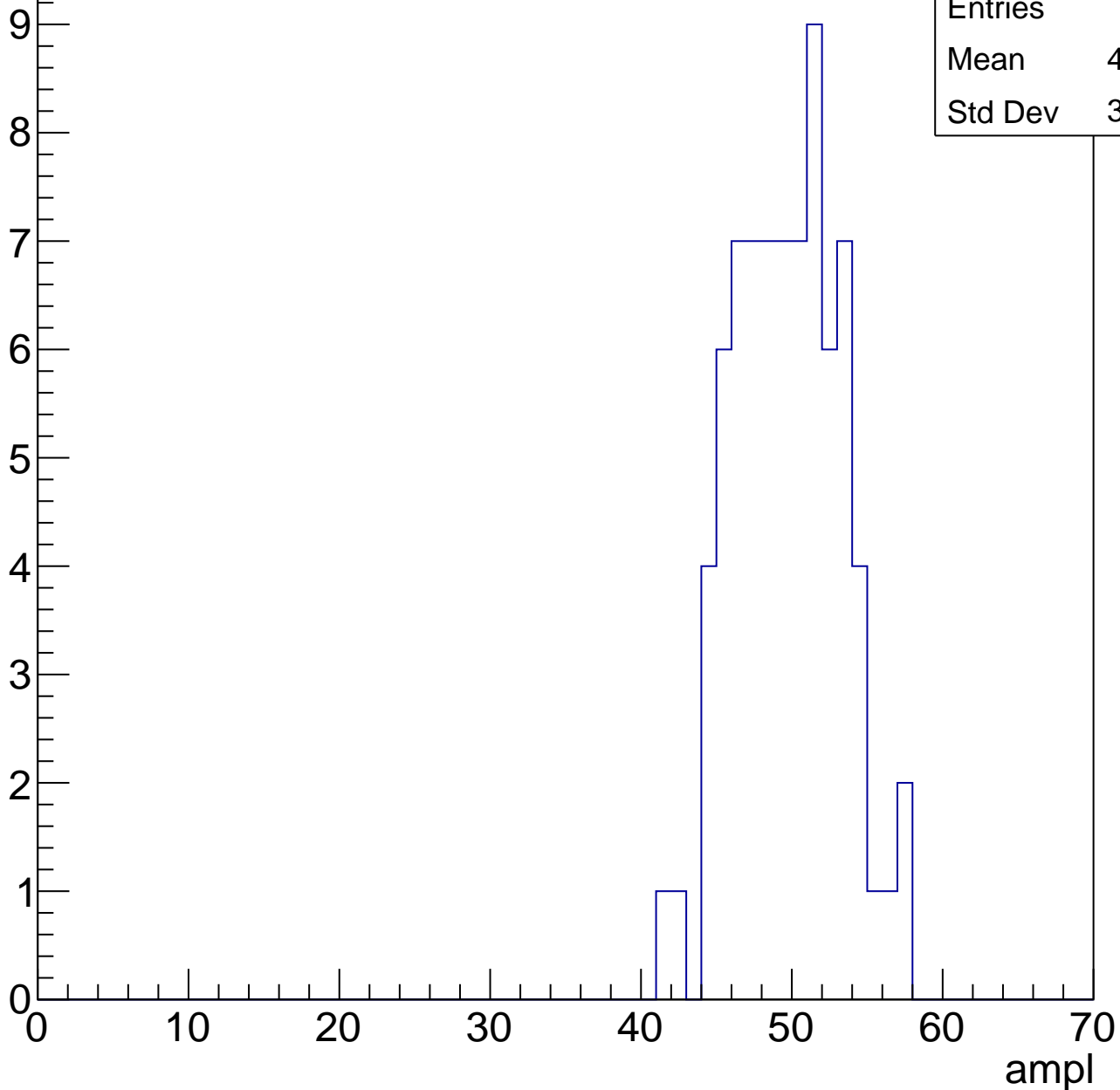


B1L102S, U22-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.25
Std Dev	3.465

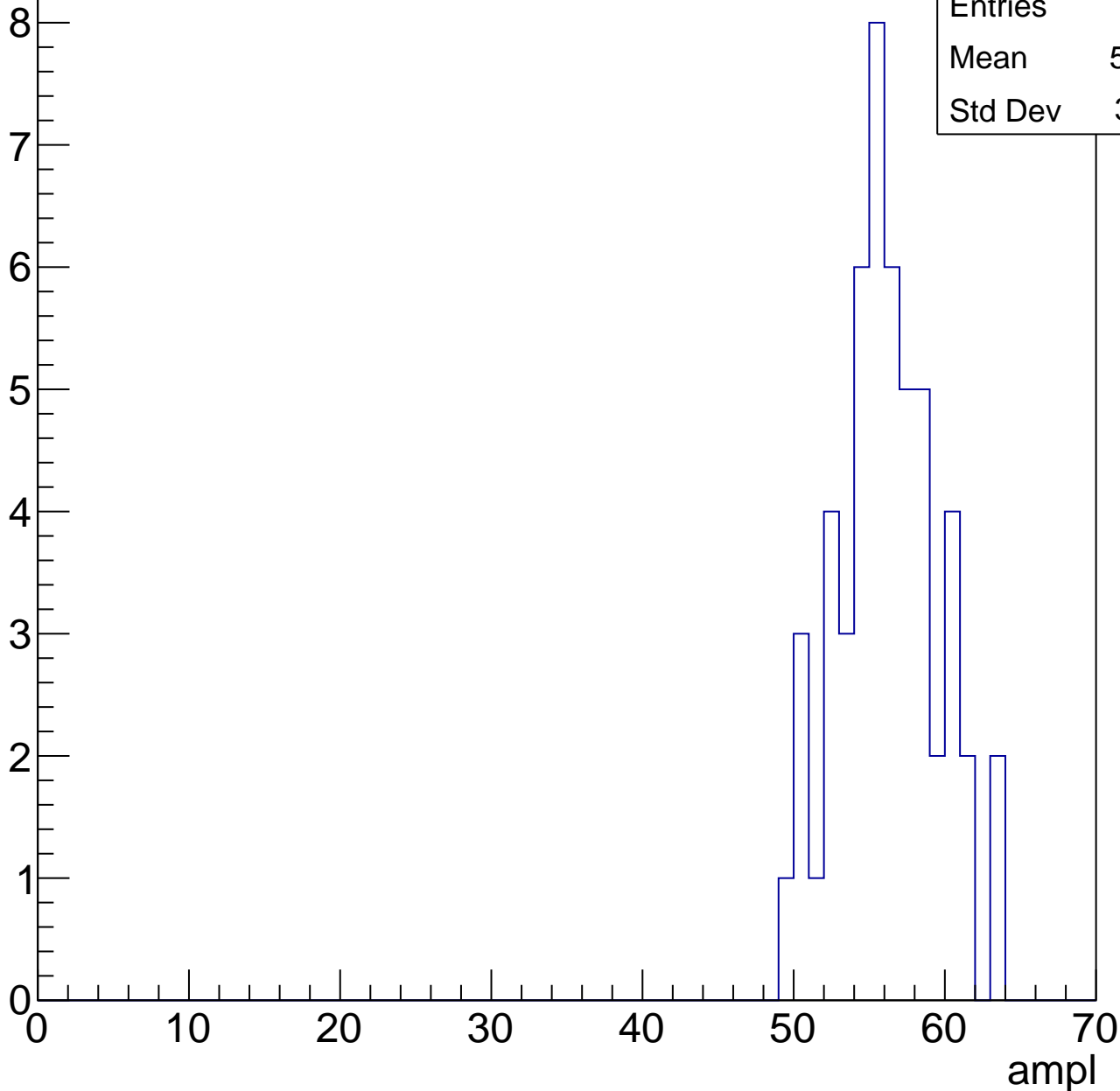


B1L102S, U22-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.73
Std Dev	3.271



B1L102S, U22-ch64, adc5

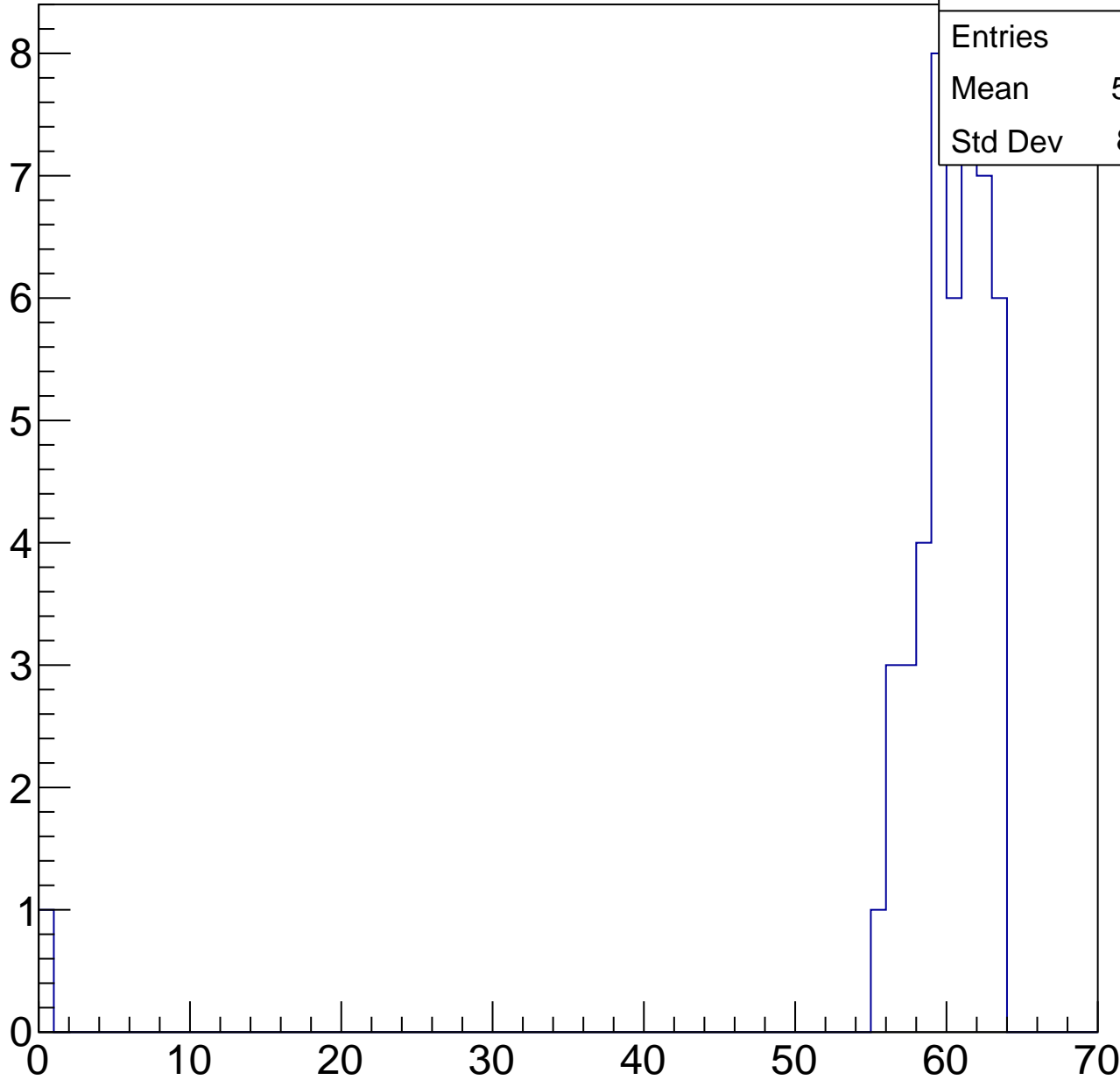
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.68
Std Dev	8.911

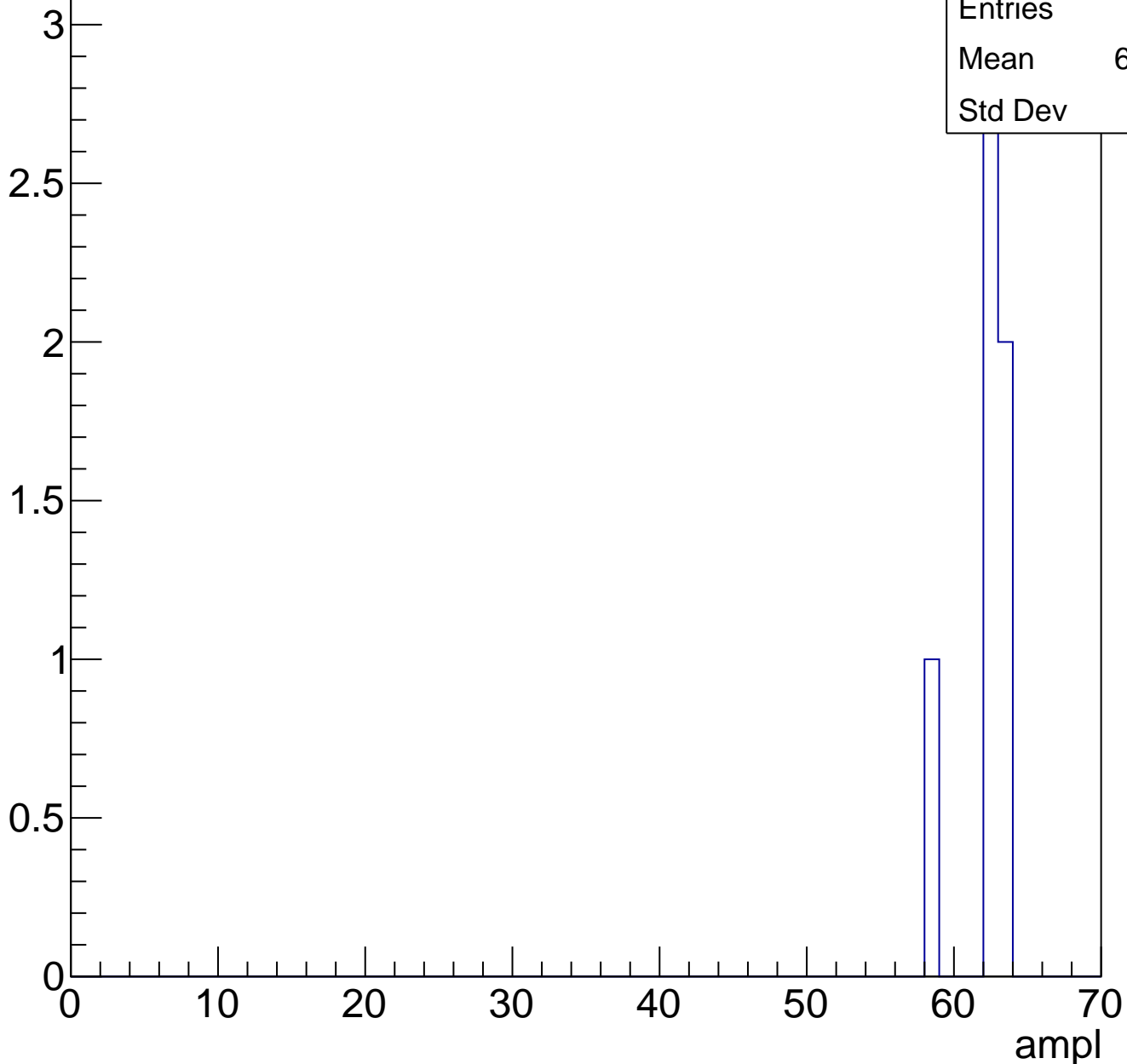
ampl



B1L102S, U22-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch65, adc0

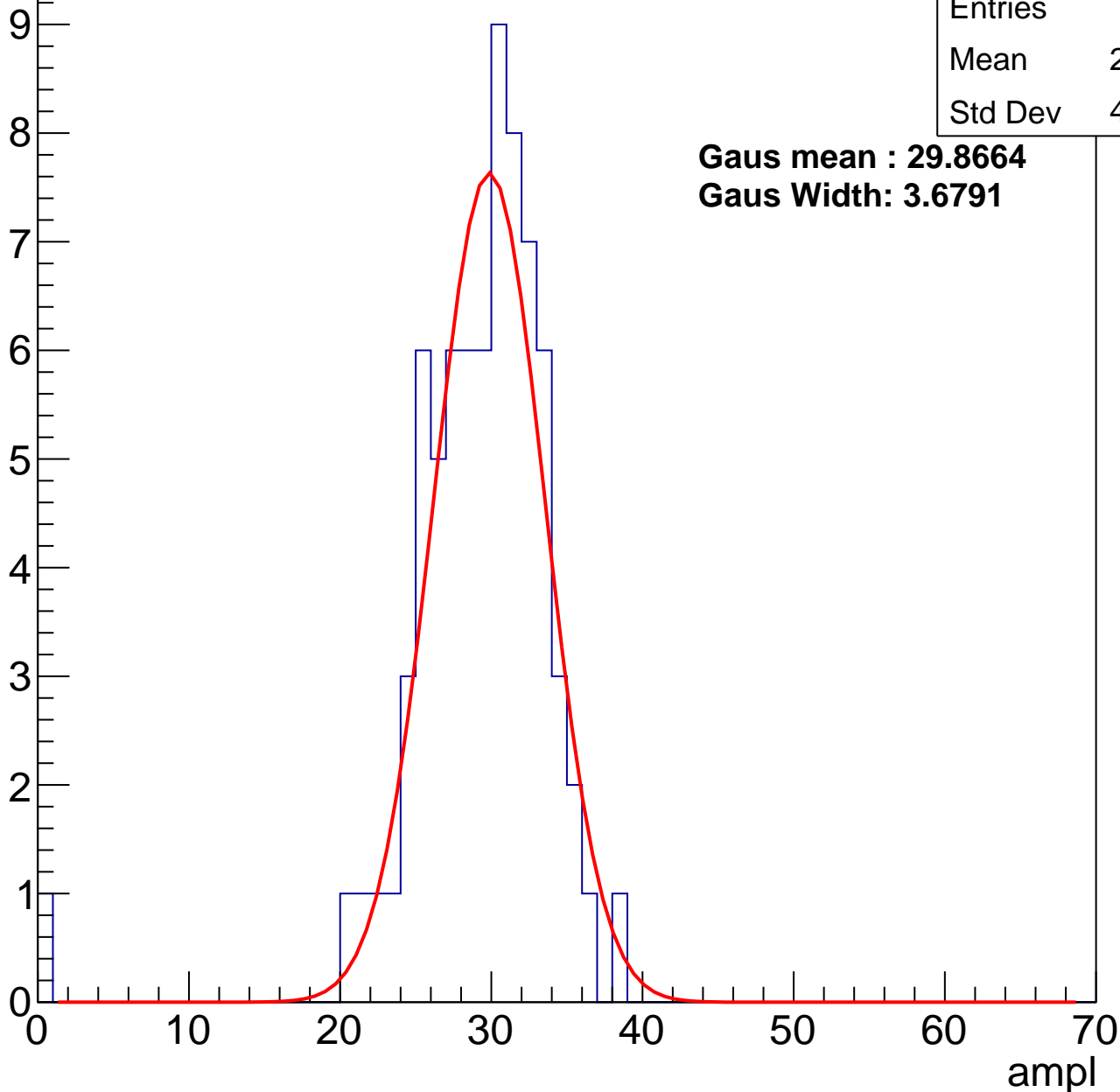
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.76
Std Dev	4.918

Gaus mean : 29.8664

Gaus Width: 3.6791



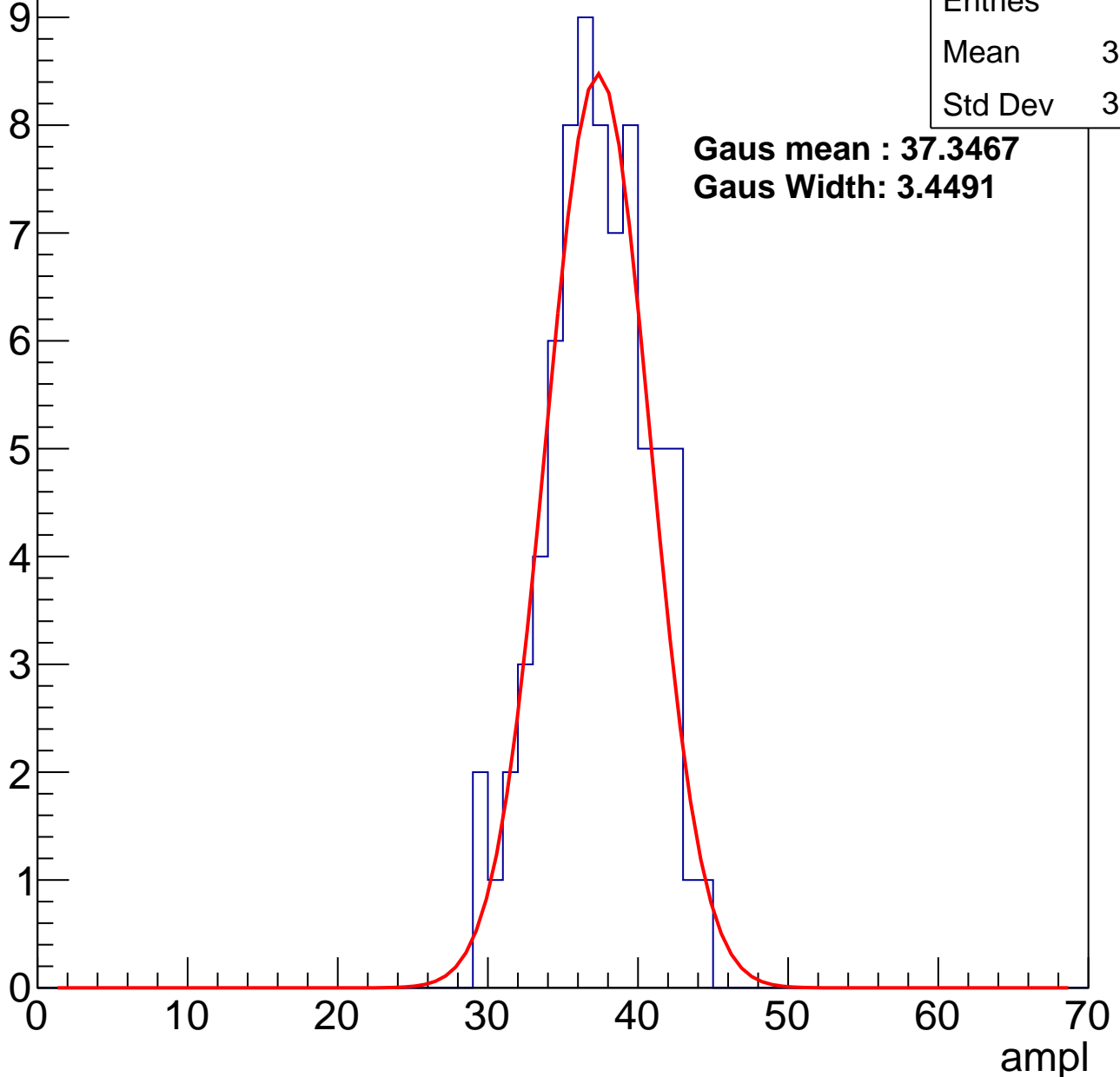
B1L102S, U22-ch65, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.83
Std Dev	3.376

Gaus mean : 37.3467
Gaus Width: 3.4491



B1L102S, U22-ch65, adc2

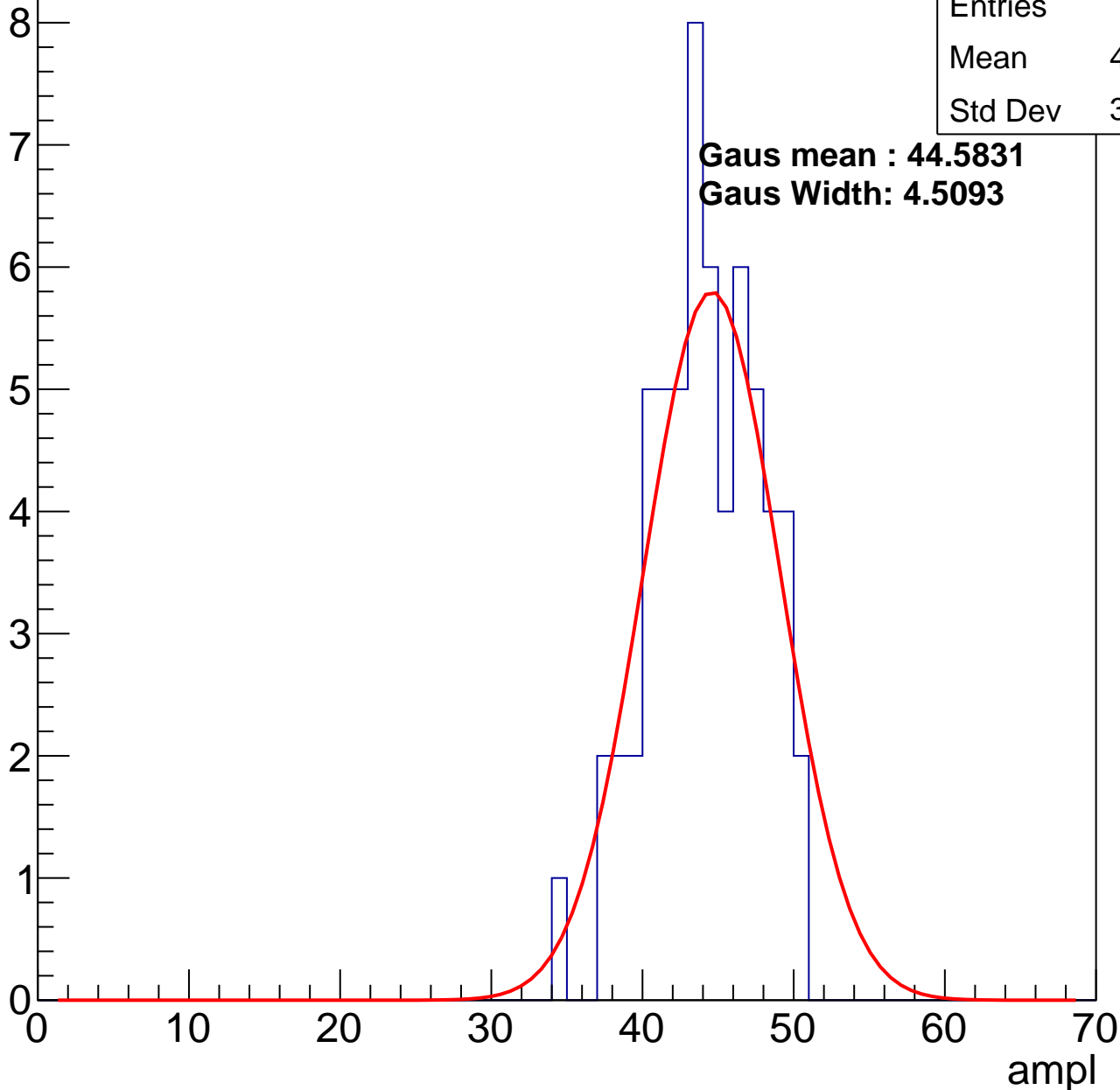
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.67
Std Dev	3.565

Gaus mean : 44.5831

Gaus Width: 4.5093

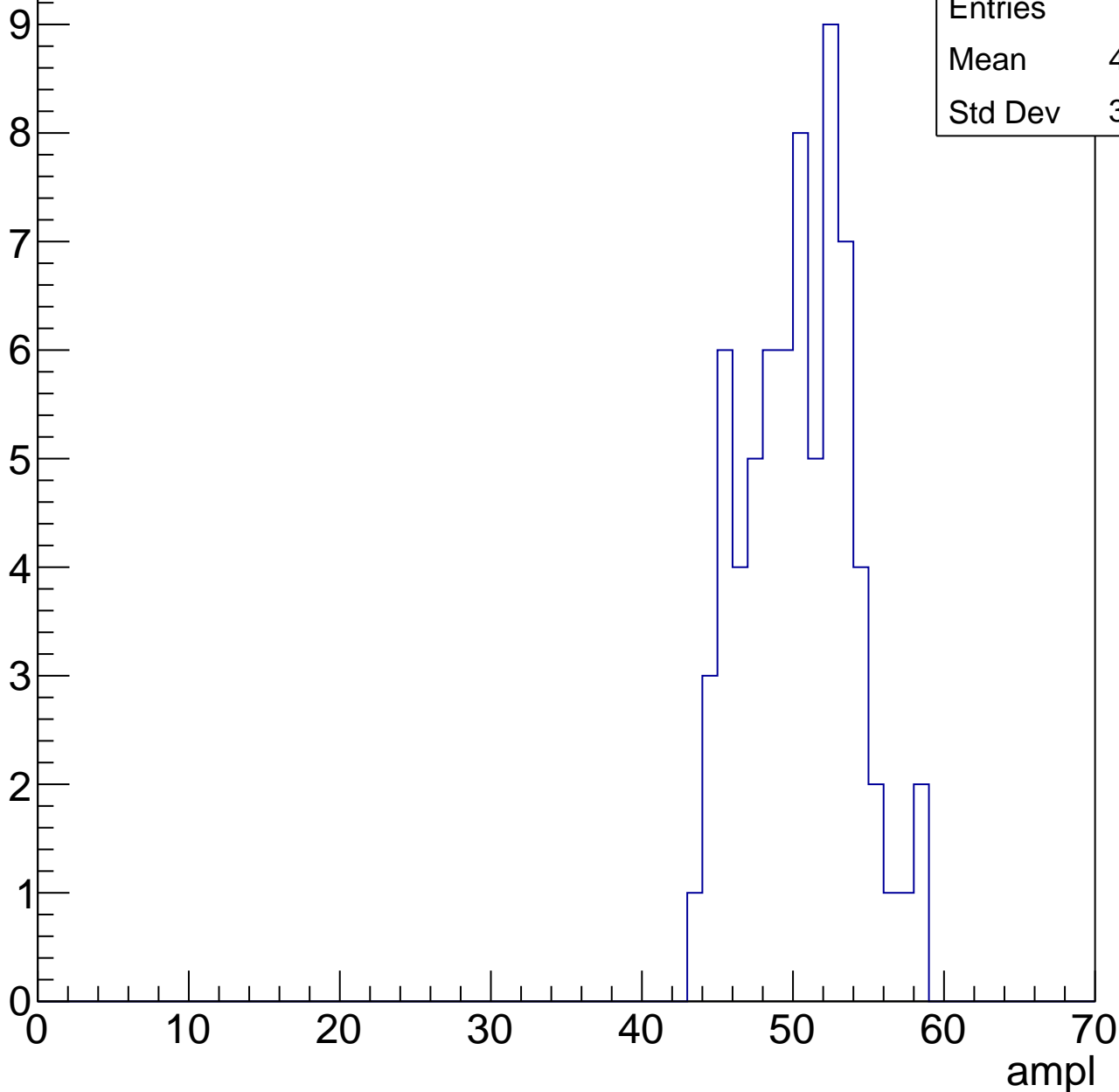


B1L102S, U22-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.93
Std Dev	3.539

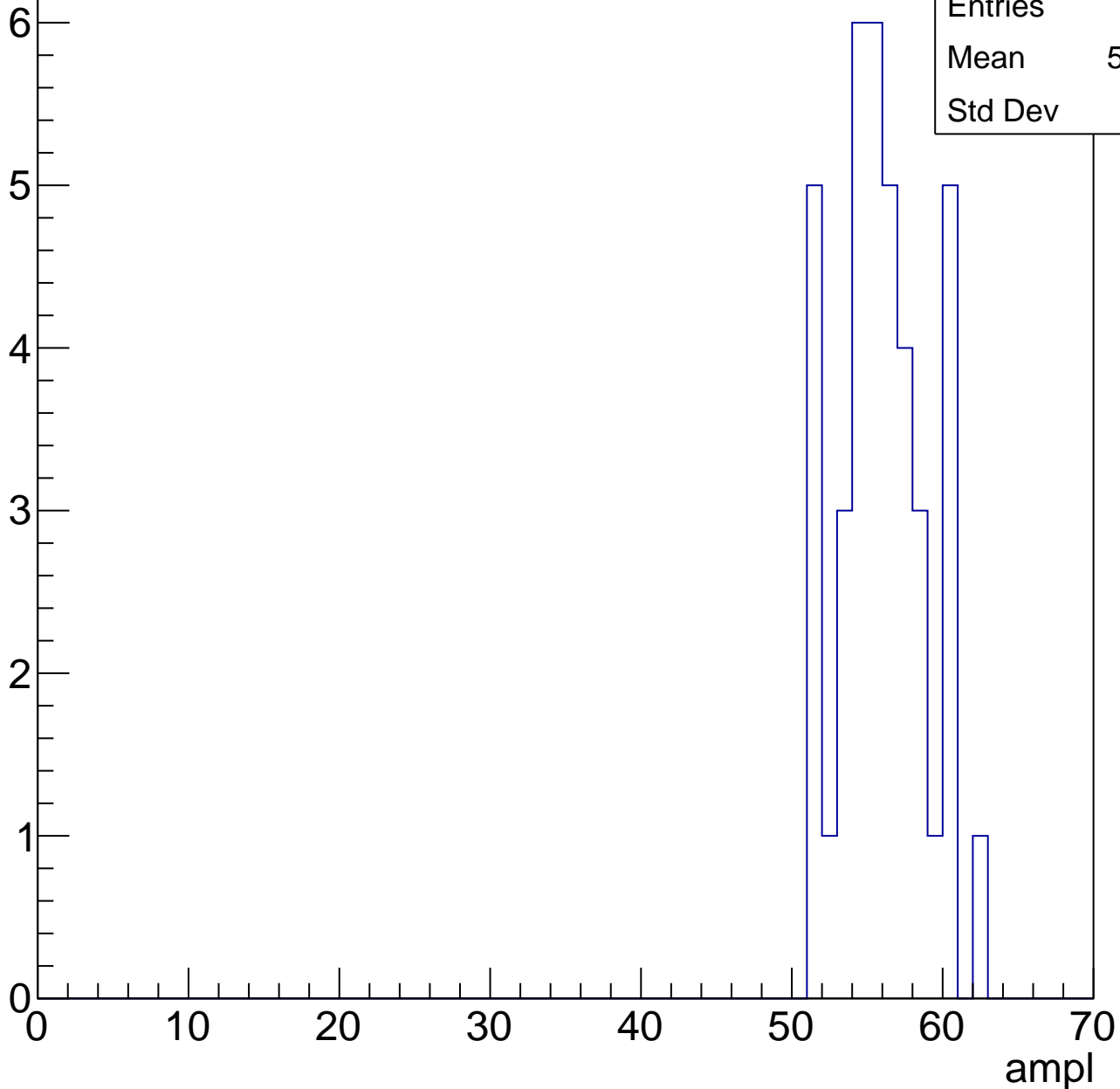


B1L102S, U22-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	55.58
Std Dev	2.88

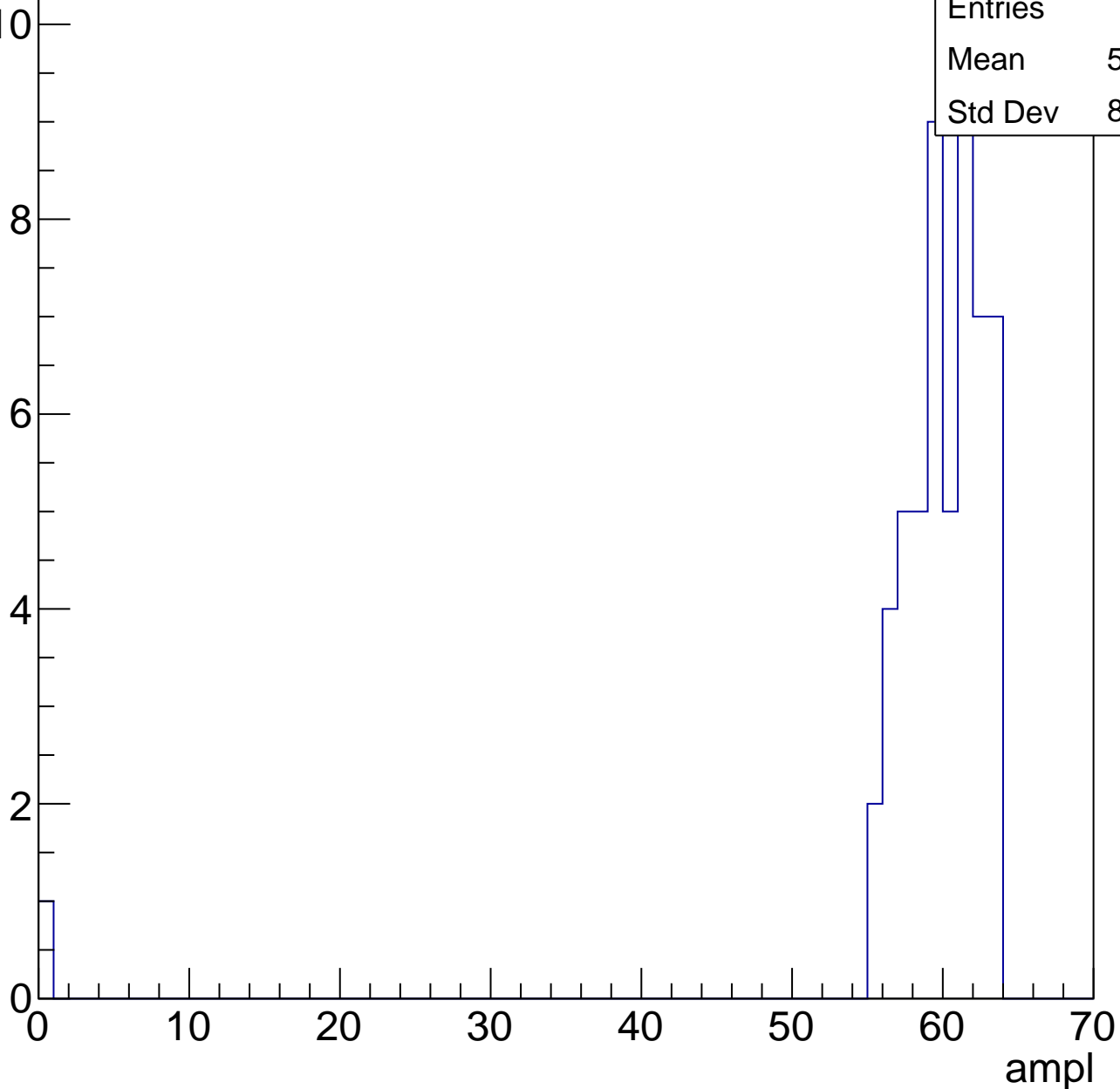


B1L102S, U22-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.64
Std Dev	8.298



B1L102S, U22-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

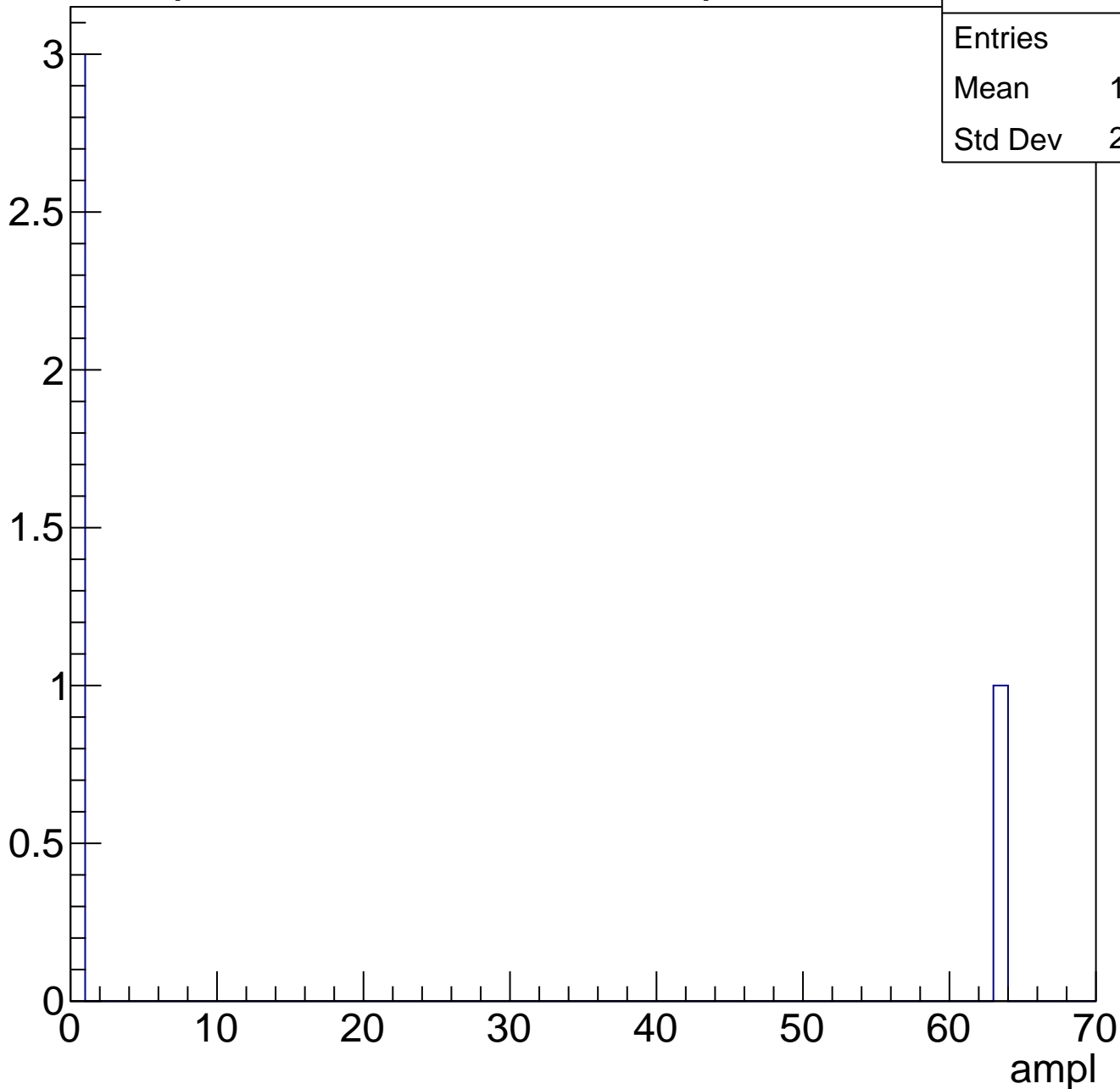
Entry



B1L102S, U22-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch66, adc0

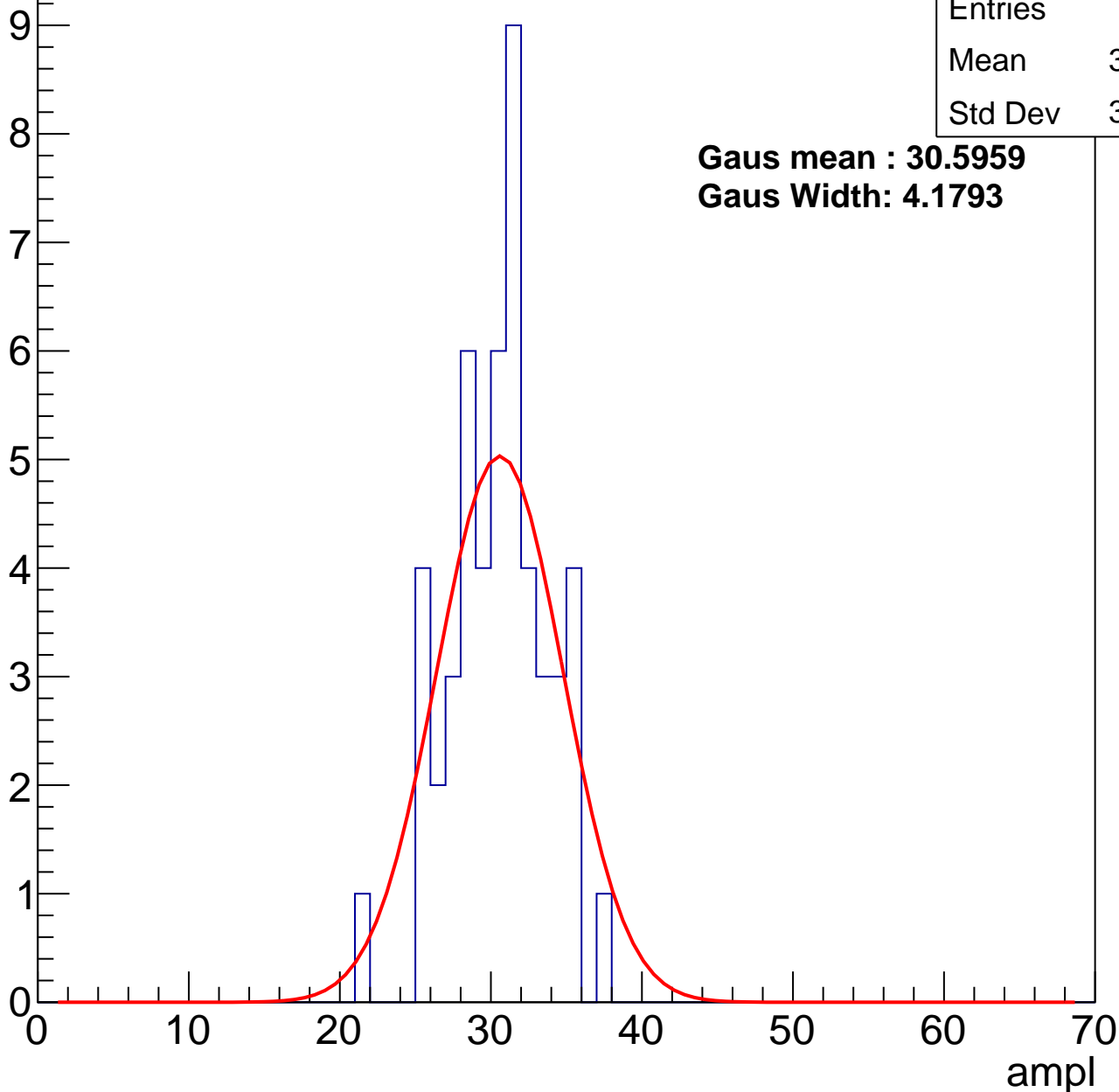
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	30.06
Std Dev	3.215

Gaus mean : 30.5959

Gaus Width: 4.1793



B1L102S, U22-ch66, adc1

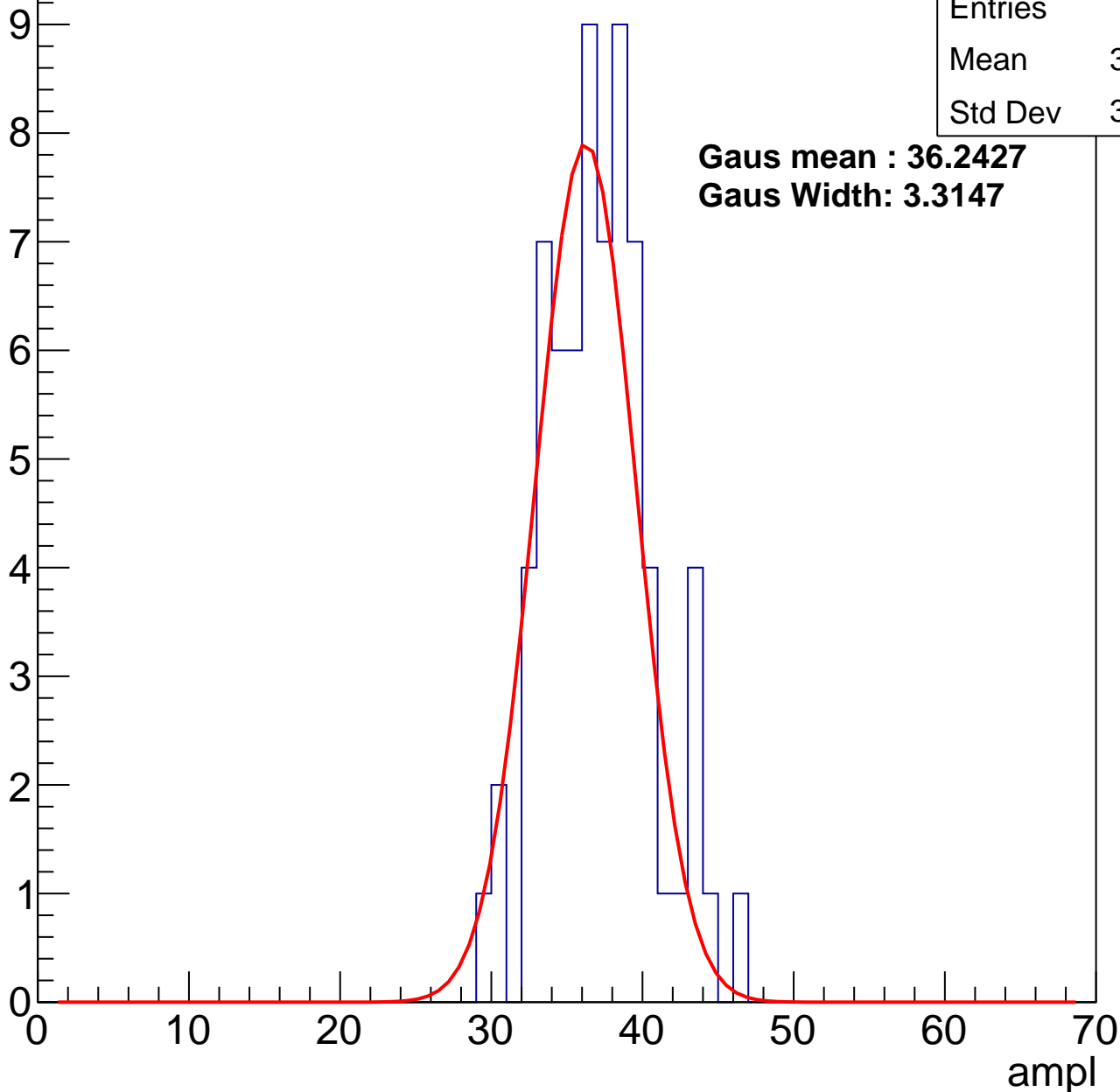
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.64
Std Dev	3.464

Gaus mean : 36.2427

Gaus Width: 3.3147



B1L102S, U22-ch66, adc2

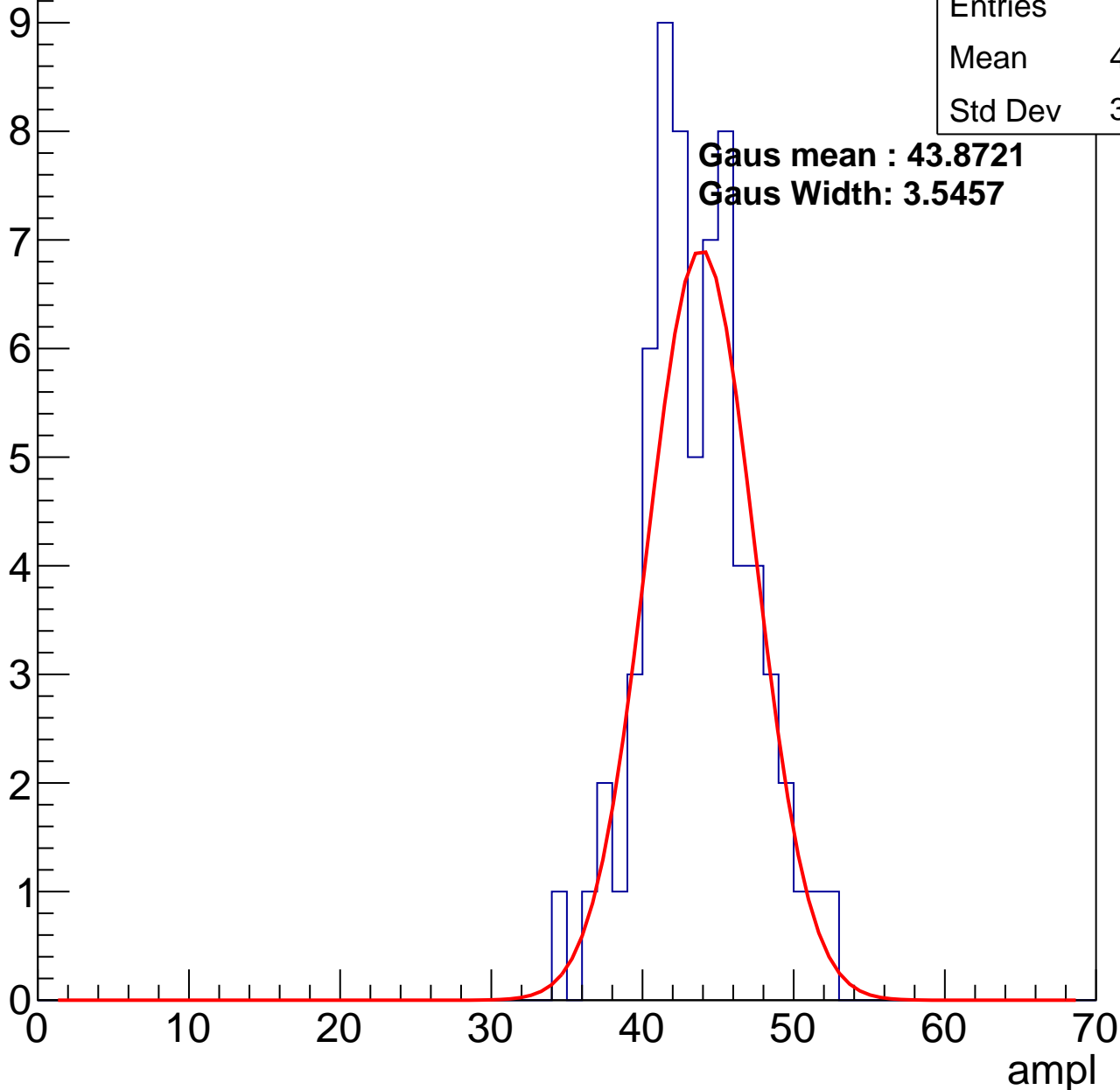
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.19
Std Dev	3.563

Gaus mean : 43.8721

Gaus Width: 3.5457

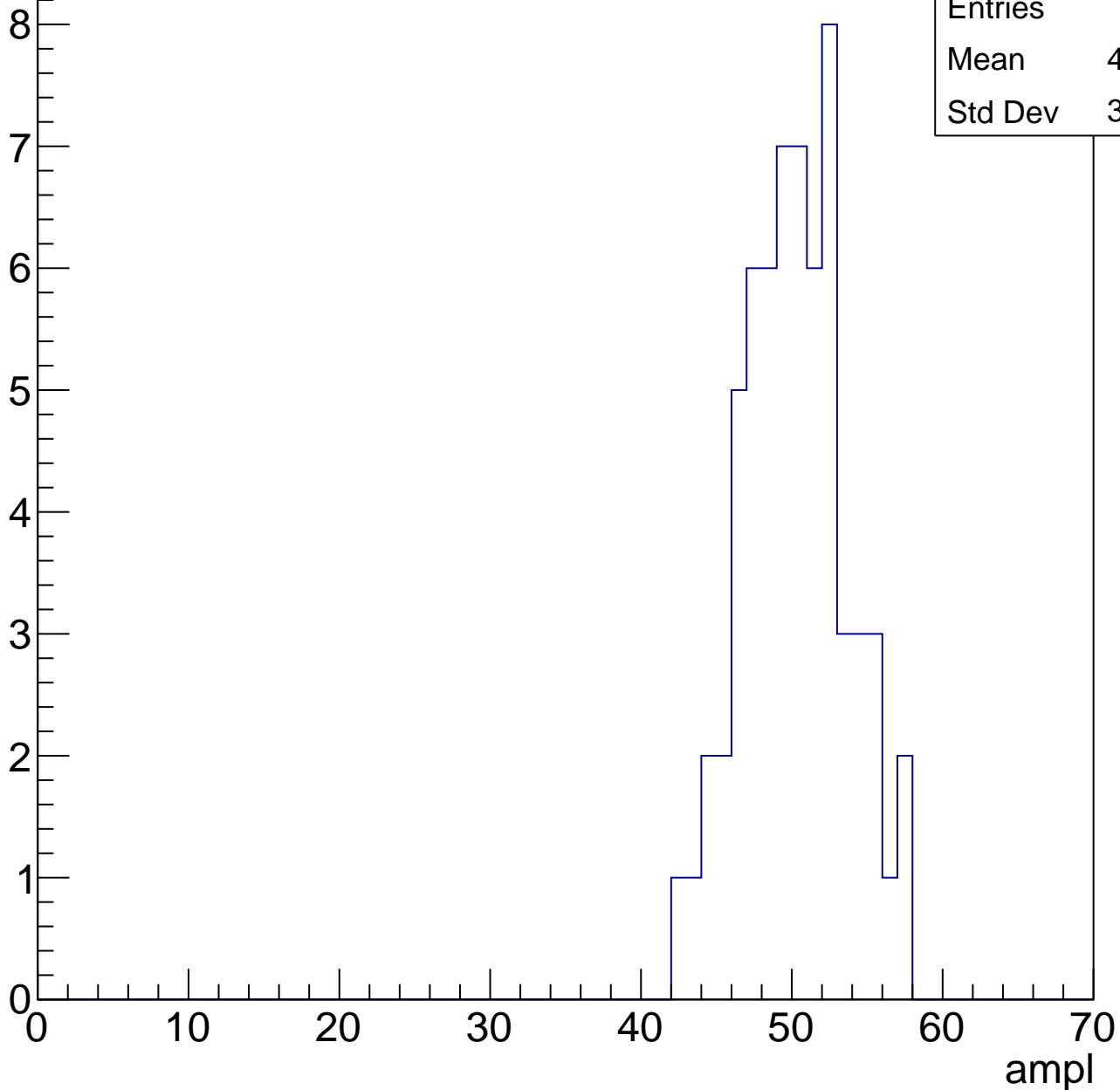


B1L102S, U22-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.75
Std Dev	3.376

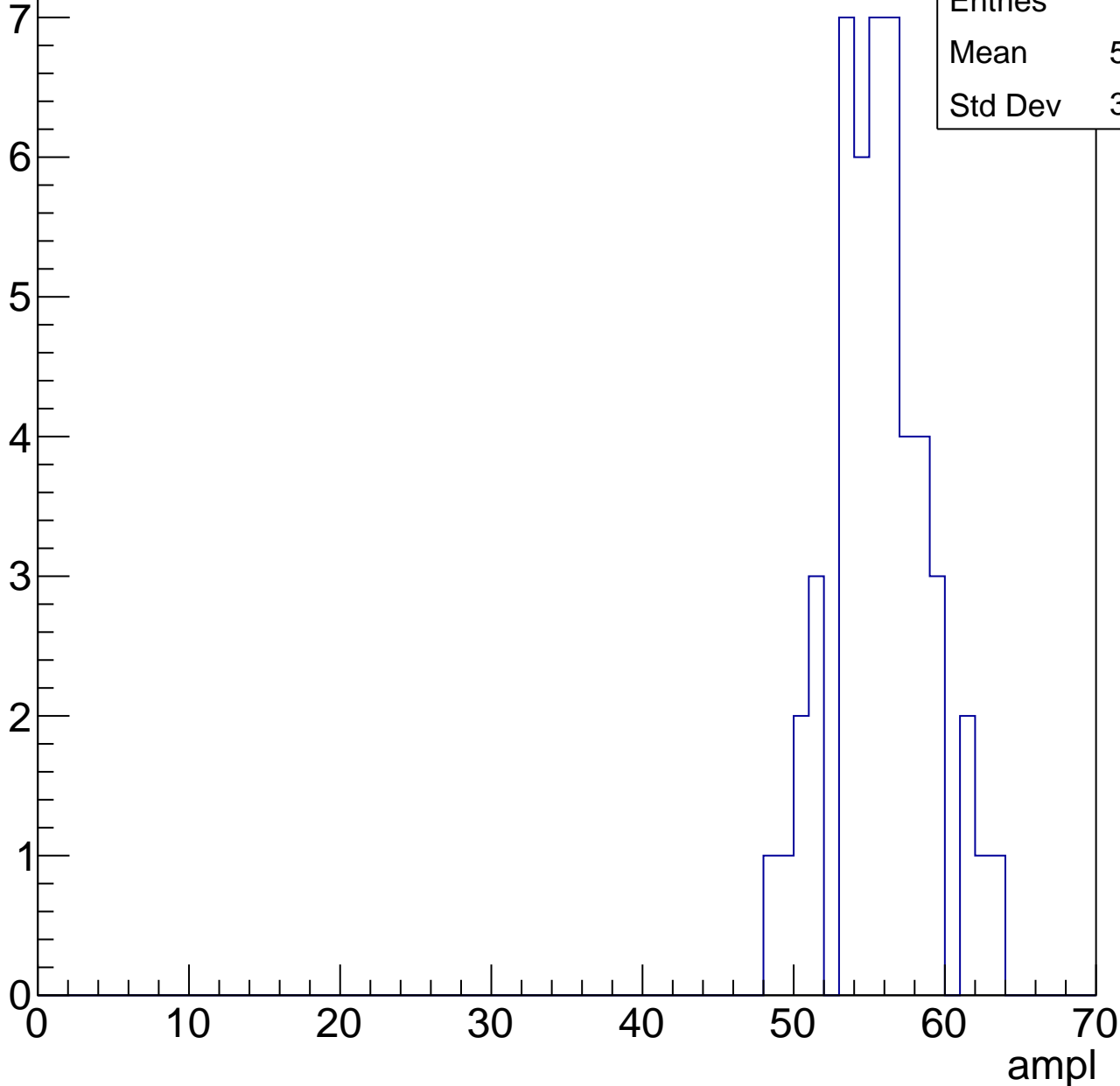


B1L102S, U22-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.22
Std Dev	3.215

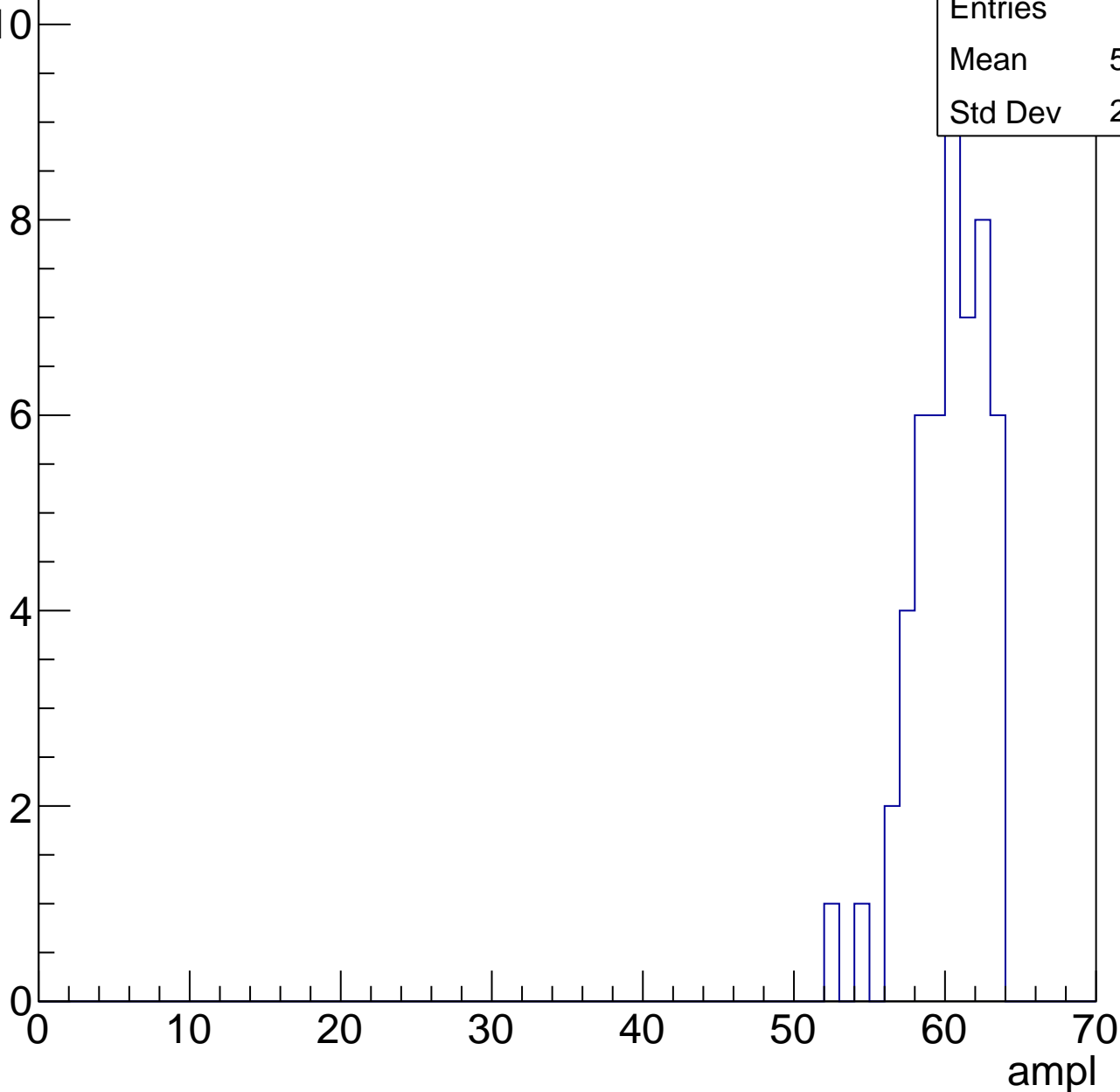


B1L102S, U22-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

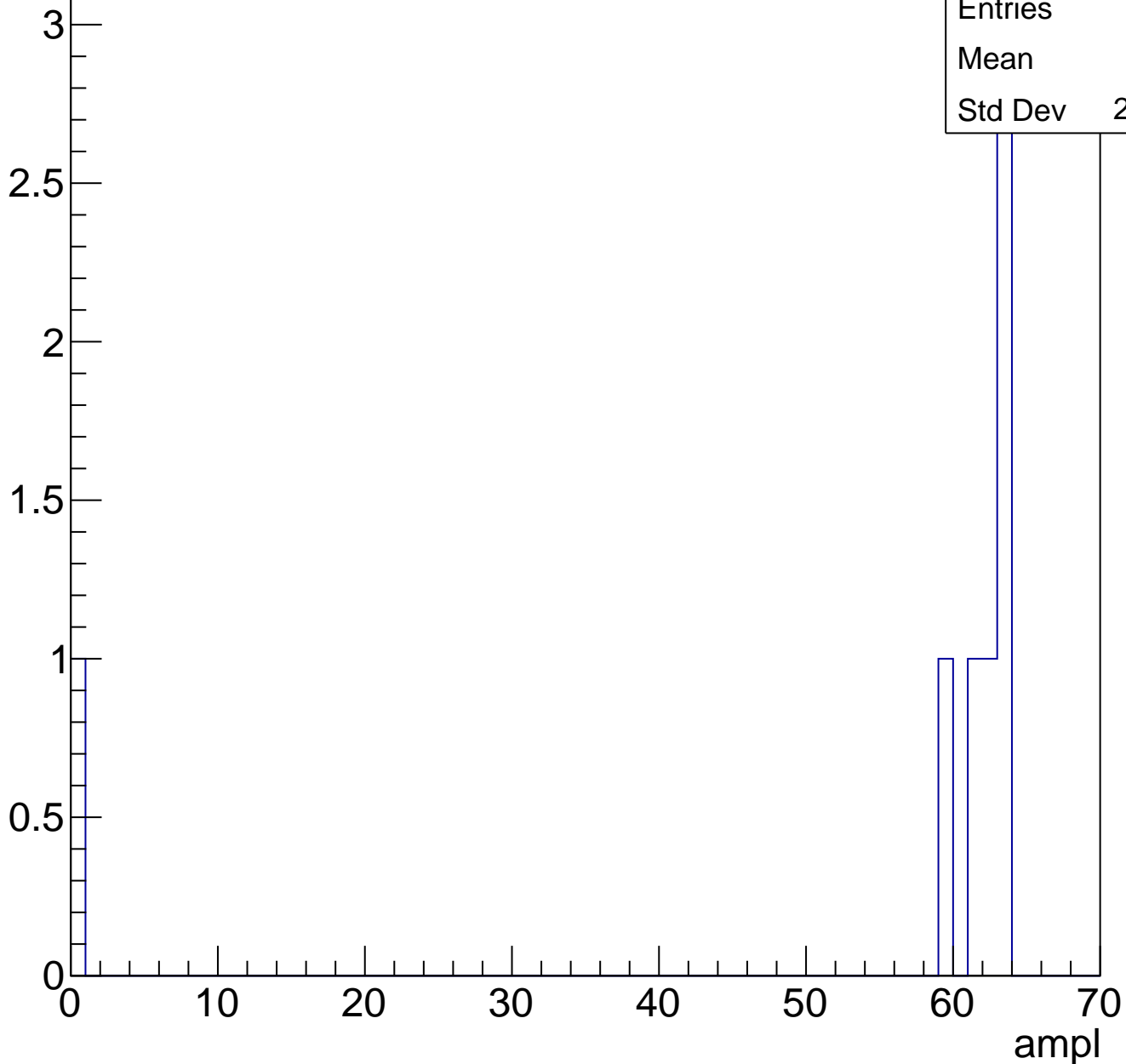
Entries	51
Mean	59.78
Std Dev	2.379



B1L102S, U22-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch67, adc0

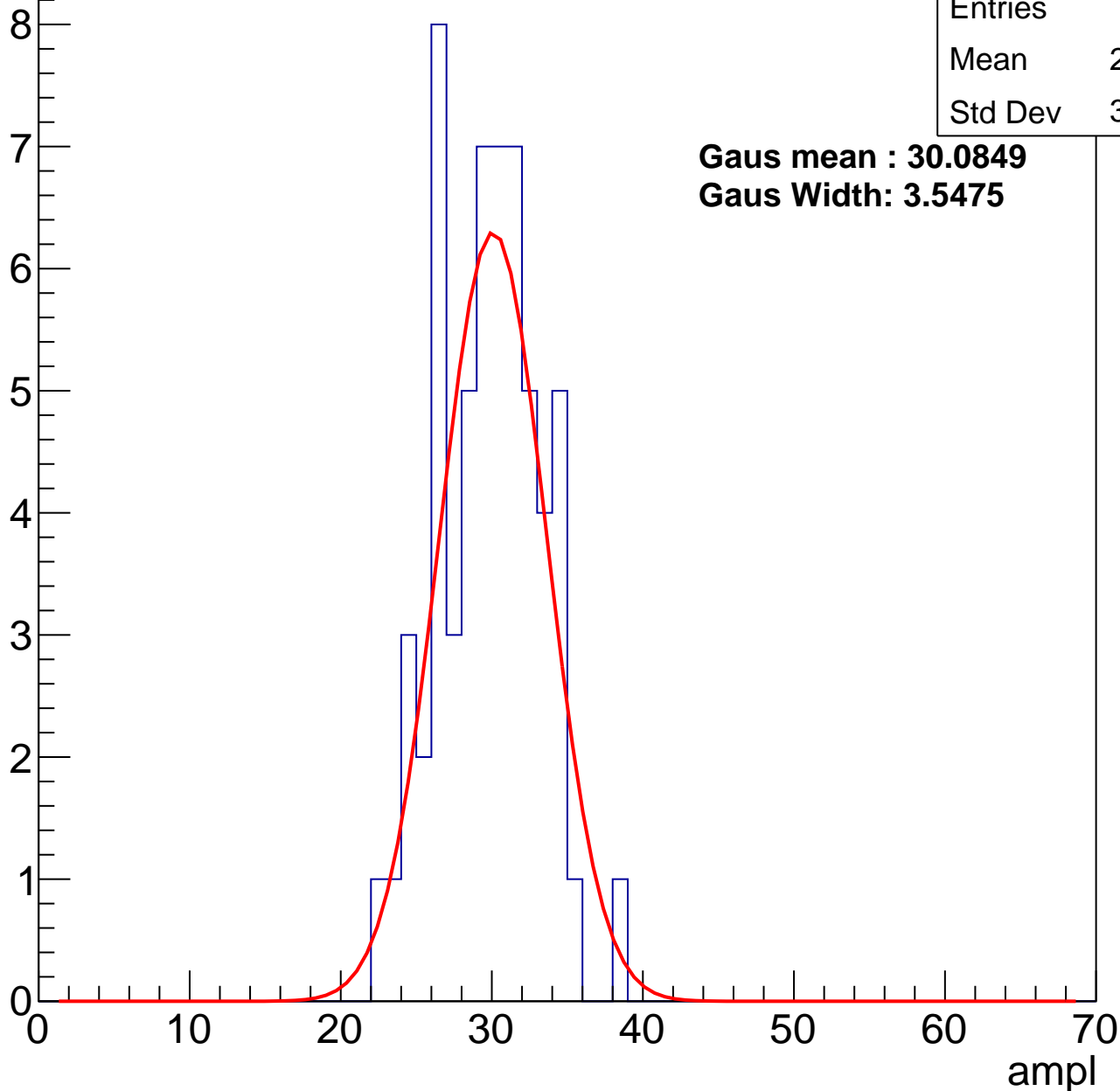
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	29.35
Std Dev	3.316

Gaus mean : 30.0849

Gaus Width: 3.5475



B1L102S, U22-ch67, adc1

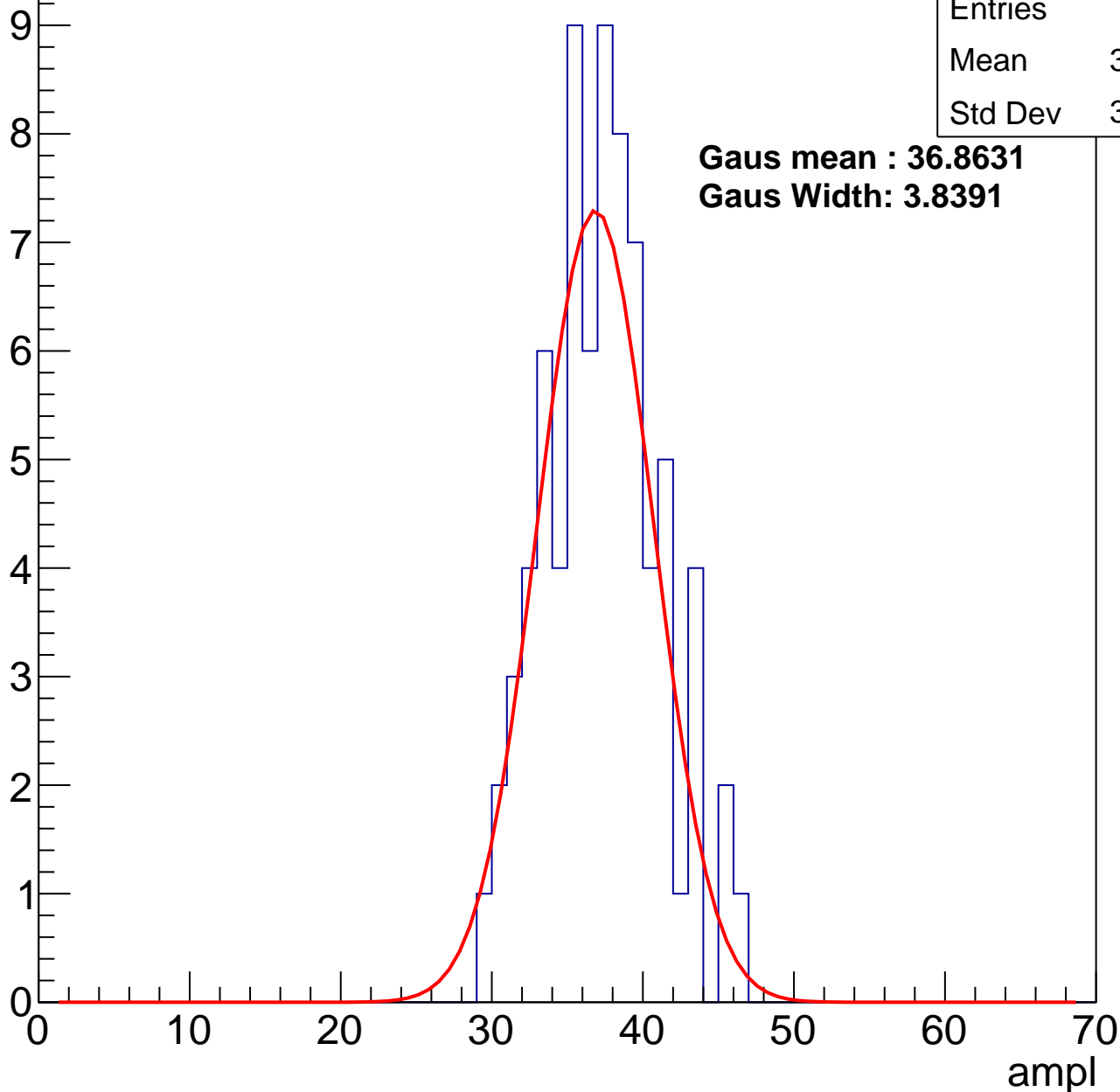
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.84
Std Dev	3.756

Gaus mean : 36.8631

Gaus Width: 3.8391

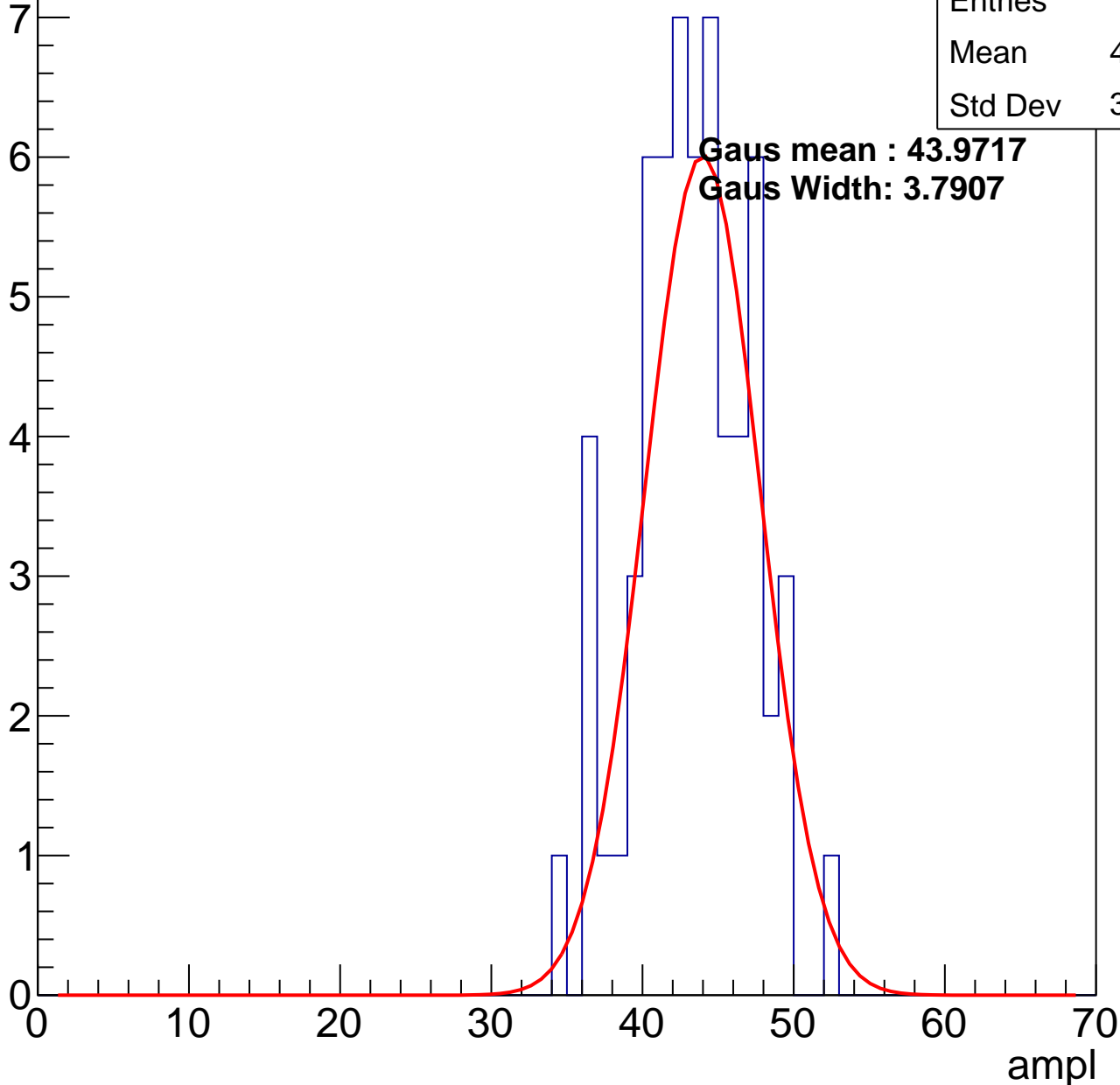


B1L102S, U22-ch67, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.85
Std Dev	3.732

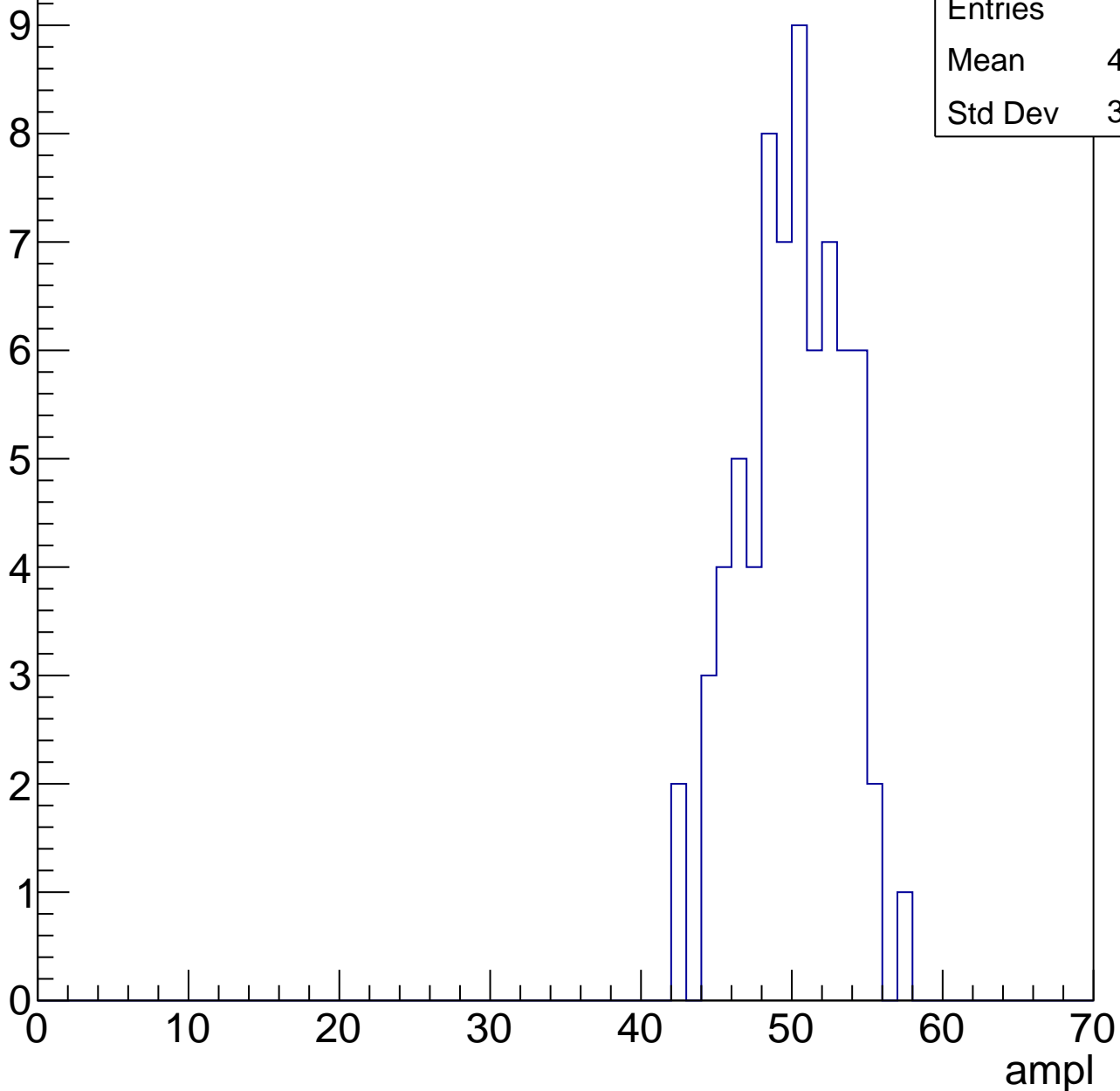


B1L102S, U22-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.57
Std Dev	3.297

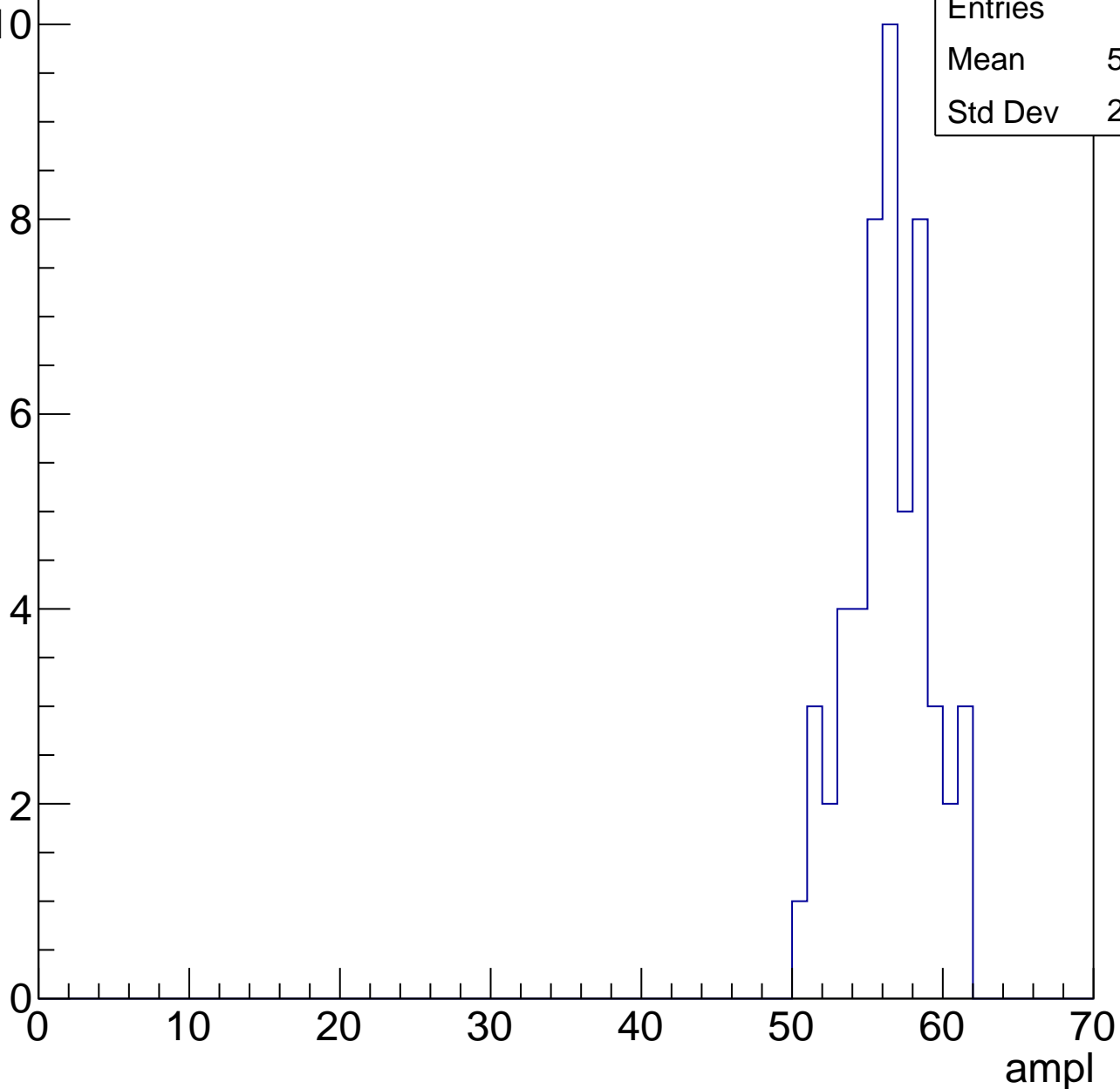


B1L102S, U22-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.92
Std Dev	2.655

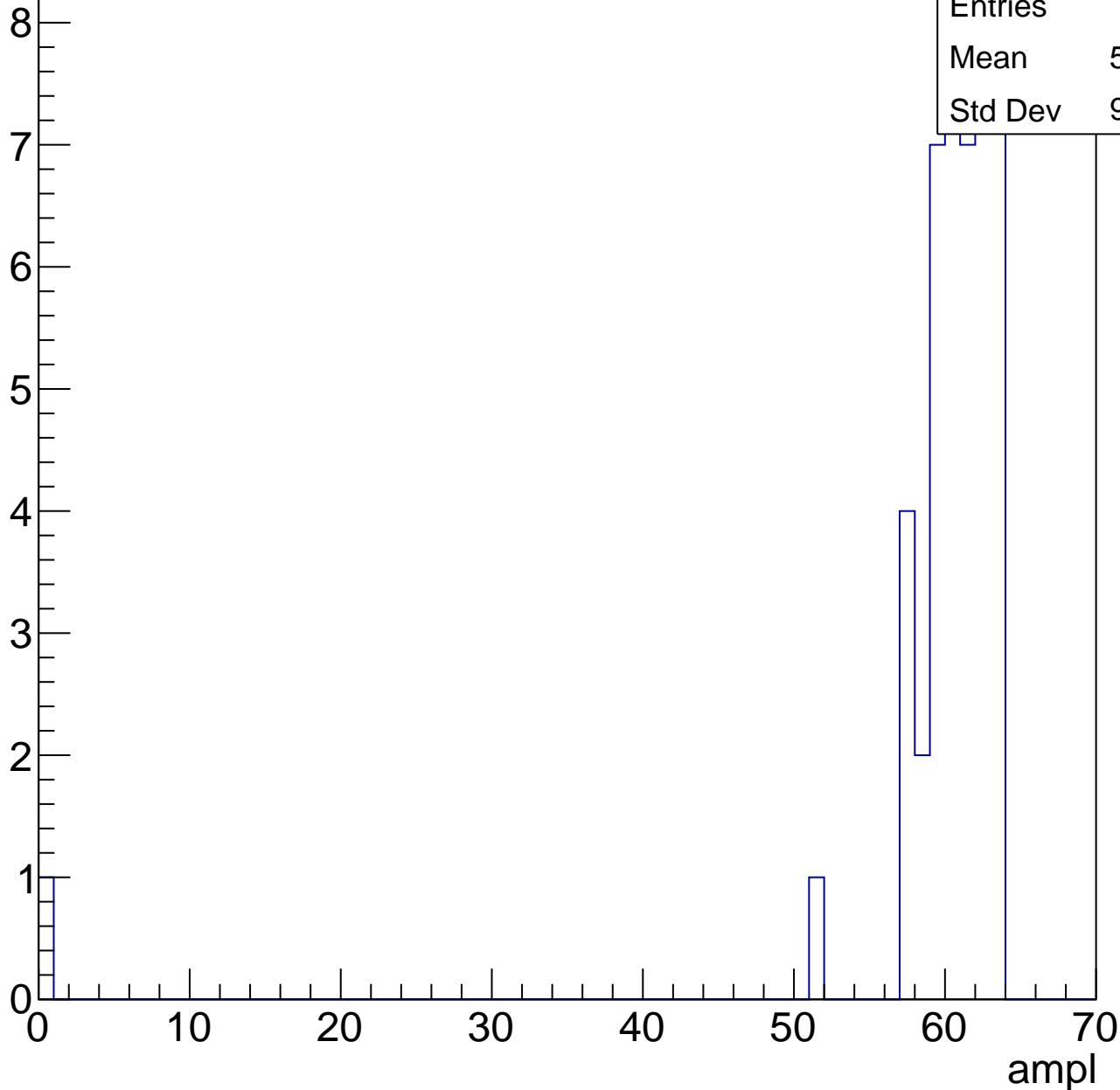


B1L102S, U22-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.02
Std Dev	9.088



B1L102S, U22-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch68, adc0

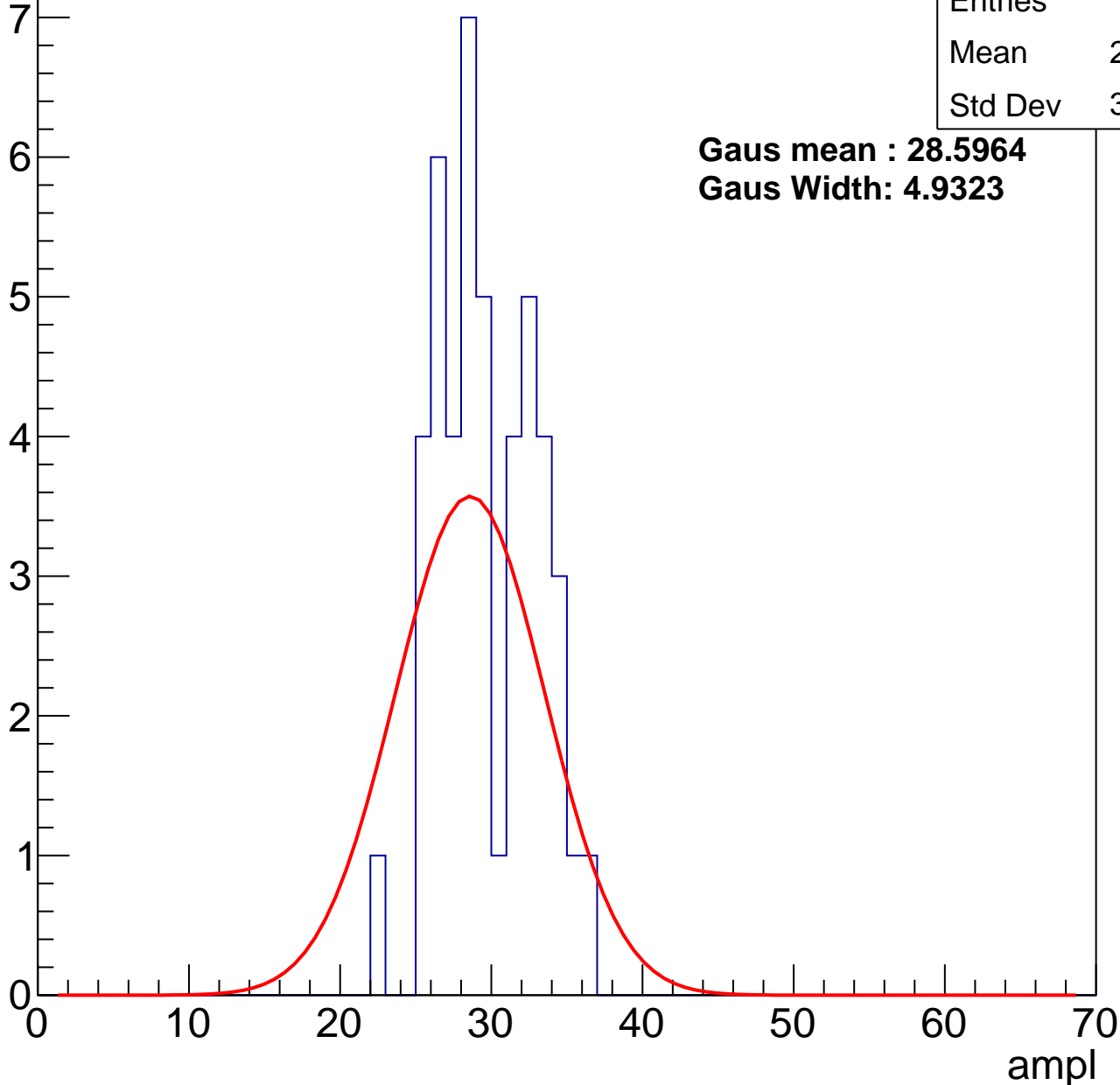
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	29.26
Std Dev	3.206

Gaus mean : 28.5964

Gaus Width: 4.9323



B1L102S, U22-ch68, adc1

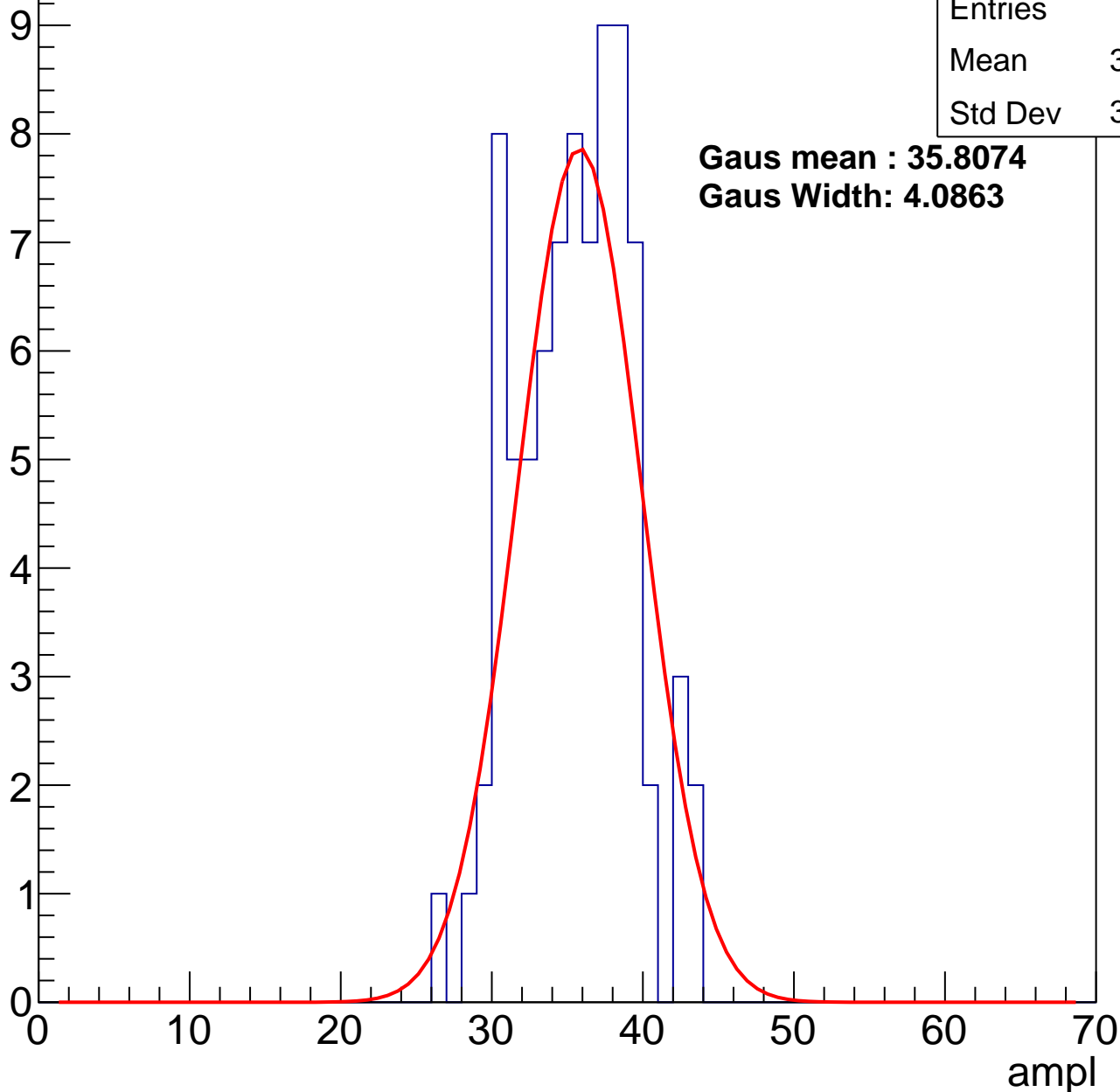
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.06
Std Dev	3.694

Gaus mean : 35.8074

Gaus Width: 4.0863



B1L102S, U22-ch68, adc2

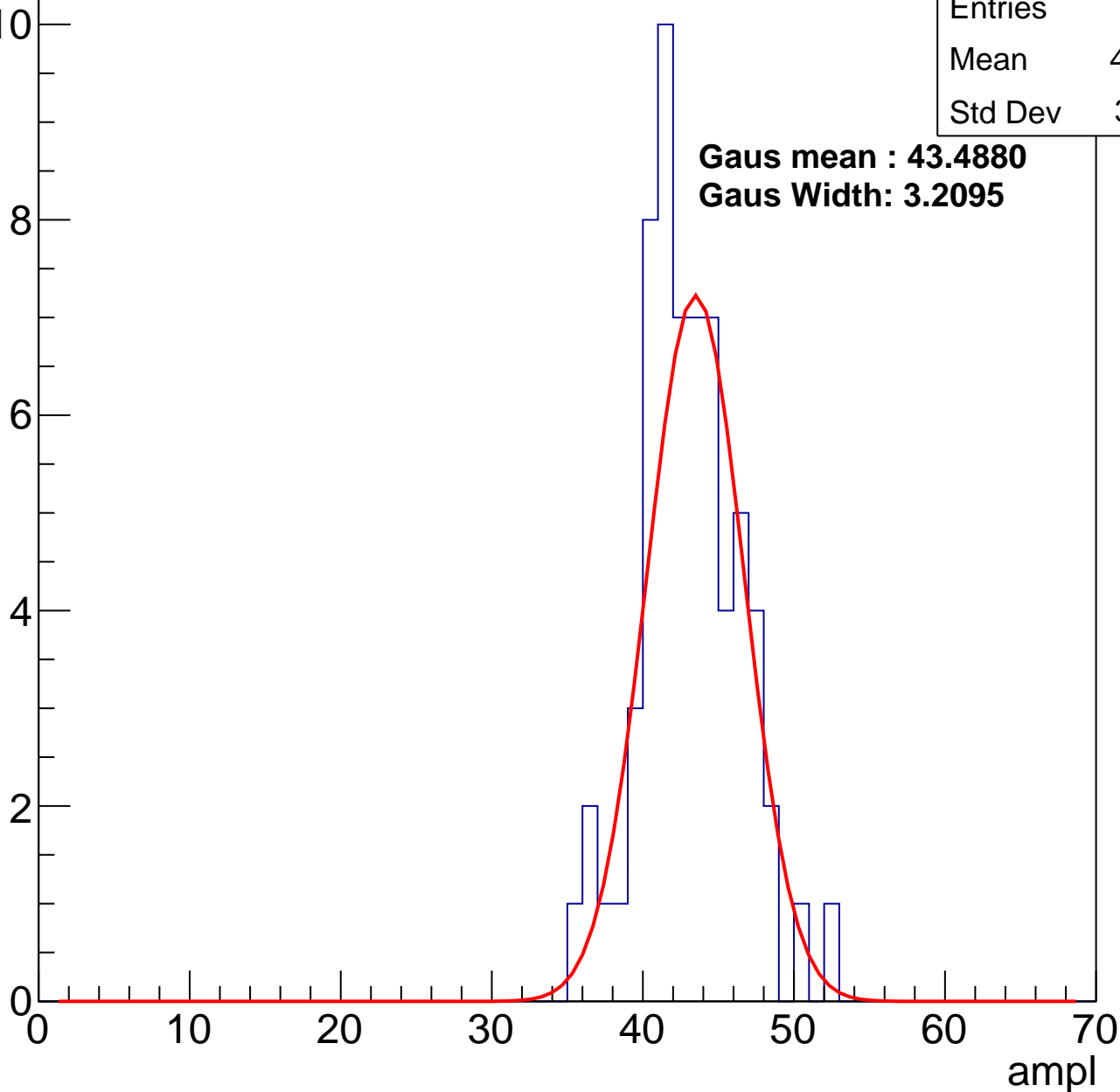
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.62
Std Dev	3.291

Gaus mean : 43.4880

Gaus Width: 3.2095

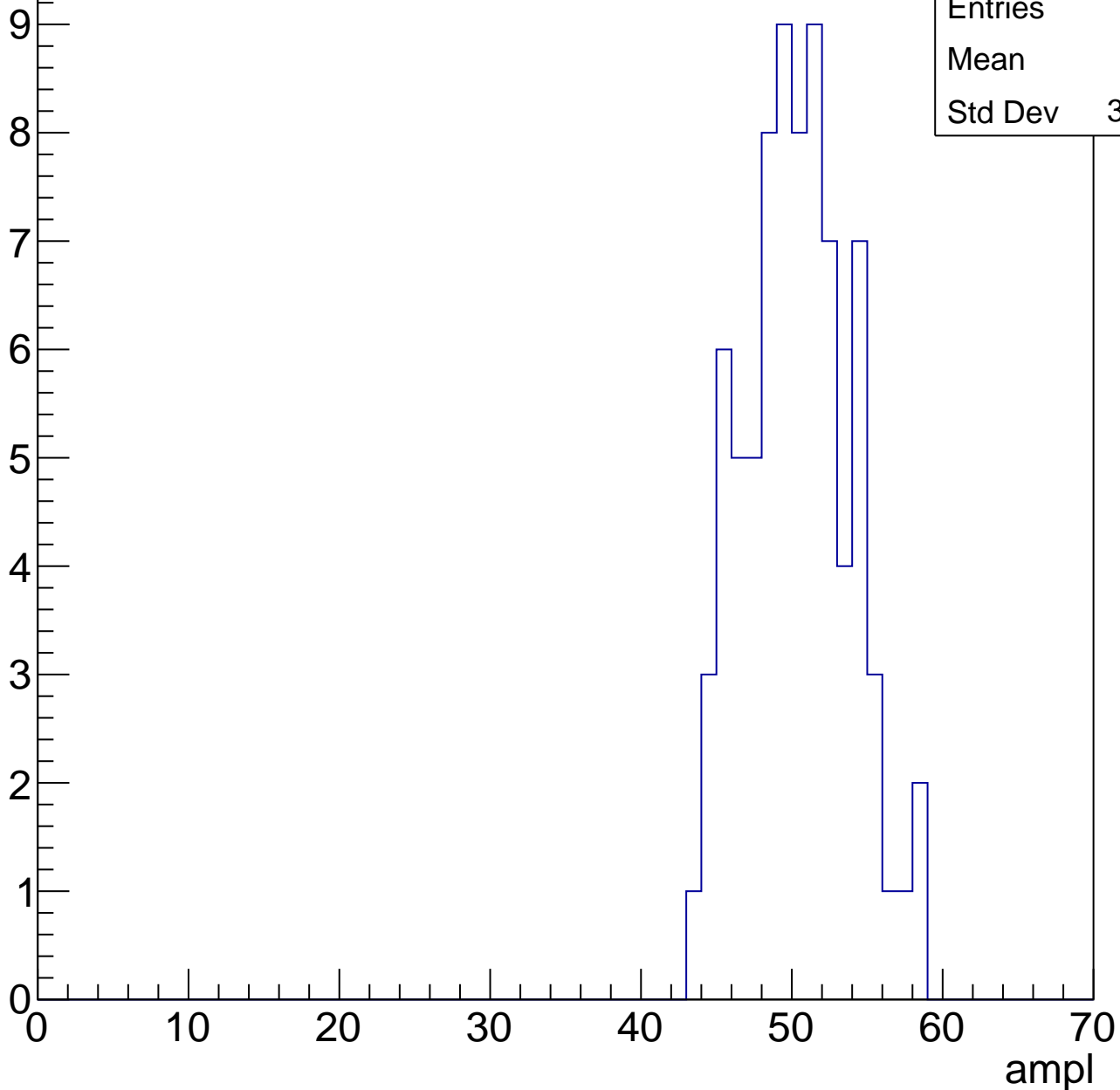


B1L102S, U22-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	49.9
Std Dev	3.459

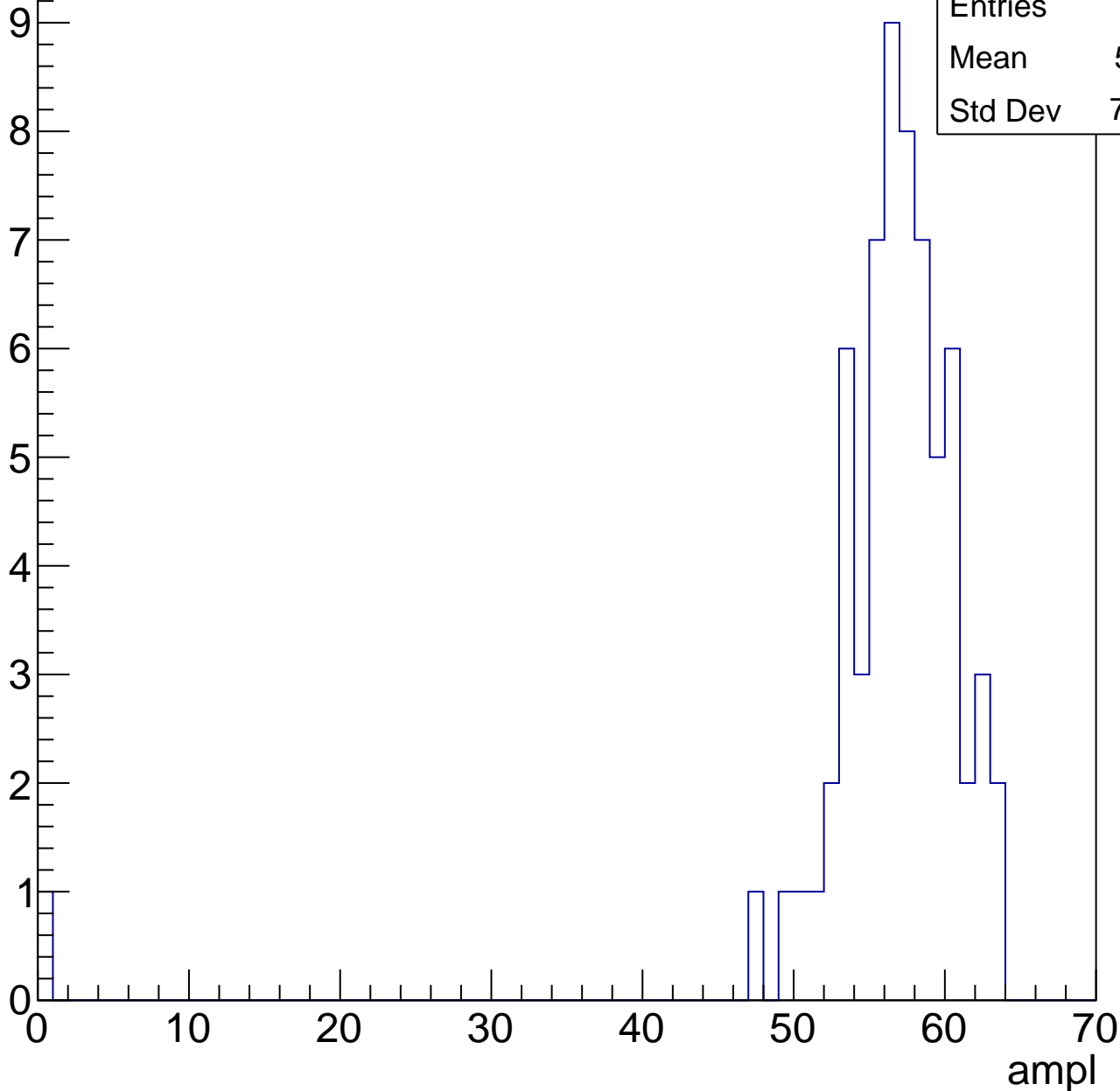


B1L102S, U22-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.71
Std Dev	7.706

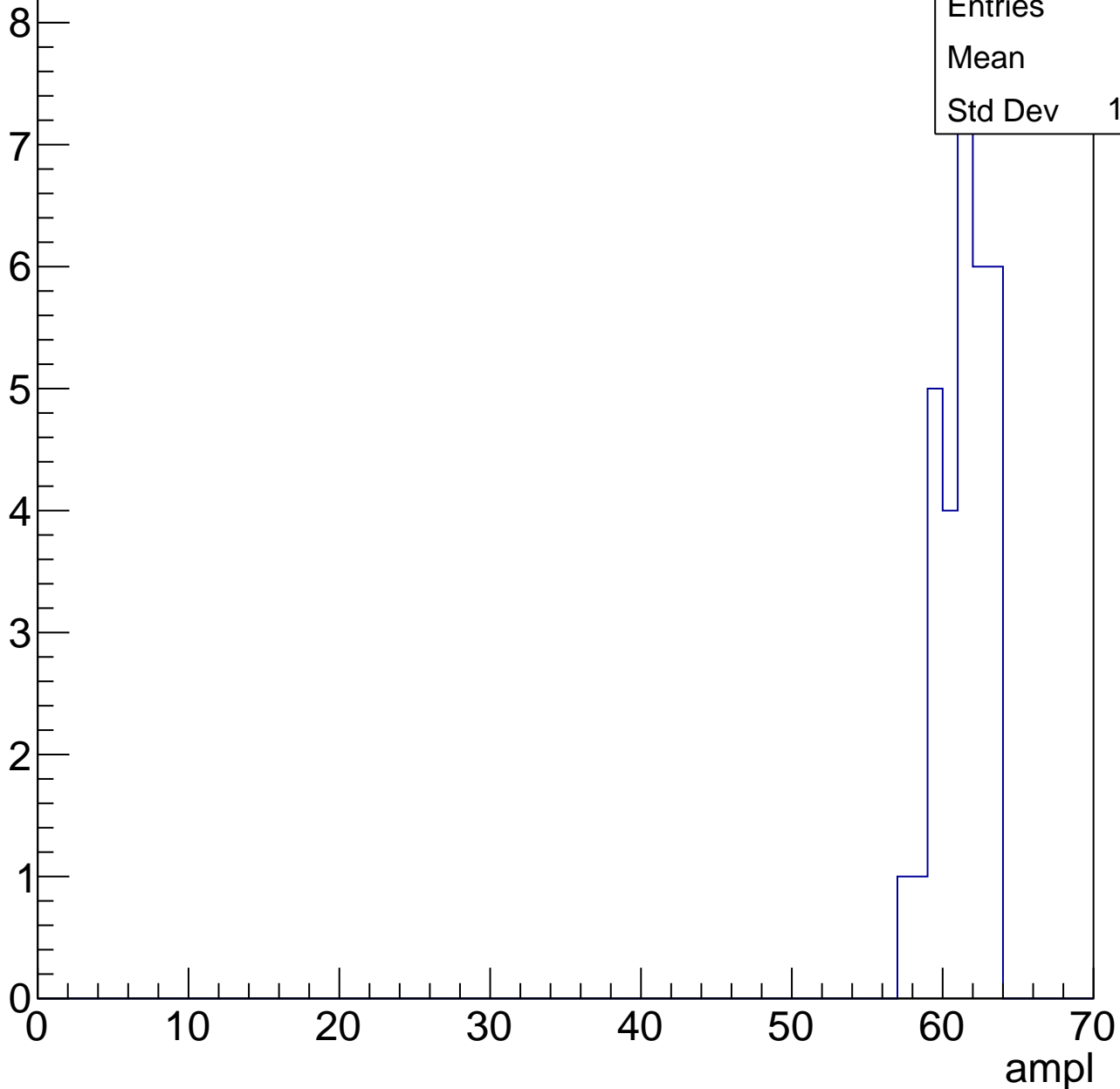


B1L102S, U22-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	60.9
Std Dev	1.593



B1L102S, U22-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U22-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch69, adc0

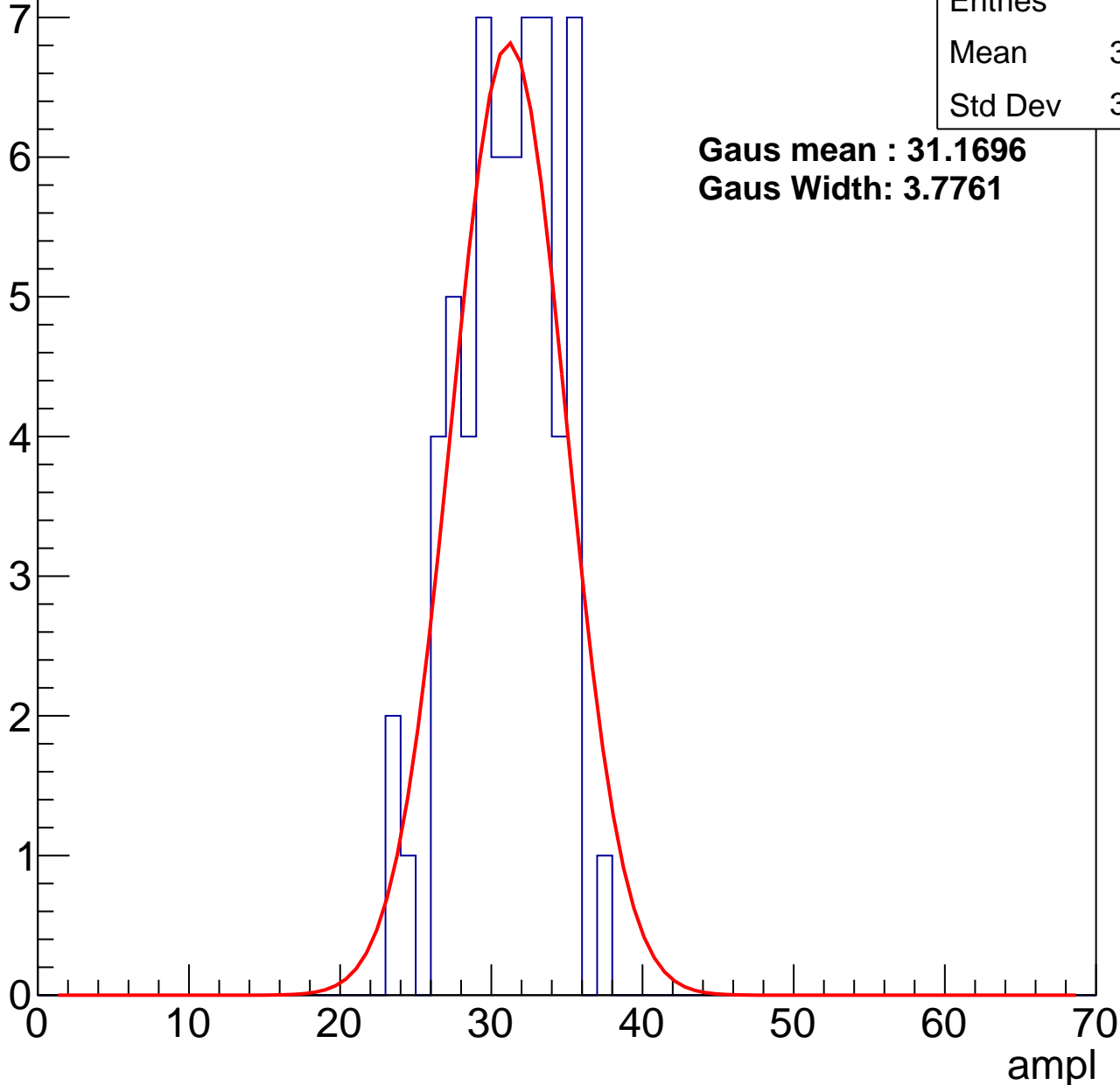
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	30.54
Std Dev	3.222

Gaus mean : 31.1696

Gaus Width: 3.7761



B1L102S, U22-ch69, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	37.26
Std Dev	3.5

Gaus mean : 37.9032

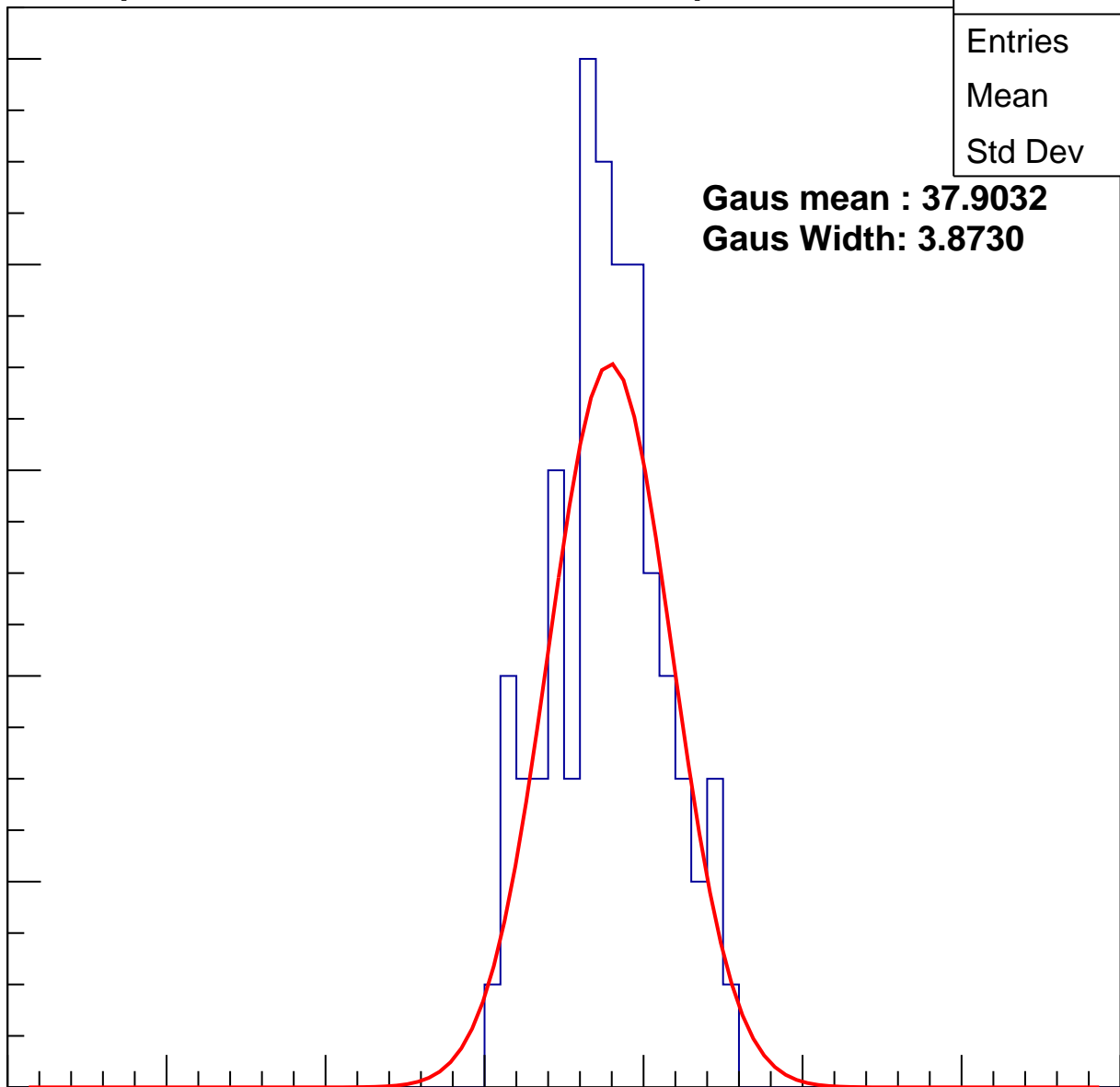
Gaus Width: 3.8730

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch69, adc2

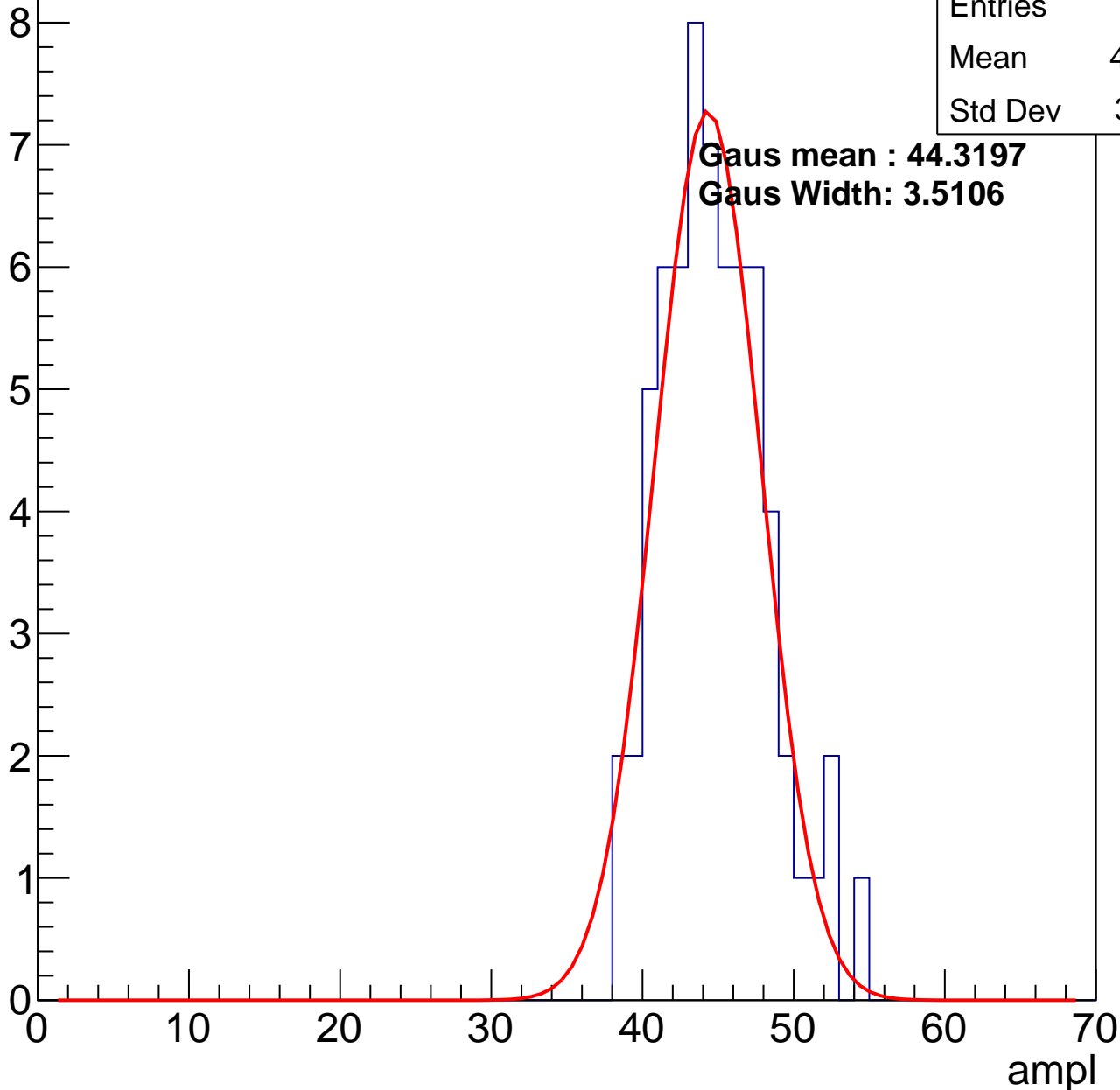
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.32
Std Dev	3.491

Gaus mean : 44.3197

Gaus Width: 3.5106

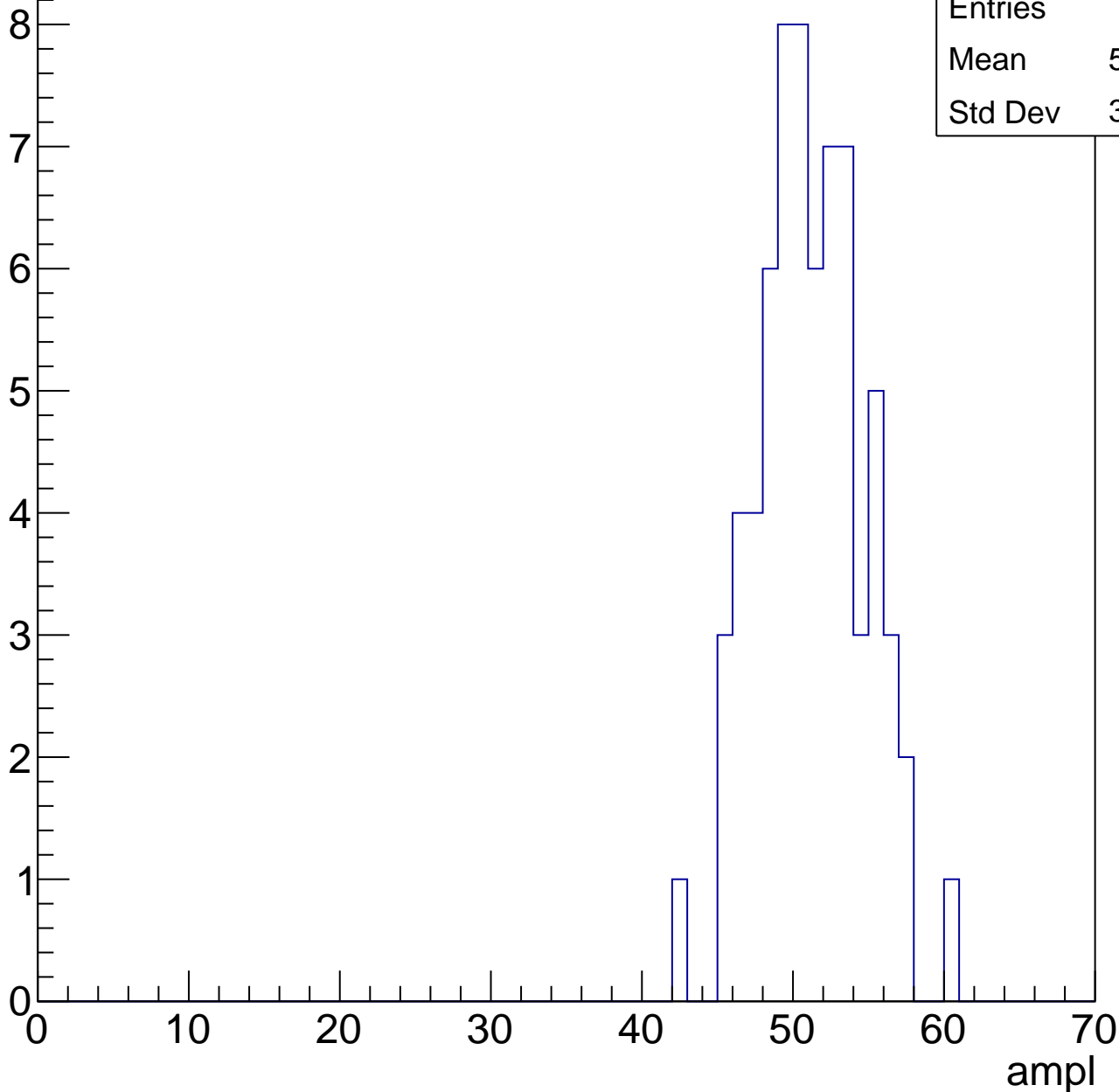


B1L102S, U22-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

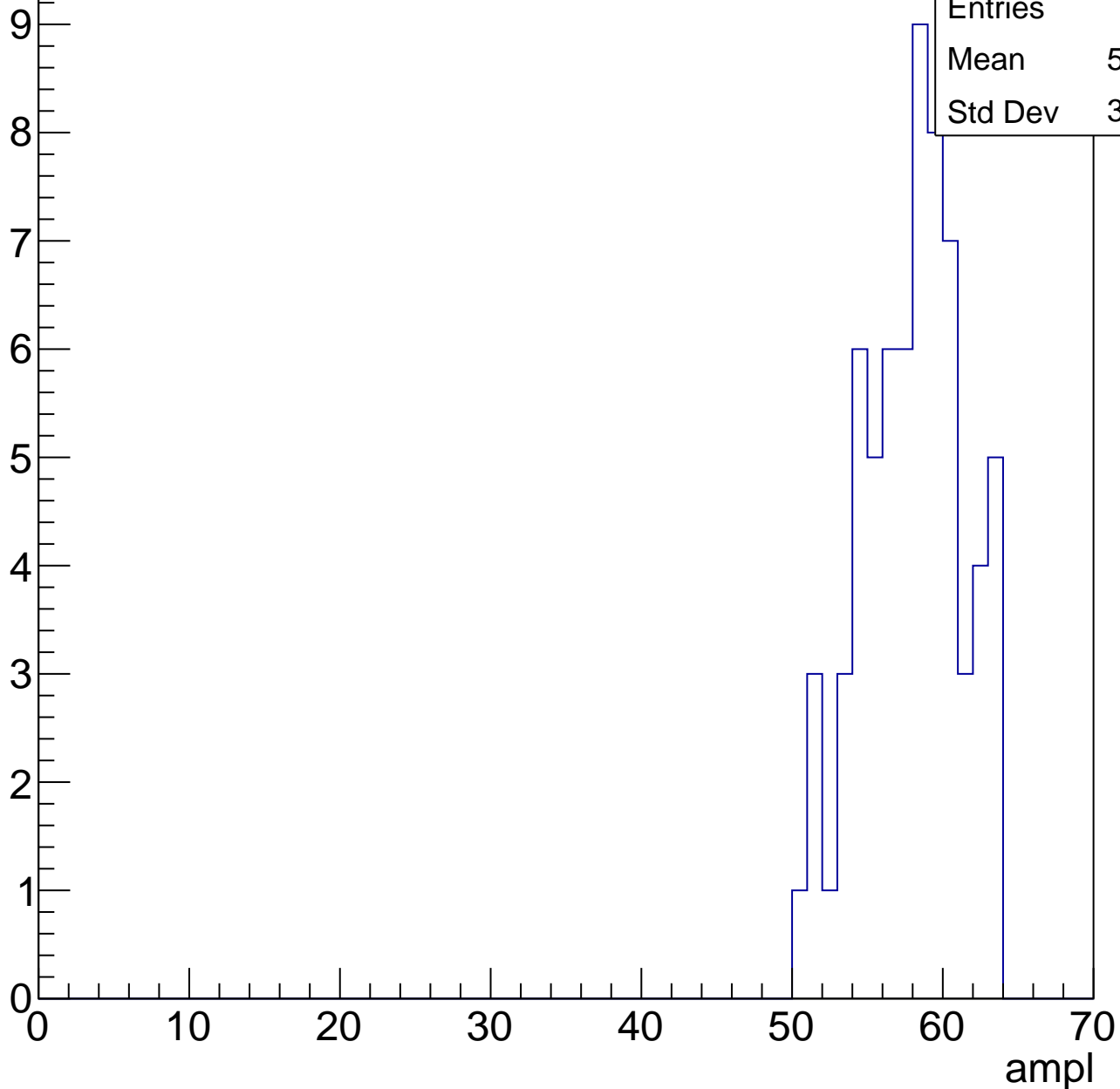
Entries	68
Mean	50.72
Std Dev	3.455



B1L102S, U22-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

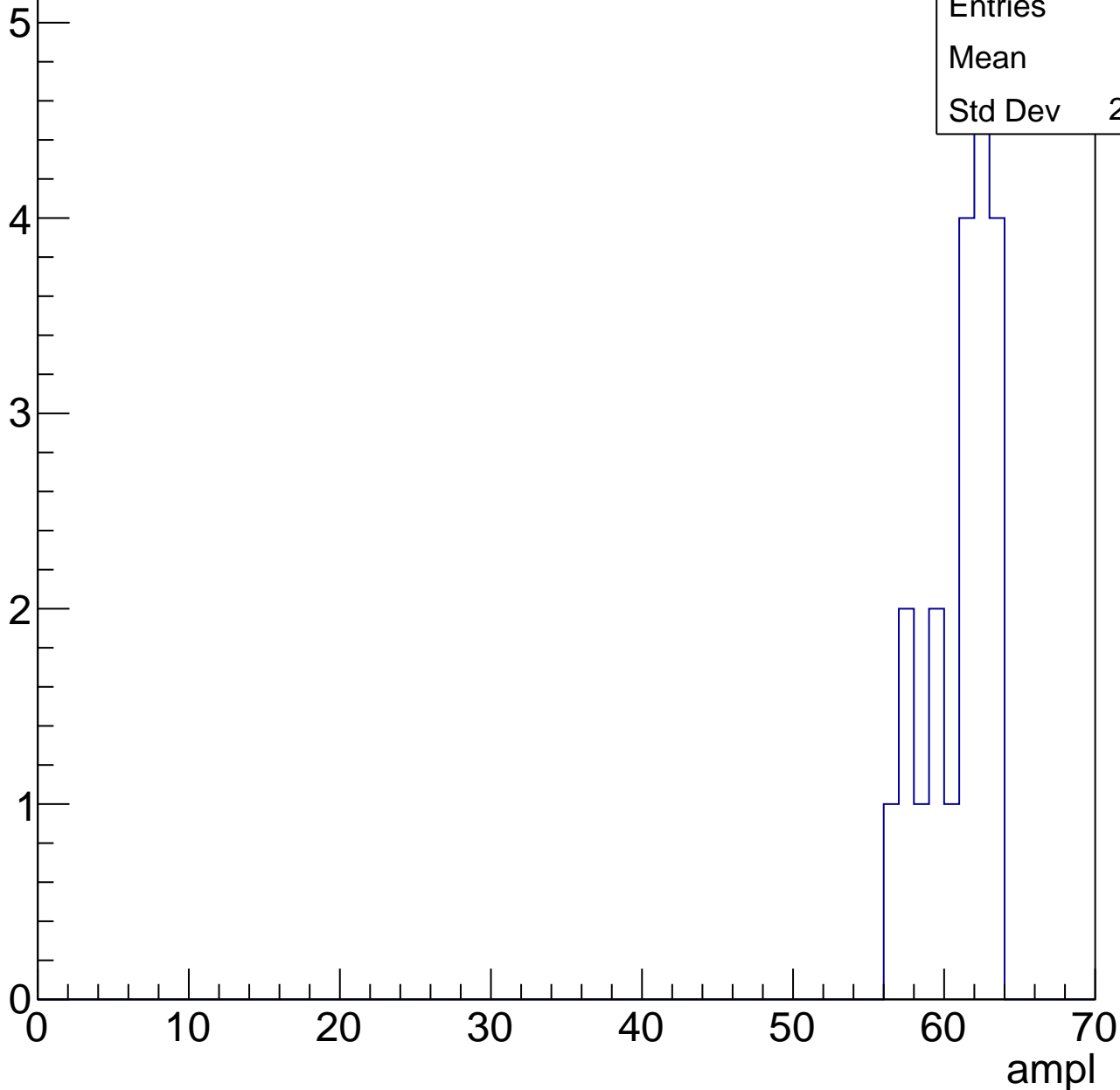


B1L102S, U22-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

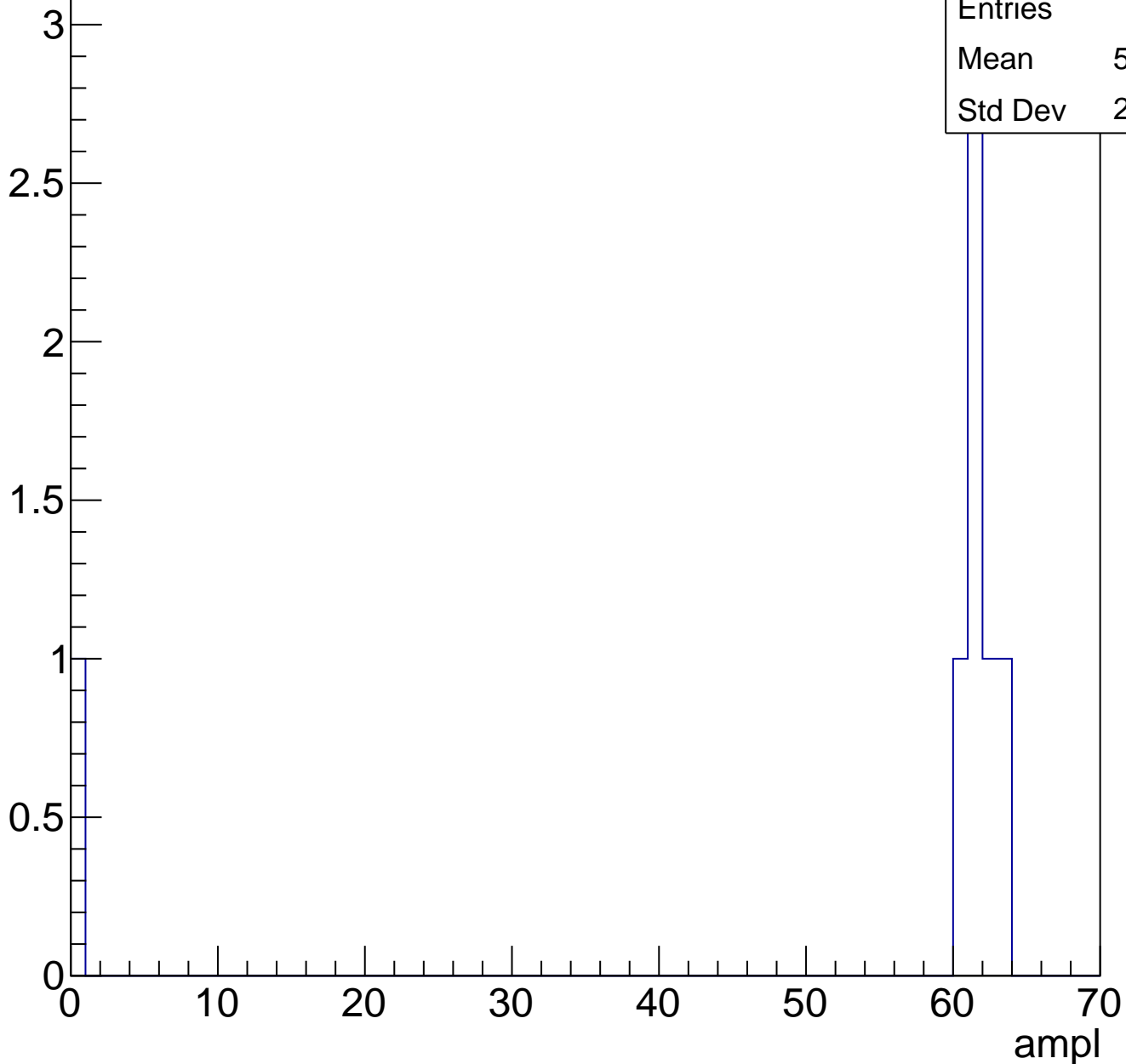
Entries	20
Mean	60.6
Std Dev	2.154



B1L102S, U22-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch70, adc0

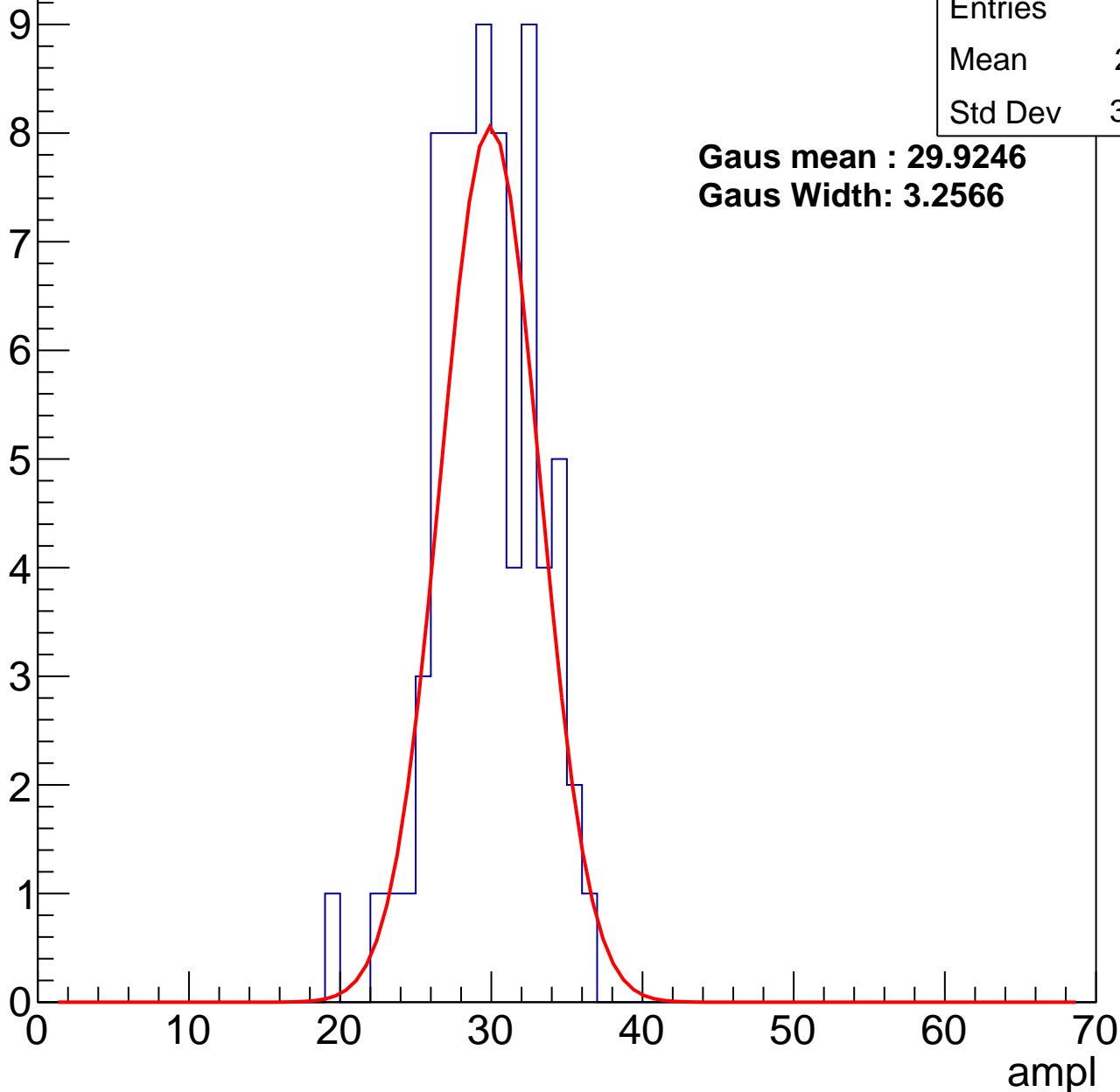
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29.21
Std Dev	3.273

Gaus mean : 29.9246

Gaus Width: 3.2566



B1L102S, U22-ch70, adc1

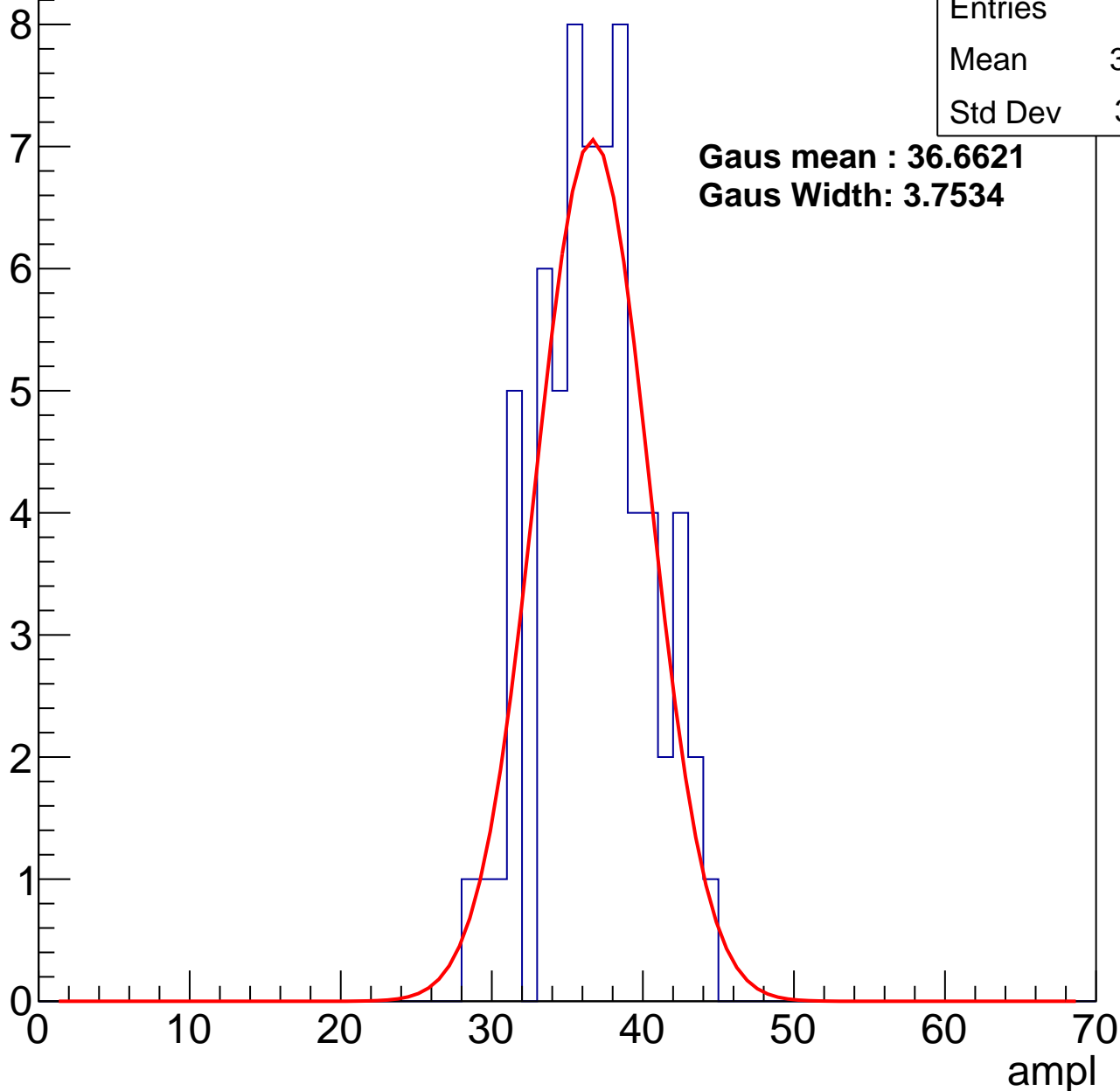
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.38
Std Dev	3.571

Gaus mean : 36.6621

Gaus Width: 3.7534



B1L102S, U22-ch70, adc2

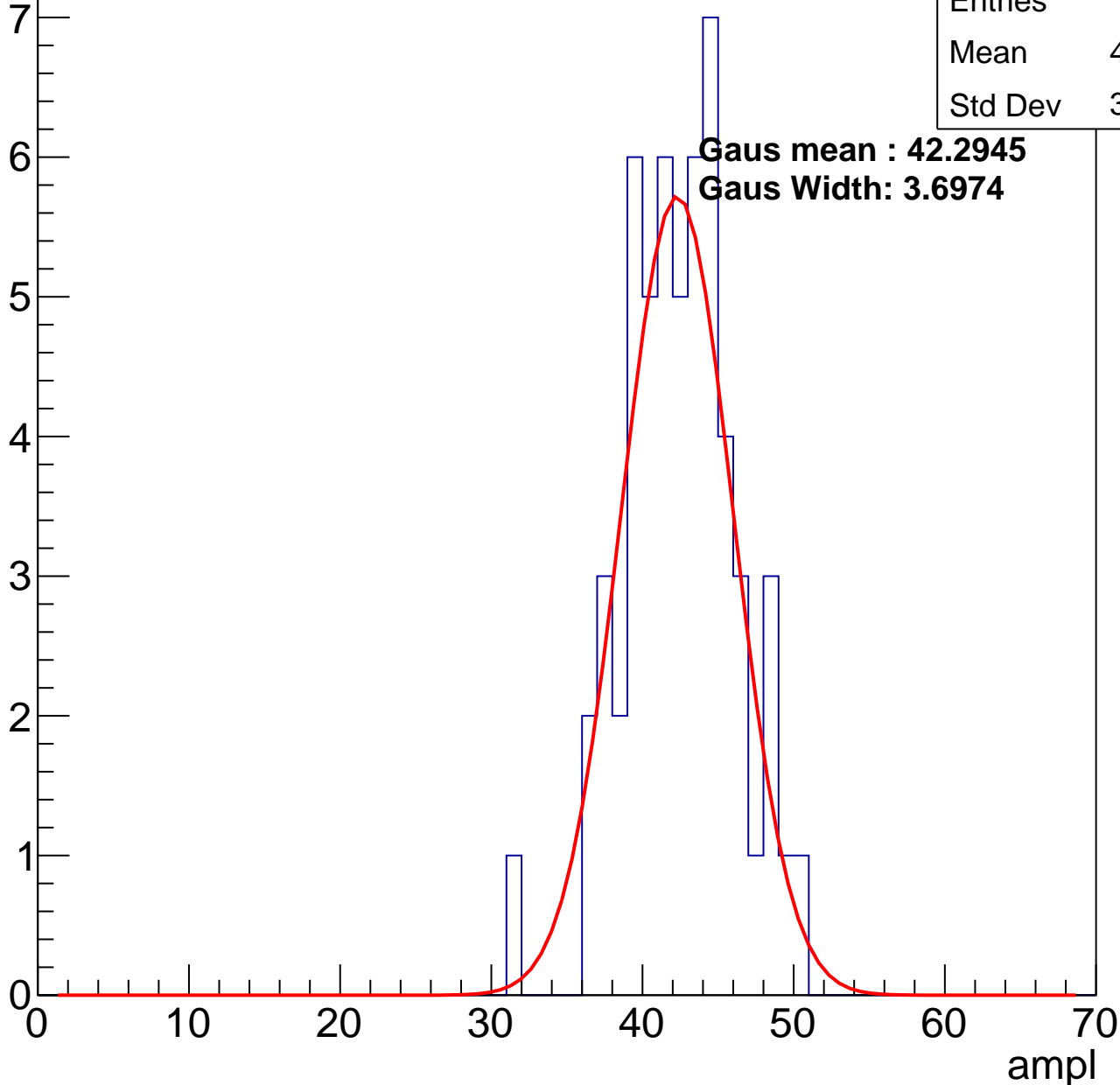
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.04
Std Dev	3.645

Gaus mean : 42.2945

Gaus Width: 3.6974

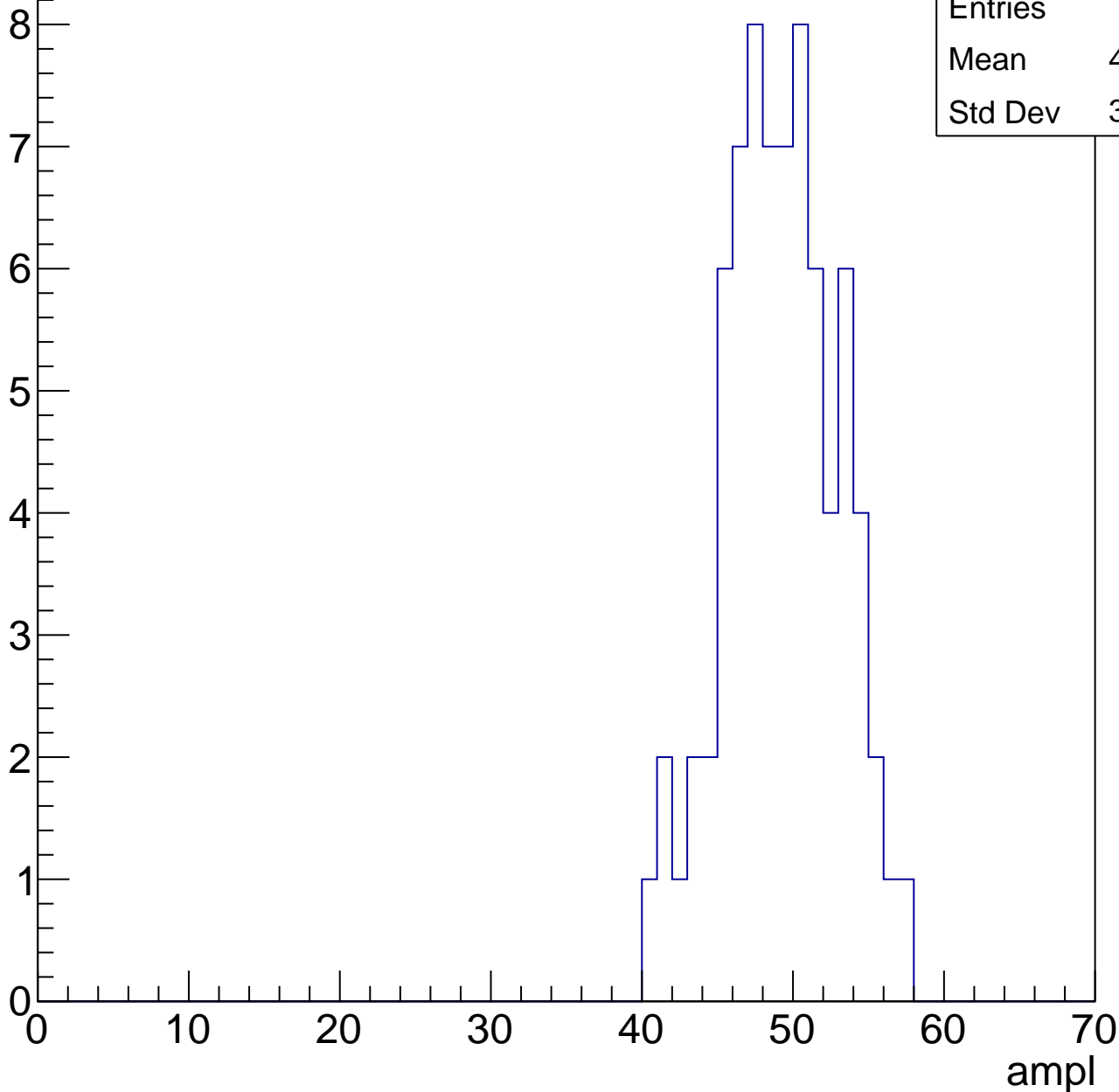


B1L102S, U22-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	48.75
Std Dev	3.692

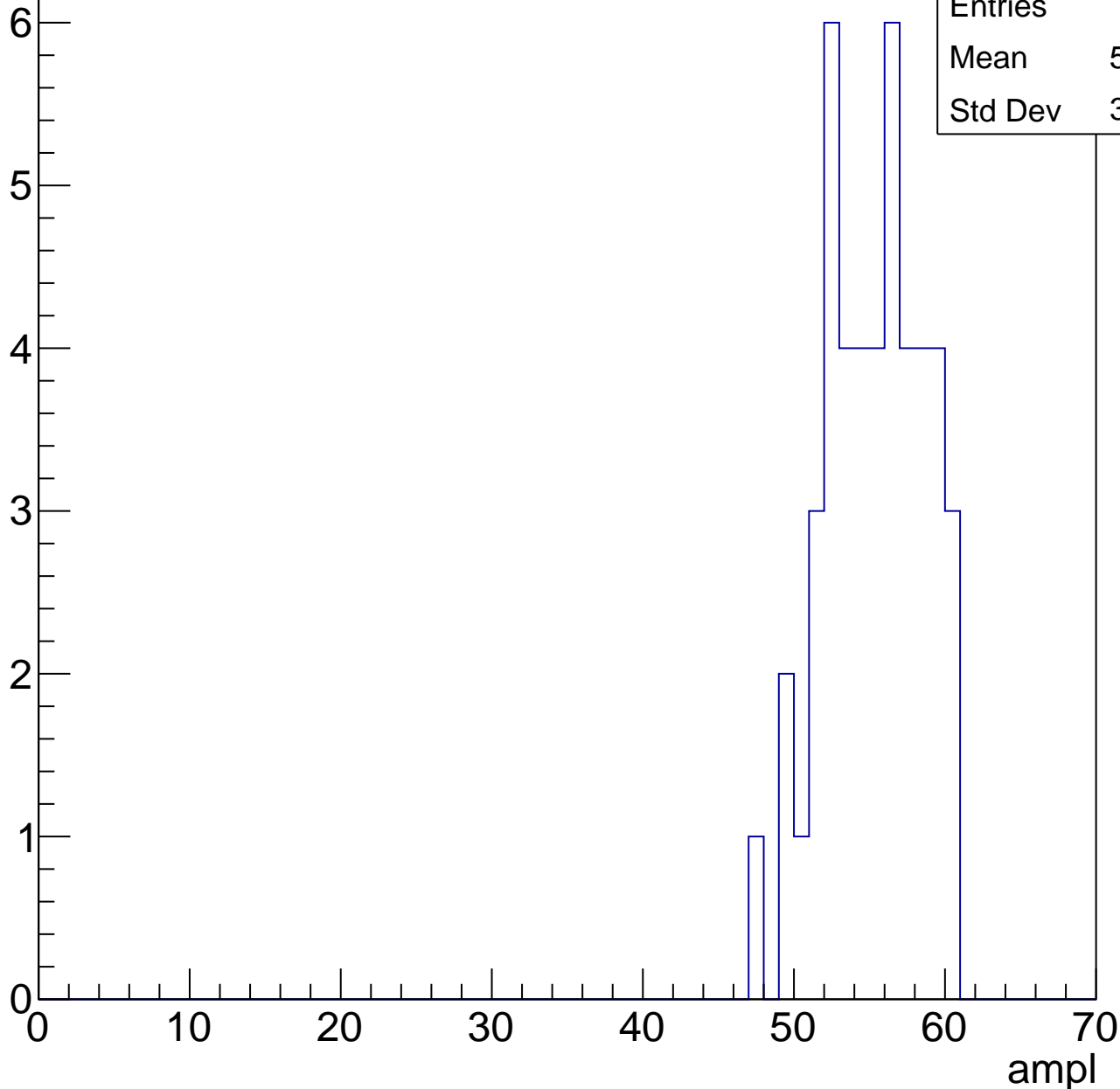


B1L102S, U22-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	54.78
Std Dev	3.223

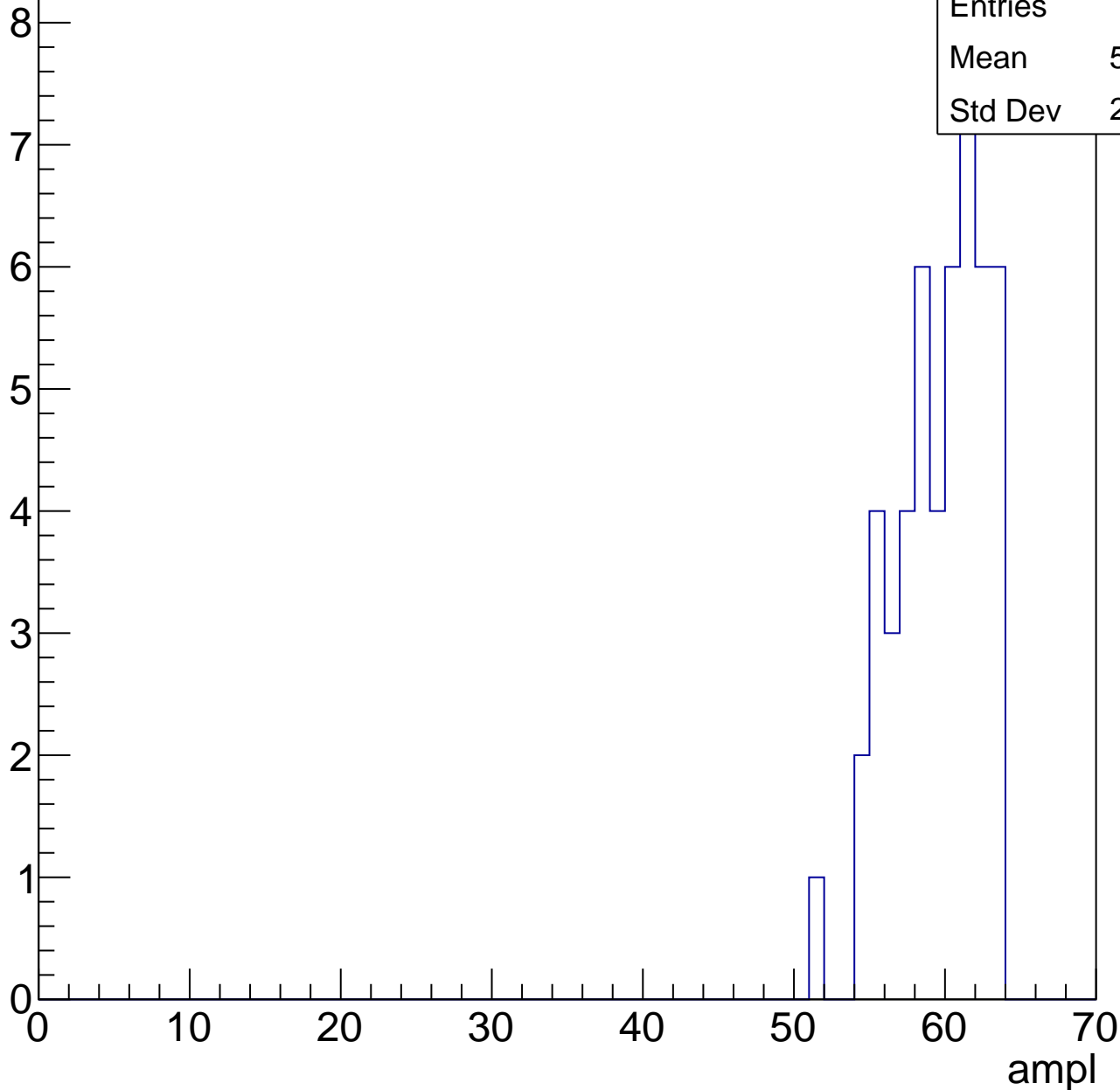


B1L102S, U22-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

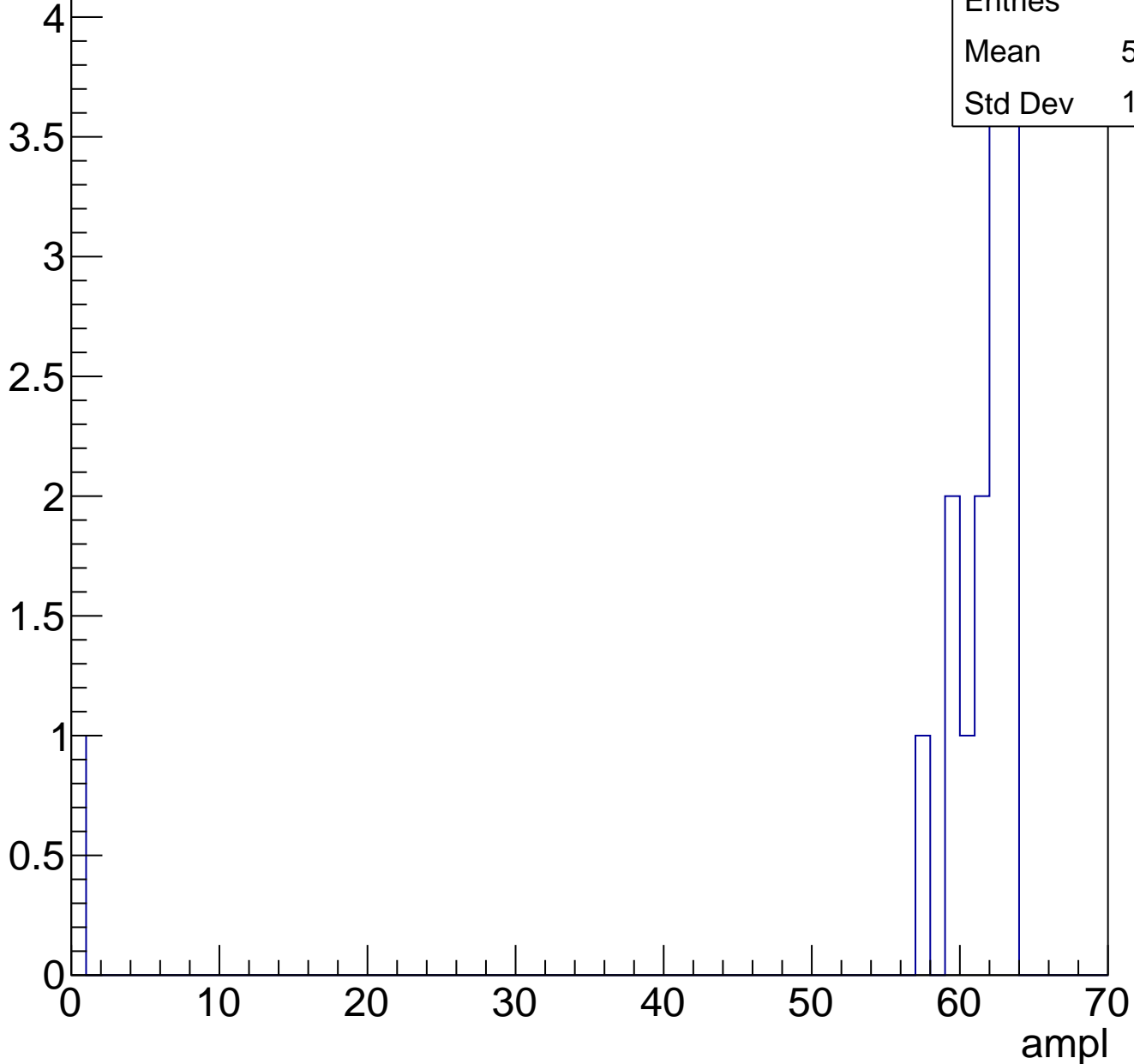
Entries	50
Mean	59.14
Std Dev	2.878



B1L102S, U22-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch71, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	29.3
Std Dev	3.809

Gaus mean : 29.6245

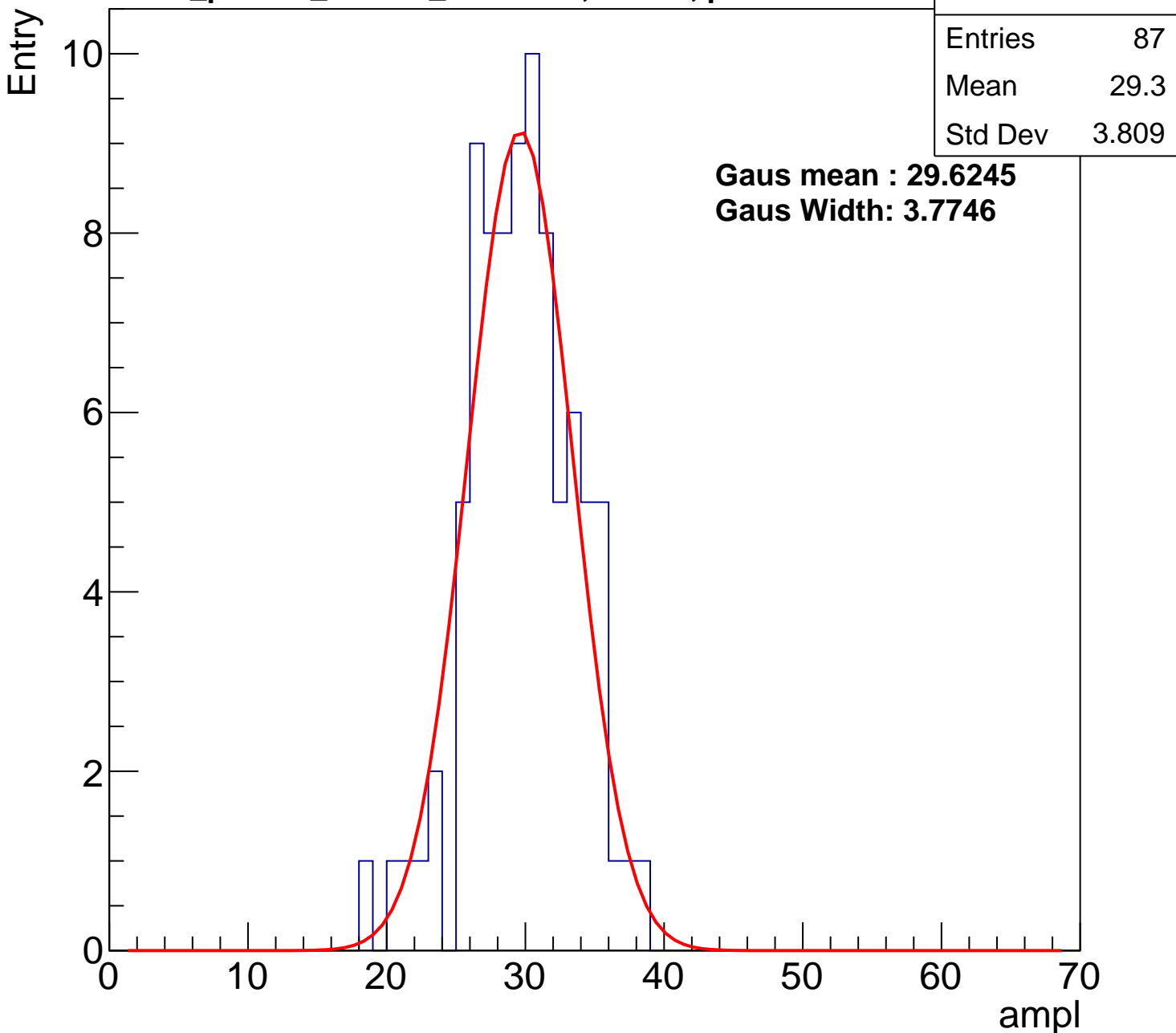
Gaus Width: 3.7746

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch71, adc1

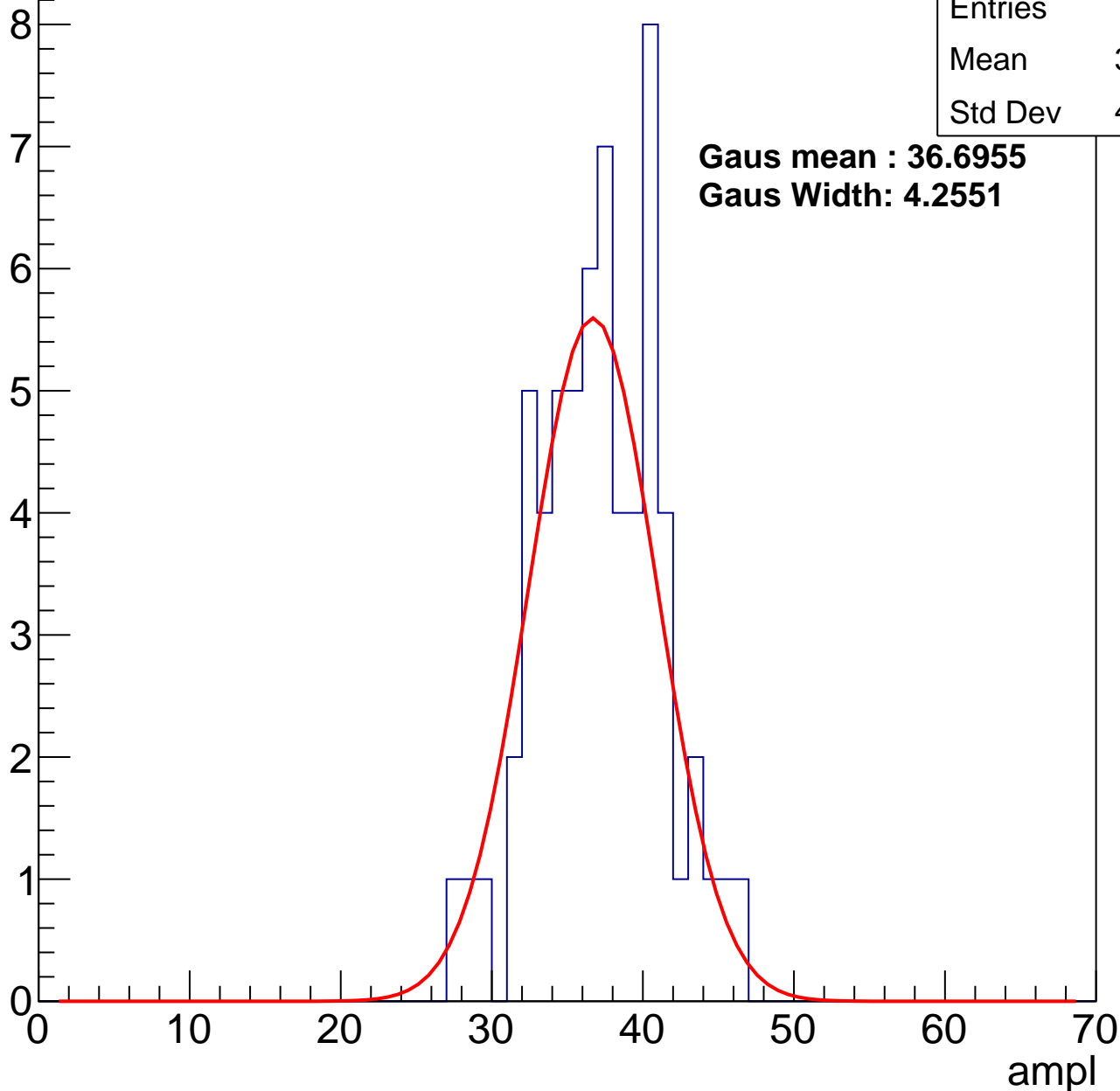
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.71
Std Dev	4.021

Gaus mean : 36.6955

Gaus Width: 4.2551



B1L102S, U22-ch71, adc2

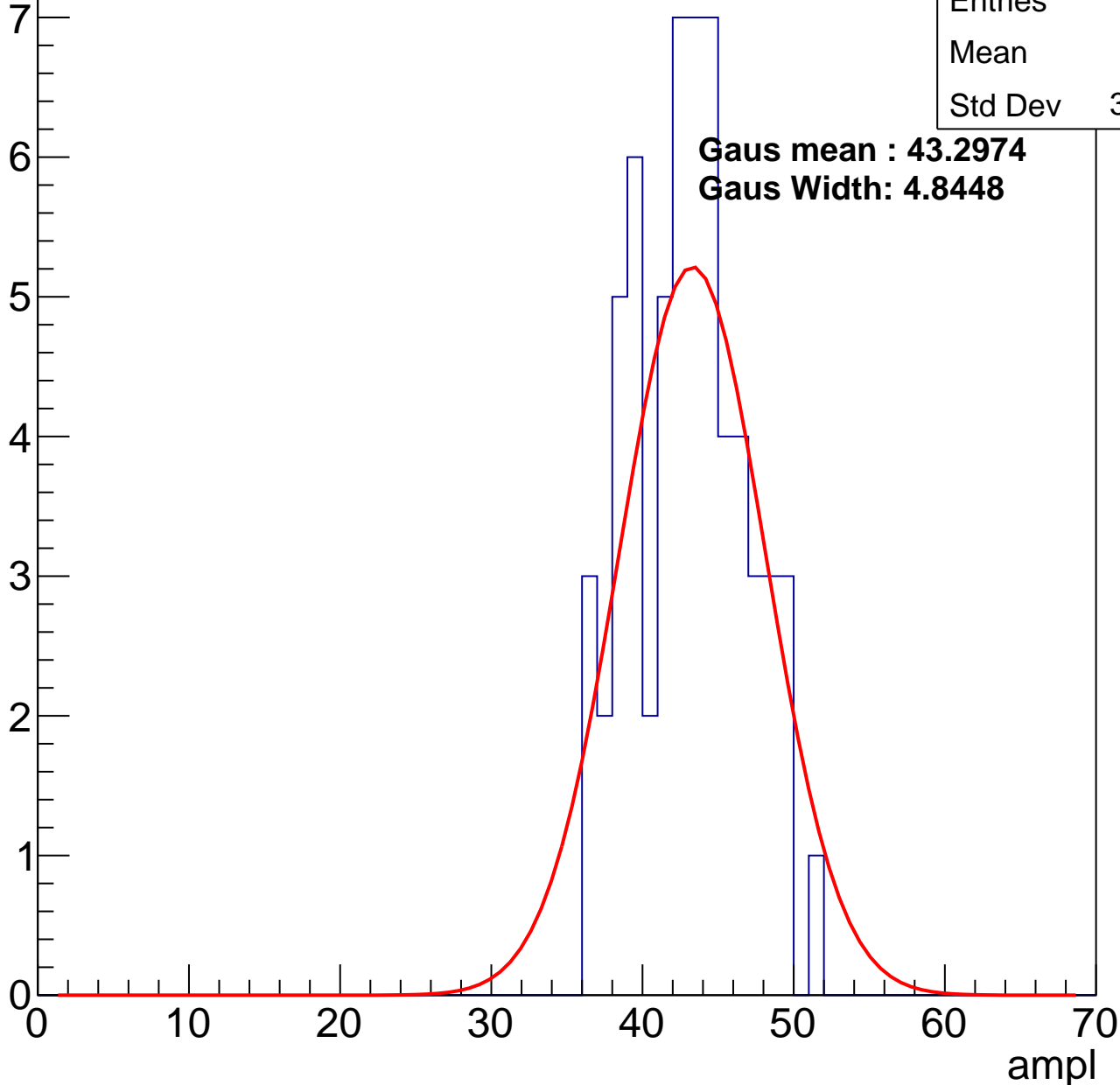
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.6
Std Dev	3.656

Gaus mean : 43.2974

Gaus Width: 4.8448

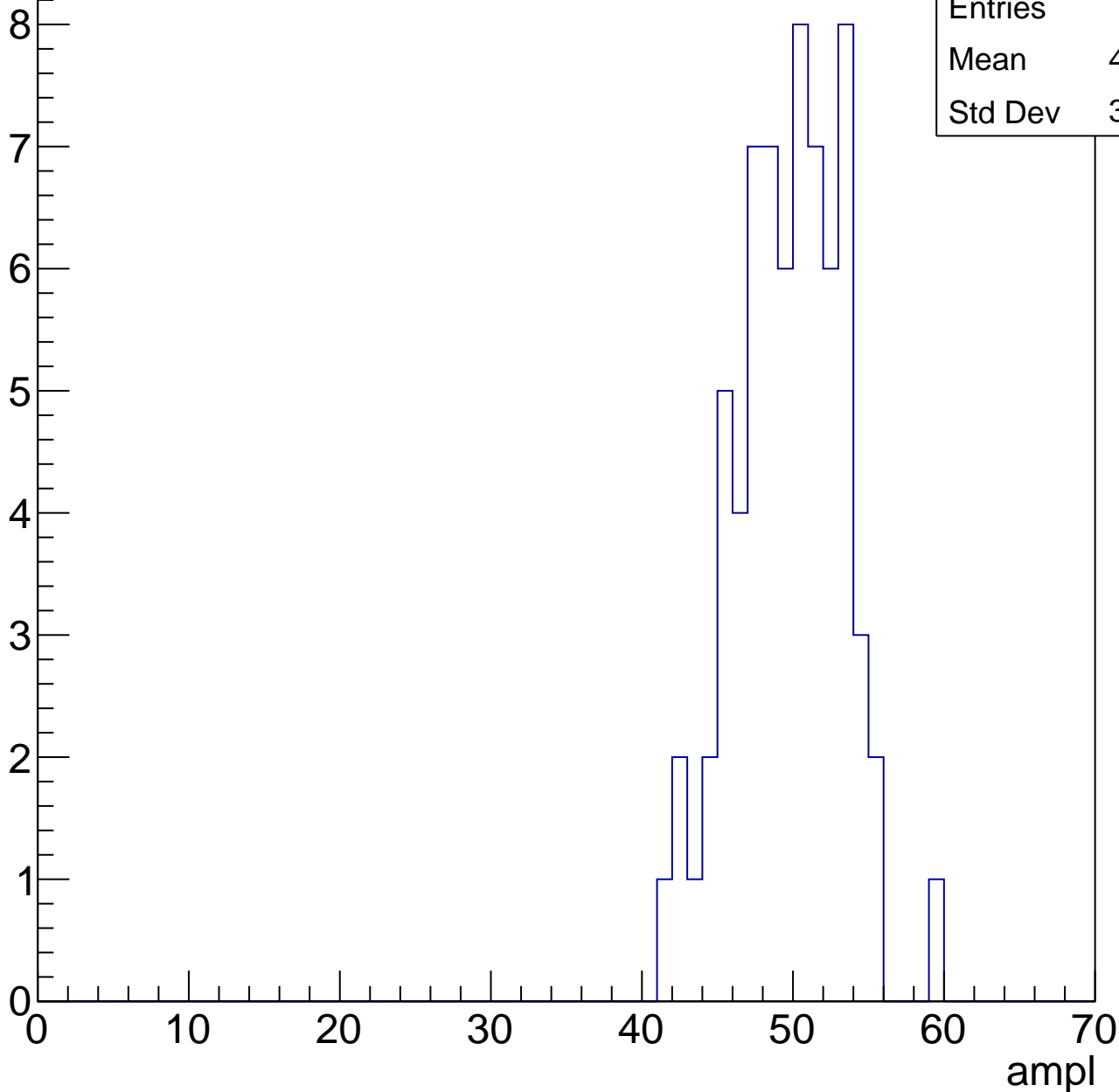


B1L102S, U22-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.26
Std Dev	3.504

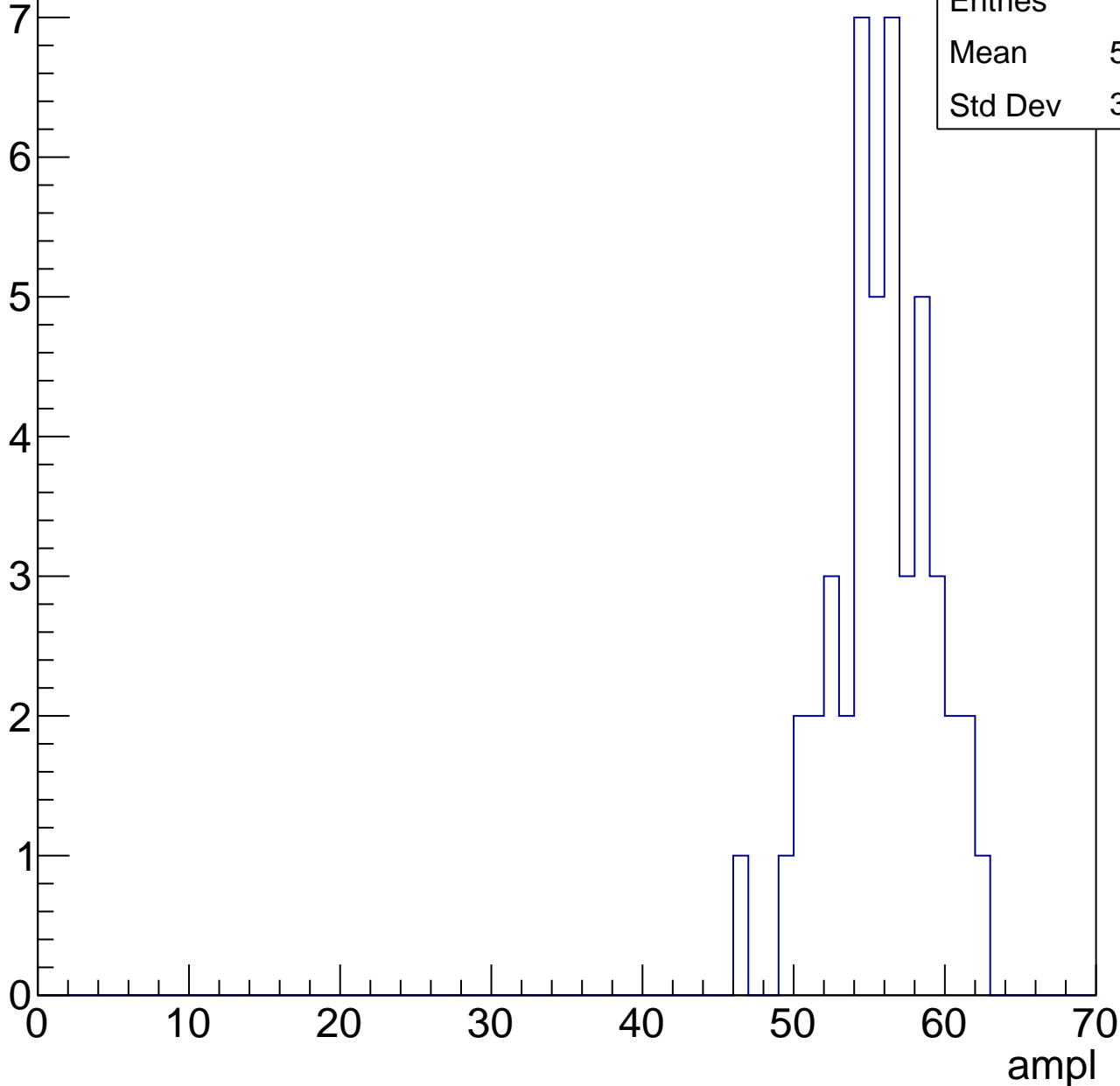


B1L102S, U22-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	55.35
Std Dev	3.357

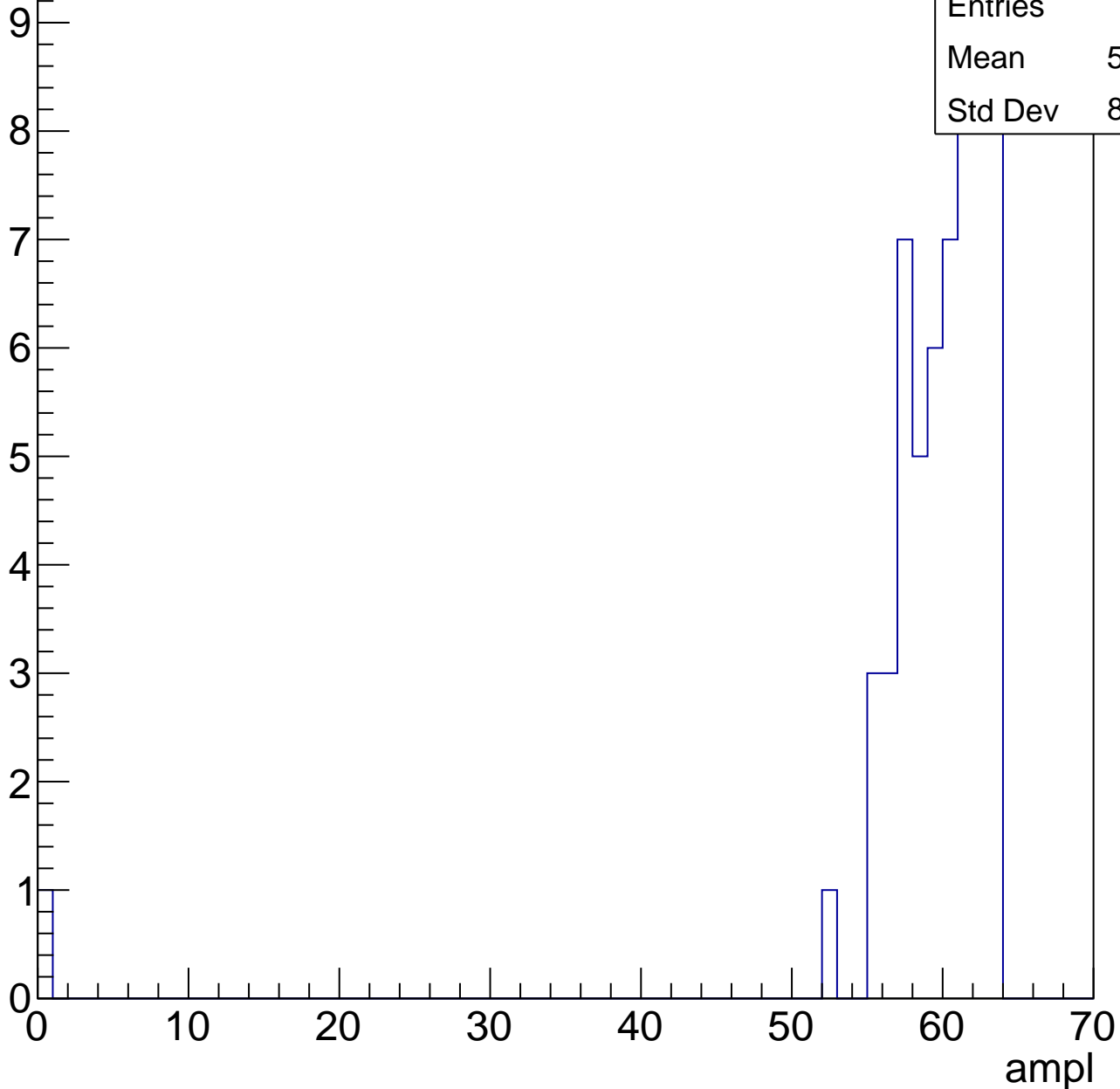


B1L102S, U22-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

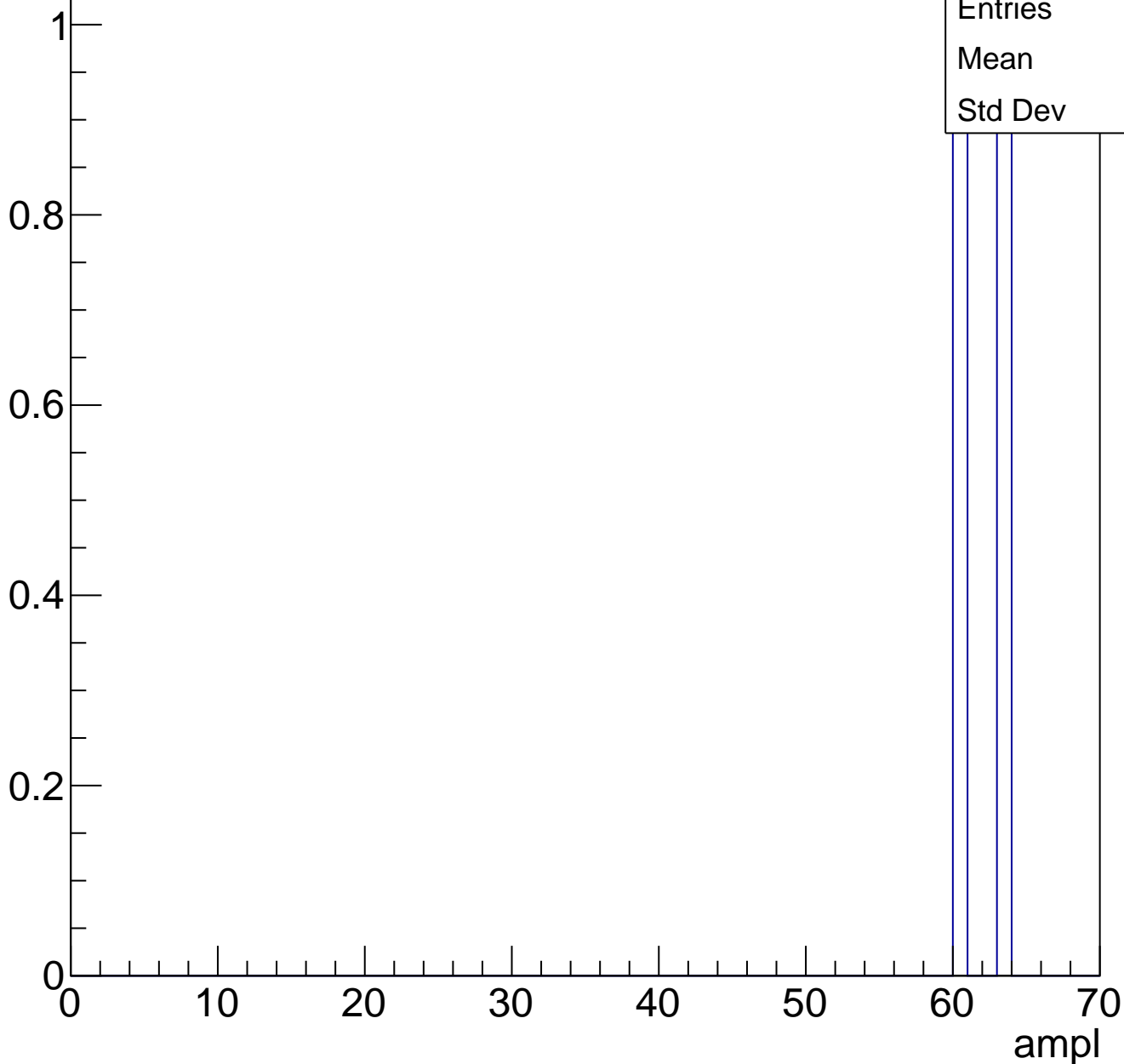
Entries	59
Mean	58.66
Std Dev	8.125



B1L102S, U22-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U22-ch72, adc0

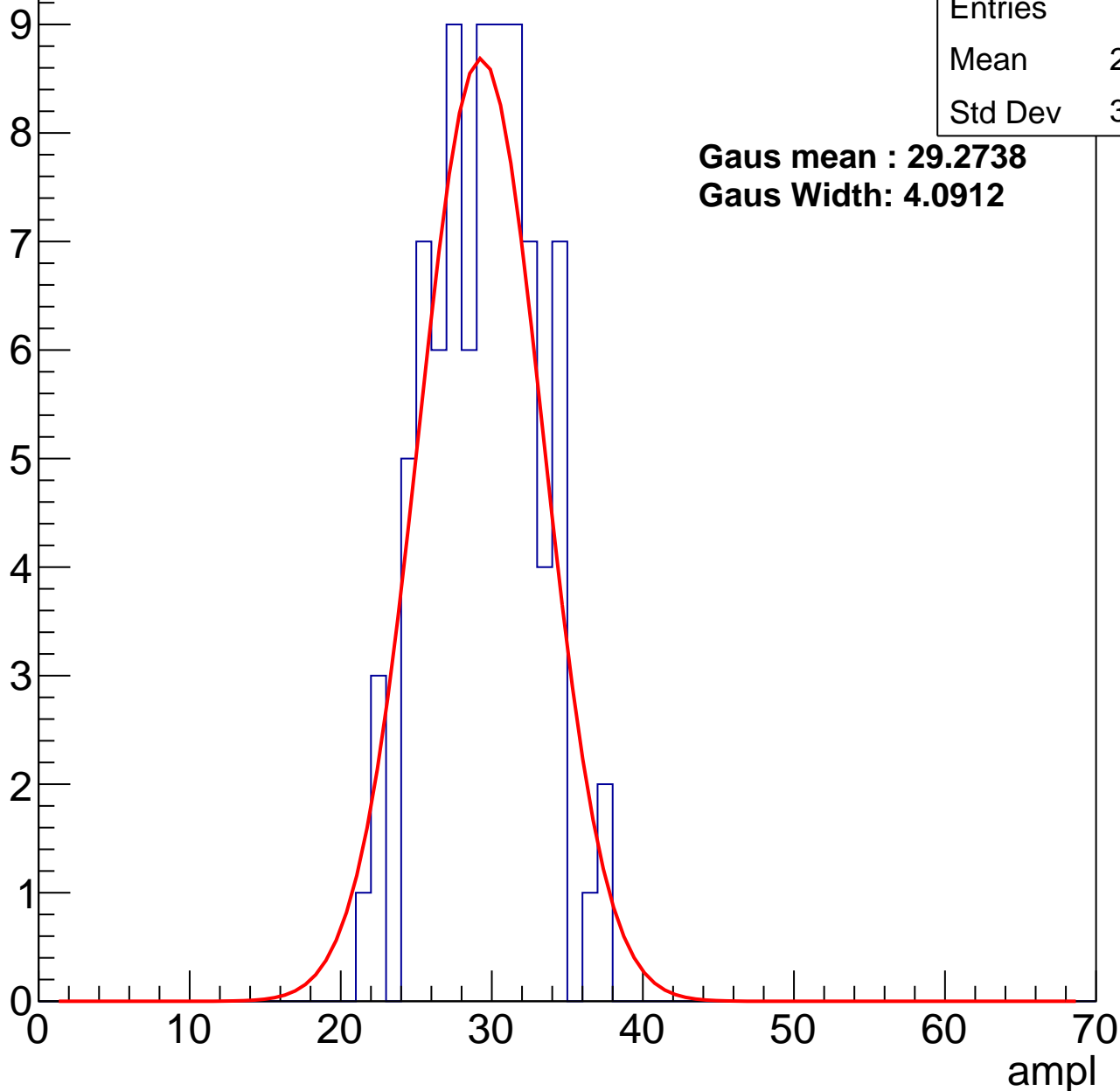
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	28.98
Std Dev	3.545

Gaus mean : 29.2738

Gaus Width: 4.0912



B1L102S, U22-ch72, adc1

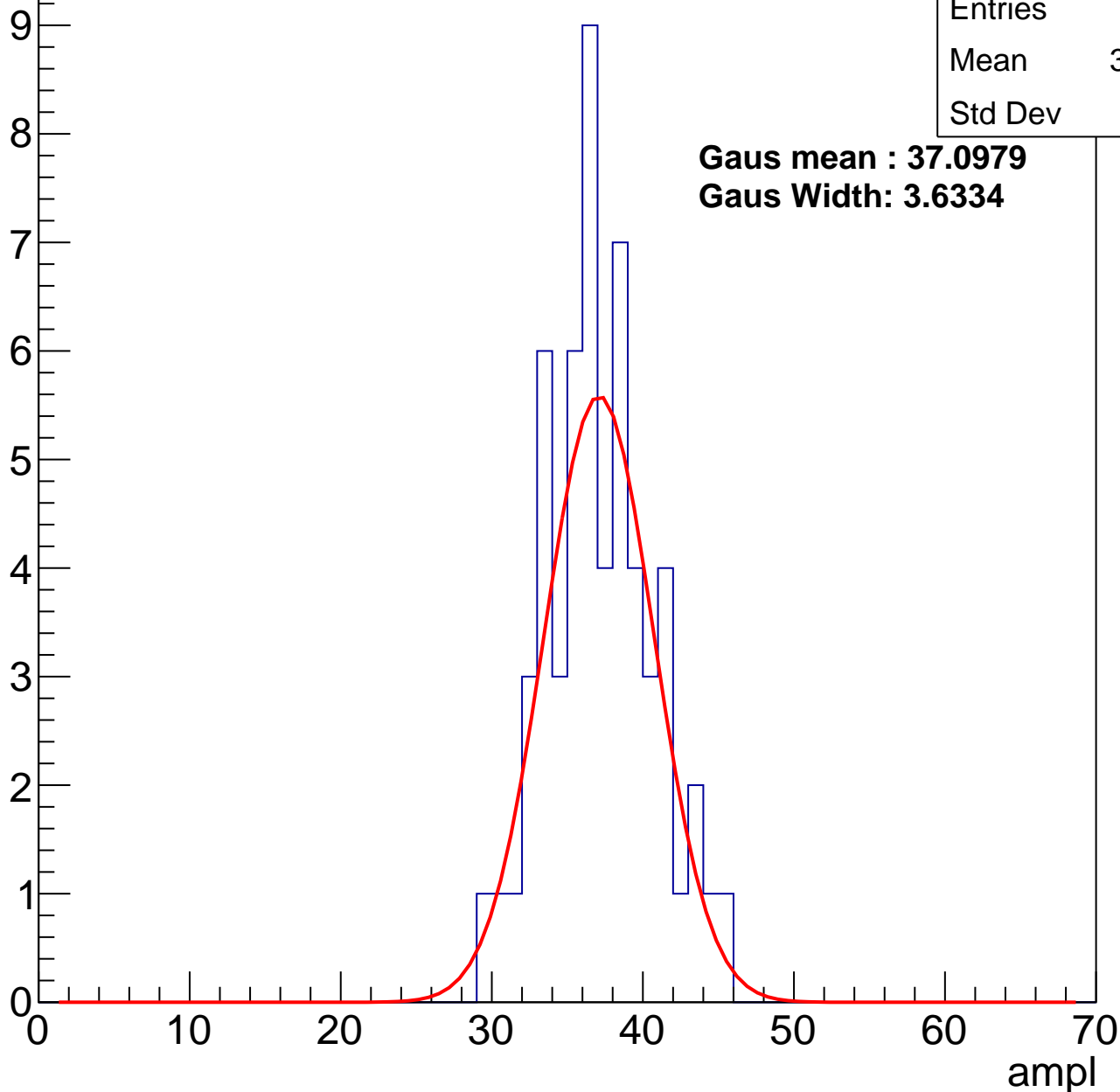
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	36.68
Std Dev	3.5

Gaus mean : 37.0979

Gaus Width: 3.6334



B1L102S, U22-ch72, adc2

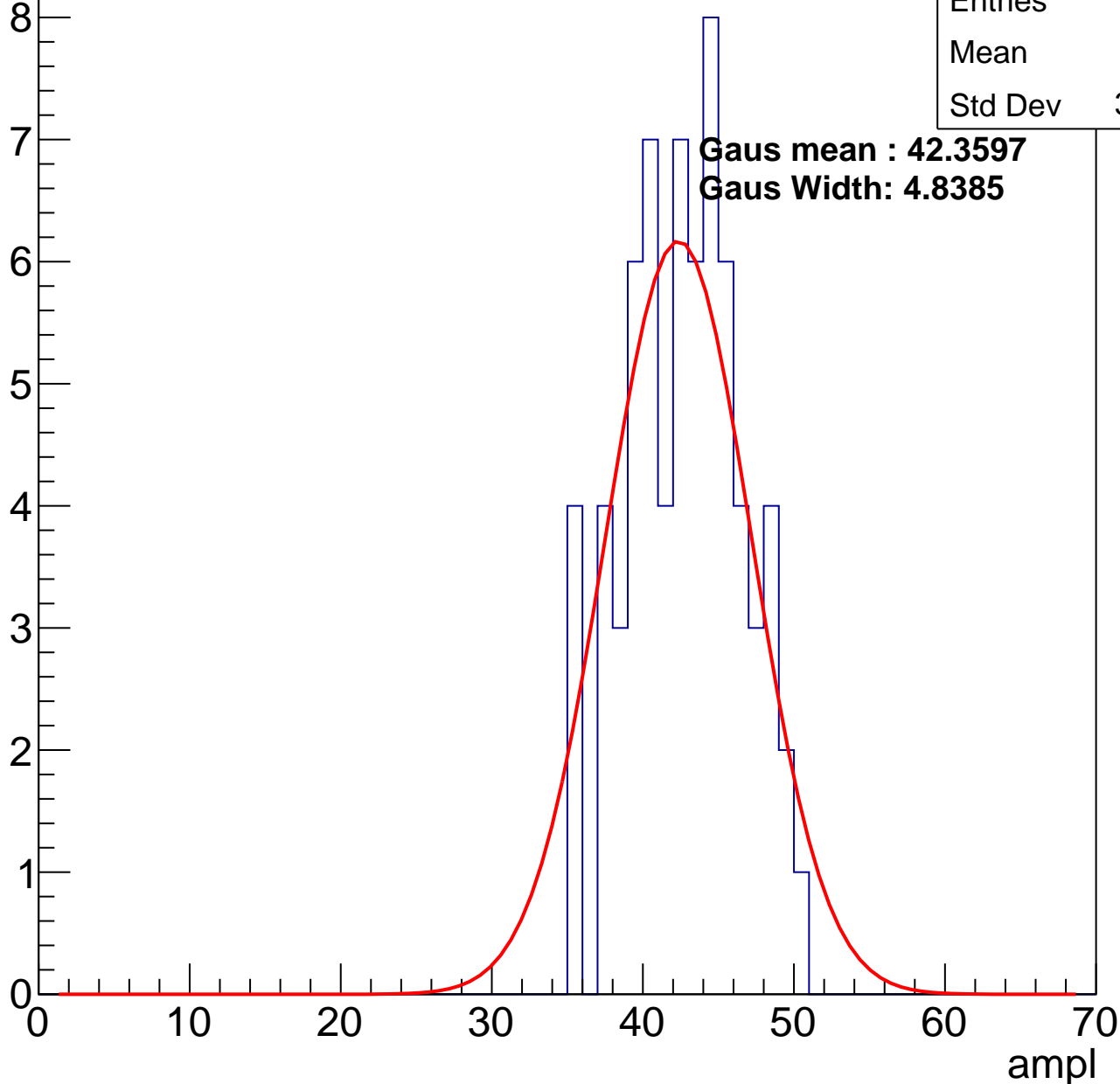
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.3
Std Dev	3.731

Gaus mean : 42.3597

Gaus Width: 4.8385

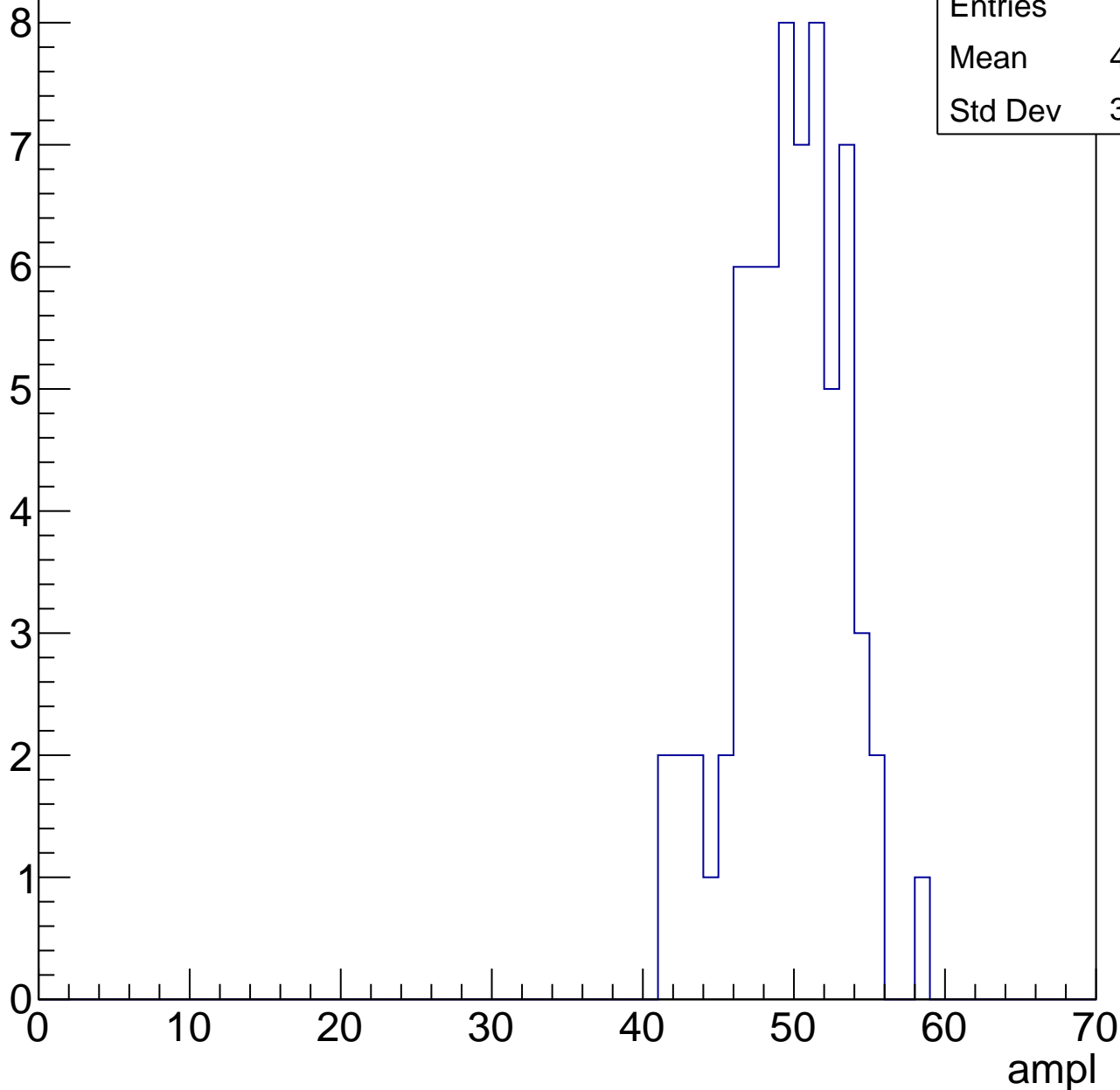


B1L102S, U22-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.16
Std Dev	3.559

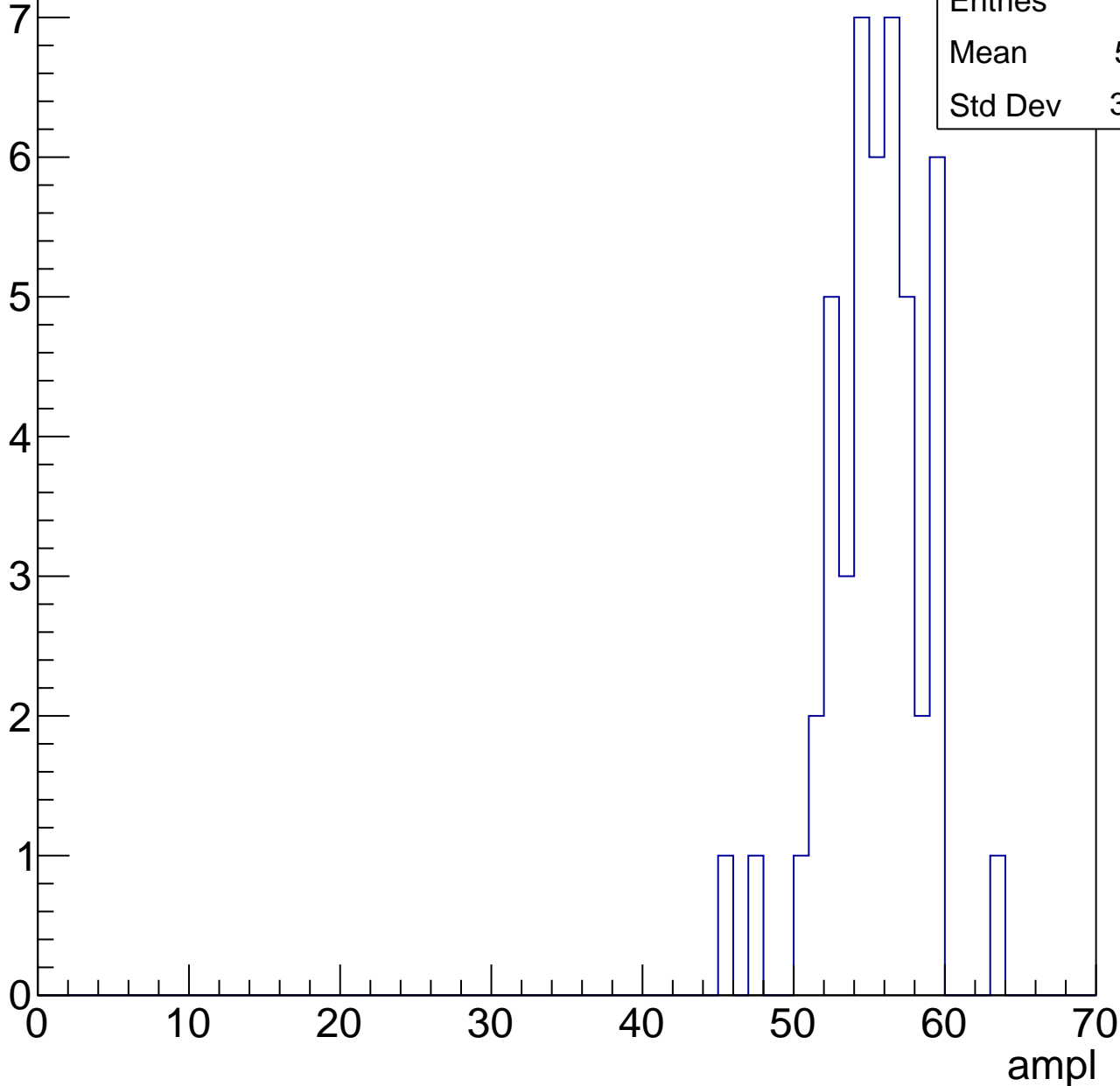


B1L102S, U22-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.91
Std Dev	3.228

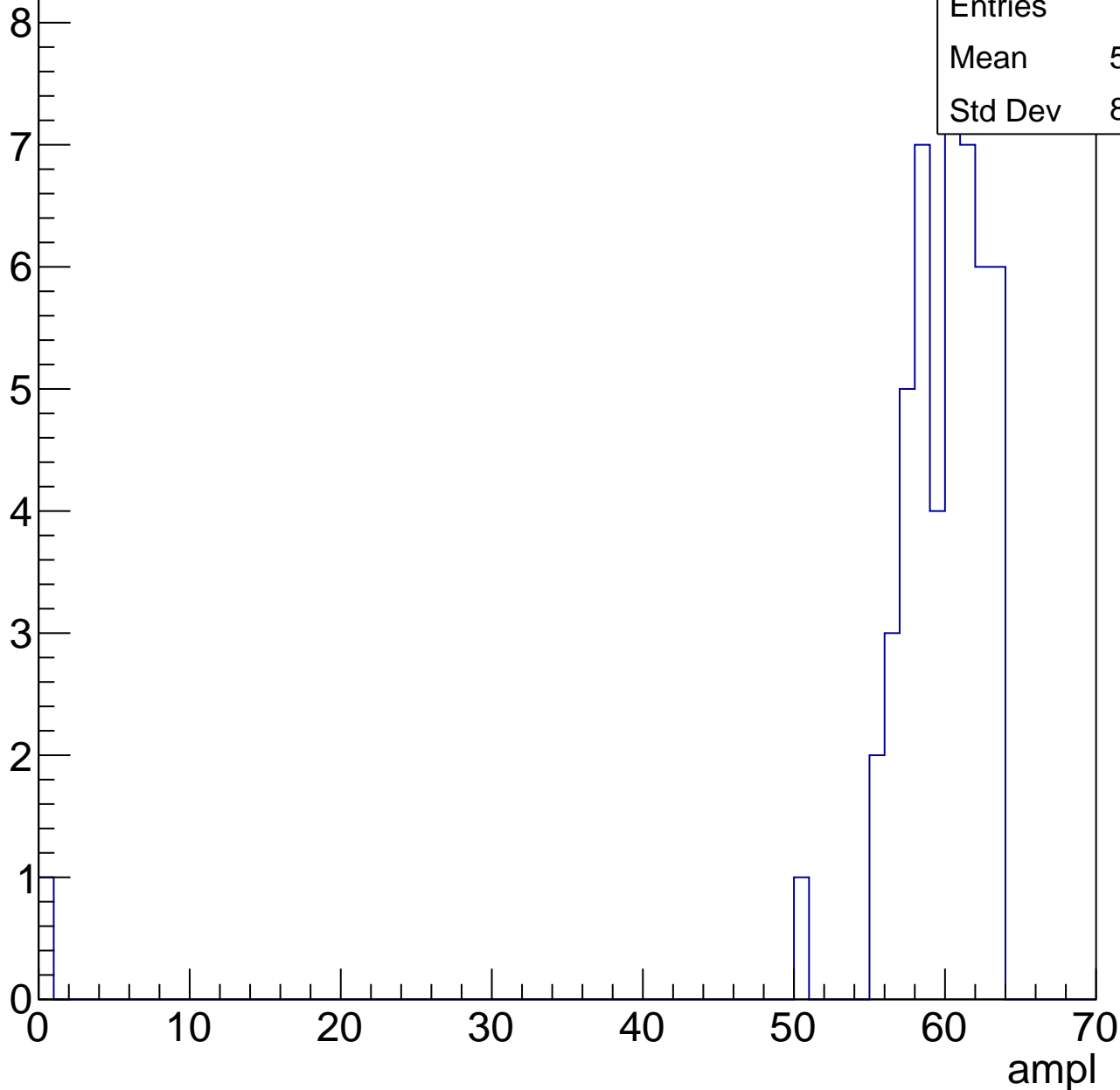


B1L102S, U22-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

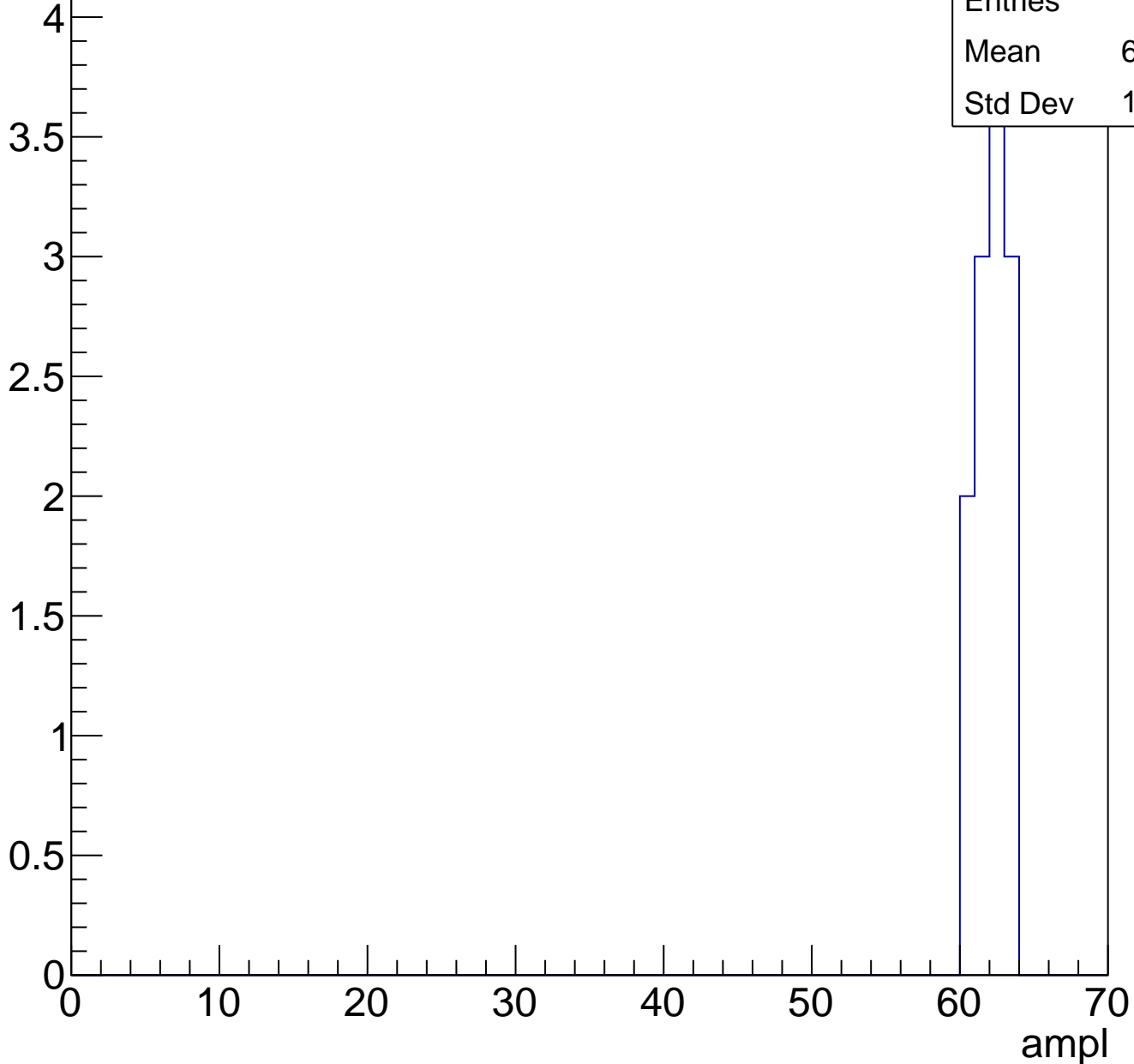
Entries	50
Mean	58.24
Std Dev	8.724



B1L102S, U22-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U22-ch73, adc0

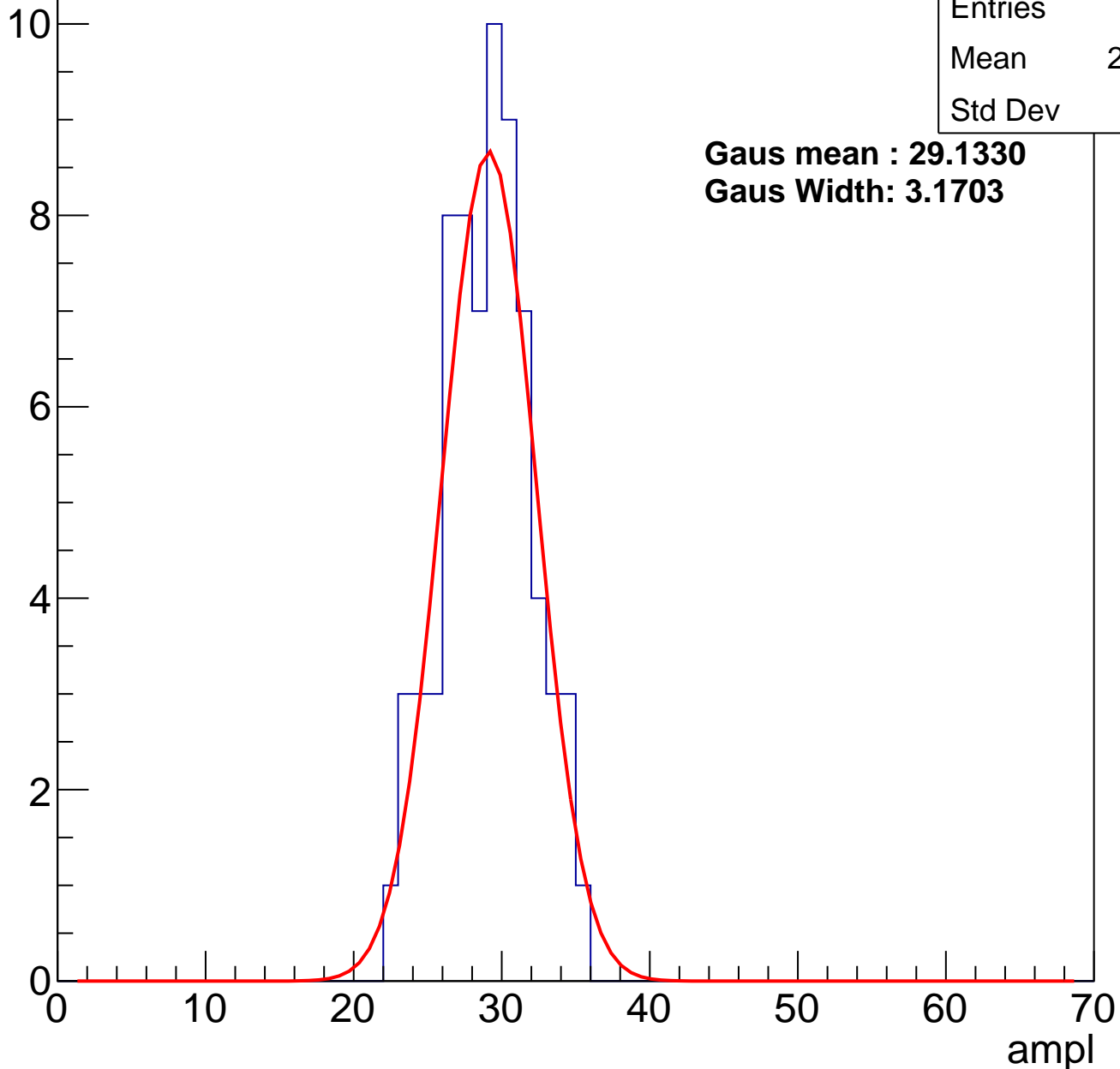
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	28.56
Std Dev	2.95

Gaus mean : 29.1330

Gaus Width: 3.1703

Entry



B1L102S, U22-ch73, adc1

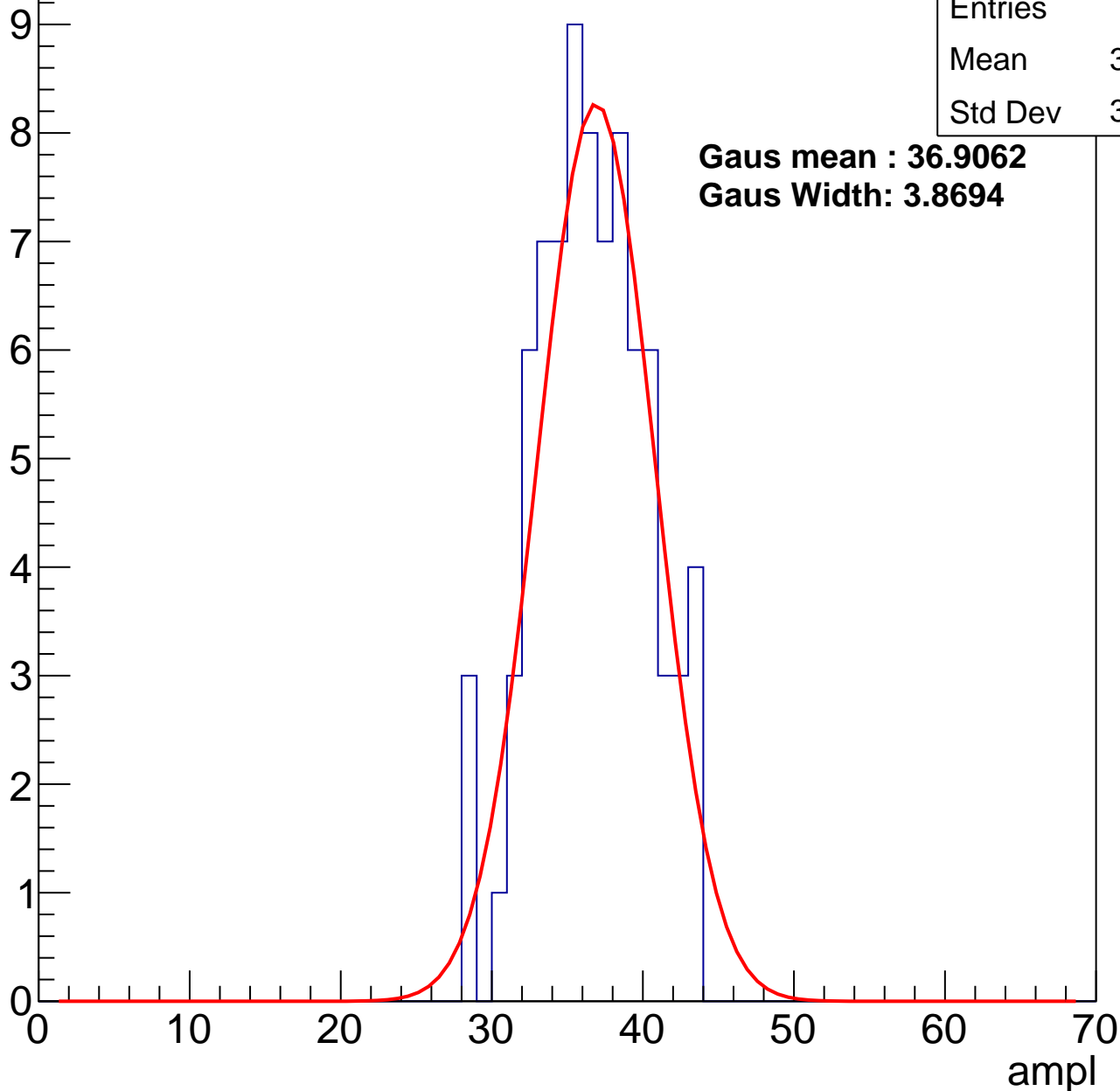
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.16
Std Dev	3.626

Gaus mean : 36.9062

Gaus Width: 3.8694



B1L102S, U22-ch73, adc2

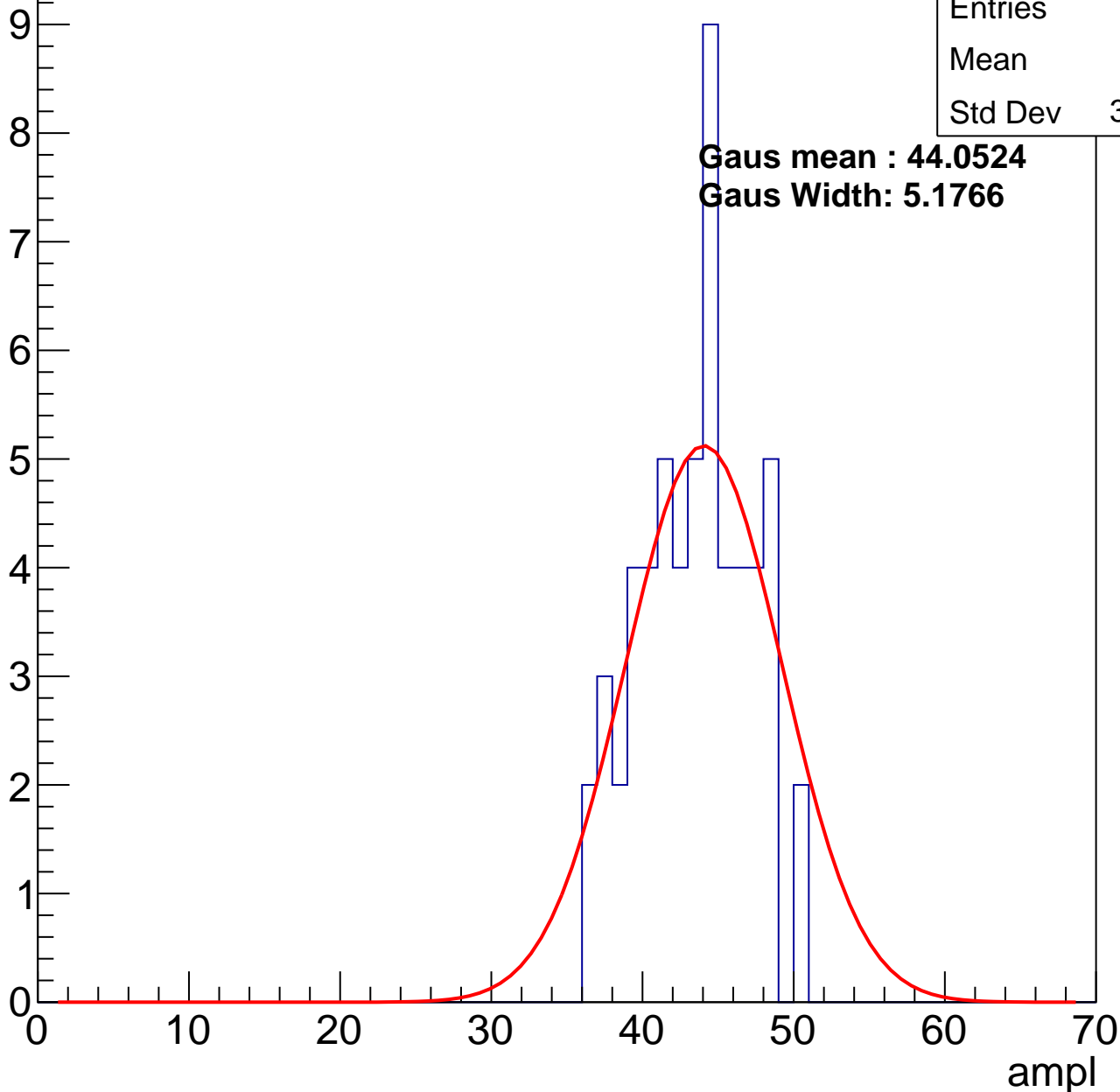
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43
Std Dev	3.574

Gaus mean : 44.0524

Gaus Width: 5.1766



B1L102S, U22-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	49.41
Std Dev	4.05

Entry

10

8

6

4

2

0

0

10

20

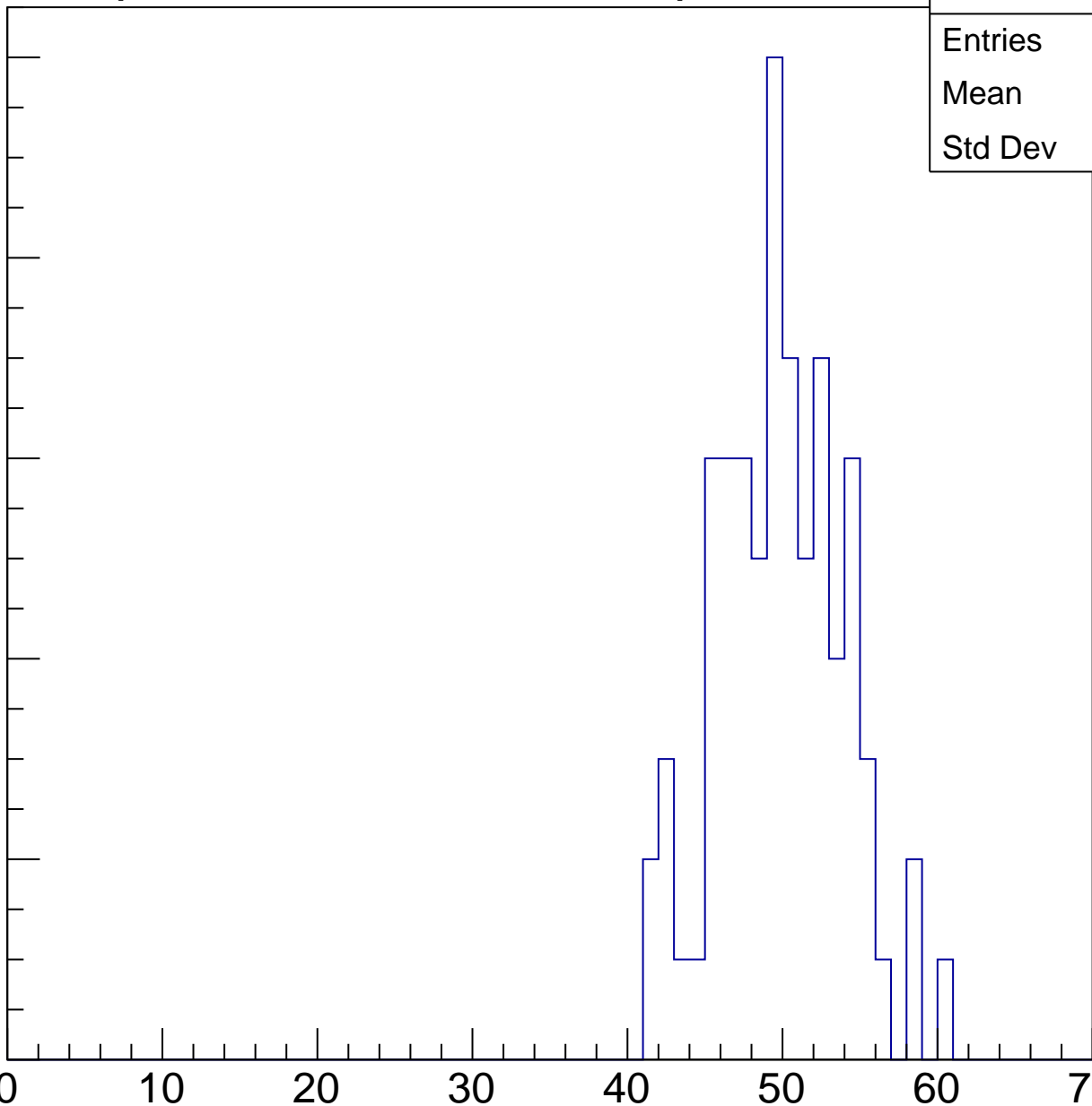
30

40

50

60

ampl

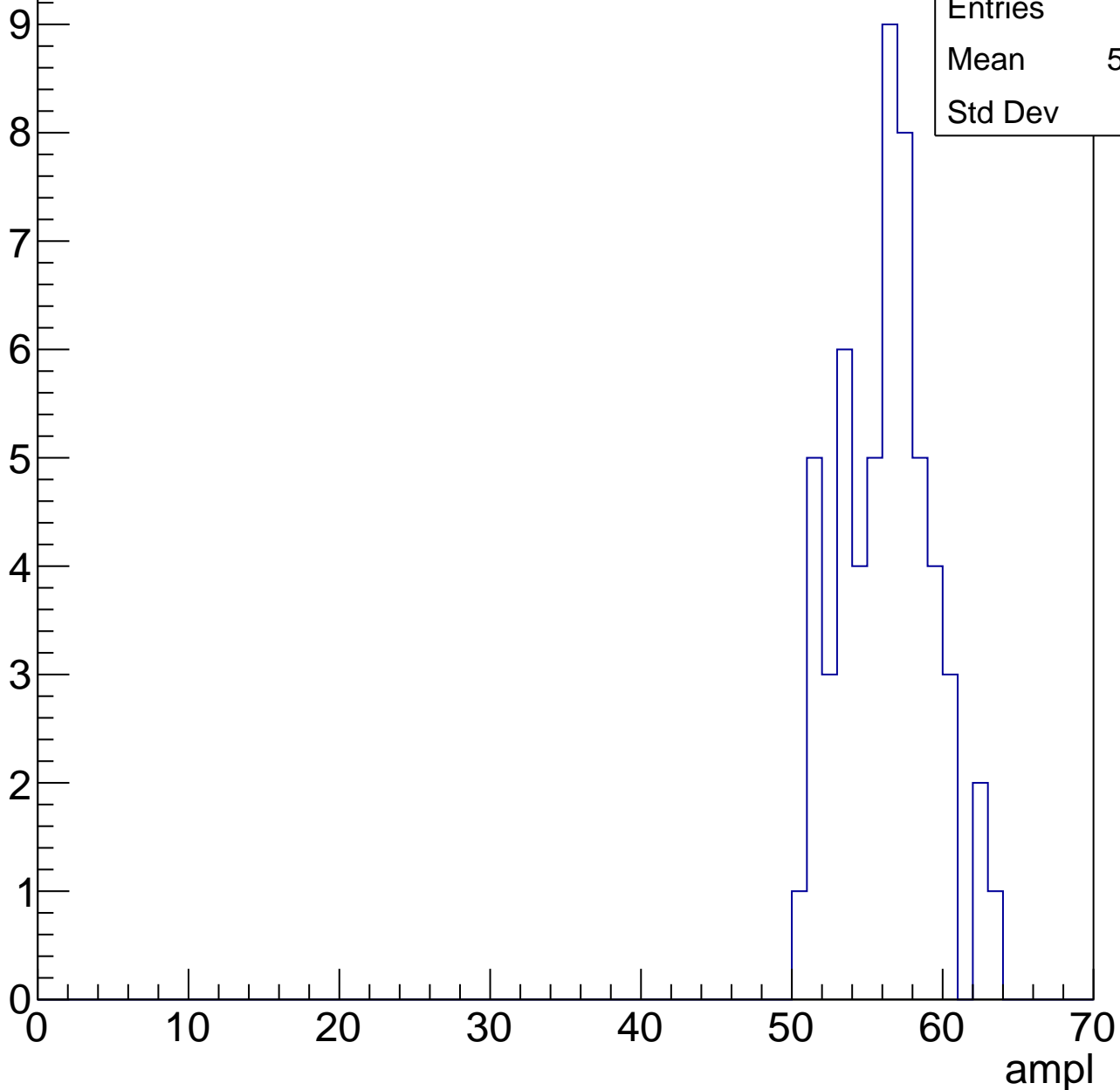


B1L102S, U22-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	55.77
Std Dev	3.03

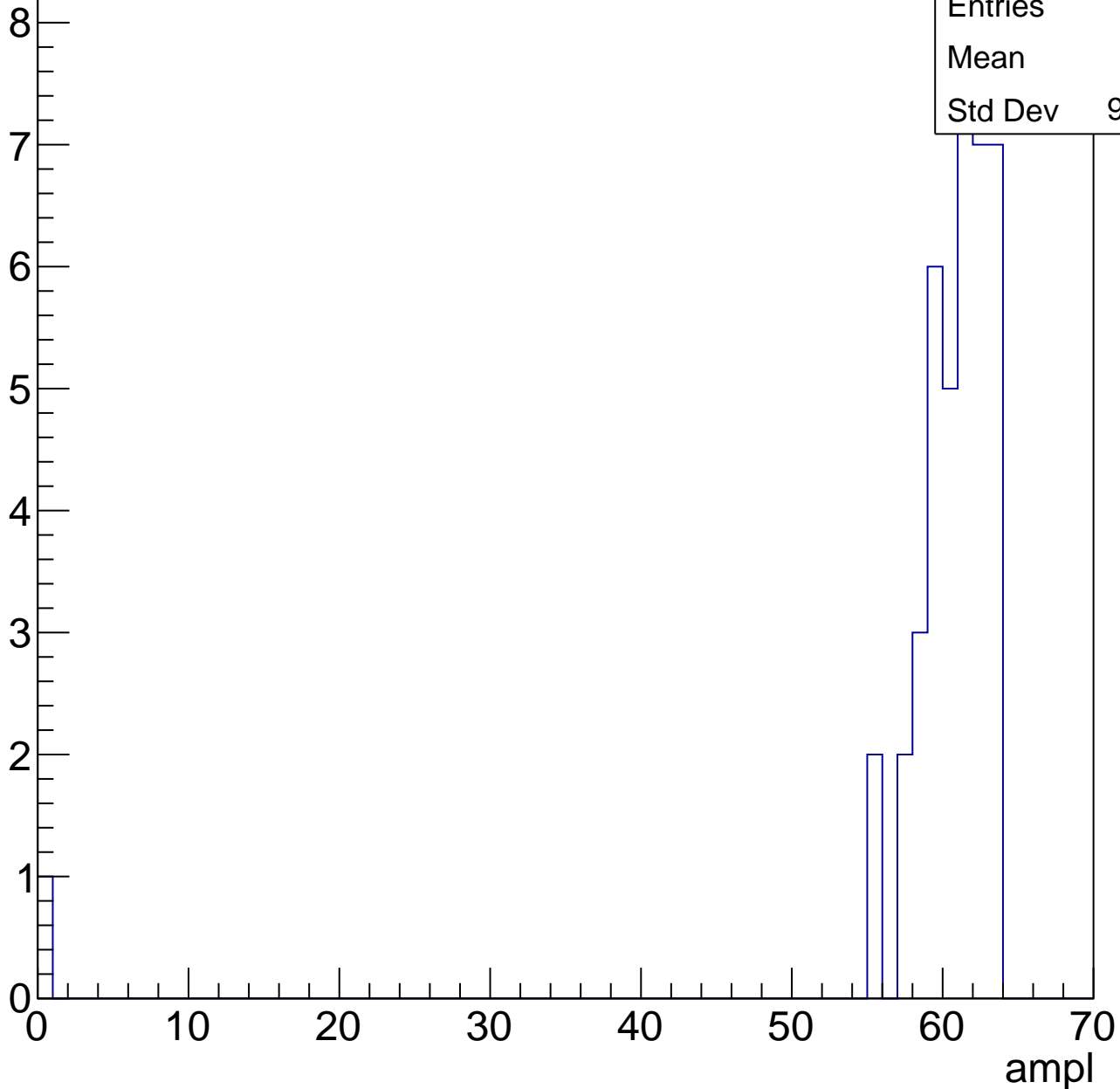


B1L102S, U22-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.9
Std Dev	9.545



B1L102S, U22-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

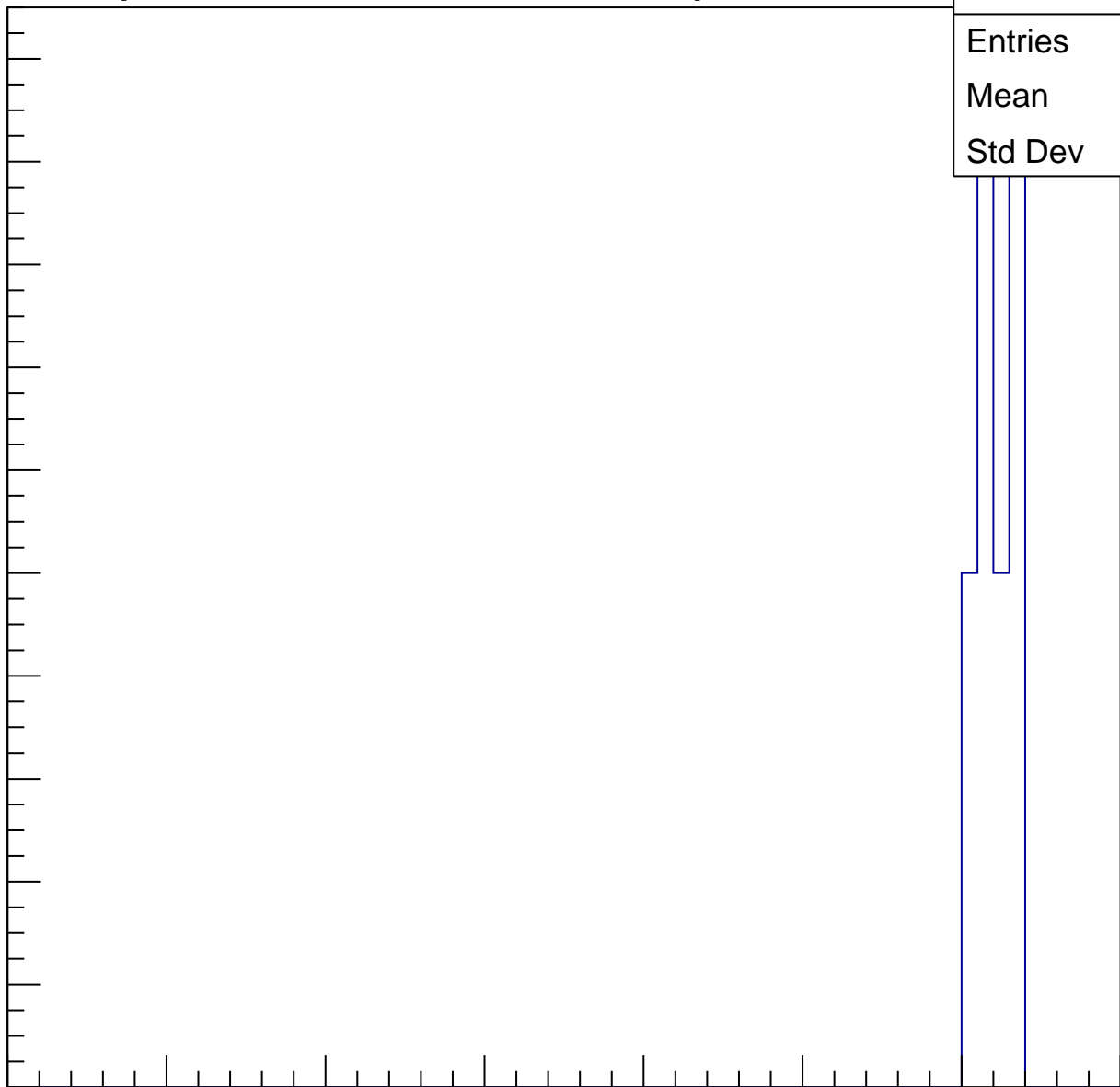
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

0 10 20 30 40 50 60 70

ampl



B1L102S, U22-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U22-ch74, adc0

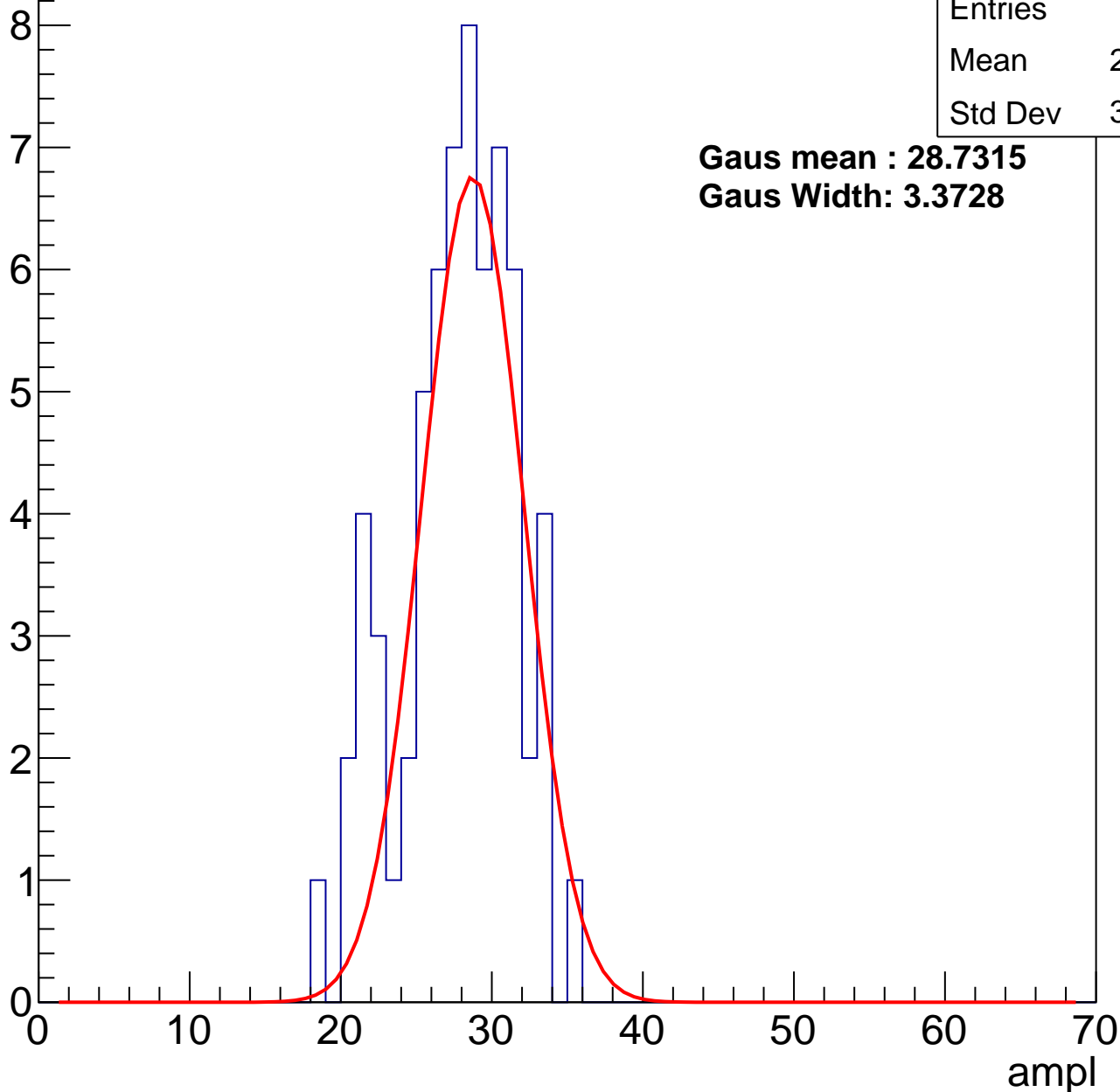
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.29
Std Dev	3.724

Gaus mean : 28.7315

Gaus Width: 3.3728



B1L102S, U22-ch74, adc1

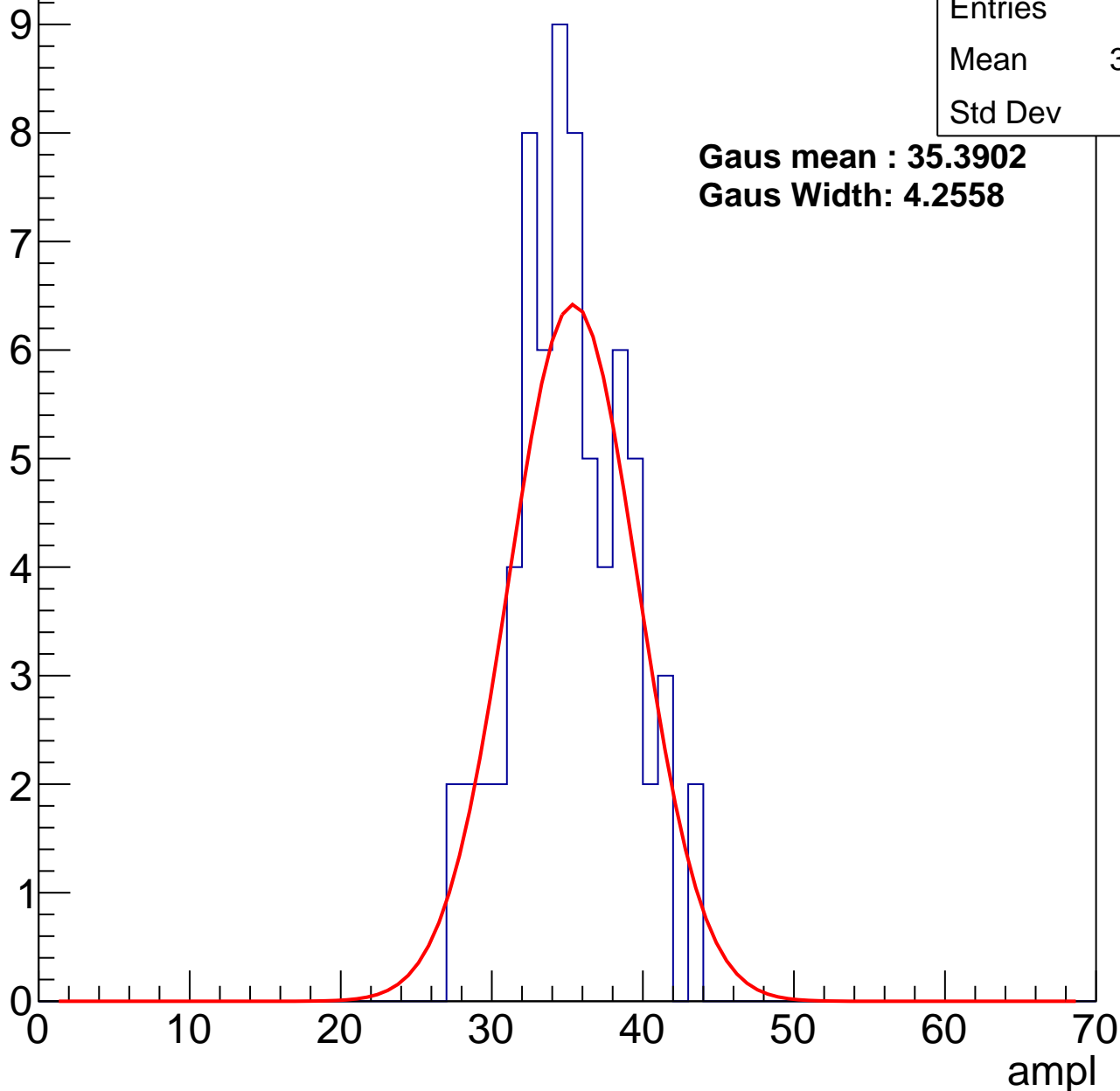
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.74
Std Dev	3.69

Gaus mean : 35.3902

Gaus Width: 4.2558



B1L102S, U22-ch74, adc2

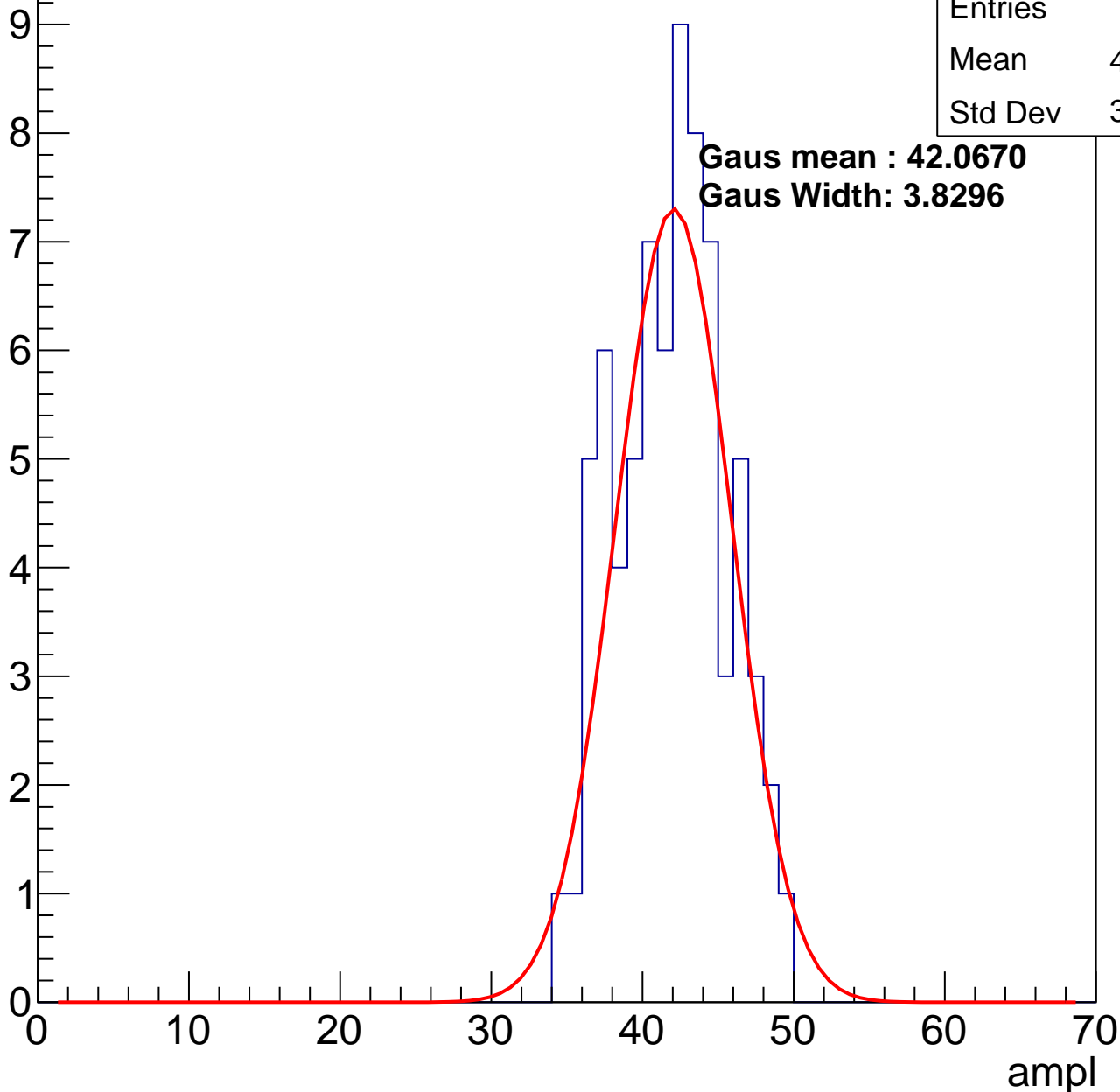
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.44
Std Dev	3.515

Gaus mean : 42.0670

Gaus Width: 3.8296

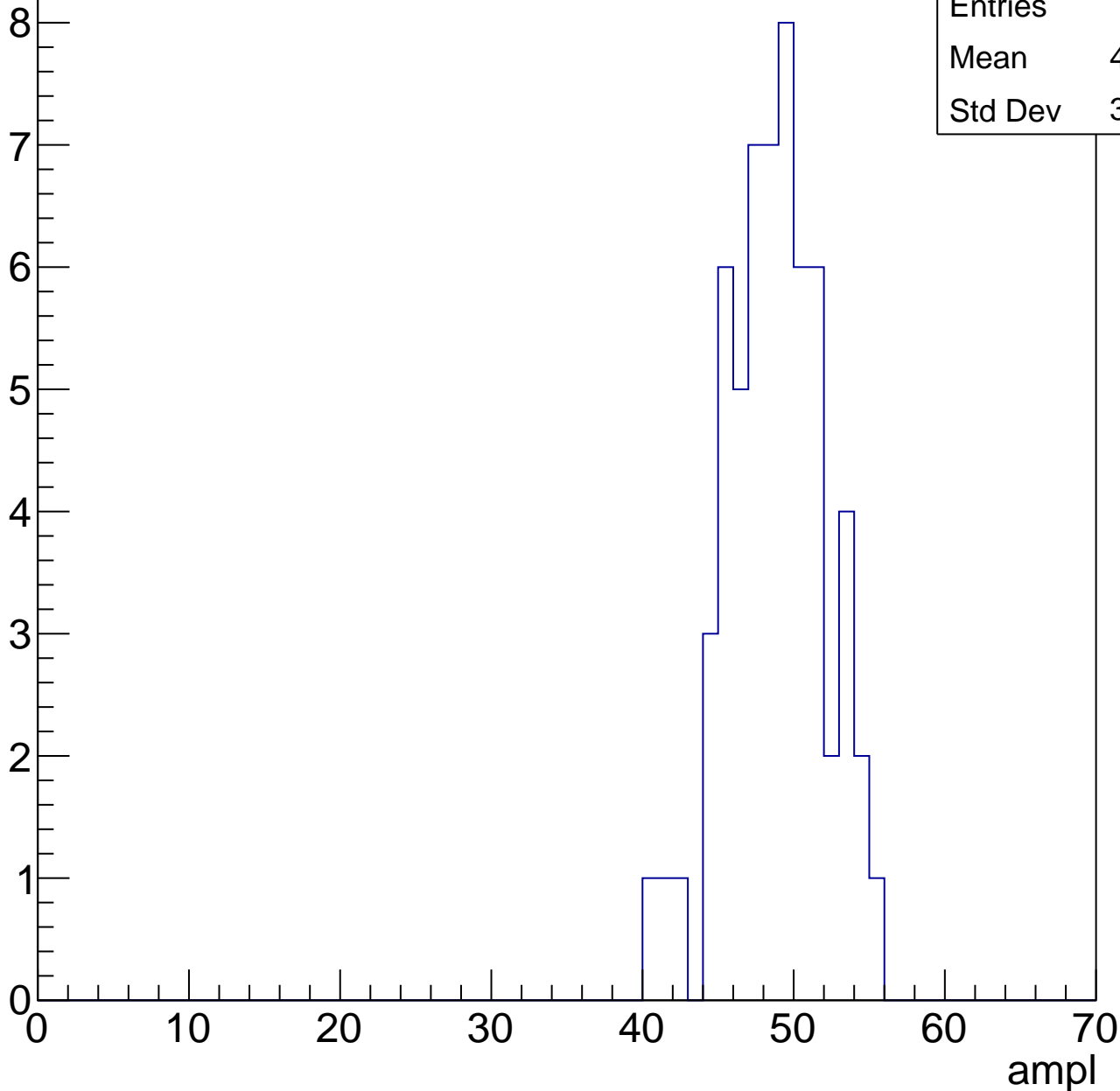


B1L102S, U22-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.28
Std Dev	3.194

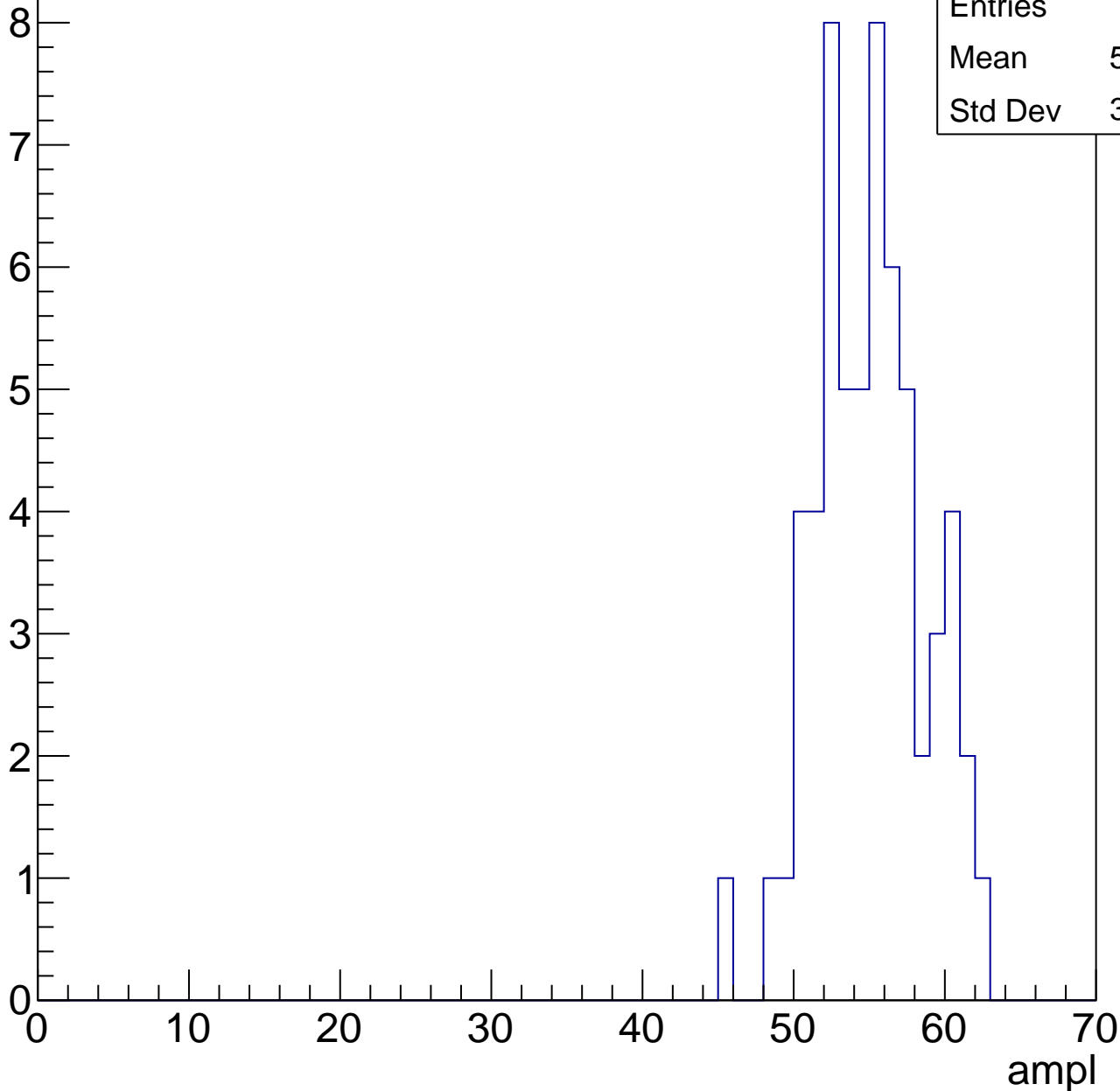


B1L102S, U22-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.58
Std Dev	3.532



B1L102S, U22-ch74, adc5

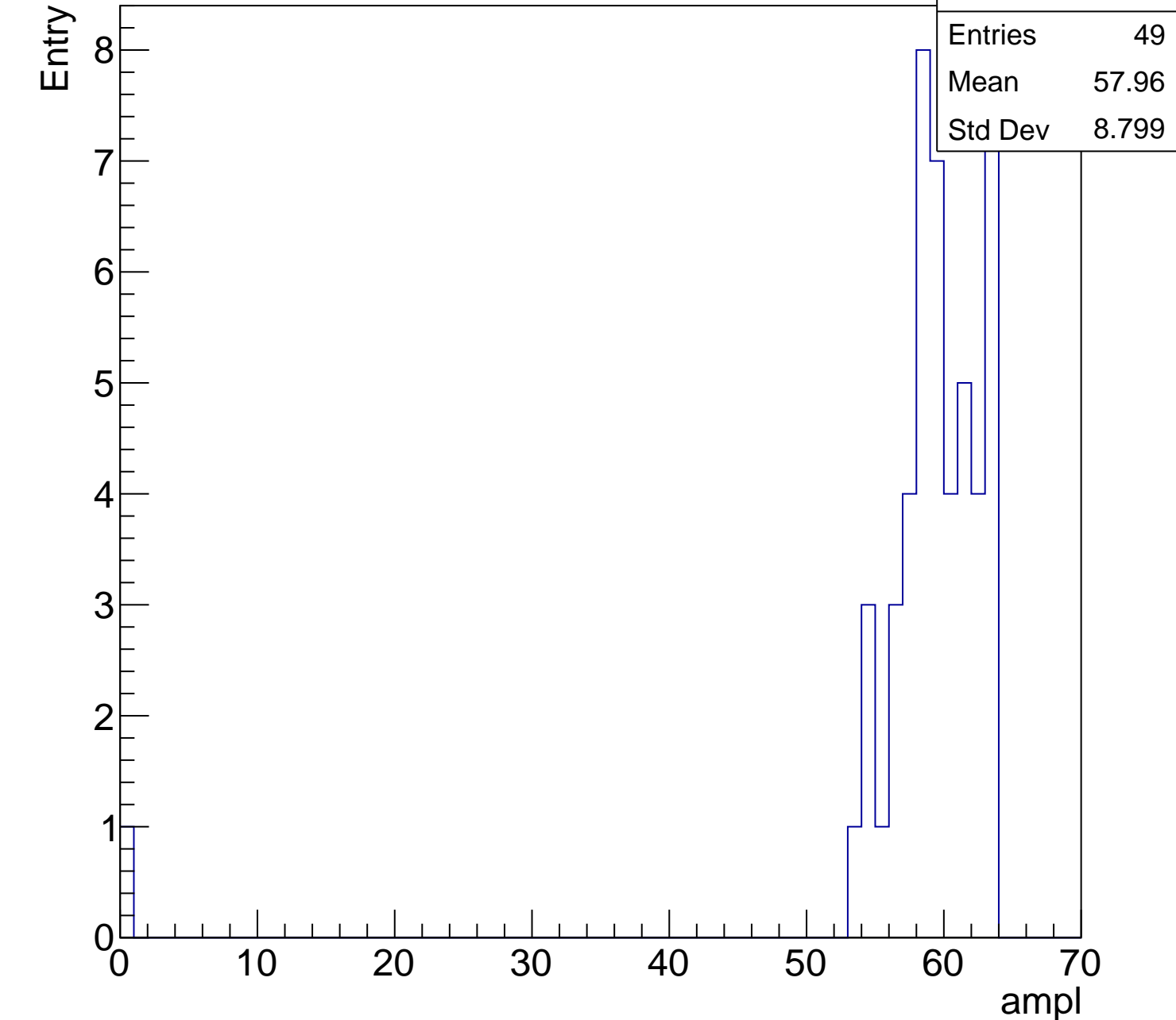
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	57.96
Std Dev	8.799

ampl

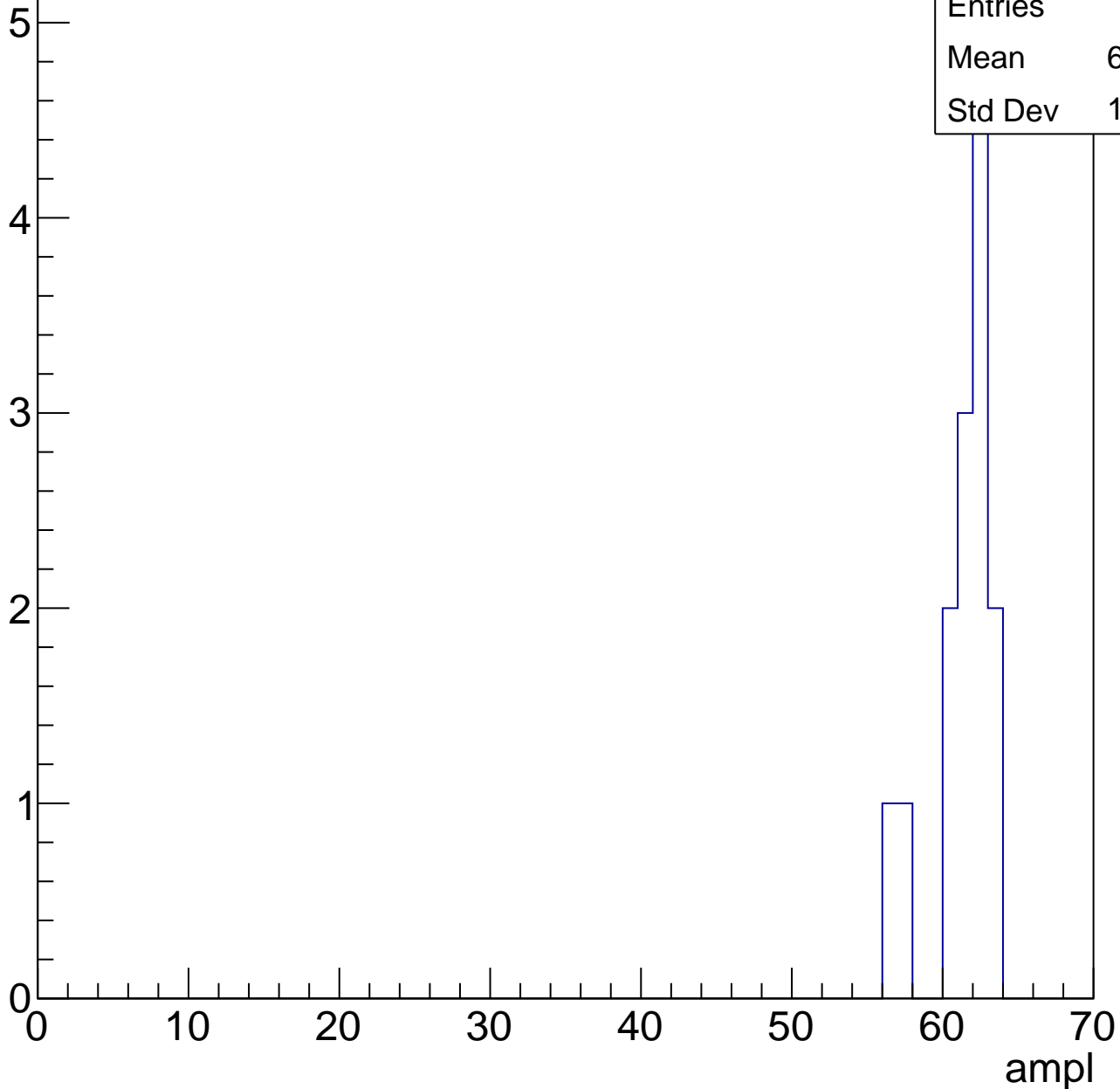


B1L102S, U22-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	60.86
Std Dev	1.995



B1L102S, U22-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch75, adc0

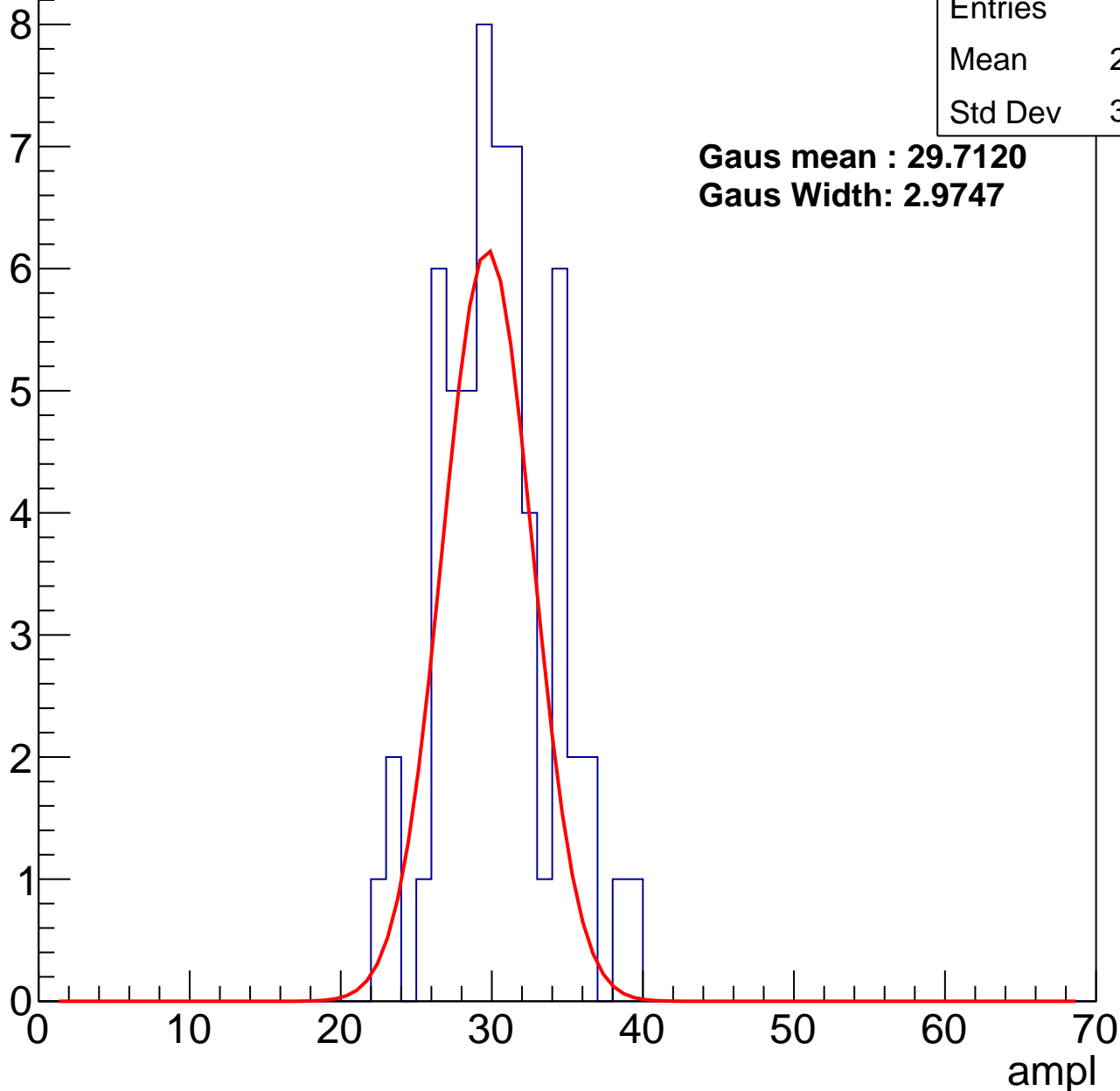
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.95
Std Dev	3.567

Gaus mean : 29.7120

Gaus Width: 2.9747



B1L102S, U22-ch75, adc1

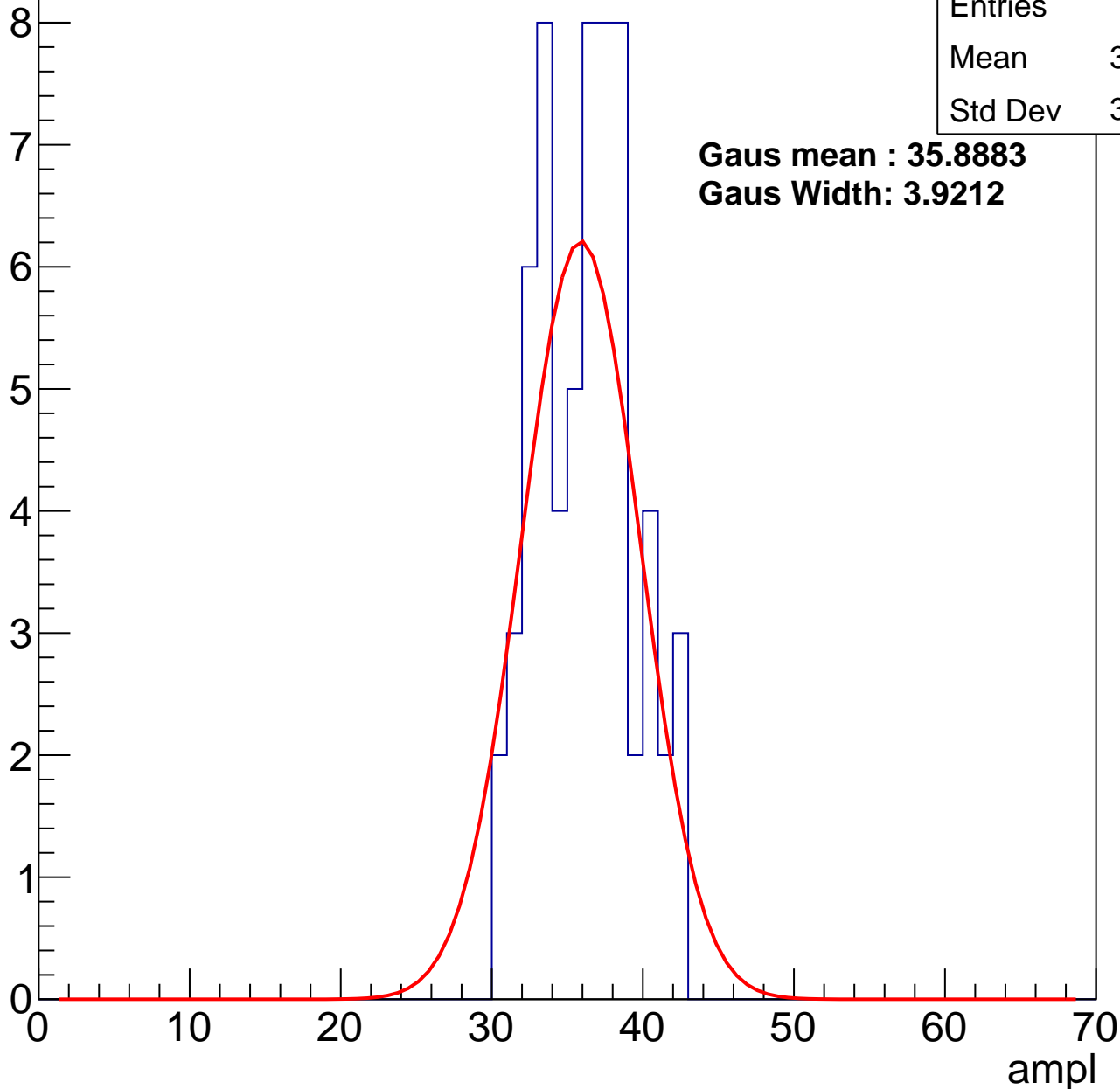
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.78
Std Dev	3.119

Gaus mean : 35.8883

Gaus Width: 3.9212



B1L102S, U22-ch75, adc2

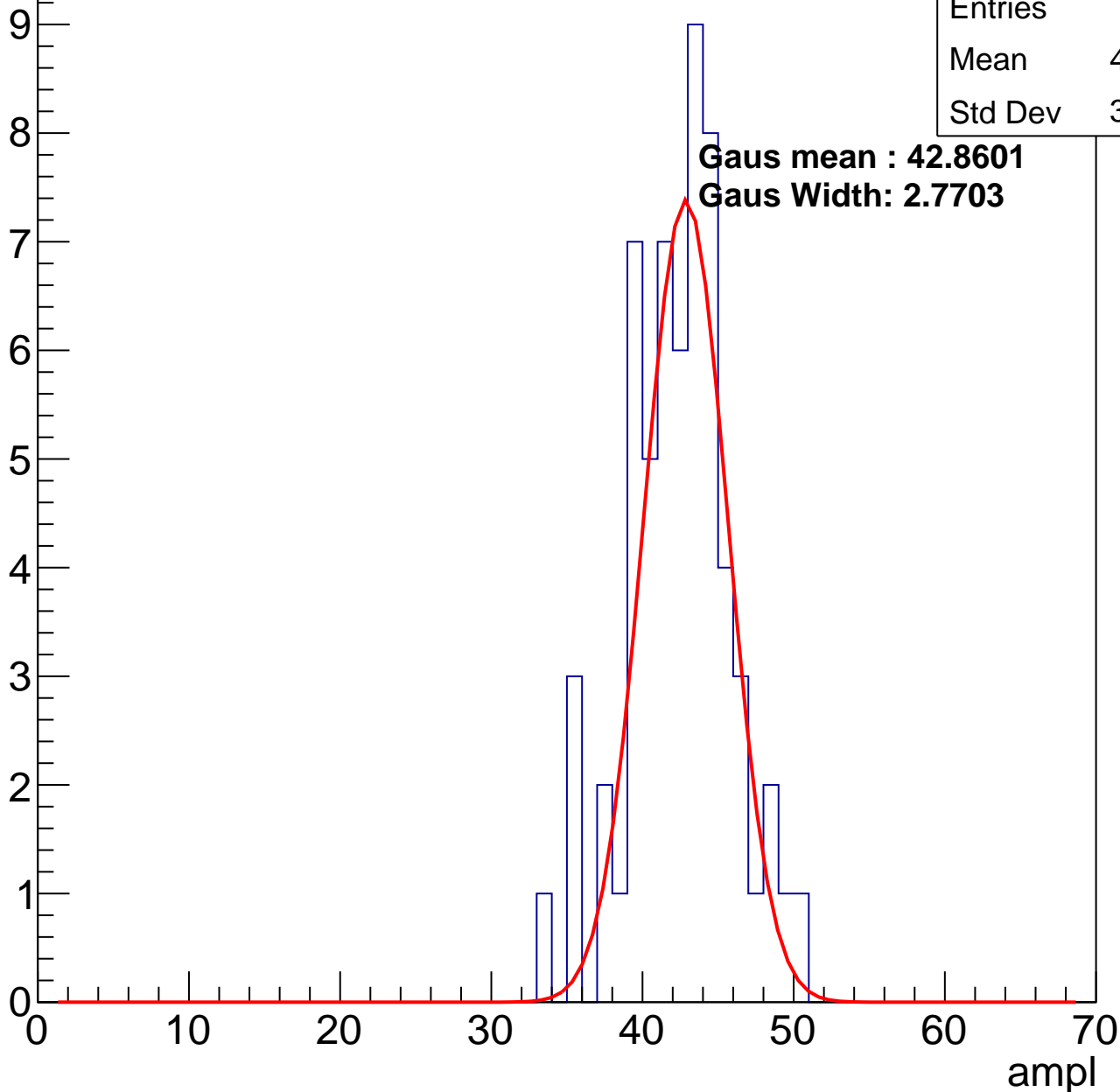
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.98
Std Dev	3.433

Gaus mean : 42.8601

Gaus Width: 2.7703

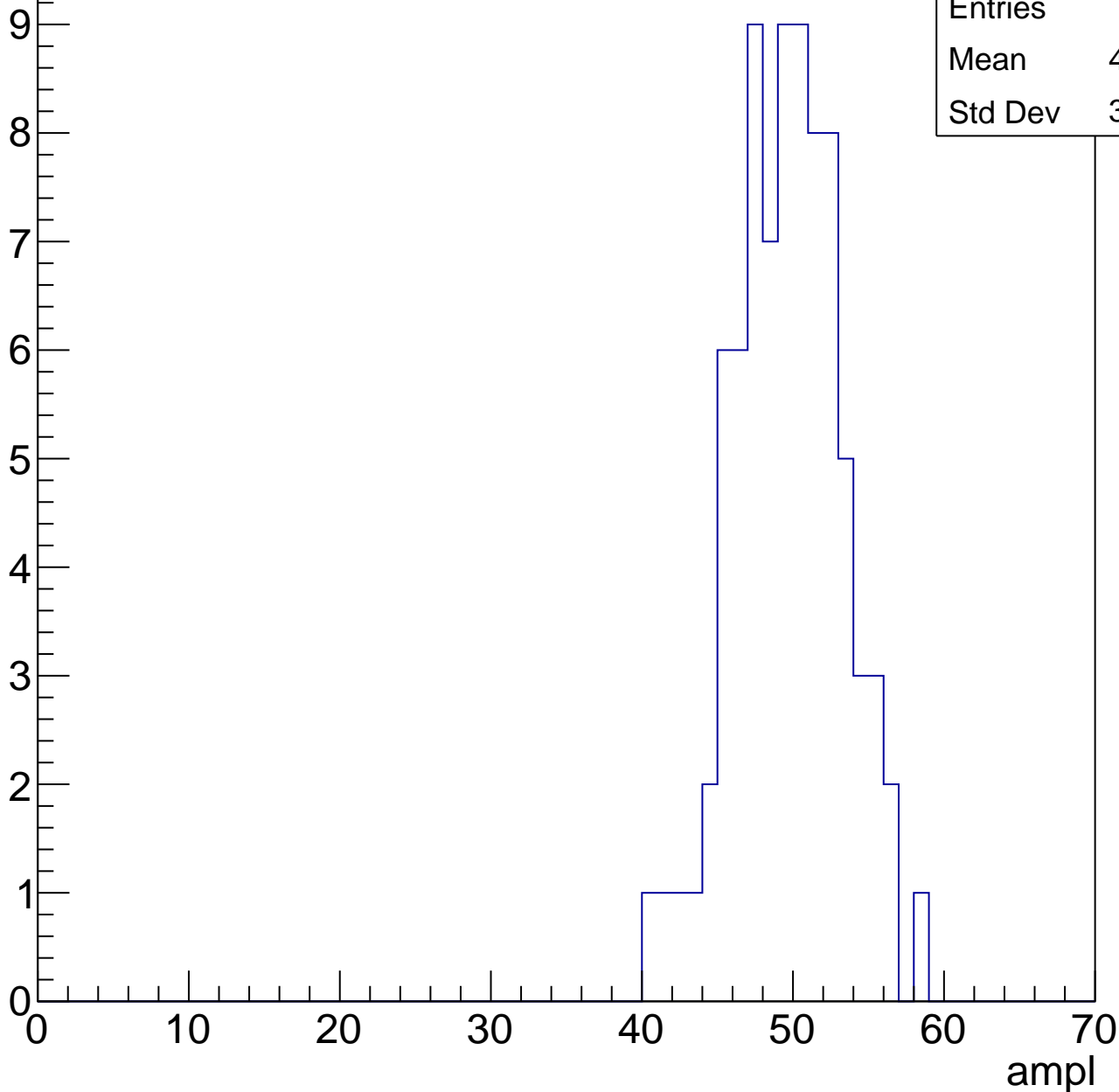


B1L102S, U22-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	49.22
Std Dev	3.534

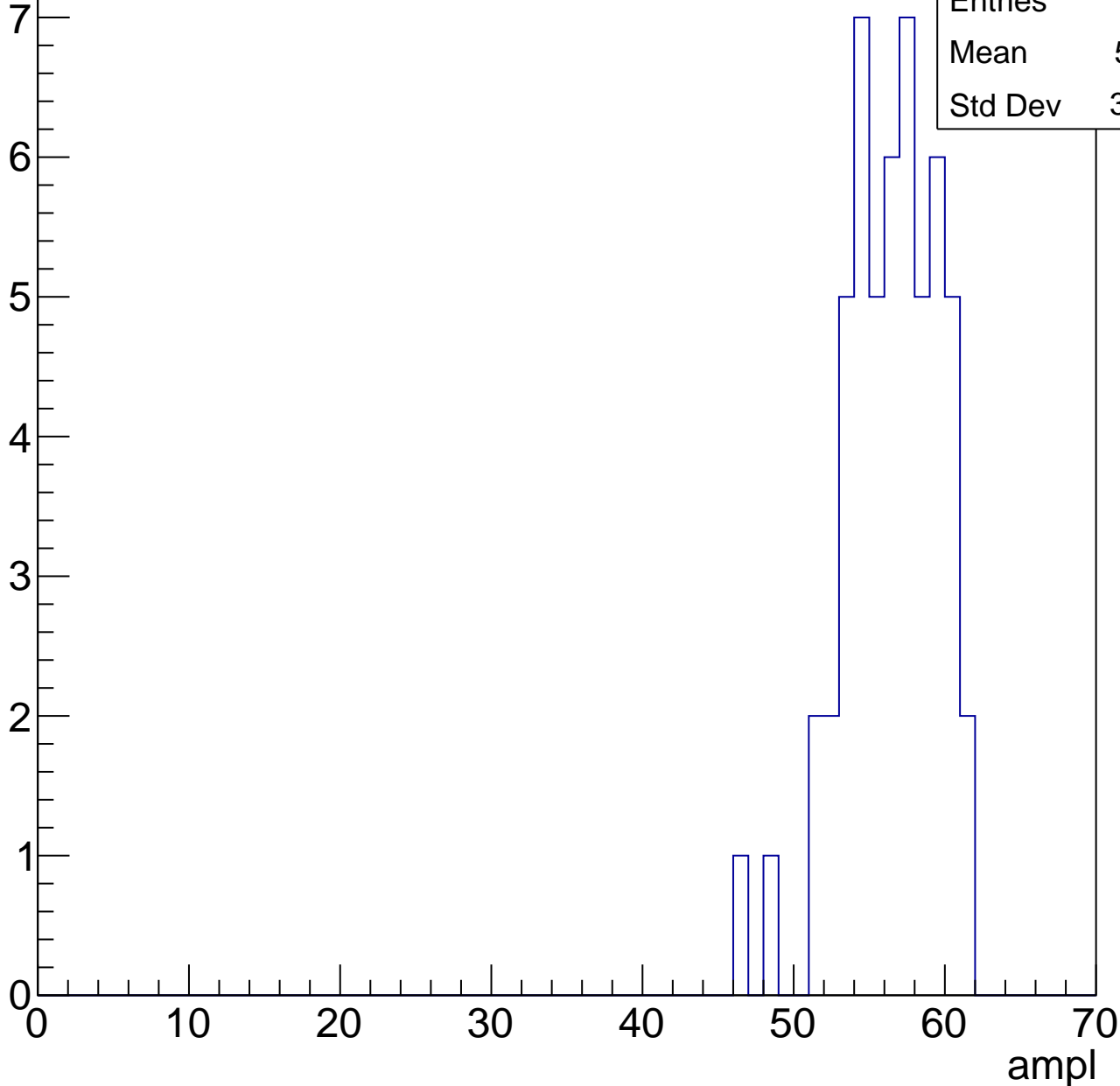


B1L102S, U22-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.91
Std Dev	3.146



B1L102S, U22-ch75, adc5

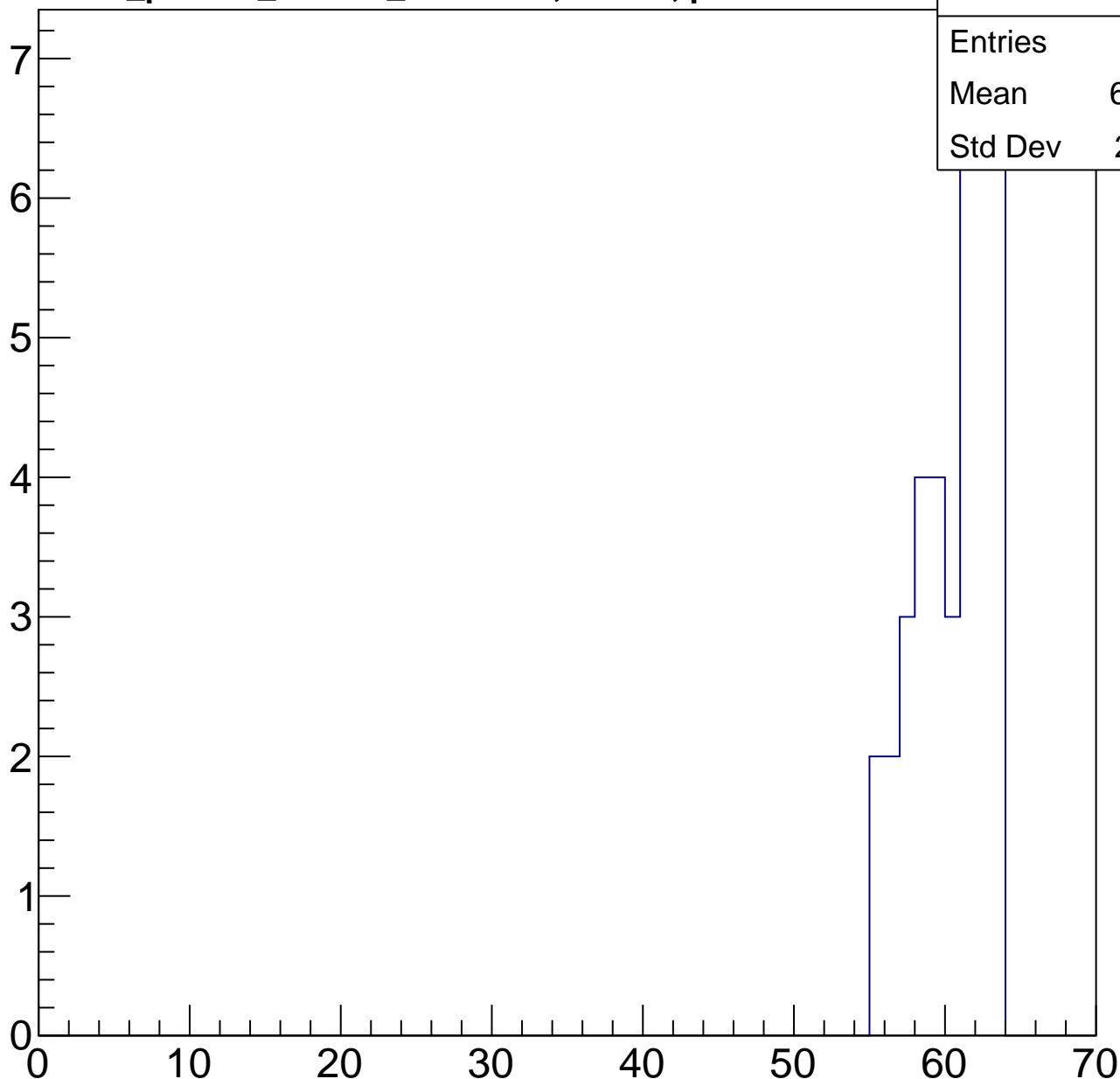
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	39
Mean	60.08
Std Dev	2.411

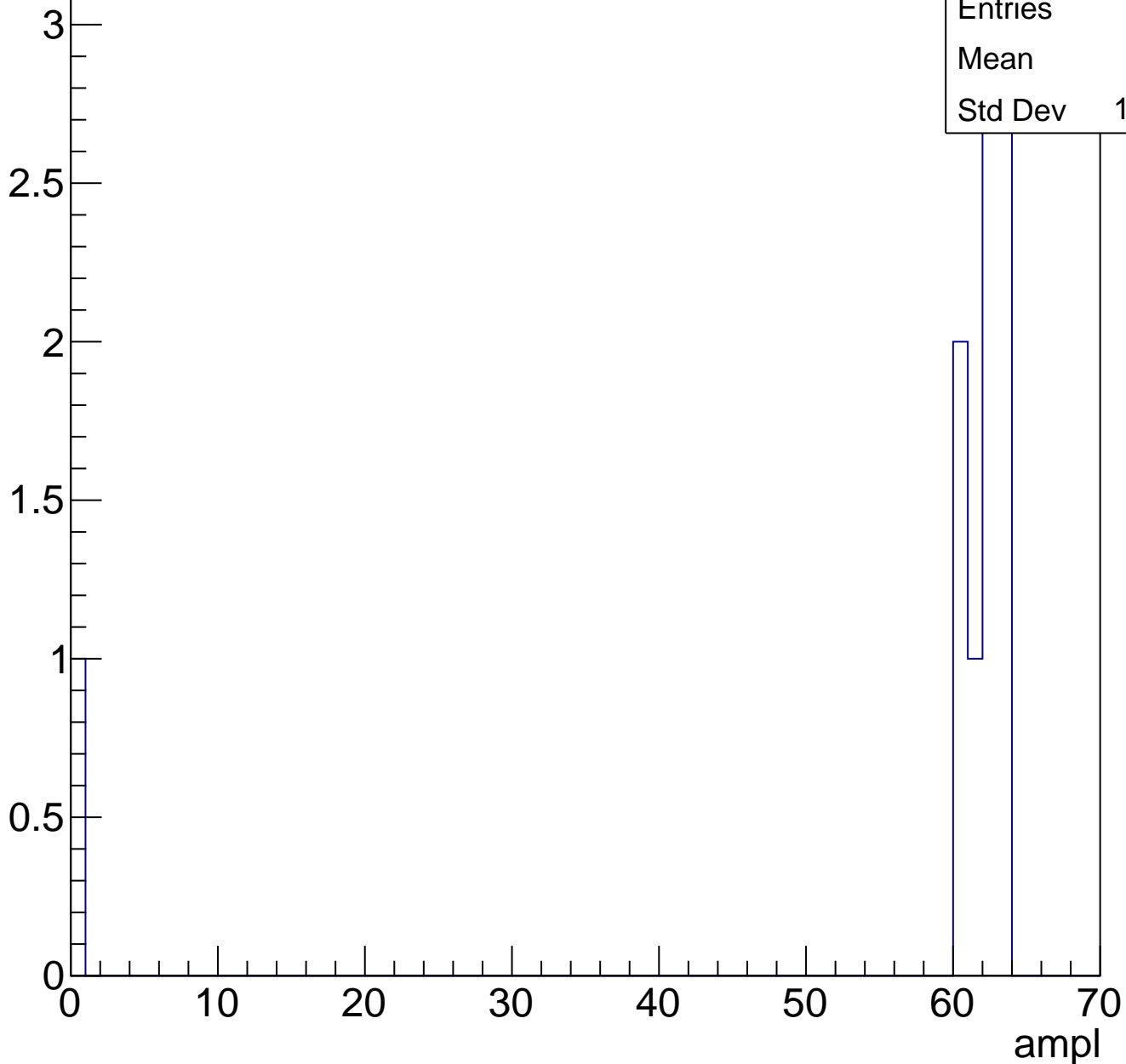
ampl



B1L102S, U22-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch76, adc0

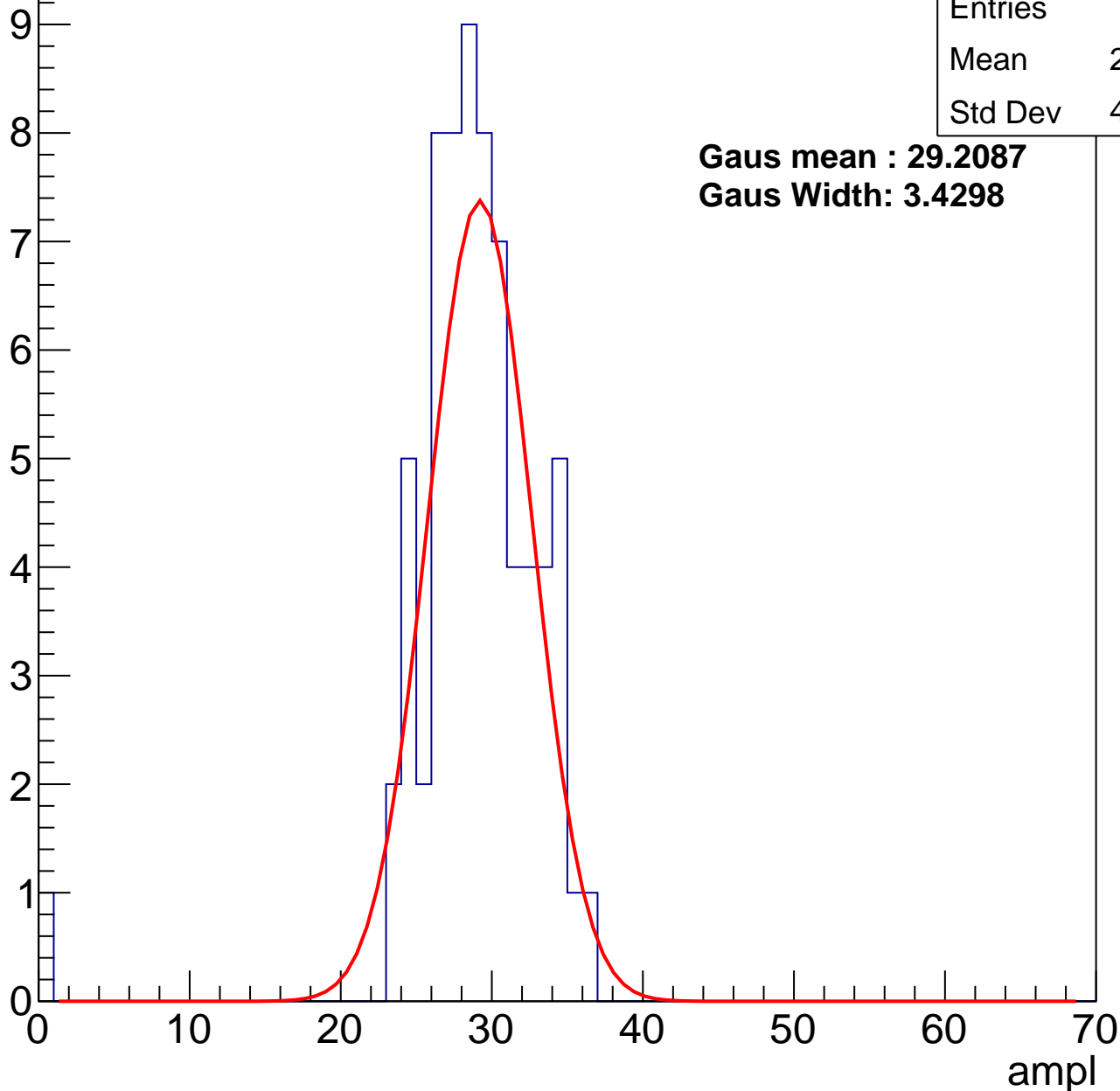
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.39
Std Dev	4.648

Gaus mean : 29.2087

Gaus Width: 3.4298



B1L102S, U22-ch76, adc1

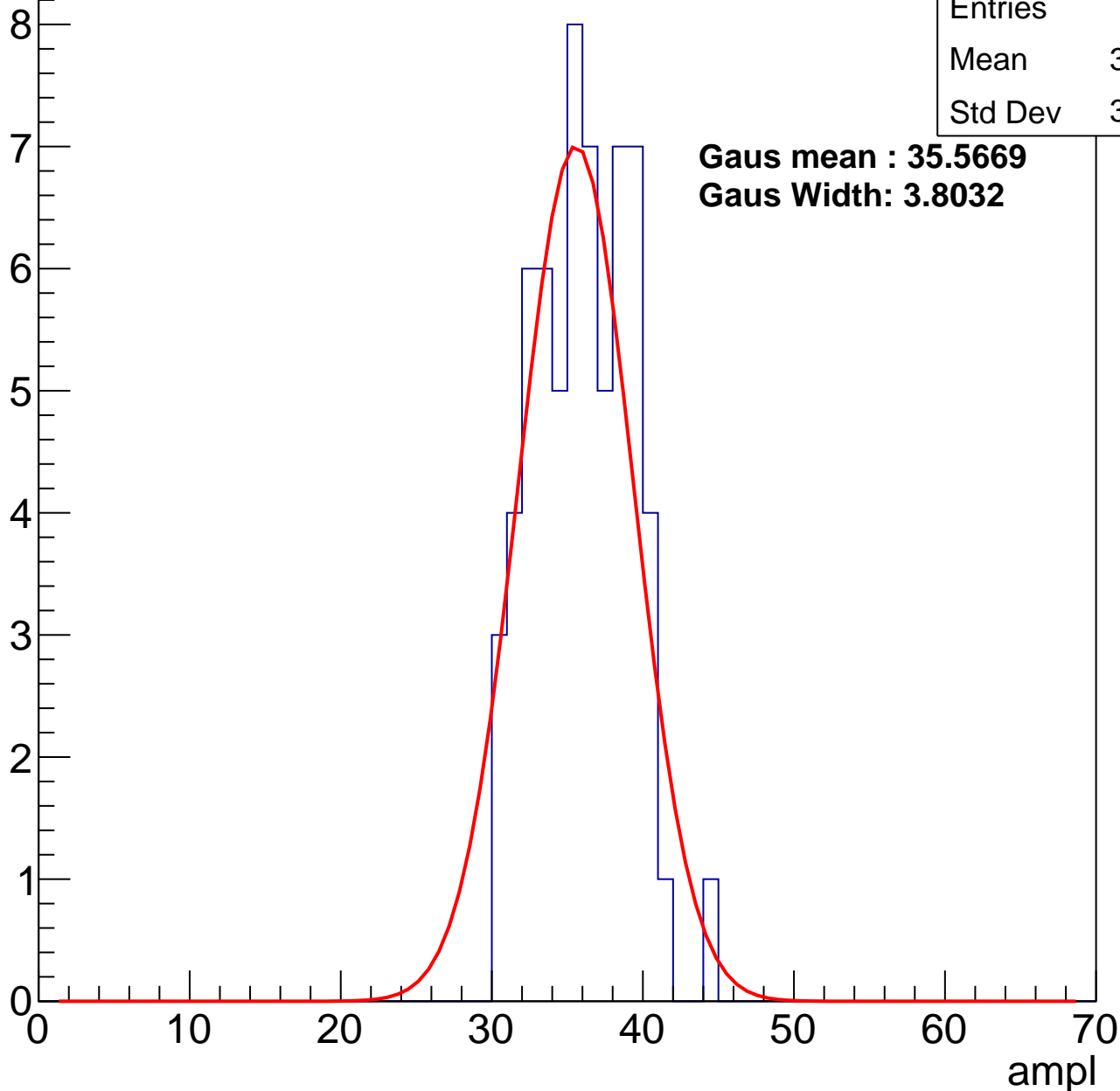
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.55
Std Dev	3.117

Gaus mean : 35.5669

Gaus Width: 3.8032



B1L102S, U22-ch76, adc2

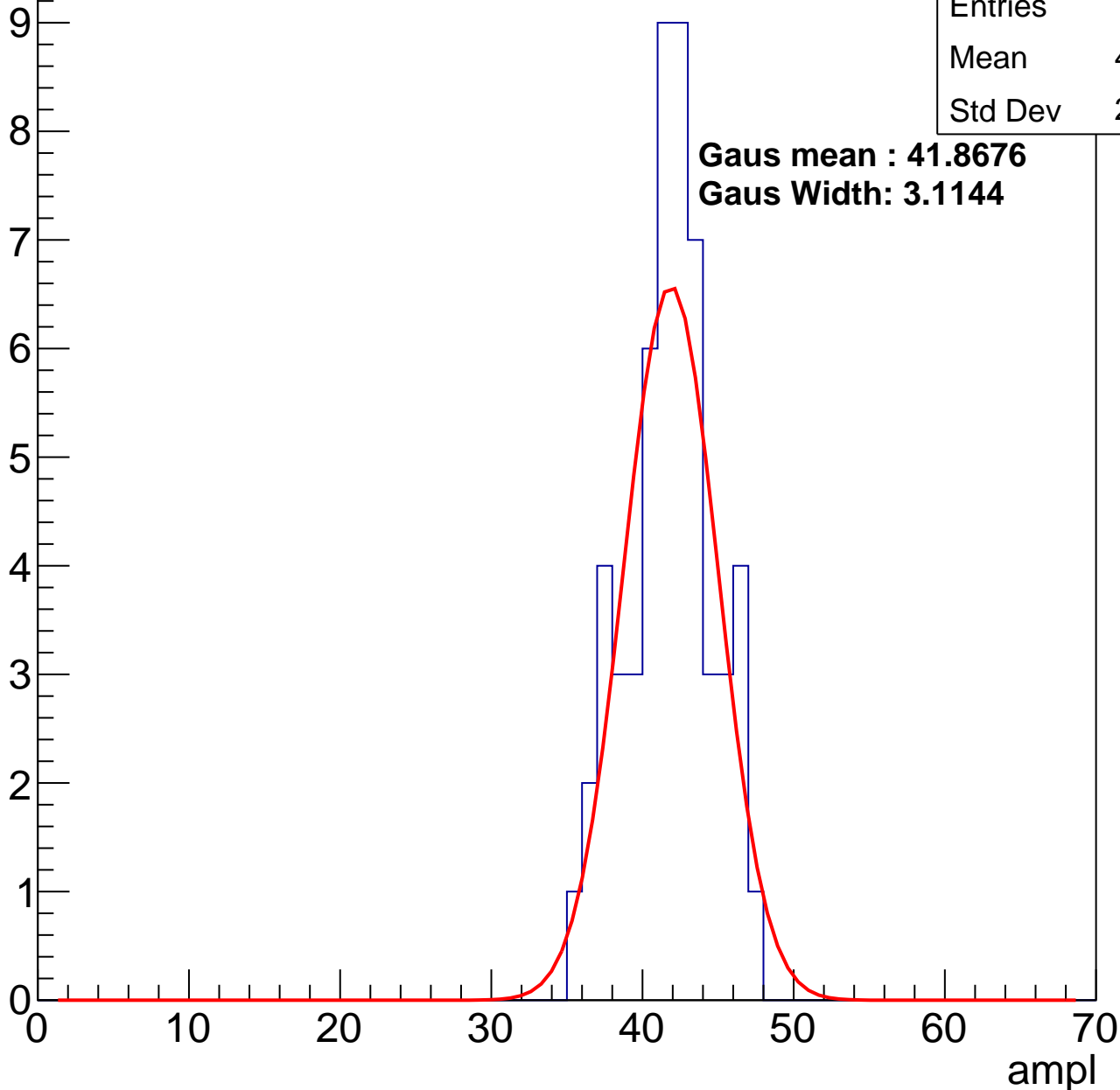
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.31
Std Dev	2.821

Gaus mean : 41.8676

Gaus Width: 3.1144

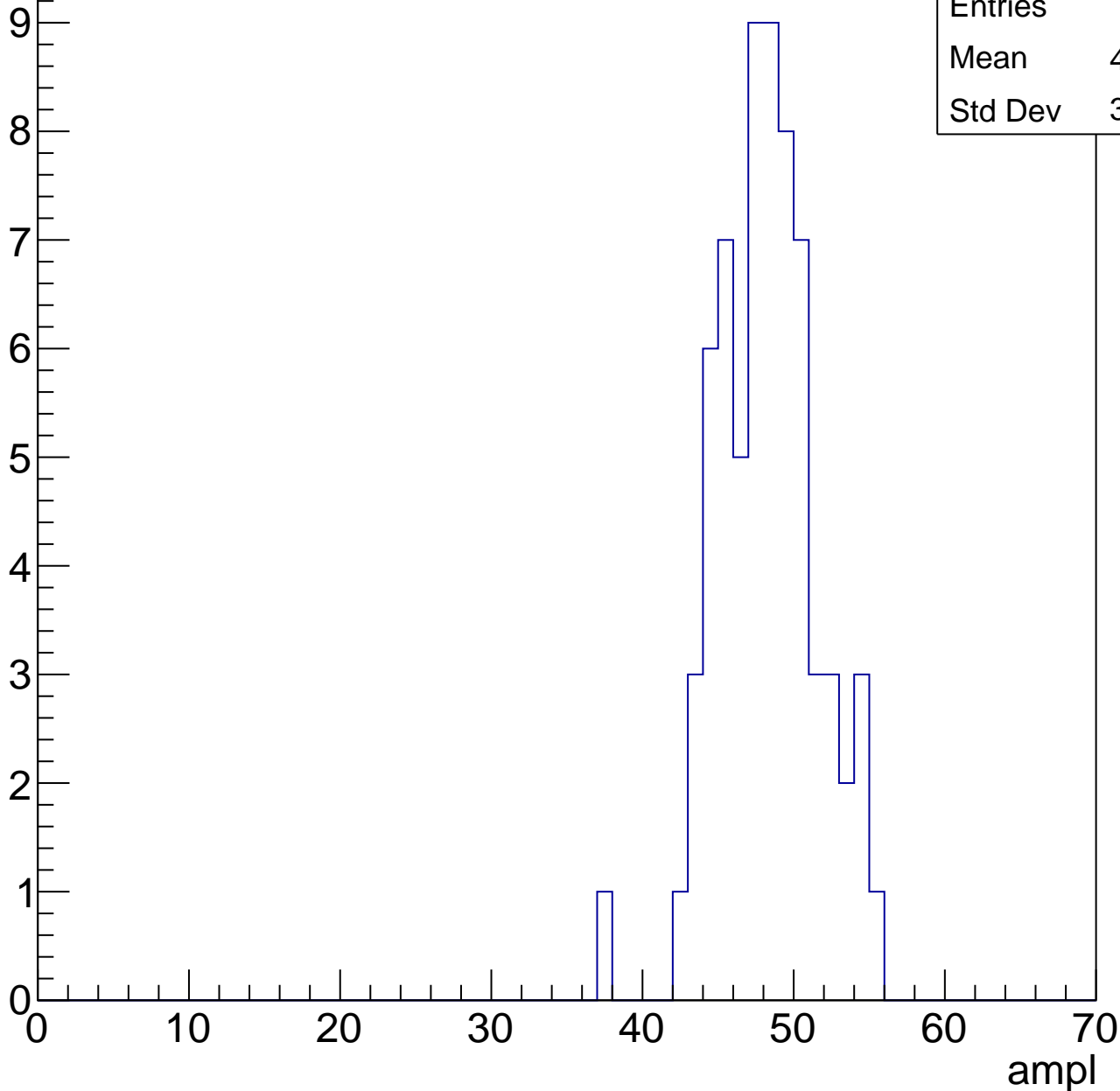


B1L102S, U22-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	47.74
Std Dev	3.284

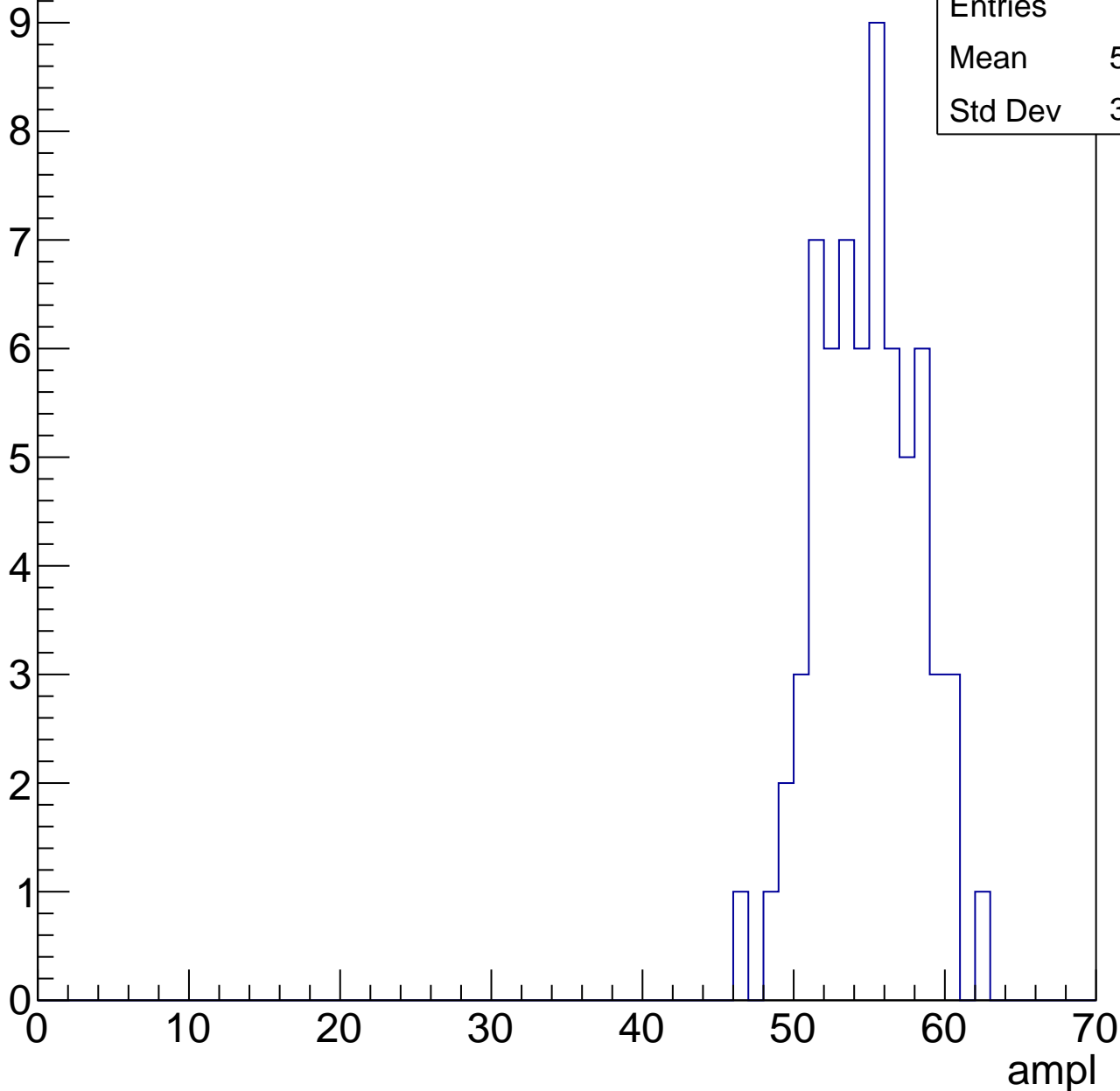


B1L102S, U22-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	54.38
Std Dev	3.265

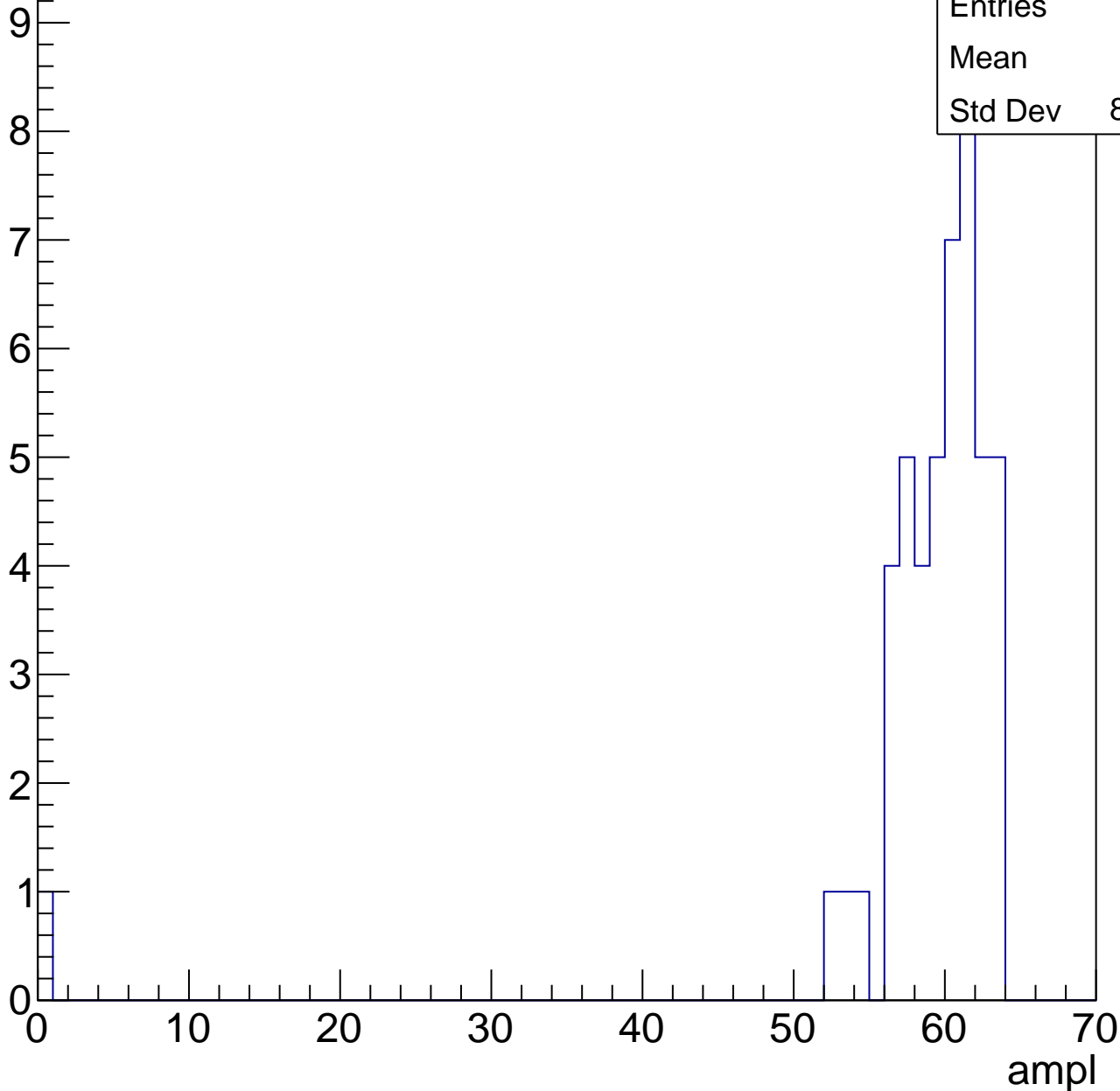


B1L102S, U22-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.1
Std Dev	8.875

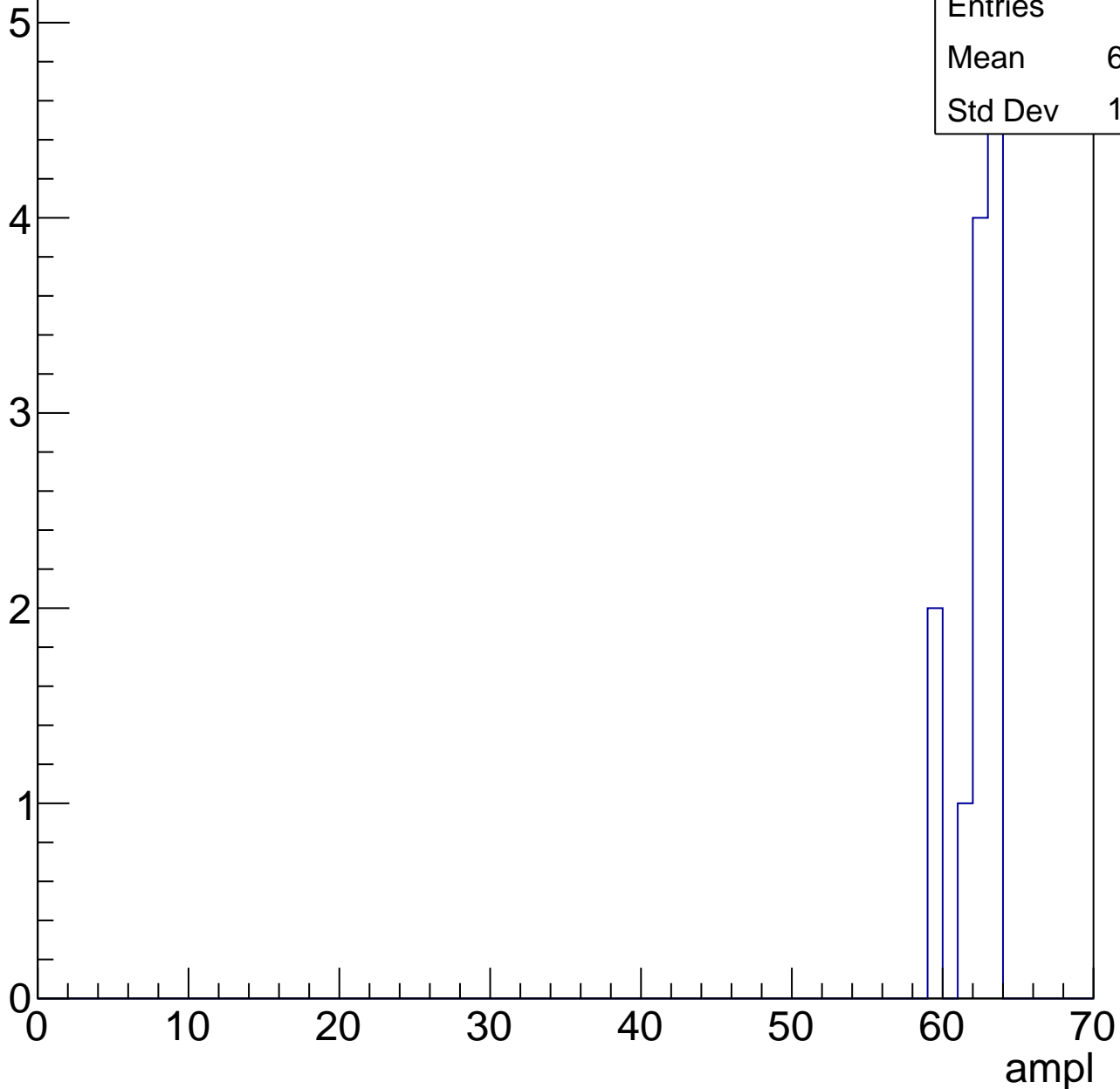


B1L102S, U22-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.83
Std Dev	1.404



B1L102S, U22-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U22-ch77, adc0

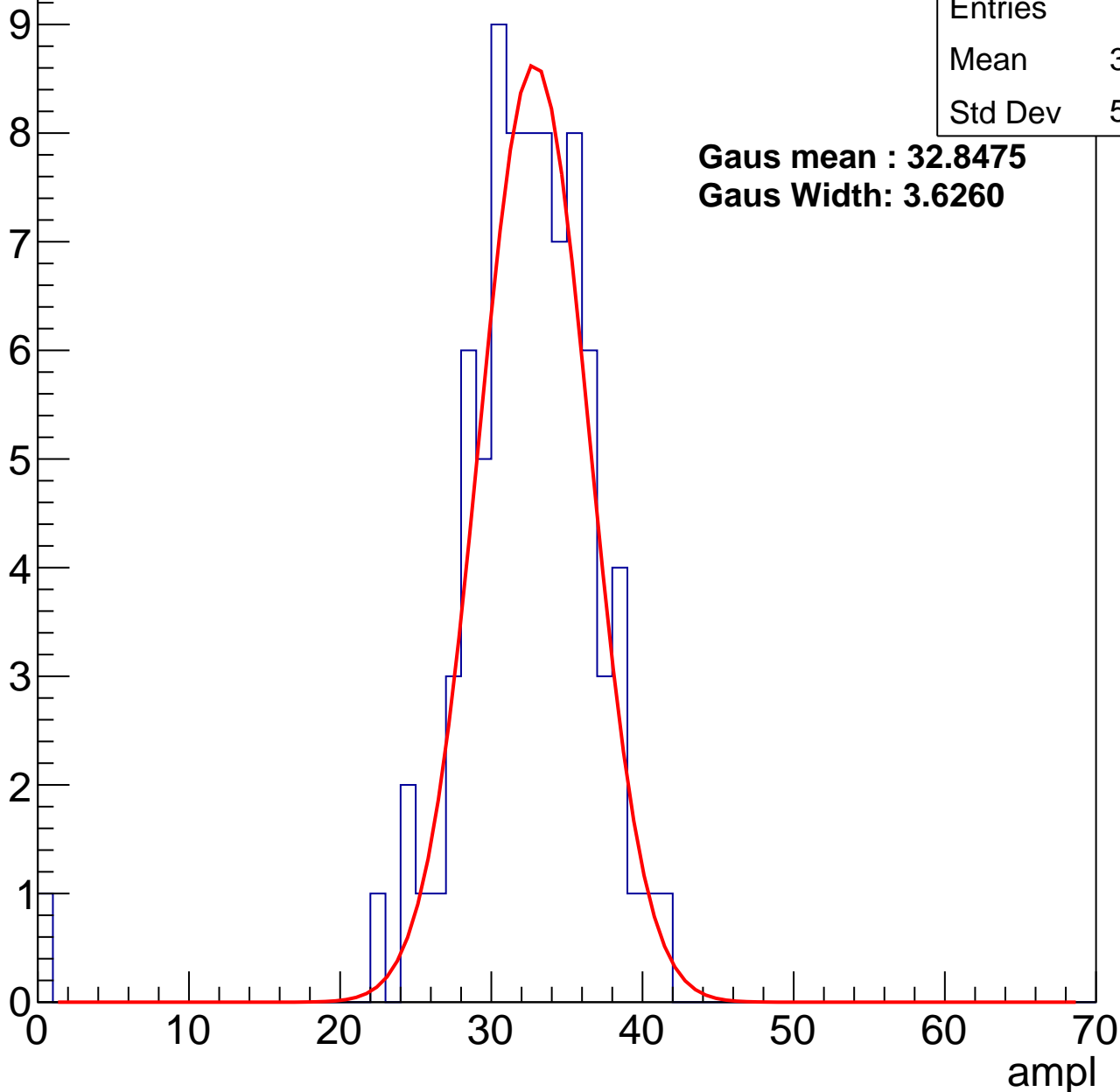
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	31.79
Std Dev	5.129

Gaus mean : 32.8475

Gaus Width: 3.6260



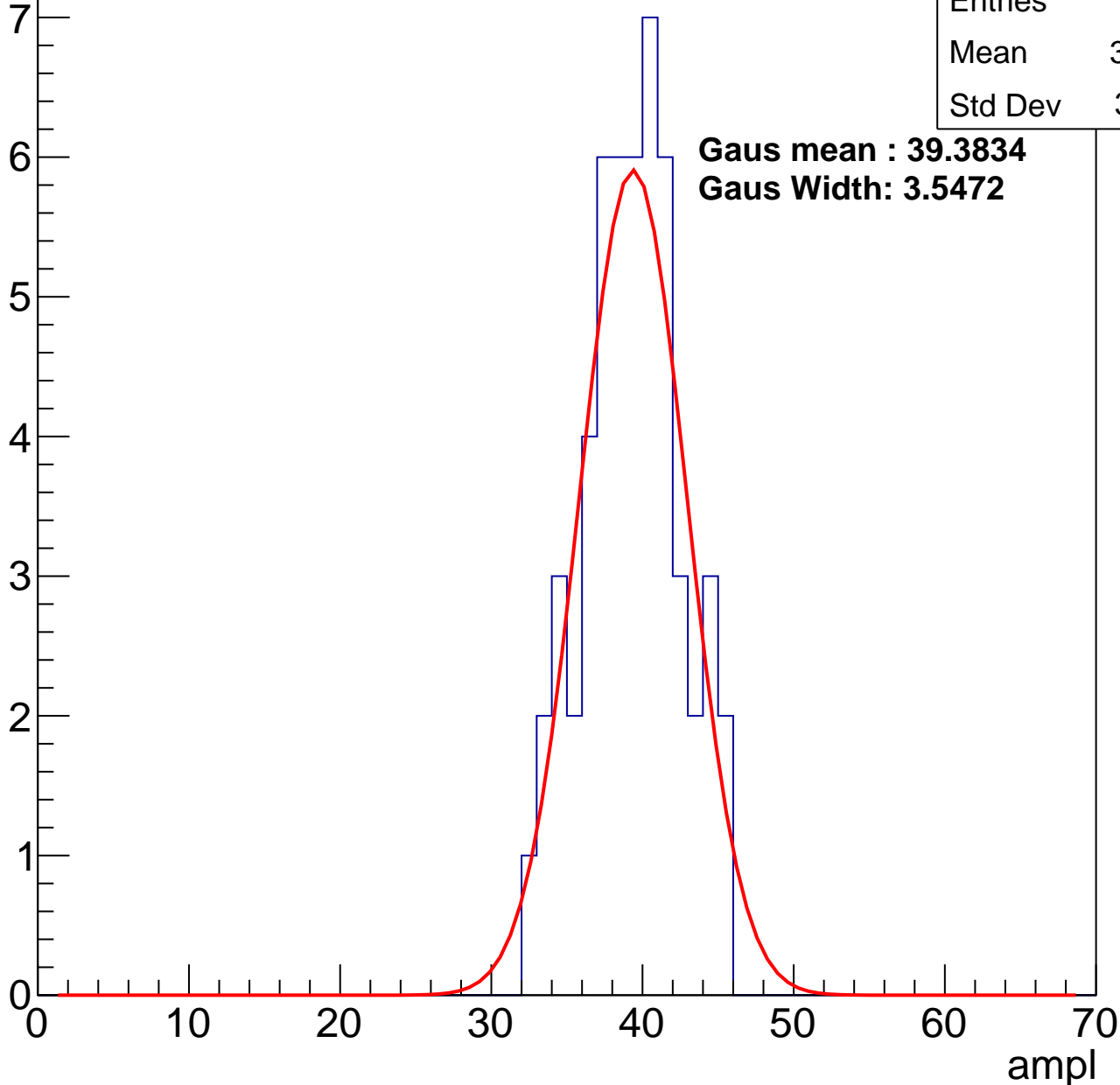
B1L102S, U22-ch77, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	38.83
Std Dev	3.161

Gaus mean : 39.3834
Gaus Width: 3.5472



B1L102S, U22-ch77, adc2

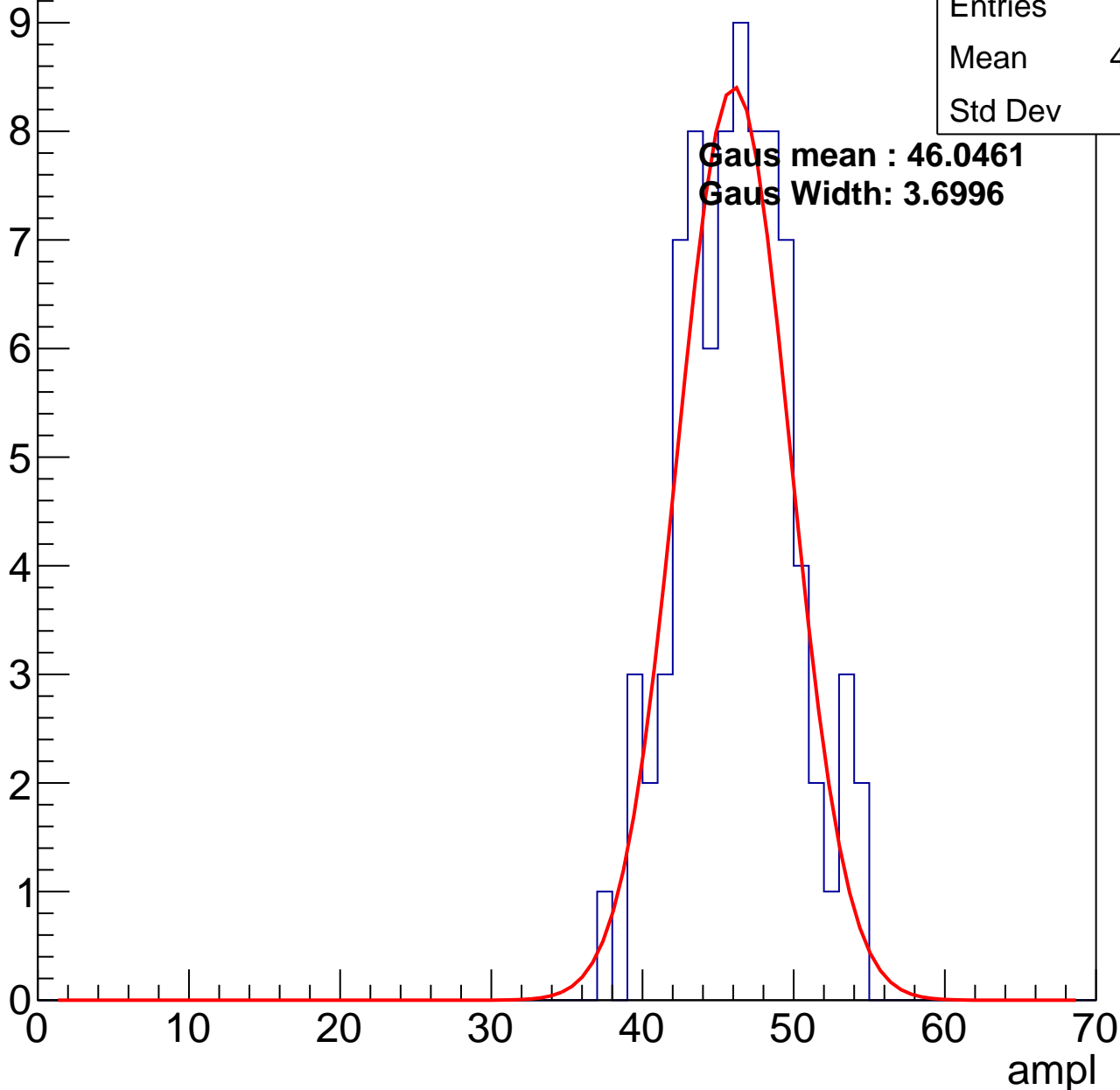
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	45.82
Std Dev	3.69

Gaus mean : 46.0461

Gaus Width: 3.6996

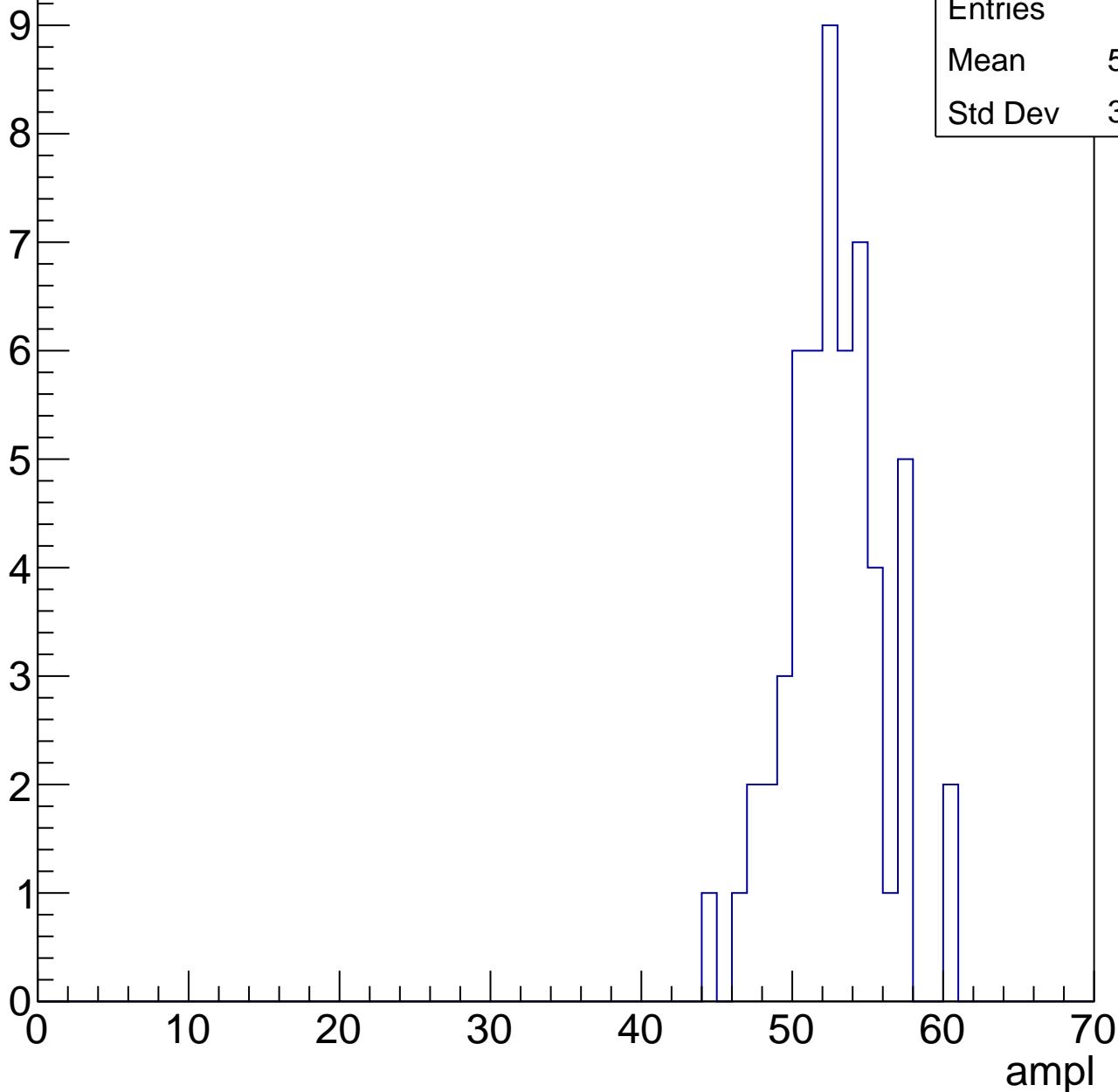


B1L102S, U22-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

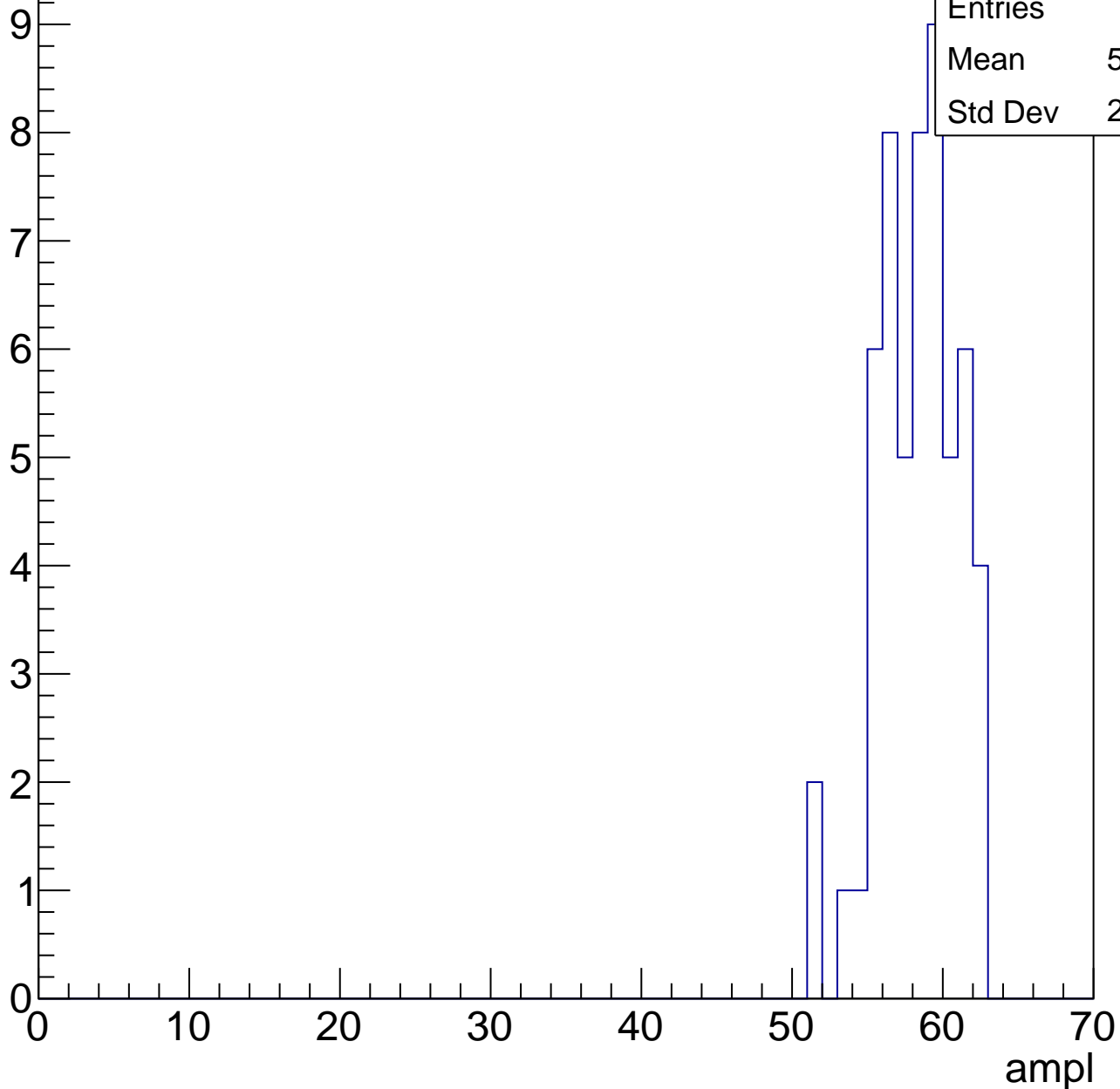
Entries	55
Mean	52.33
Std Dev	3.225



B1L102S, U22-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	28
Mean	59.18
Std Dev	11.53

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

10

B1L102S, U22-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch78, adc0

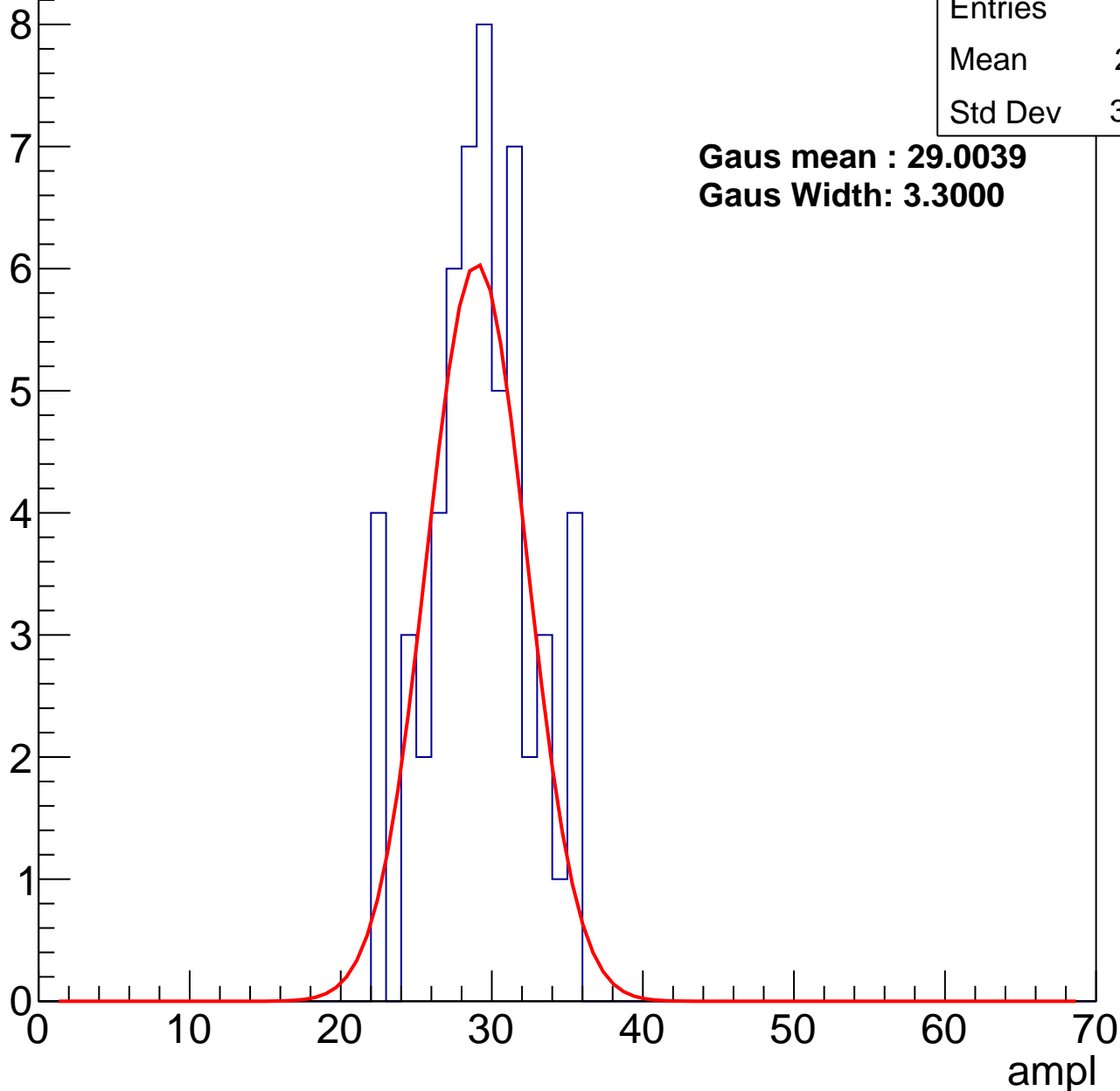
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.71
Std Dev	3.363

Gaus mean : 29.0039

Gaus Width: 3.3000



B1L102S, U22-ch78, adc1

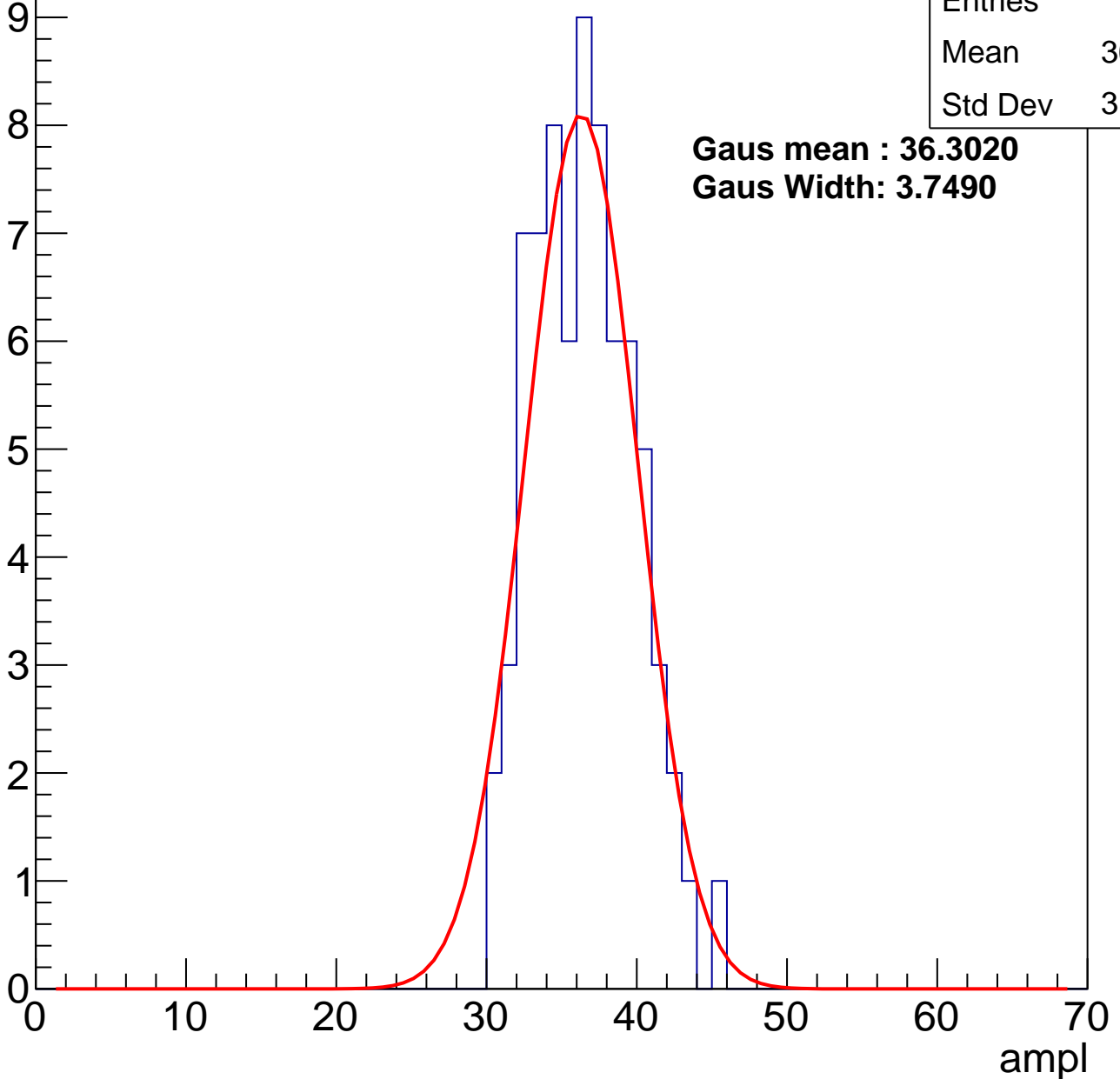
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.04
Std Dev	3.294

Gaus mean : 36.3020

Gaus Width: 3.7490



B1L102S, U22-ch78, adc2

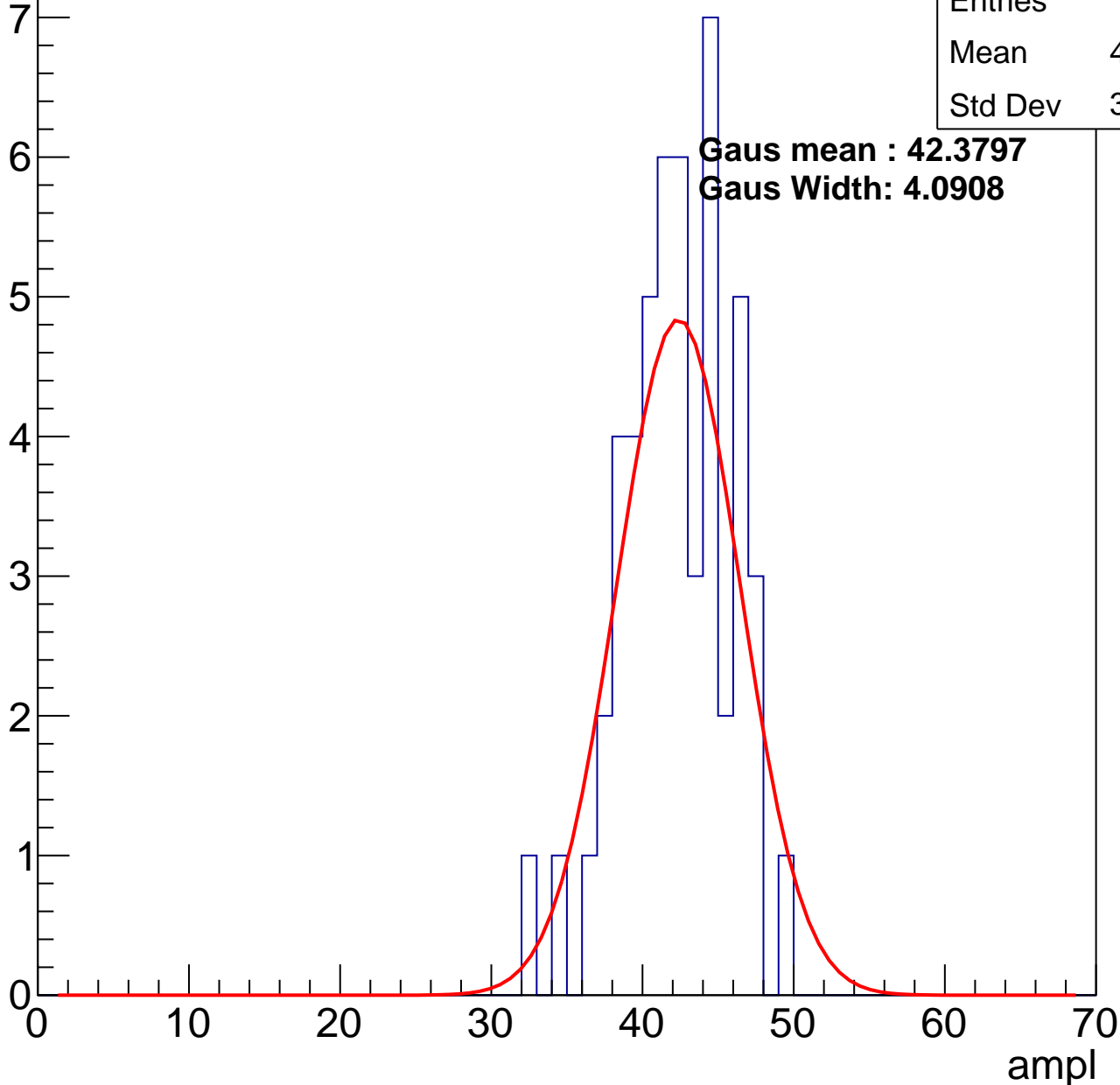
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	41.75
Std Dev	3.503

Gaus mean : 42.3797

Gaus Width: 4.0908



B1L102S, U22-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

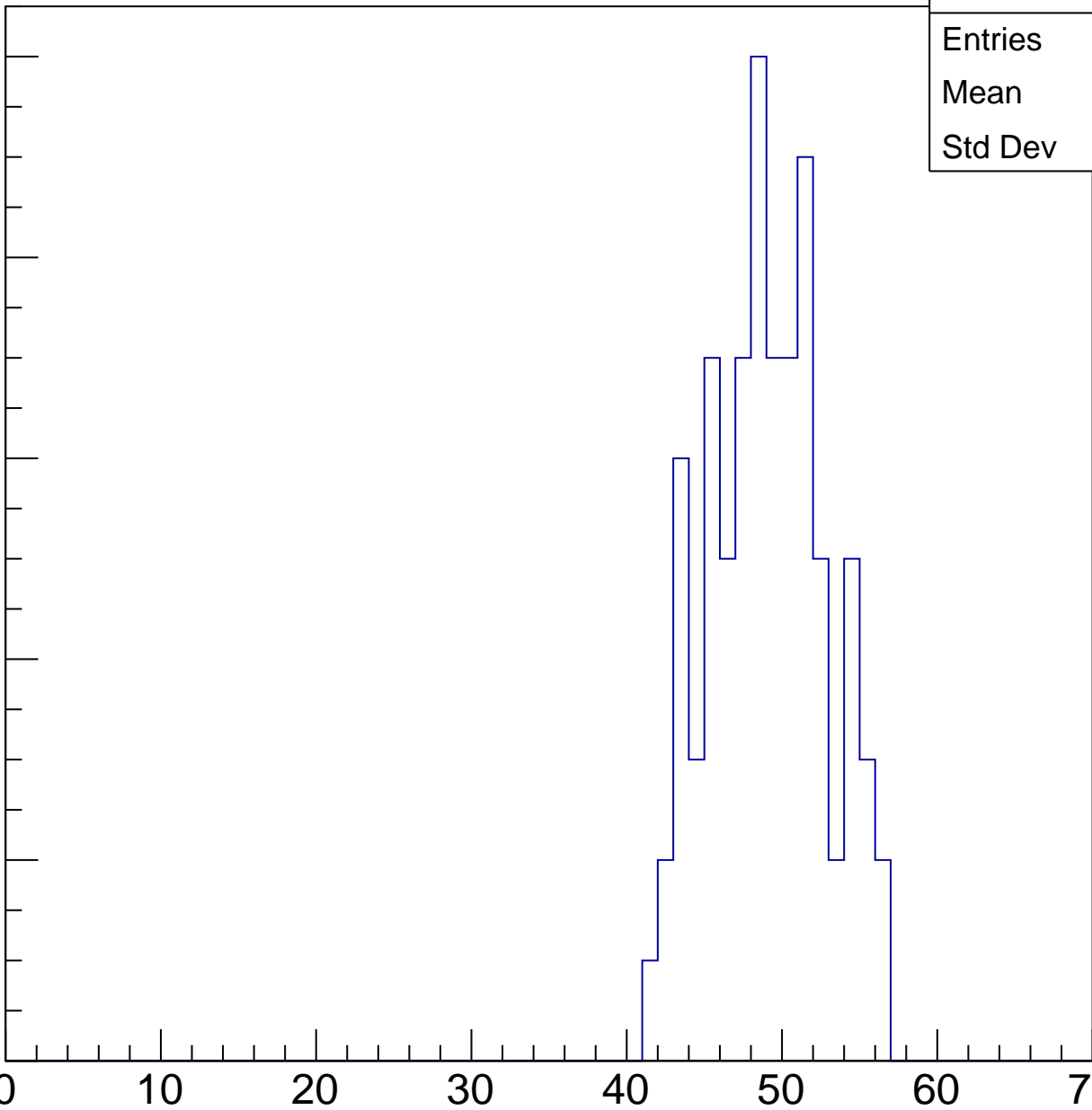
Entries	81
Mean	48.57
Std Dev	3.658

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

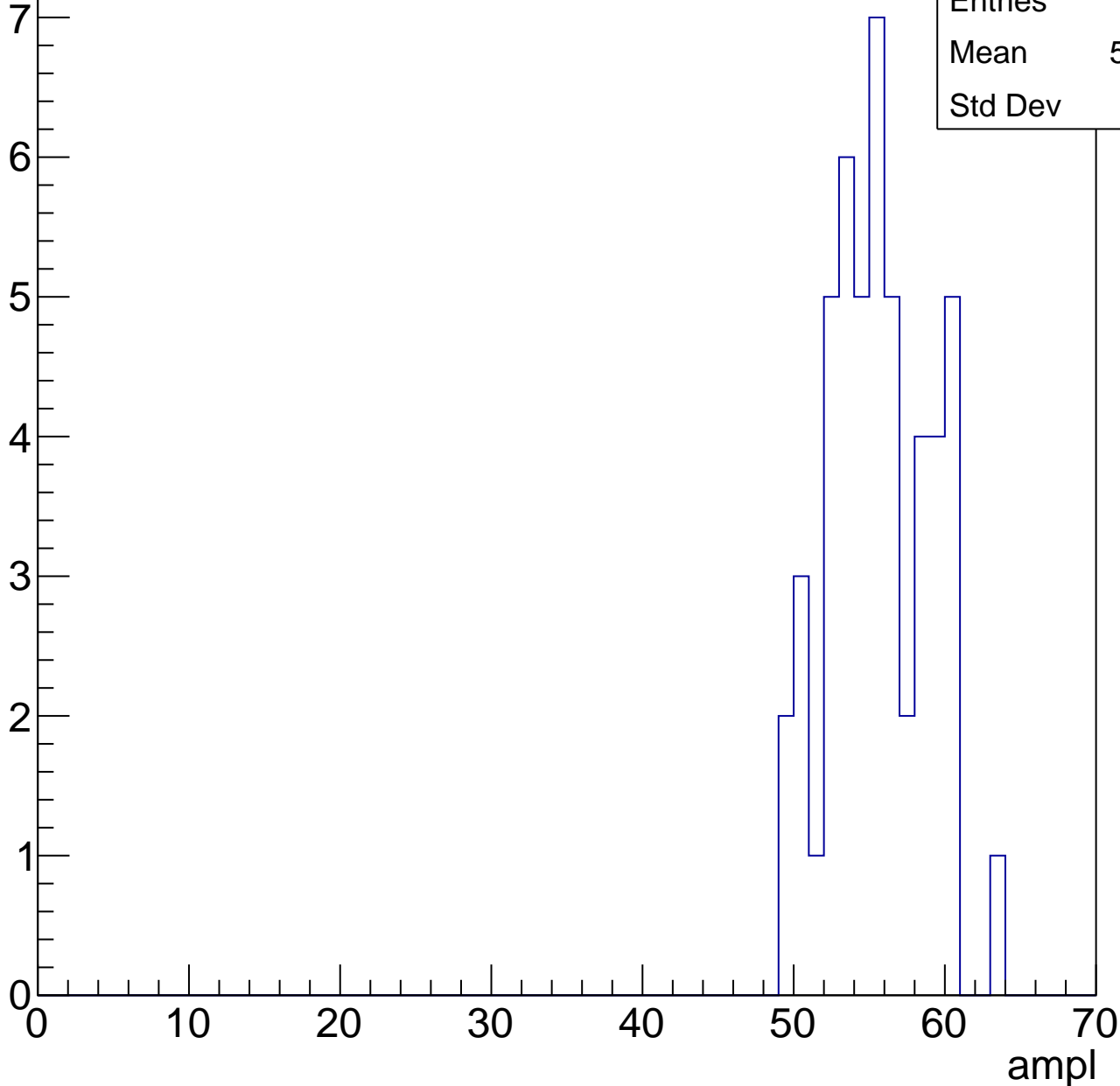


B1L102S, U22-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.14
Std Dev	3.28

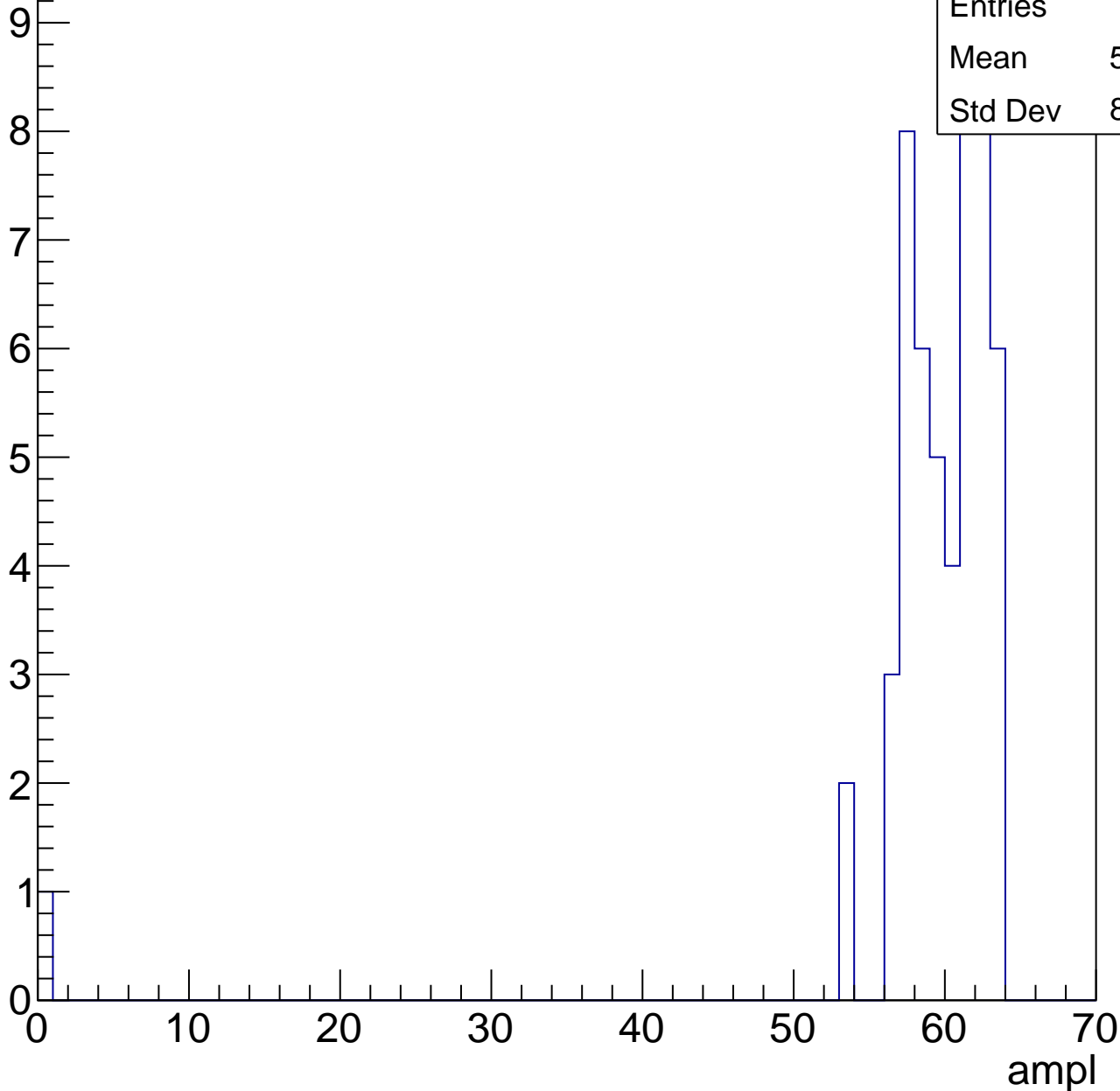


B1L102S, U22-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

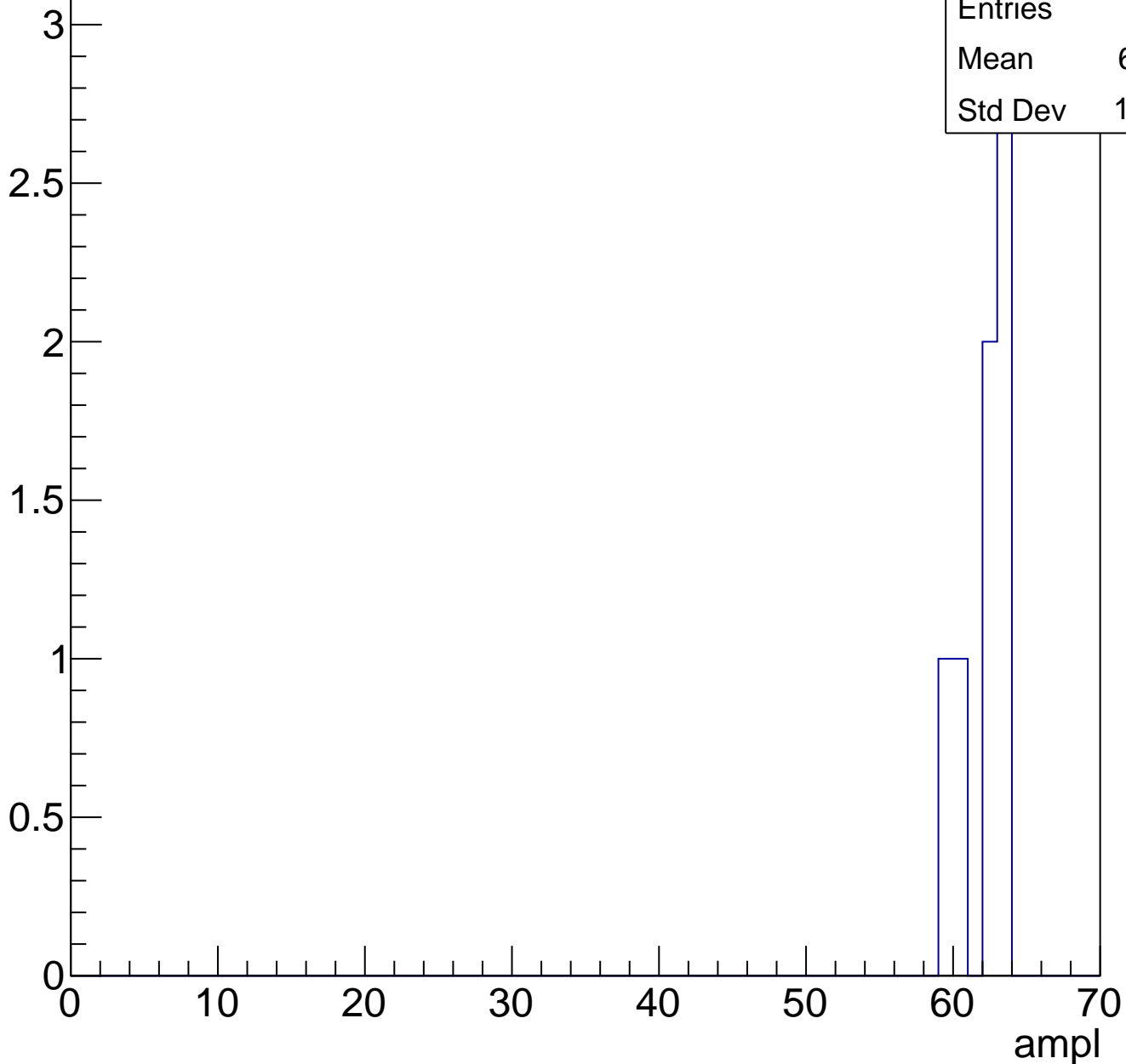
Entries	52
Mean	58.38
Std Dev	8.556



B1L102S, U22-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

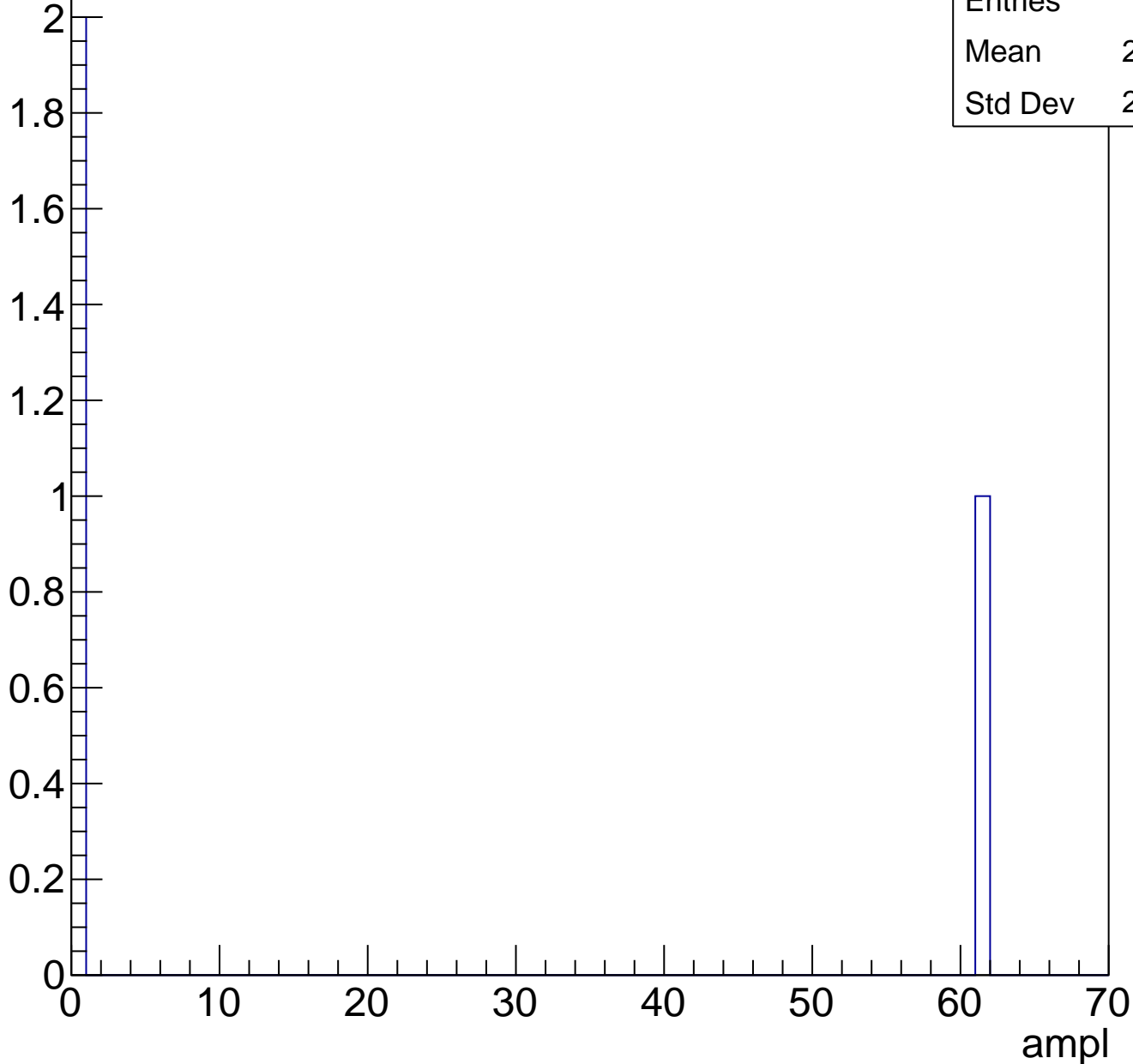
Entry



B1L102S, U22-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch79, adc0

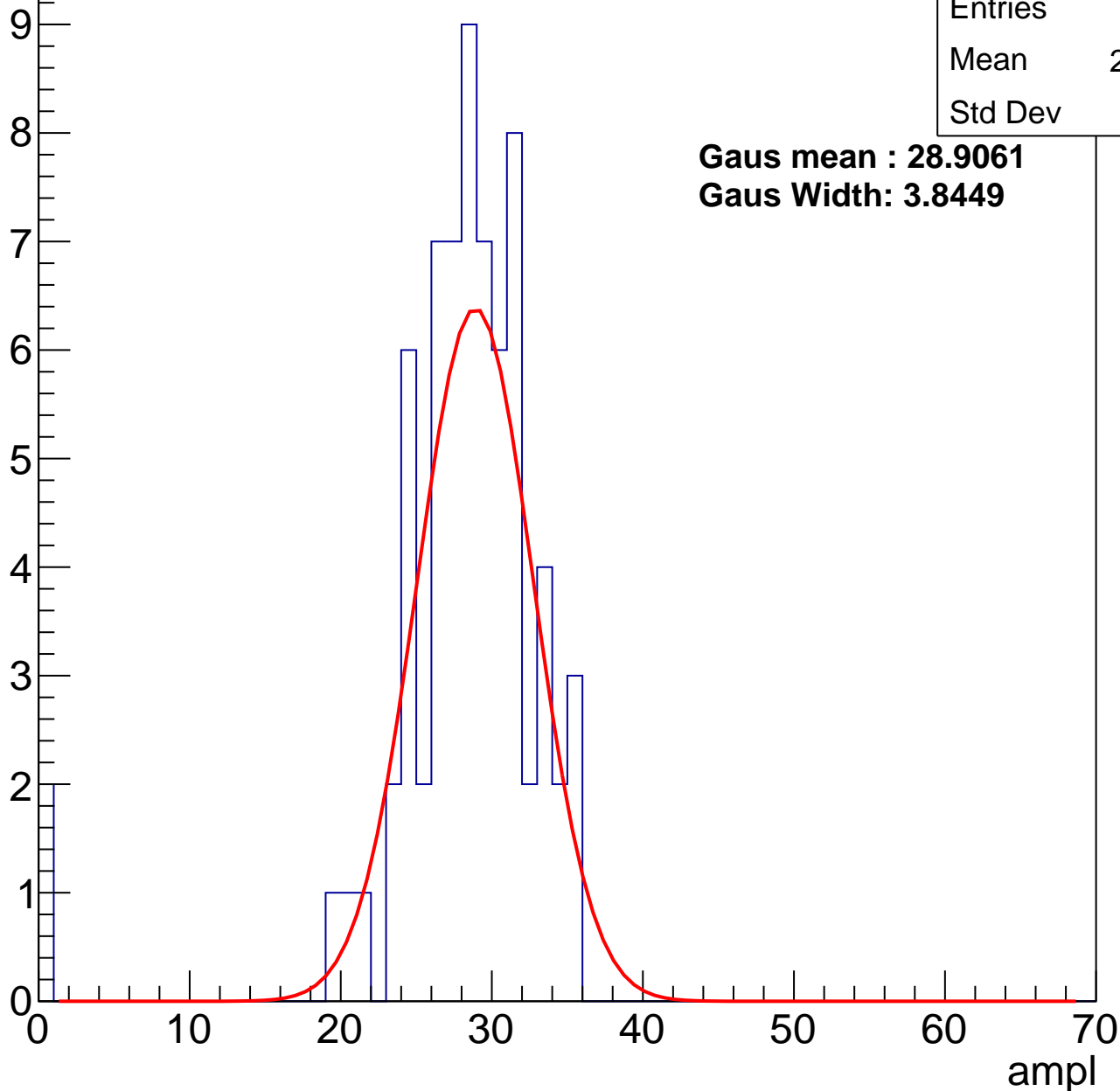
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.47
Std Dev	5.85

Gaus mean : 28.9061

Gaus Width: 3.8449



B1L102S, U22-ch79, adc1

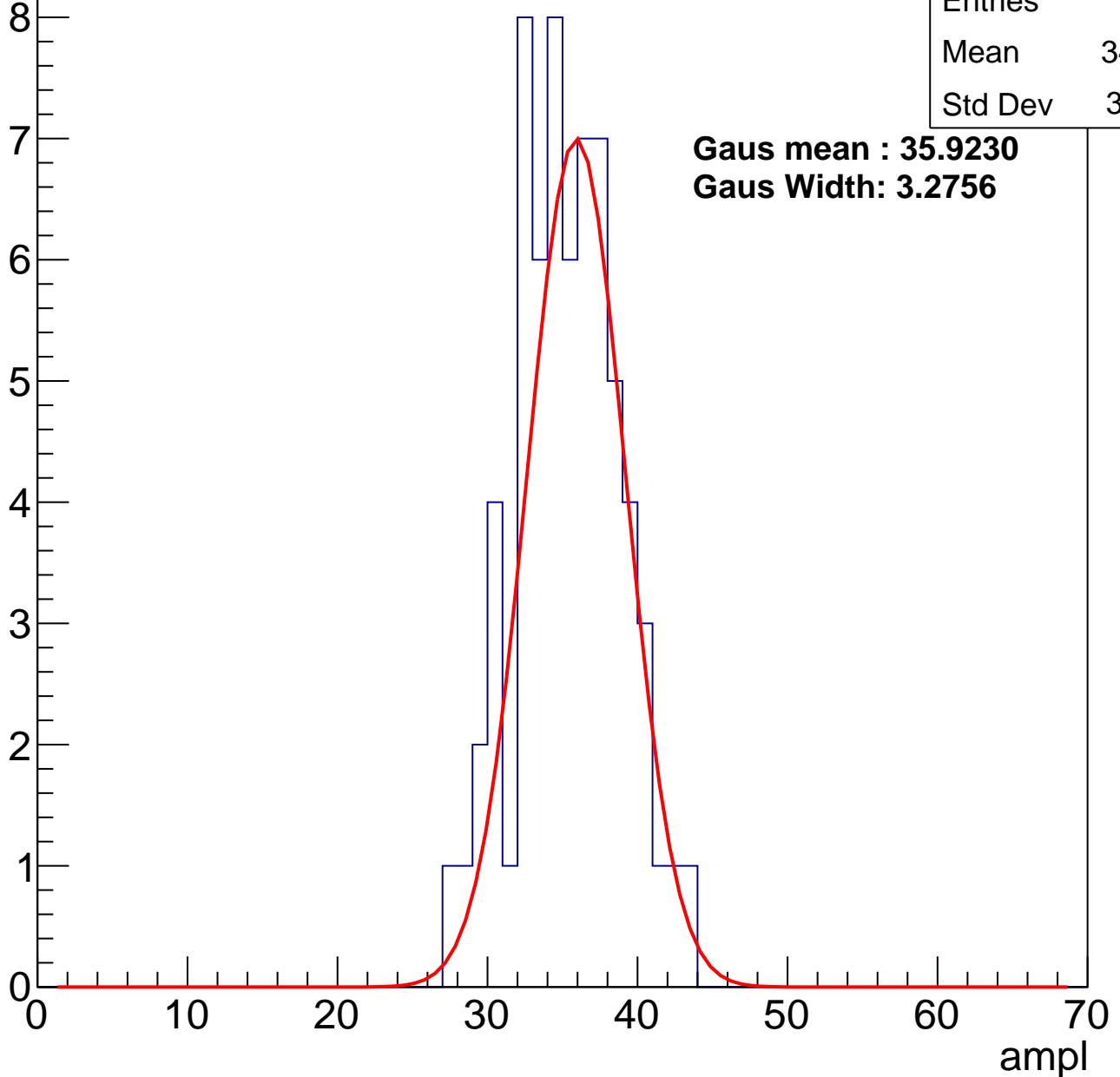
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.89
Std Dev	3.421

Gaus mean : 35.9230

Gaus Width: 3.2756



B1L102S, U22-ch79, adc2

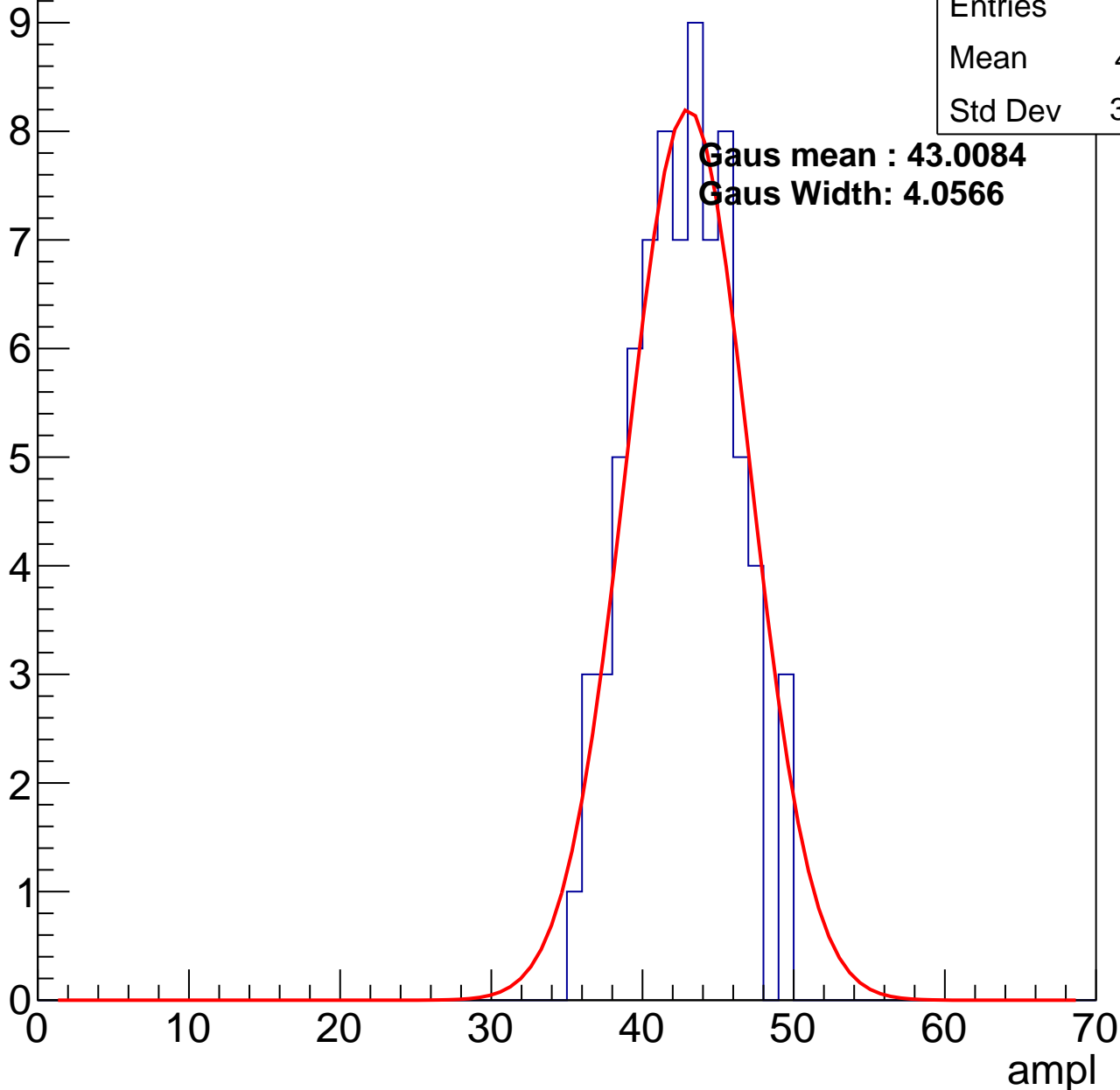
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.11
Std Dev	3.319

Gaus mean : 43.0084

Gaus Width: 4.0566

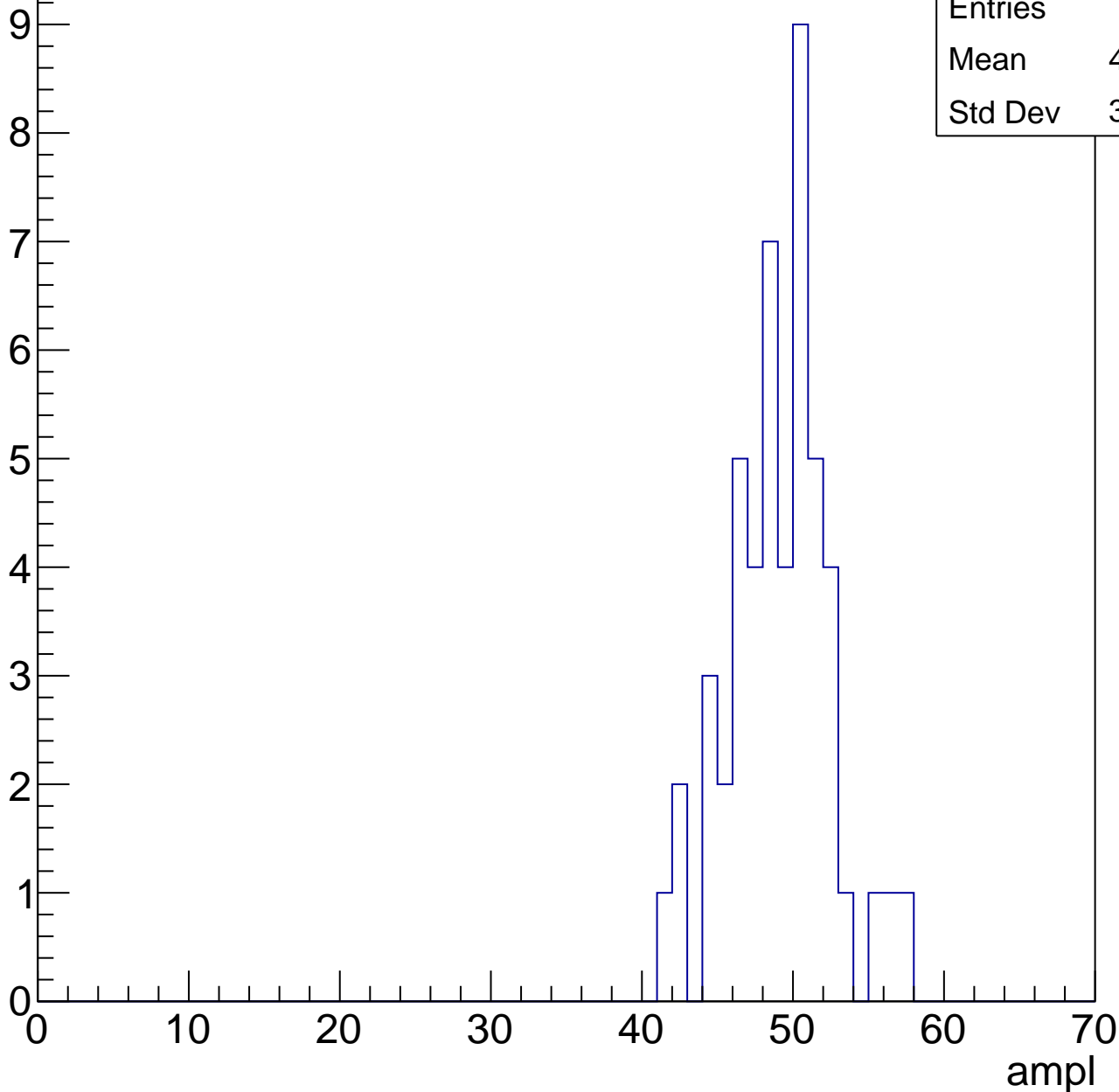


B1L102S, U22-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	48.62
Std Dev	3.346

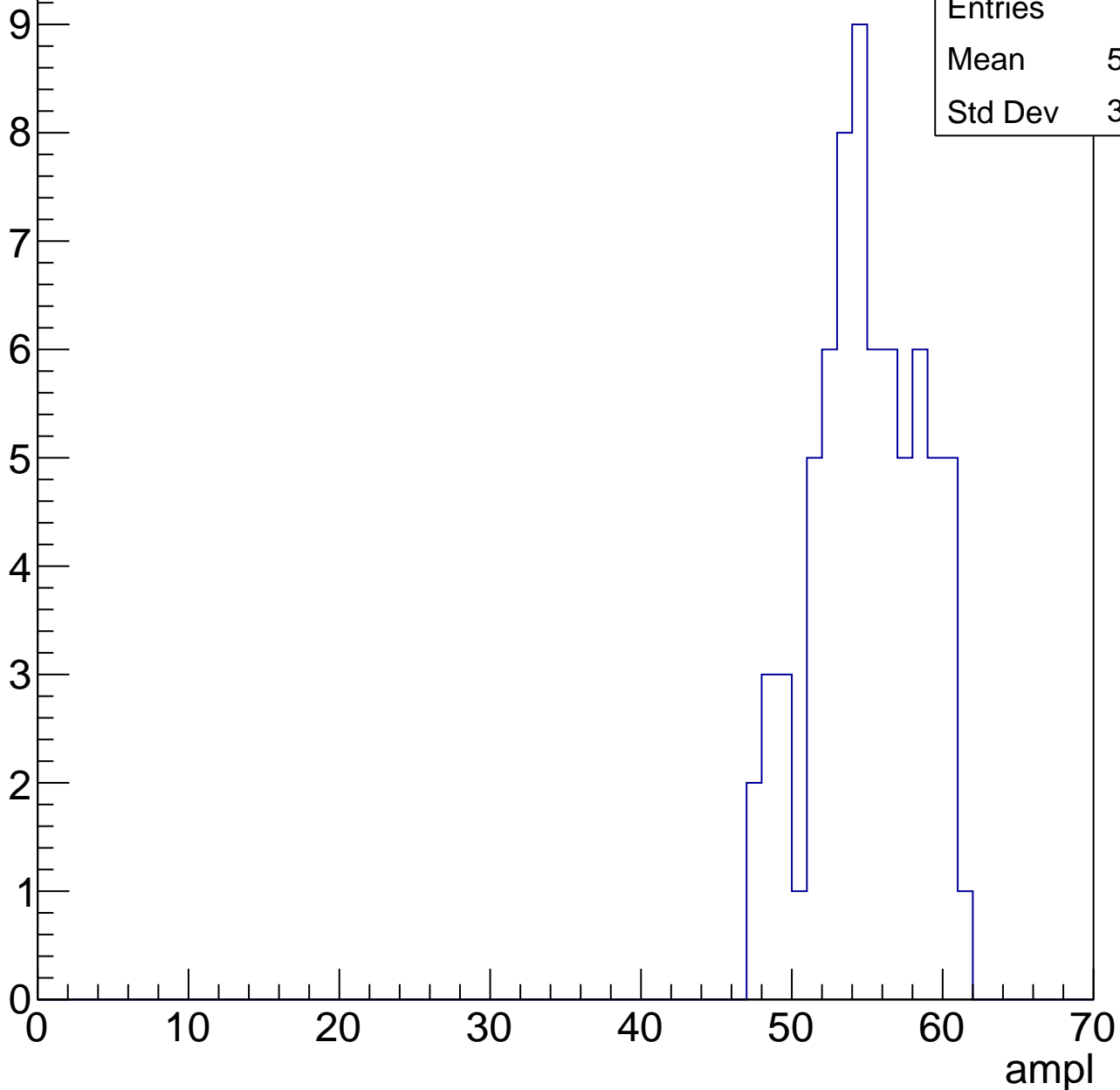


B1L102S, U22-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	54.46
Std Dev	3.528

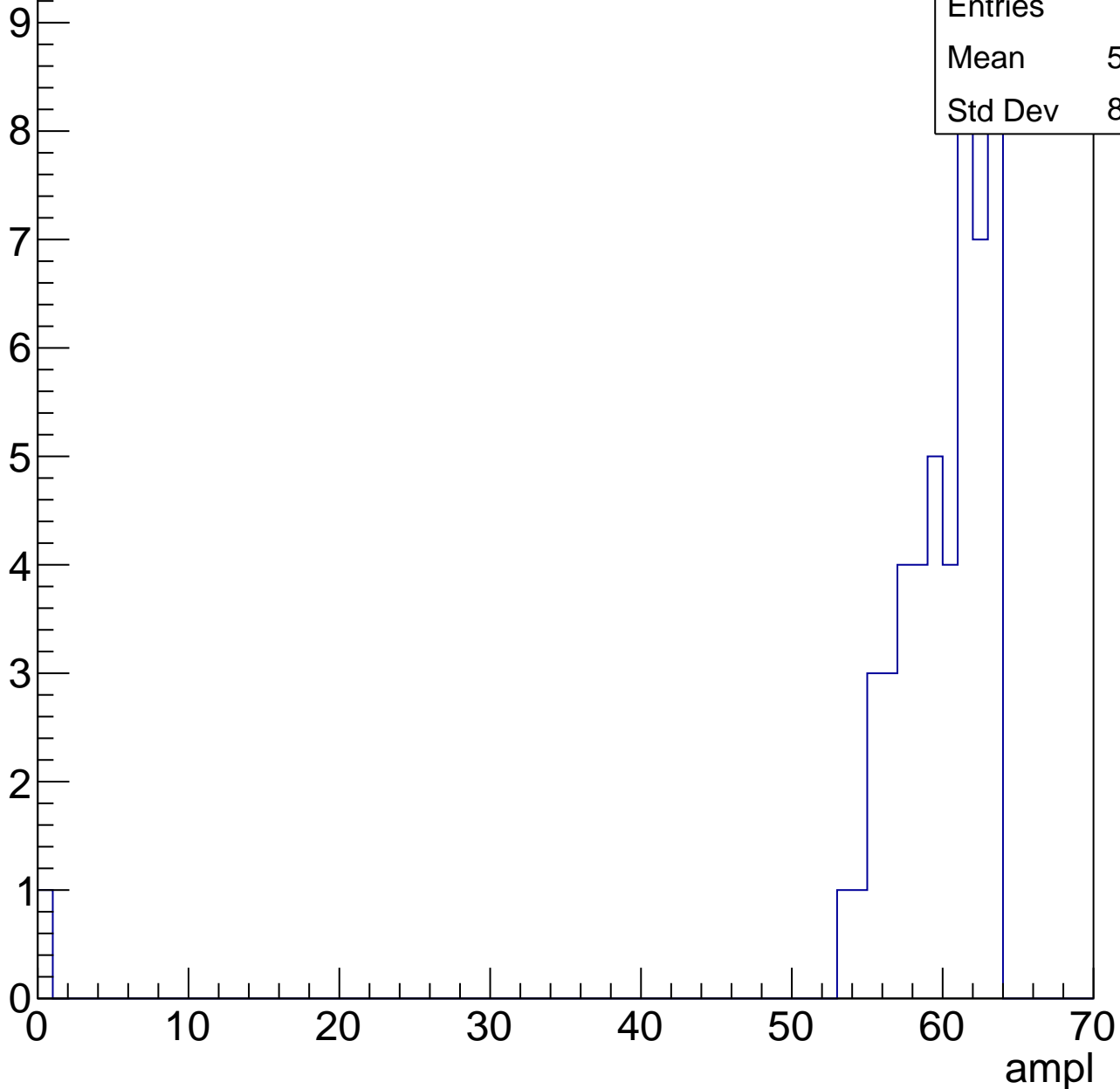


B1L102S, U22-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

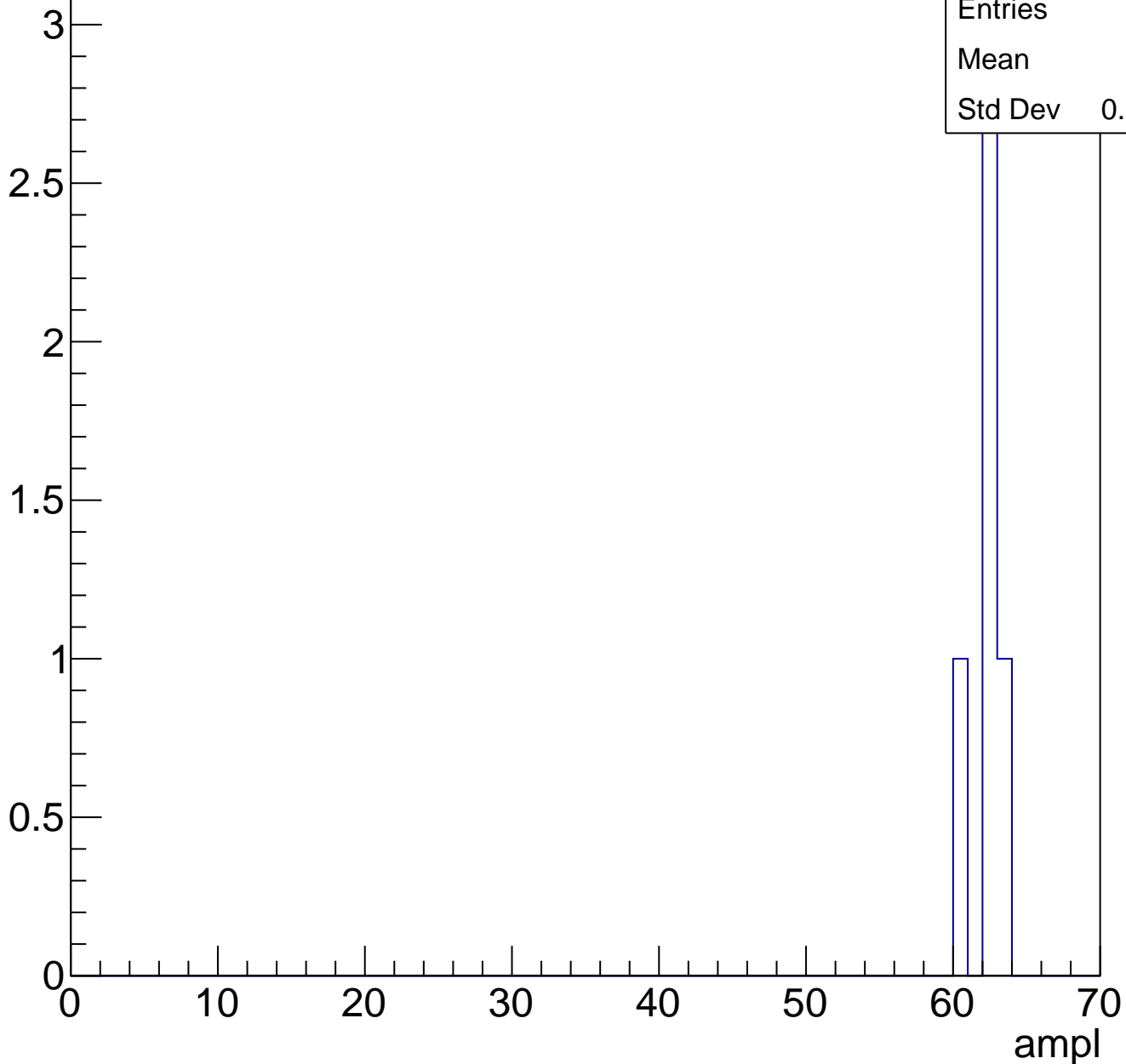
Entries	51
Mean	58.53
Std Dev	8.714



B1L102S, U22-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch80, adc0

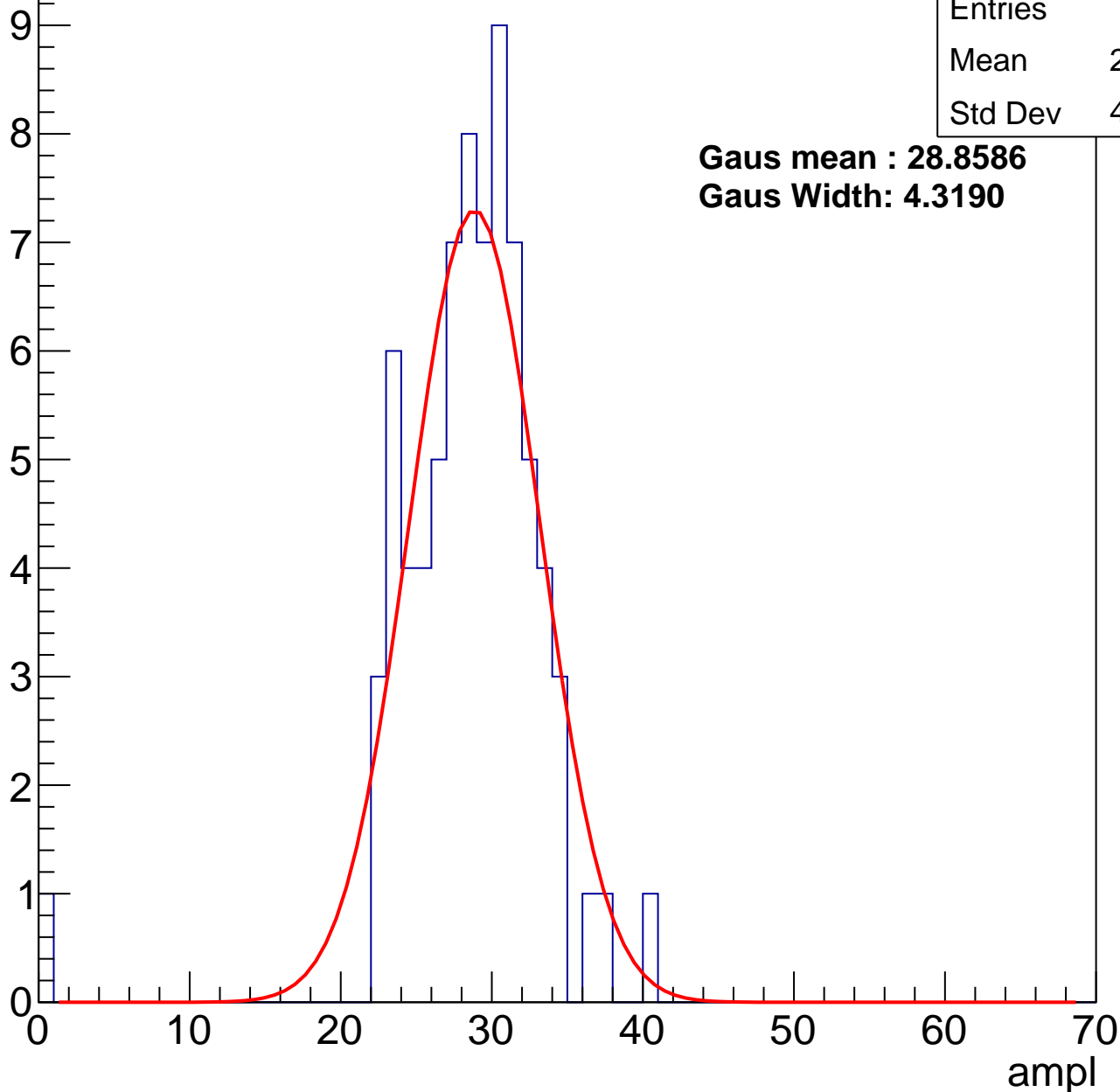
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.16
Std Dev	4.934

Gaus mean : 28.8586

Gaus Width: 4.3190



B1L102S, U22-ch80, adc1

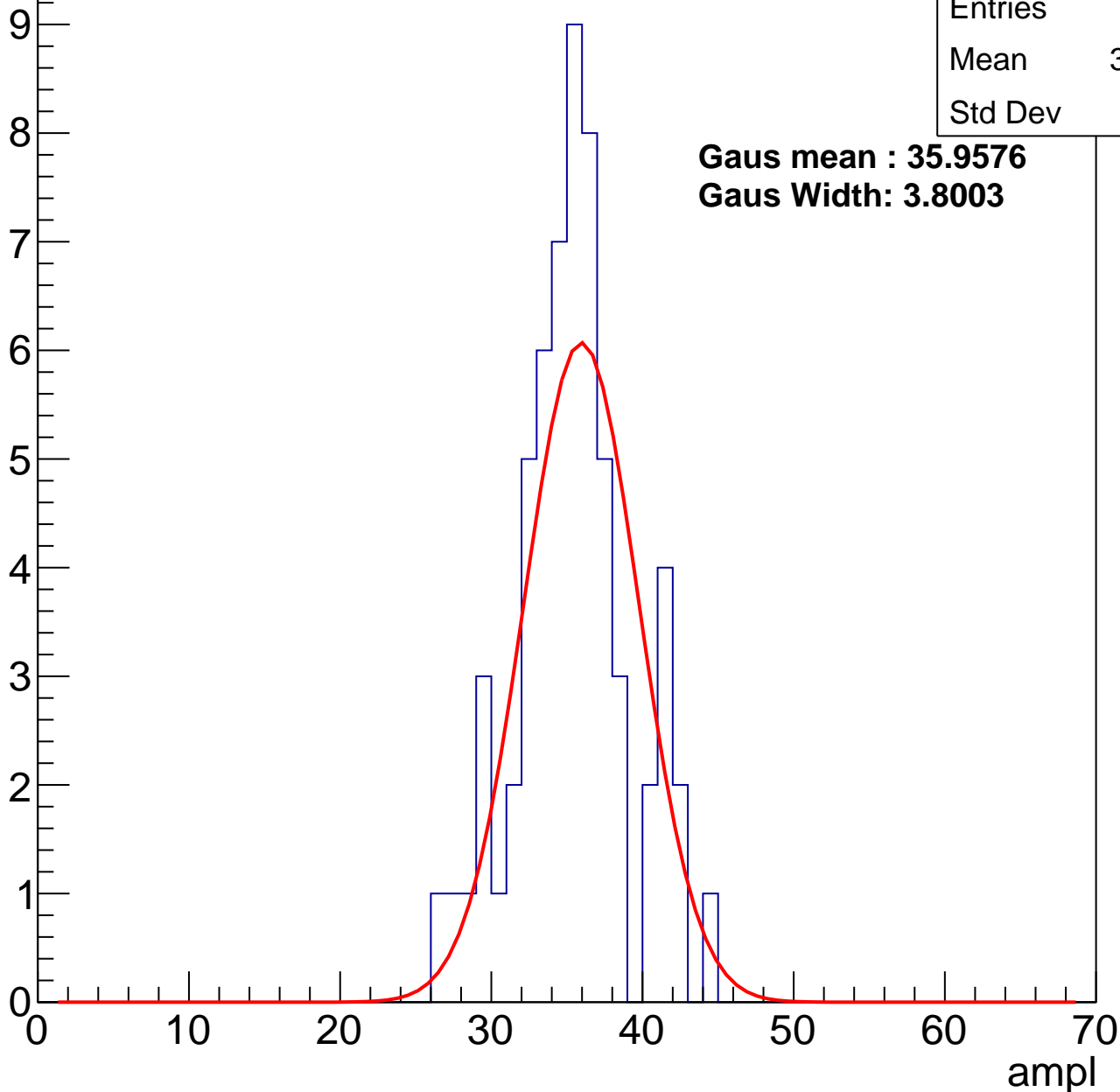
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.92
Std Dev	3.76

Gaus mean : 35.9576

Gaus Width: 3.8003



B1L102S, U22-ch80, adc2

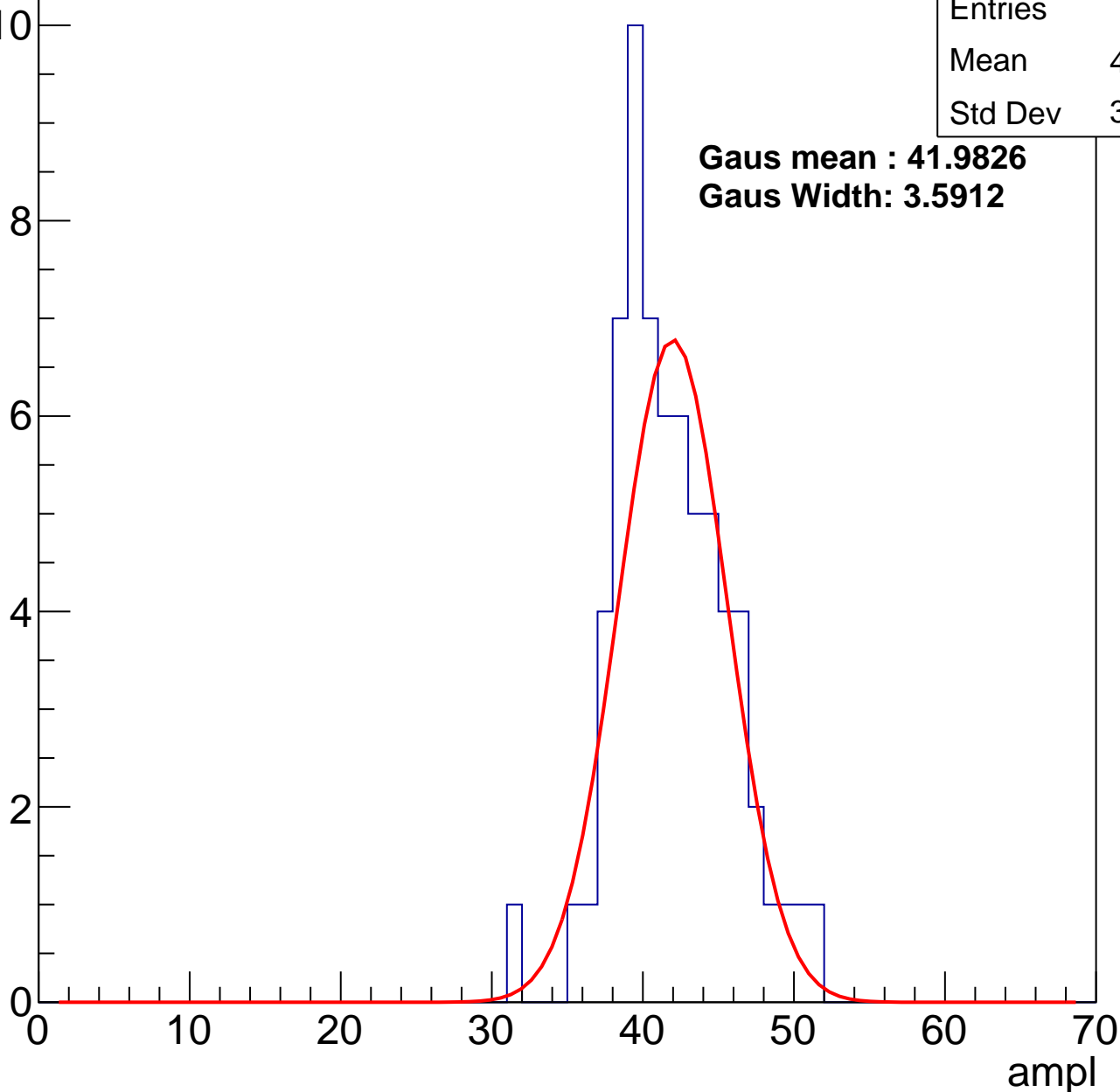
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.42
Std Dev	3.714

Gaus mean : 41.9826

Gaus Width: 3.5912

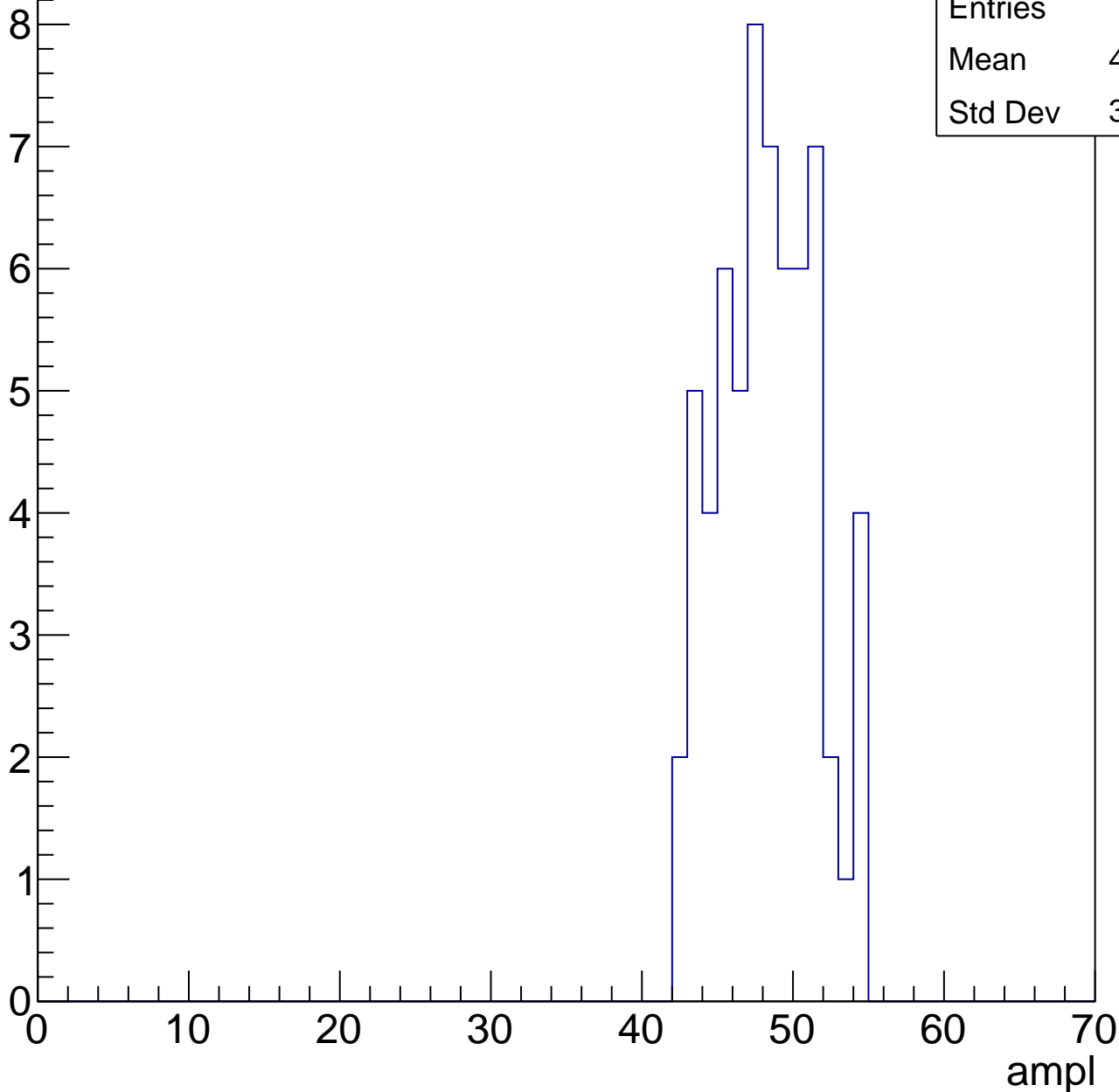


B1L102S, U22-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

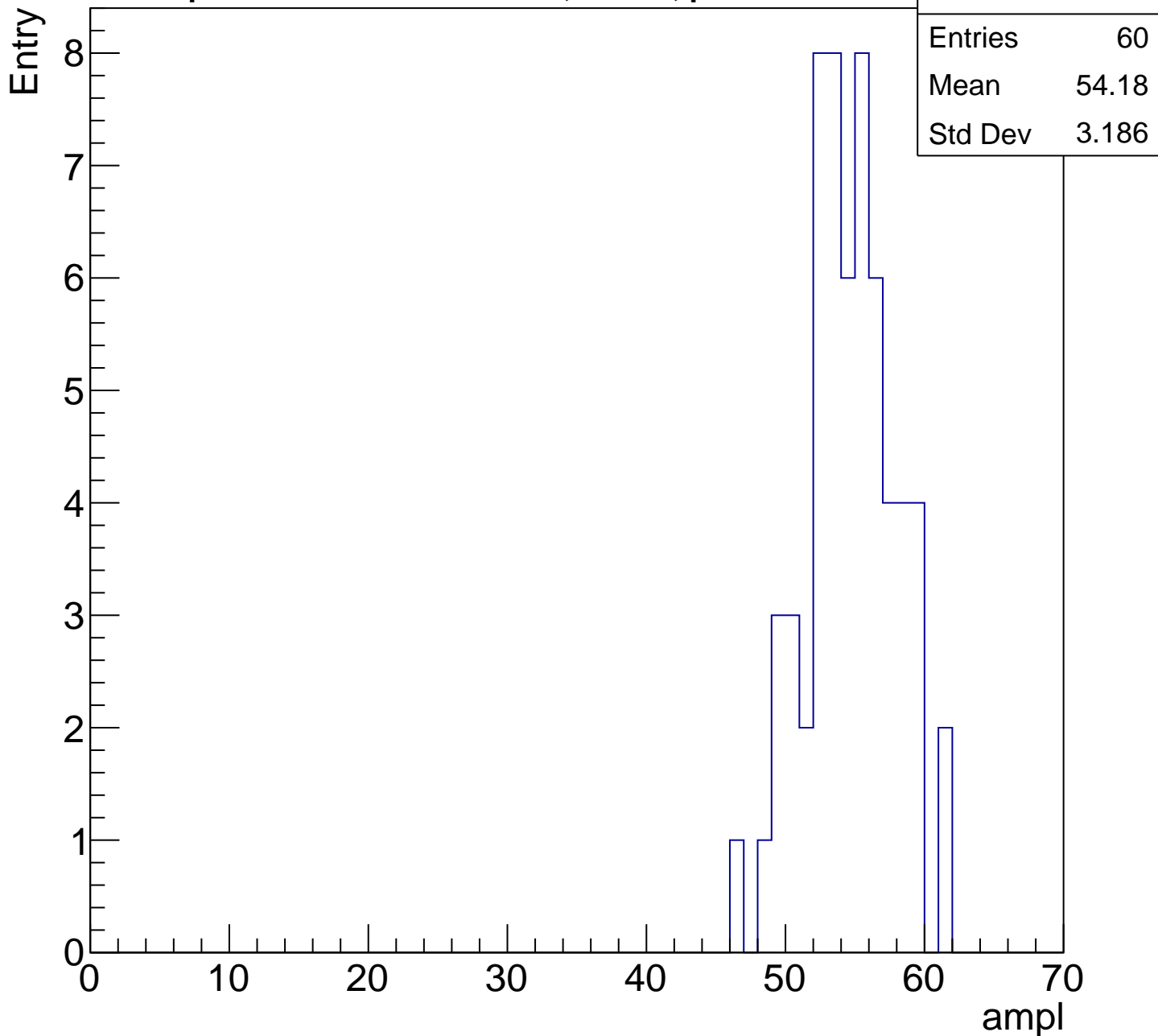
Entry

Entries	63
Mean	47.79
Std Dev	3.173



B1L102S, U22-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

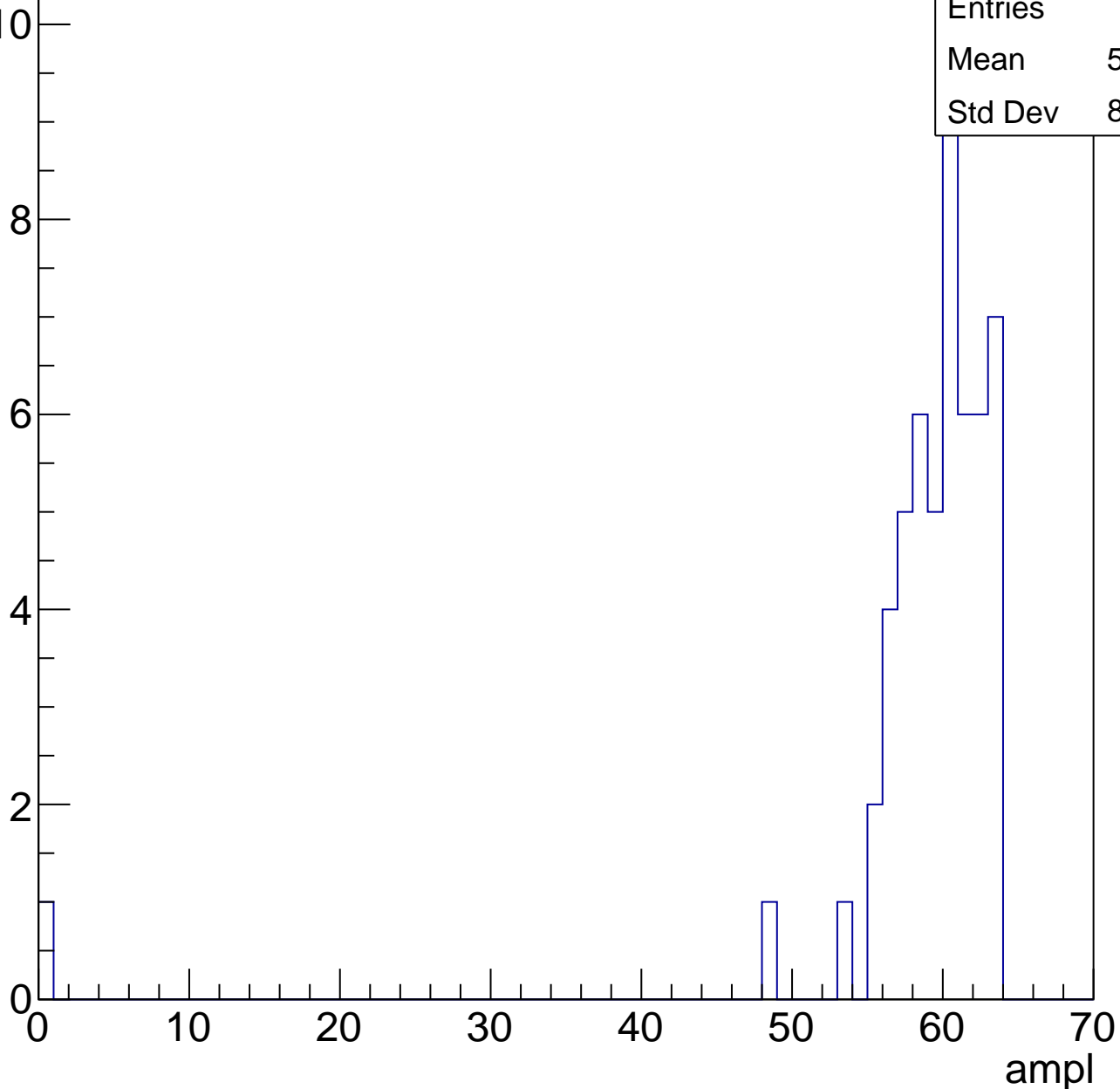


B1L102S, U22-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

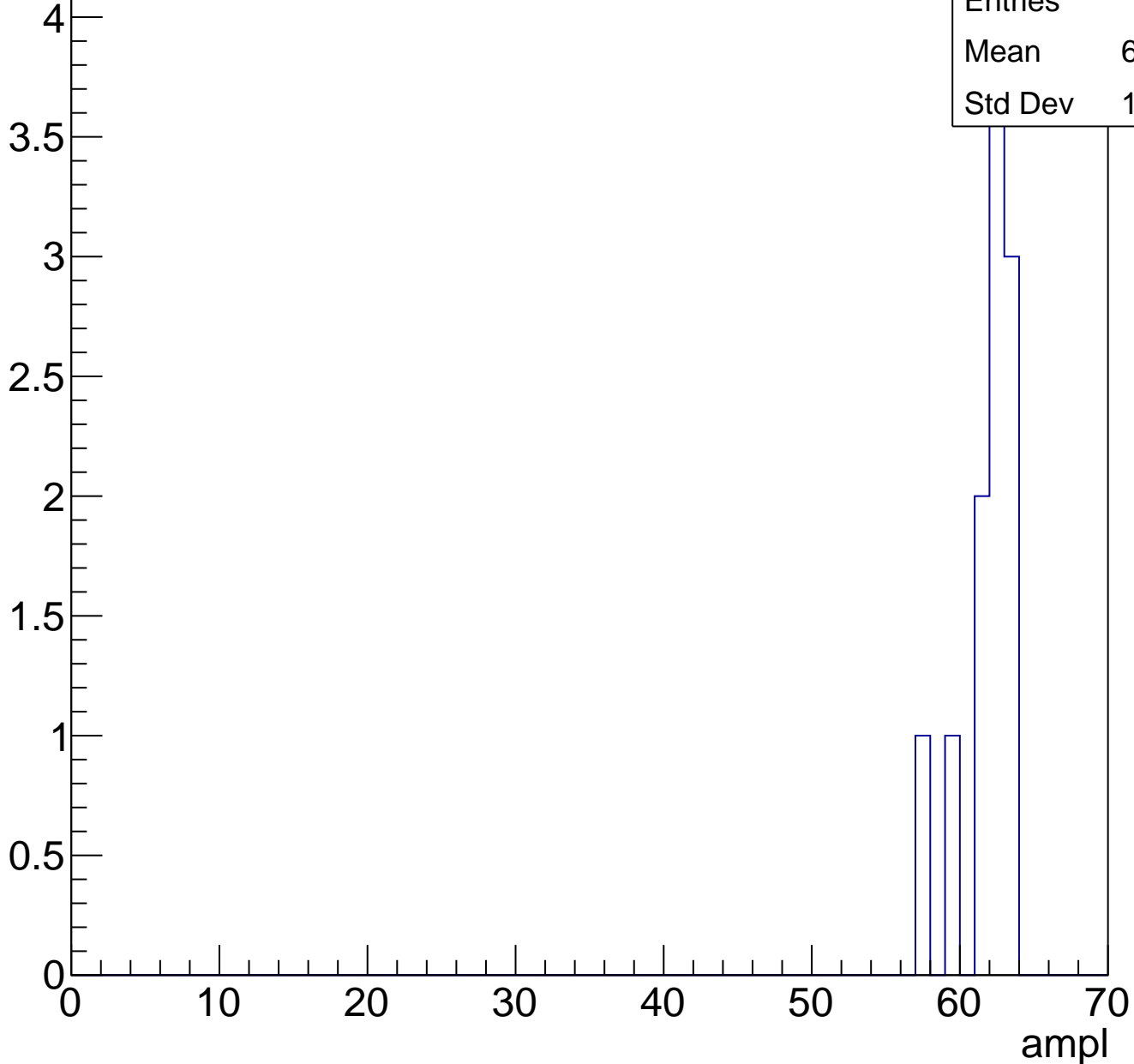
Entries	54
Mean	58.19
Std Dev	8.494



B1L102S, U22-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch81, adc0

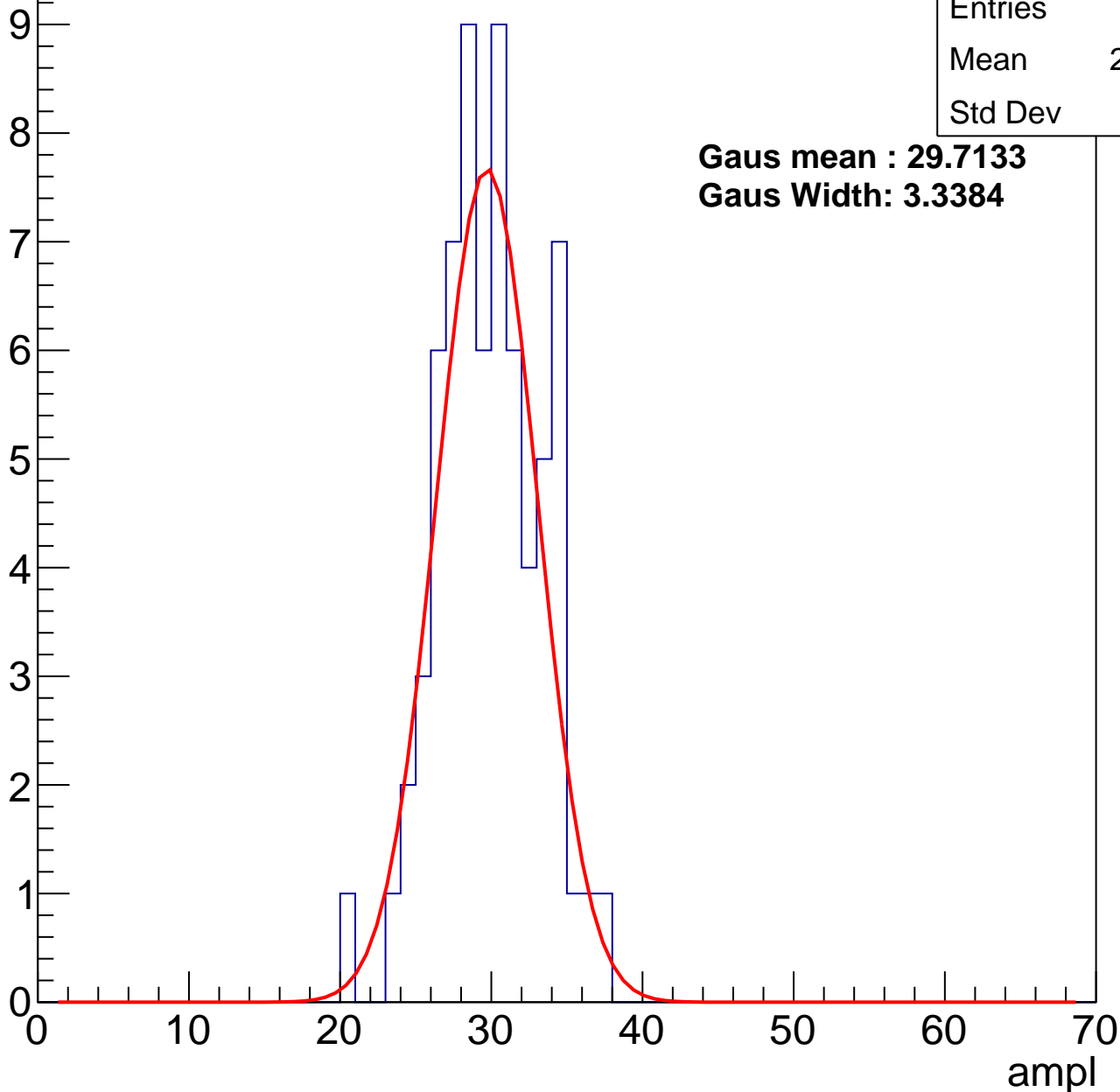
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.45
Std Dev	3.33

Gaus mean : 29.7133

Gaus Width: 3.3384



B1L102S, U22-ch81, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	36.43
Std Dev	3.123

Gaus mean : 37.2644

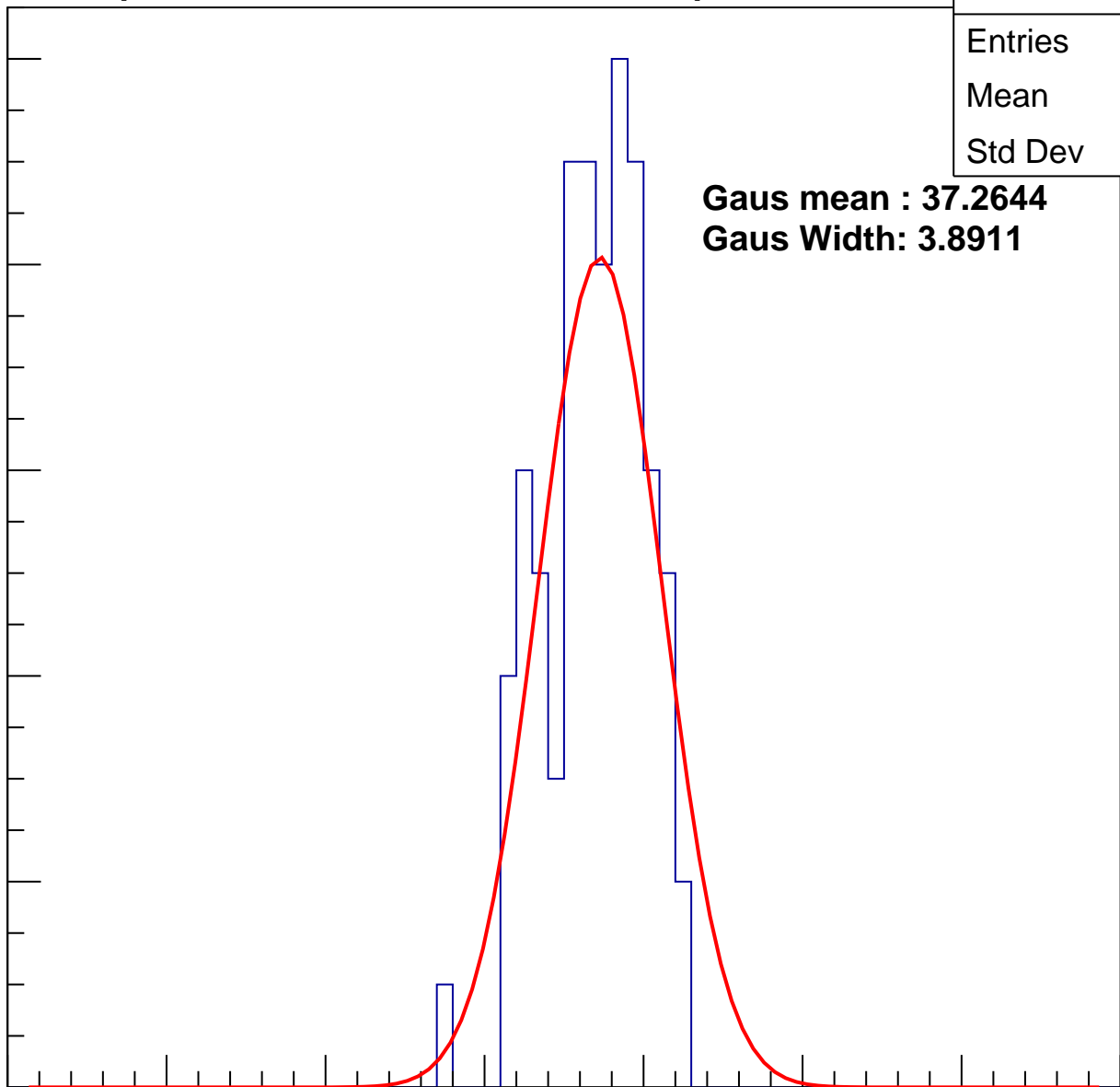
Gaus Width: 3.8911

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch81, adc2

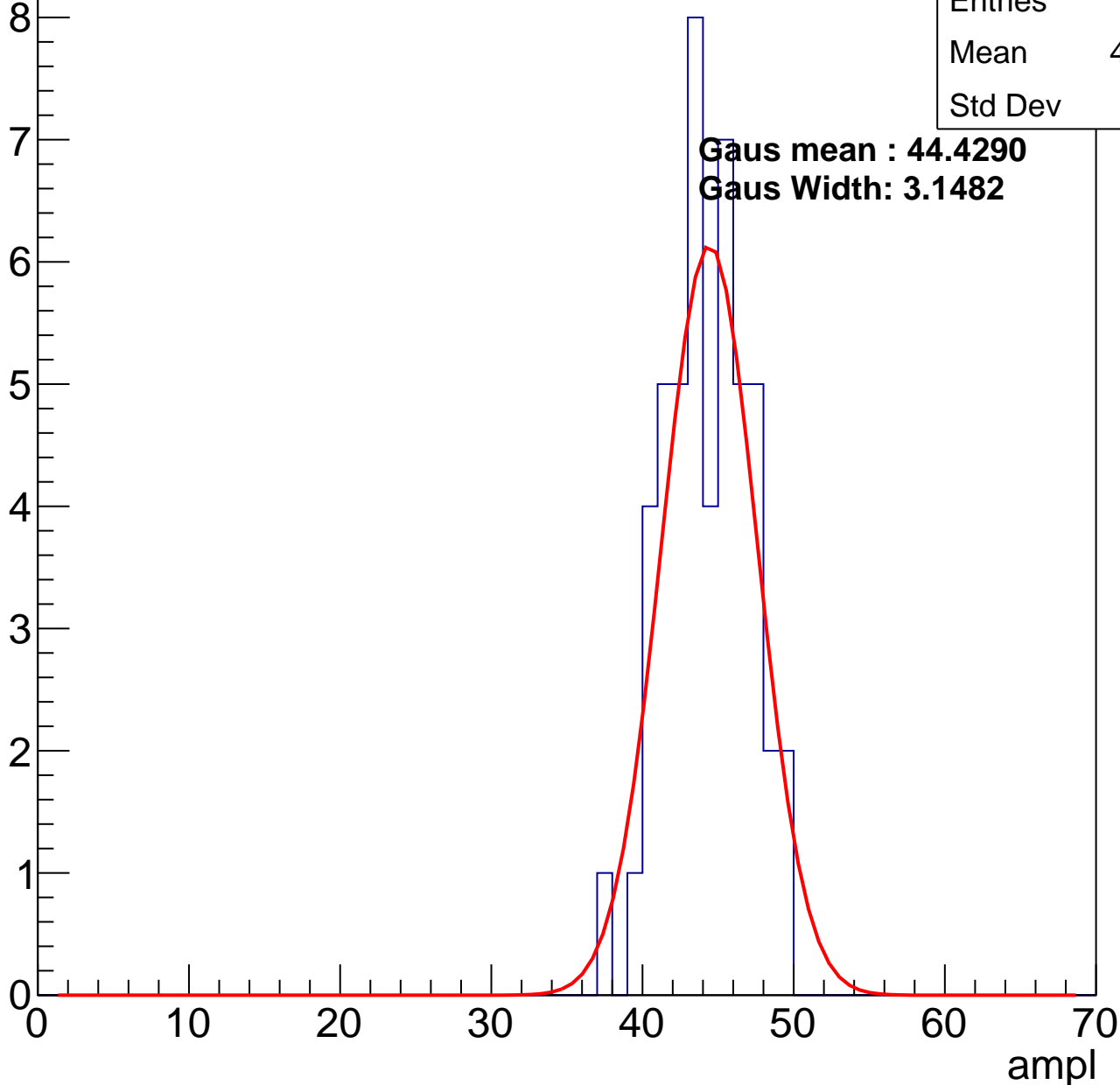
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	43.78
Std Dev	2.72

Gaus mean : 44.4290

Gaus Width: 3.1482

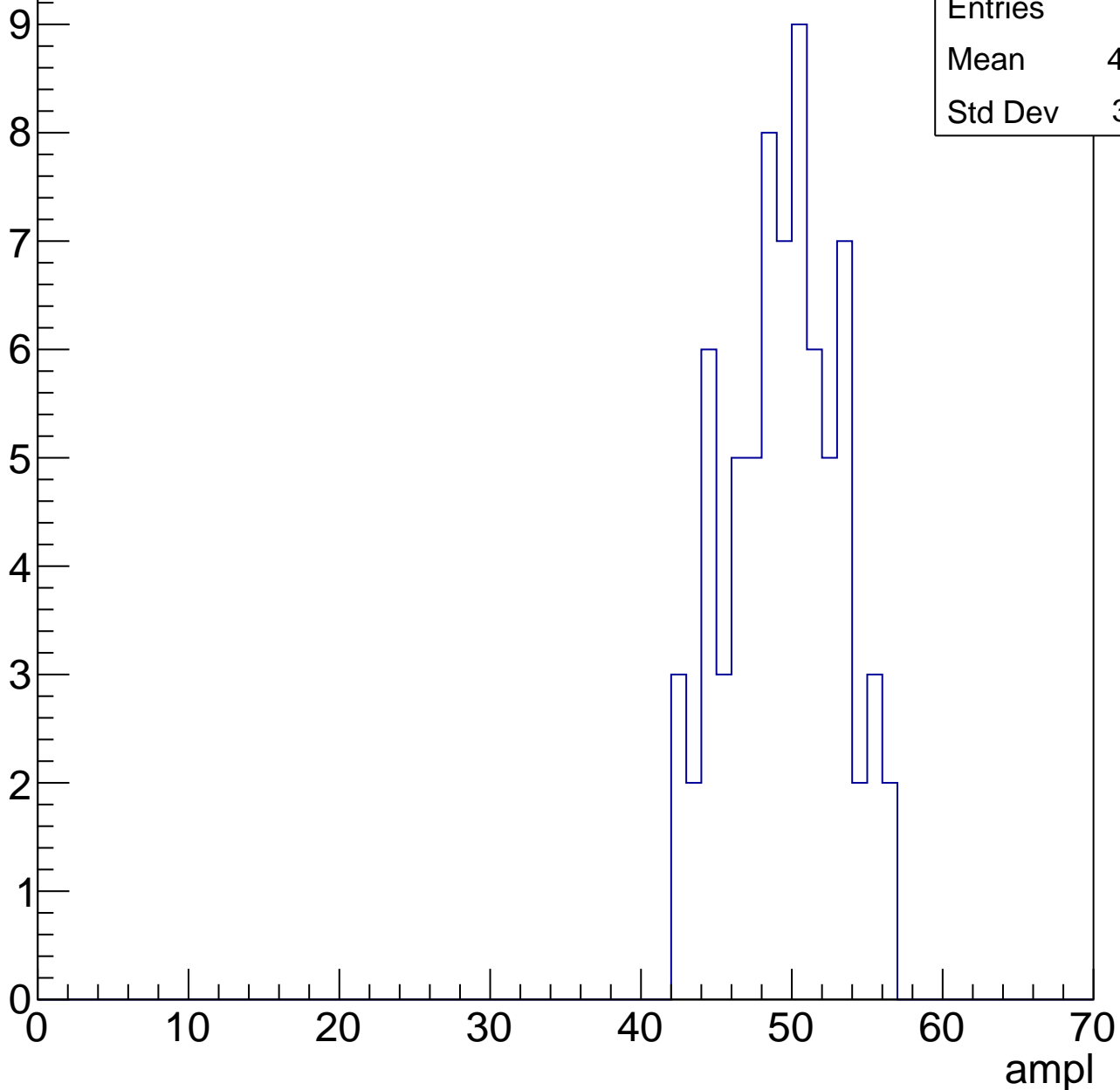


B1L102S, U22-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

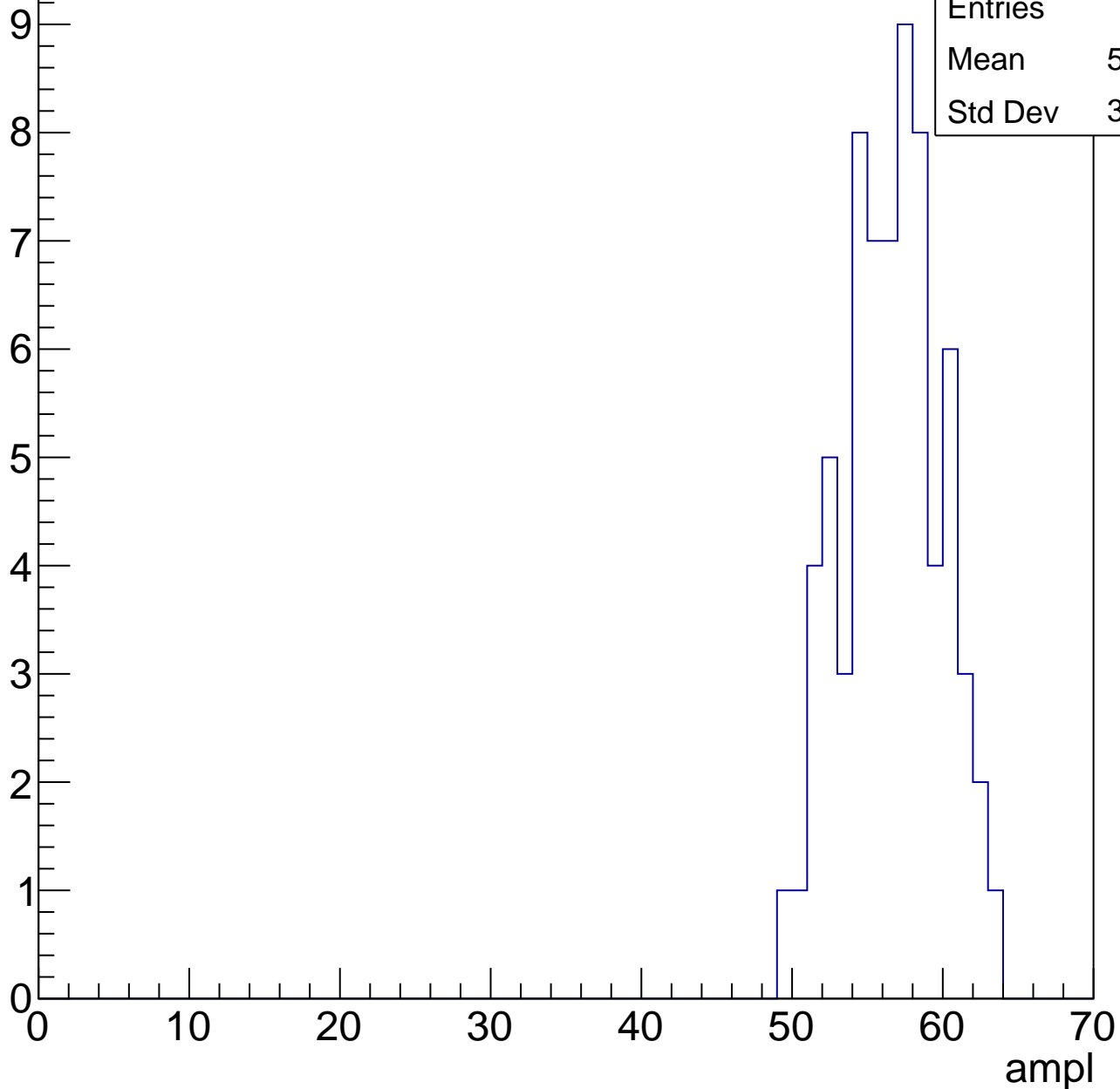
Entries	73
Mean	48.97
Std Dev	3.581



B1L102S, U22-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

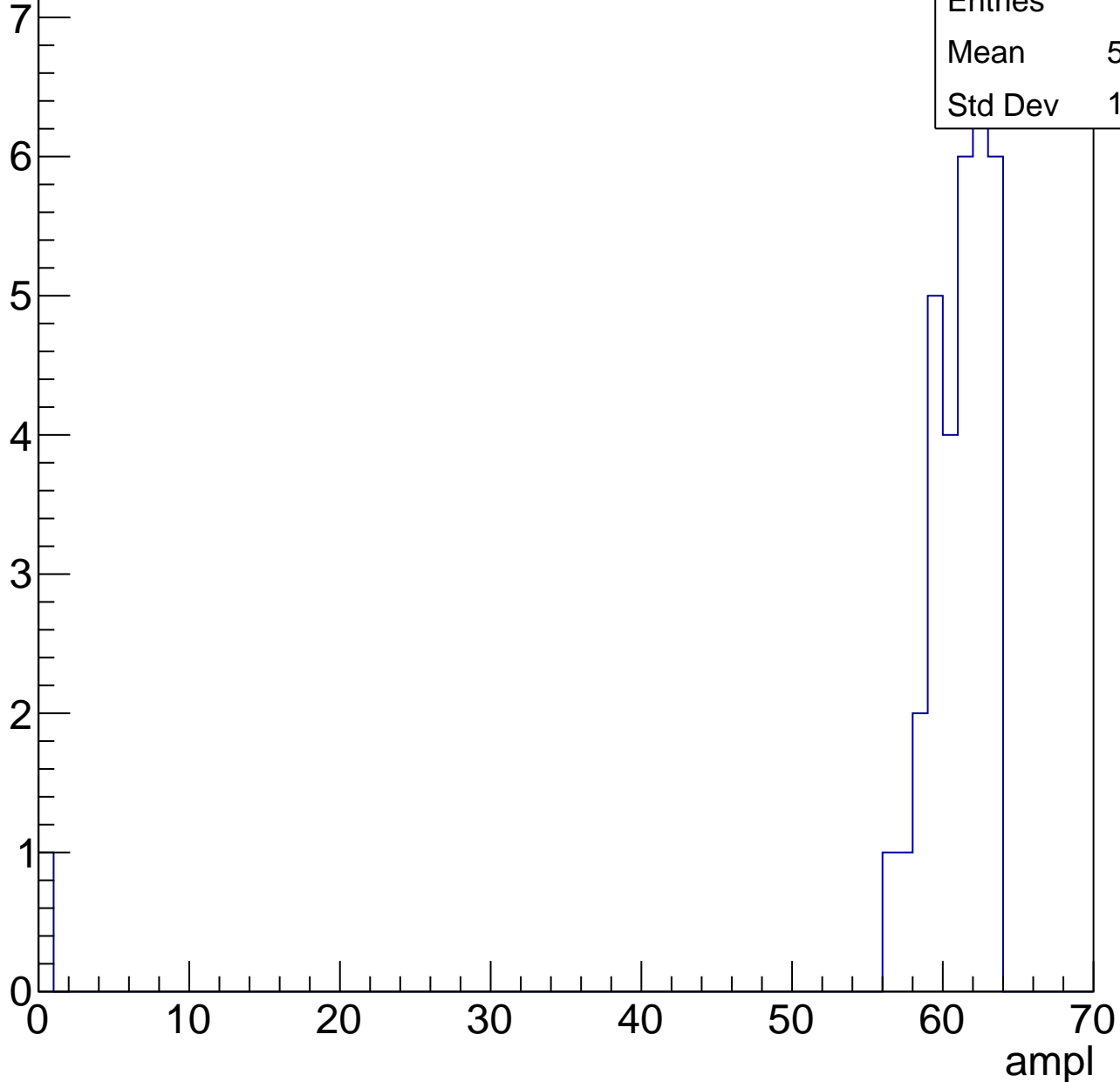


Entries	69
Mean	56.14
Std Dev	3.182

B1L102S, U22-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch82, adc0

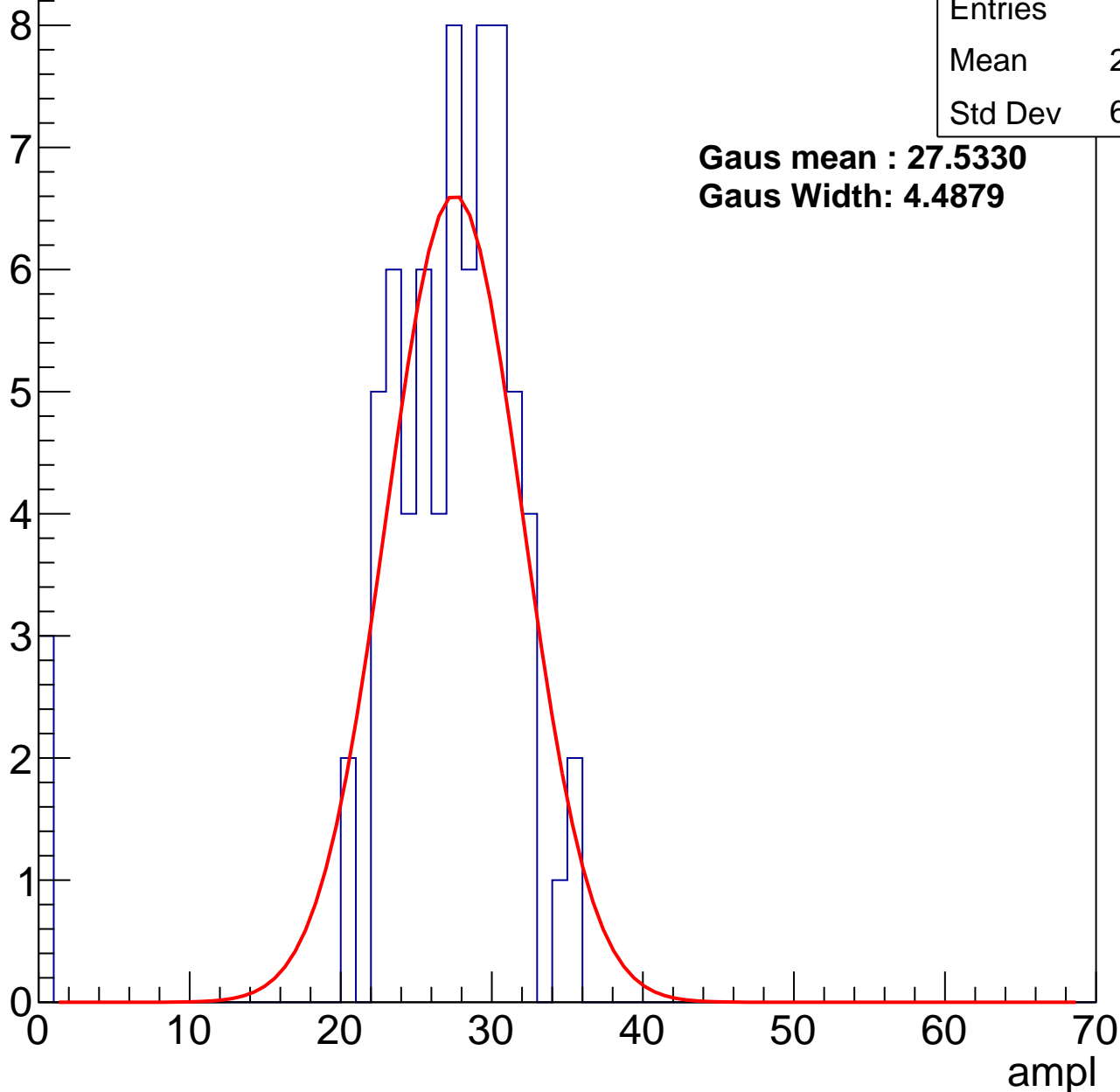
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	26.12
Std Dev	6.435

Gaus mean : 27.5330

Gaus Width: 4.4879



B1L102S, U22-ch82, adc1

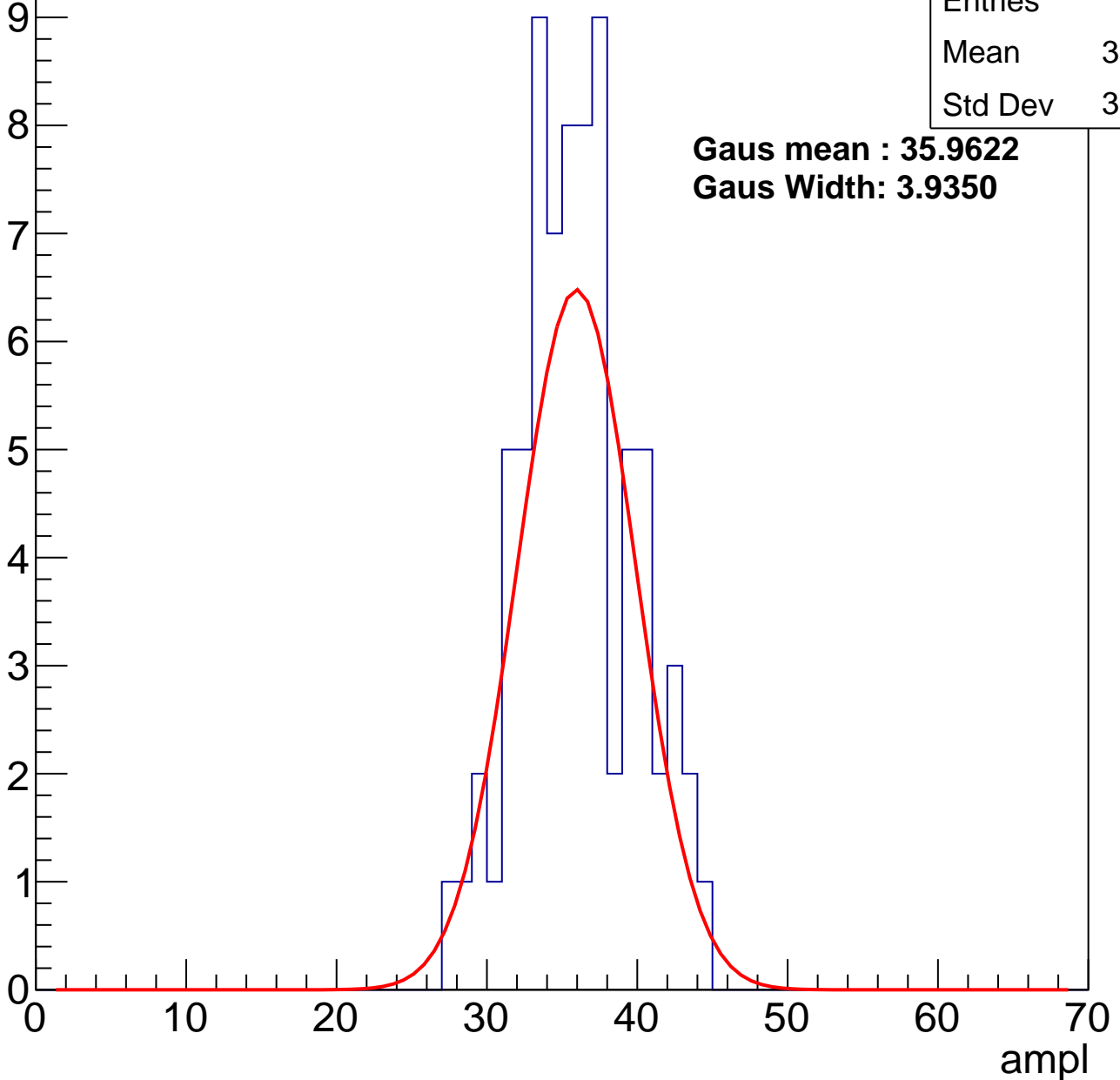
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.57
Std Dev	3.715

Gaus mean : 35.9622

Gaus Width: 3.9350



B1L102S, U22-ch82, adc2

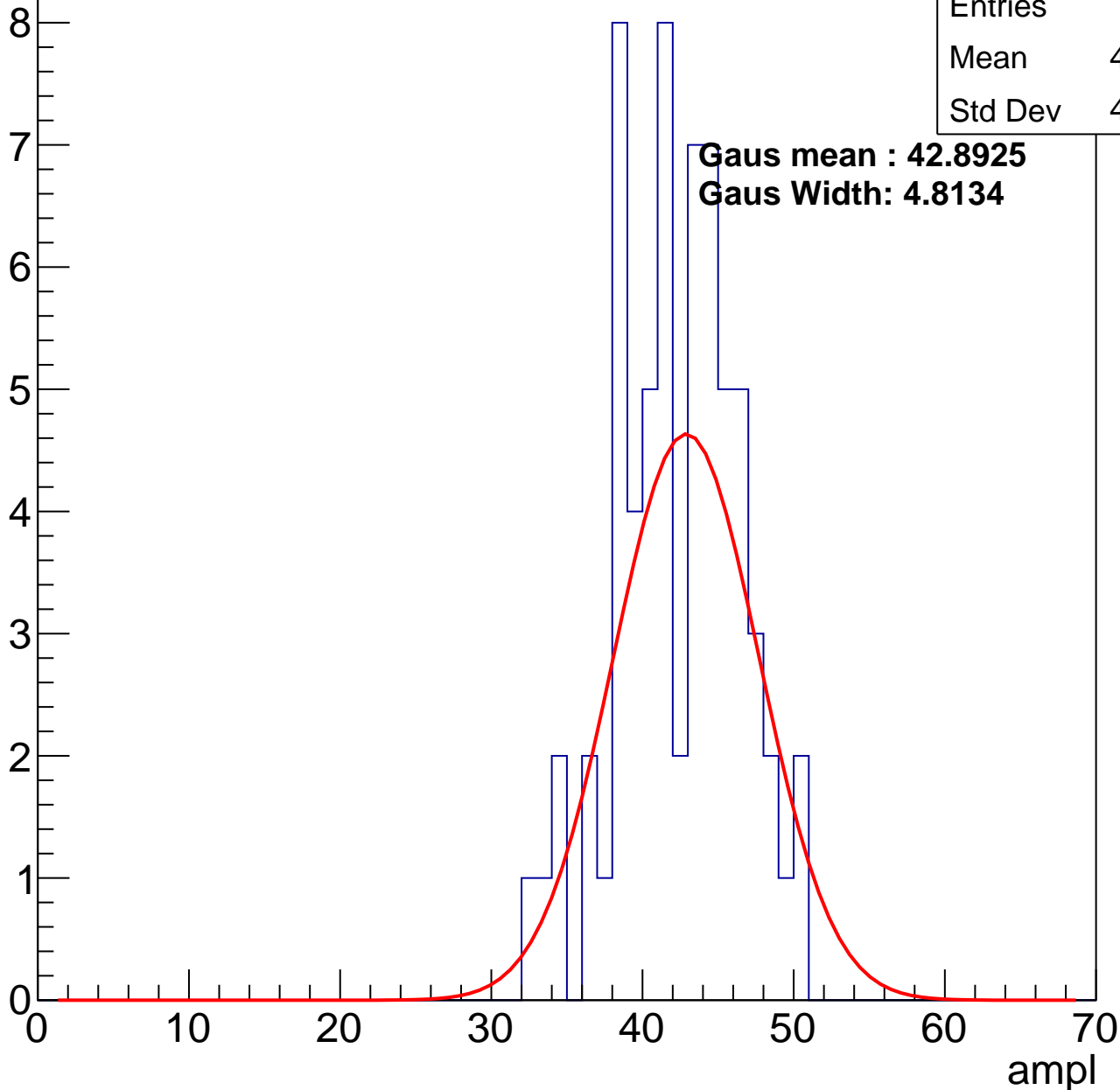
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.88
Std Dev	4.047

Gaus mean : 42.8925

Gaus Width: 4.8134

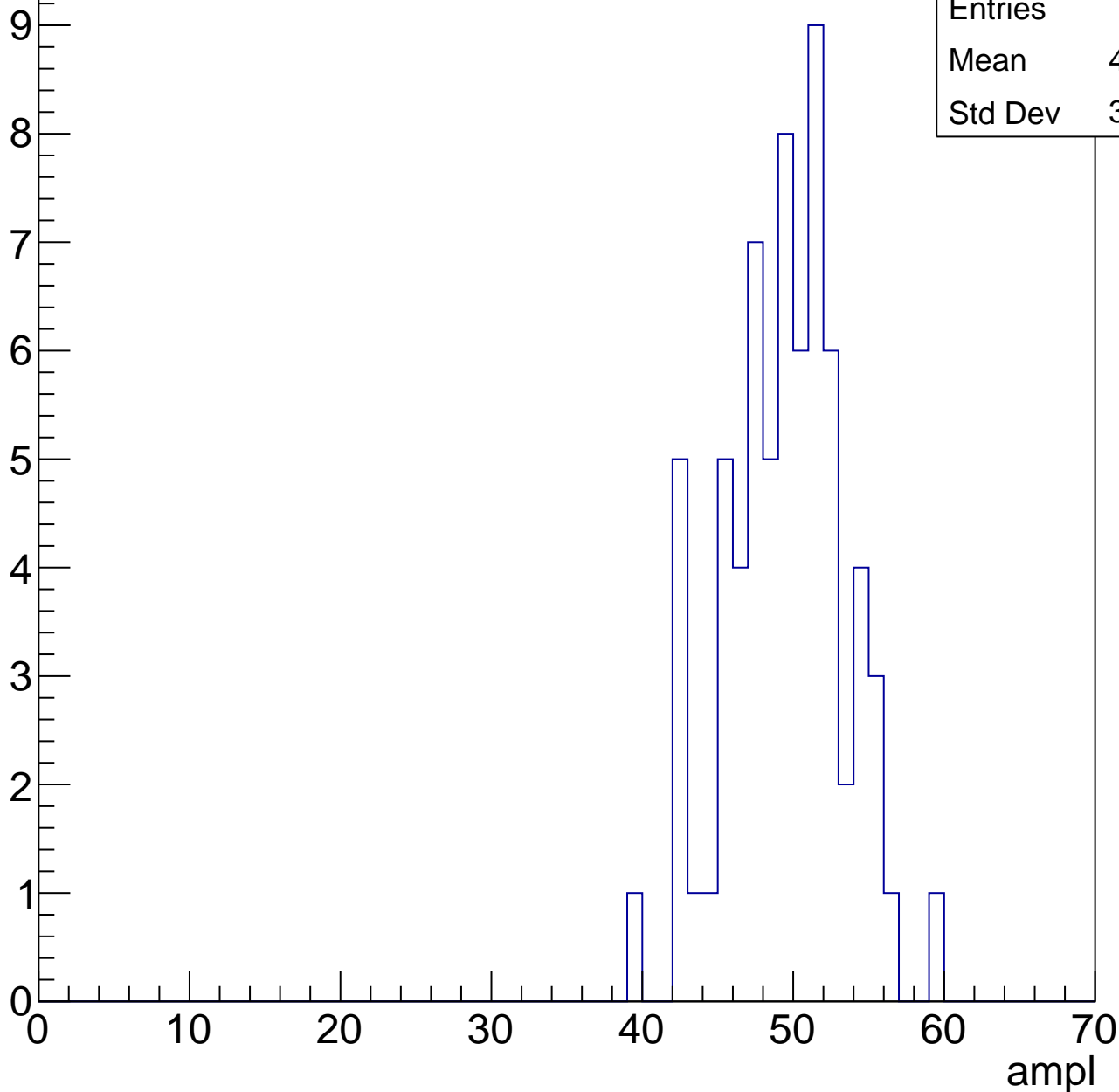


B1L102S, U22-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.97
Std Dev	3.882

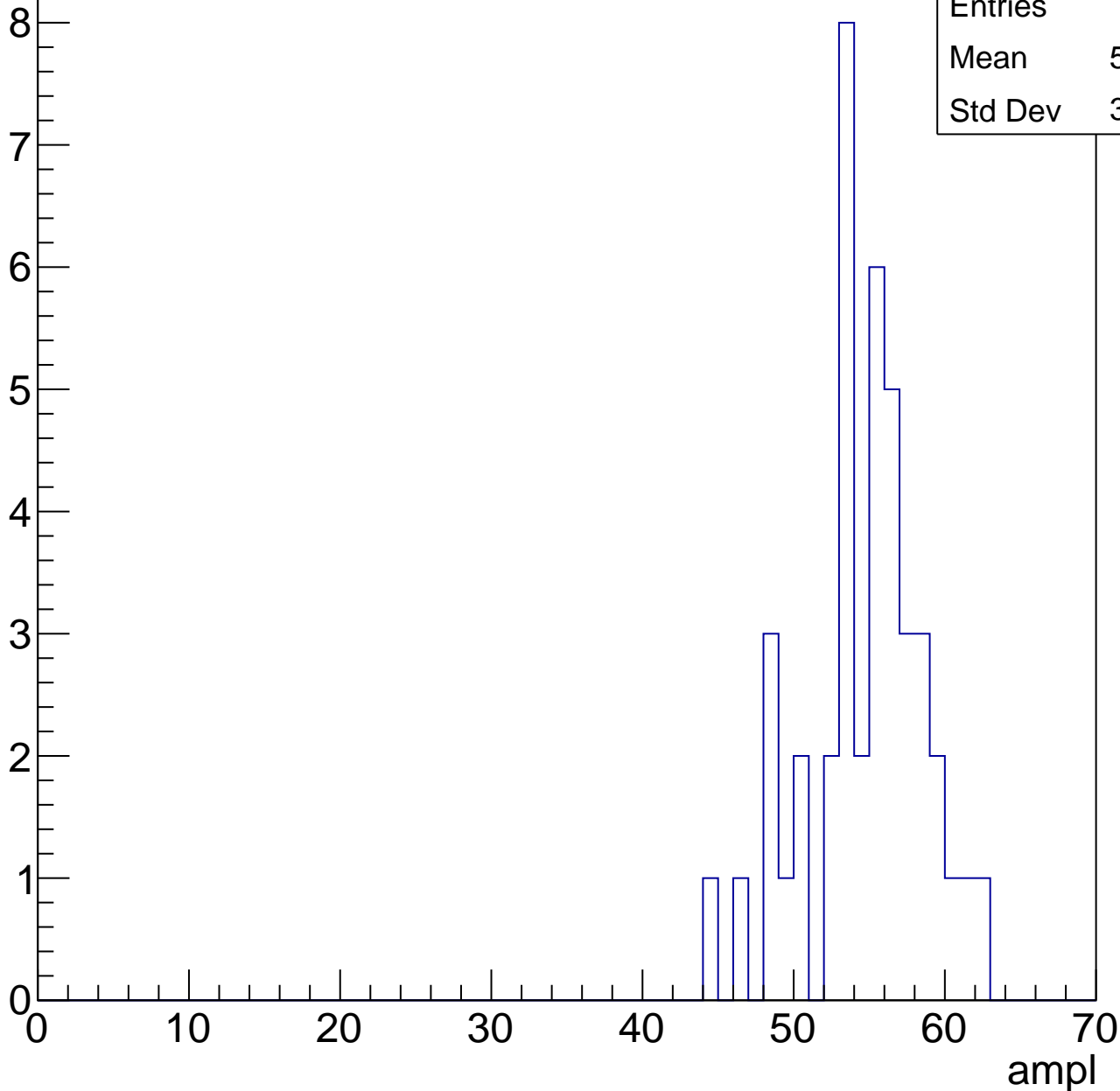


B1L102S, U22-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	54.17
Std Dev	3.915

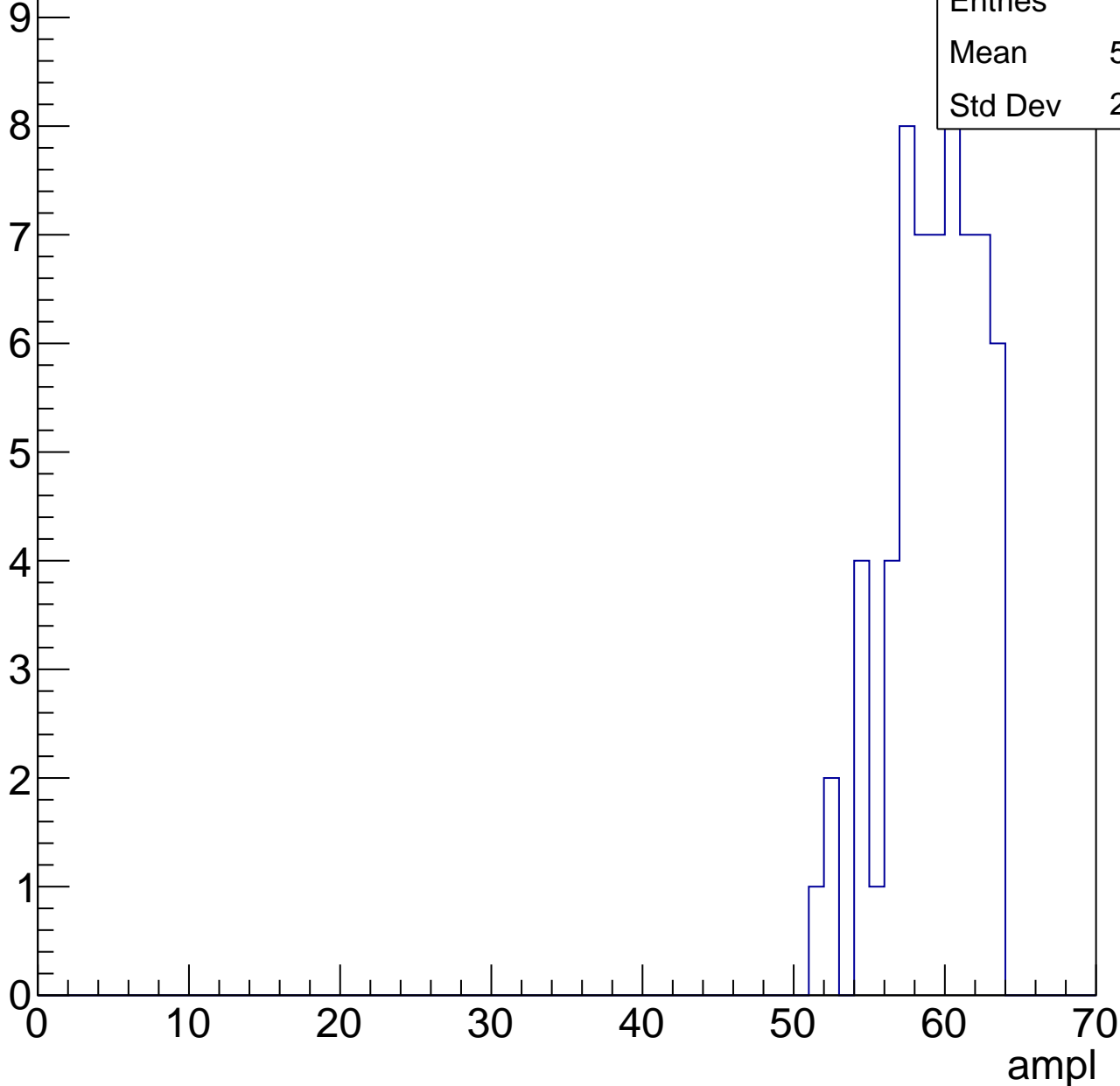


B1L102S, U22-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

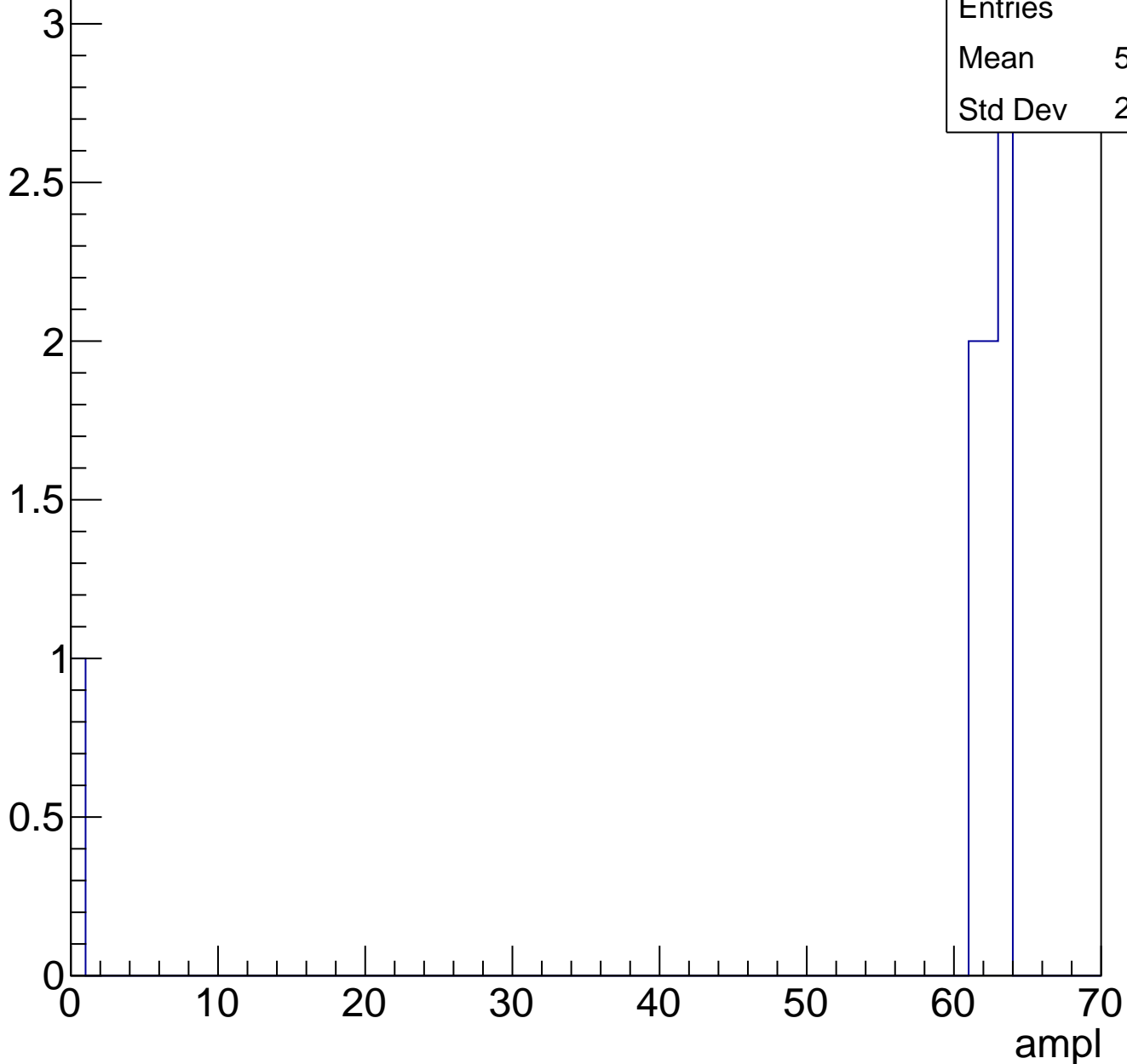
Entries	63
Mean	58.79
Std Dev	2.945



B1L102S, U22-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch83, adc0

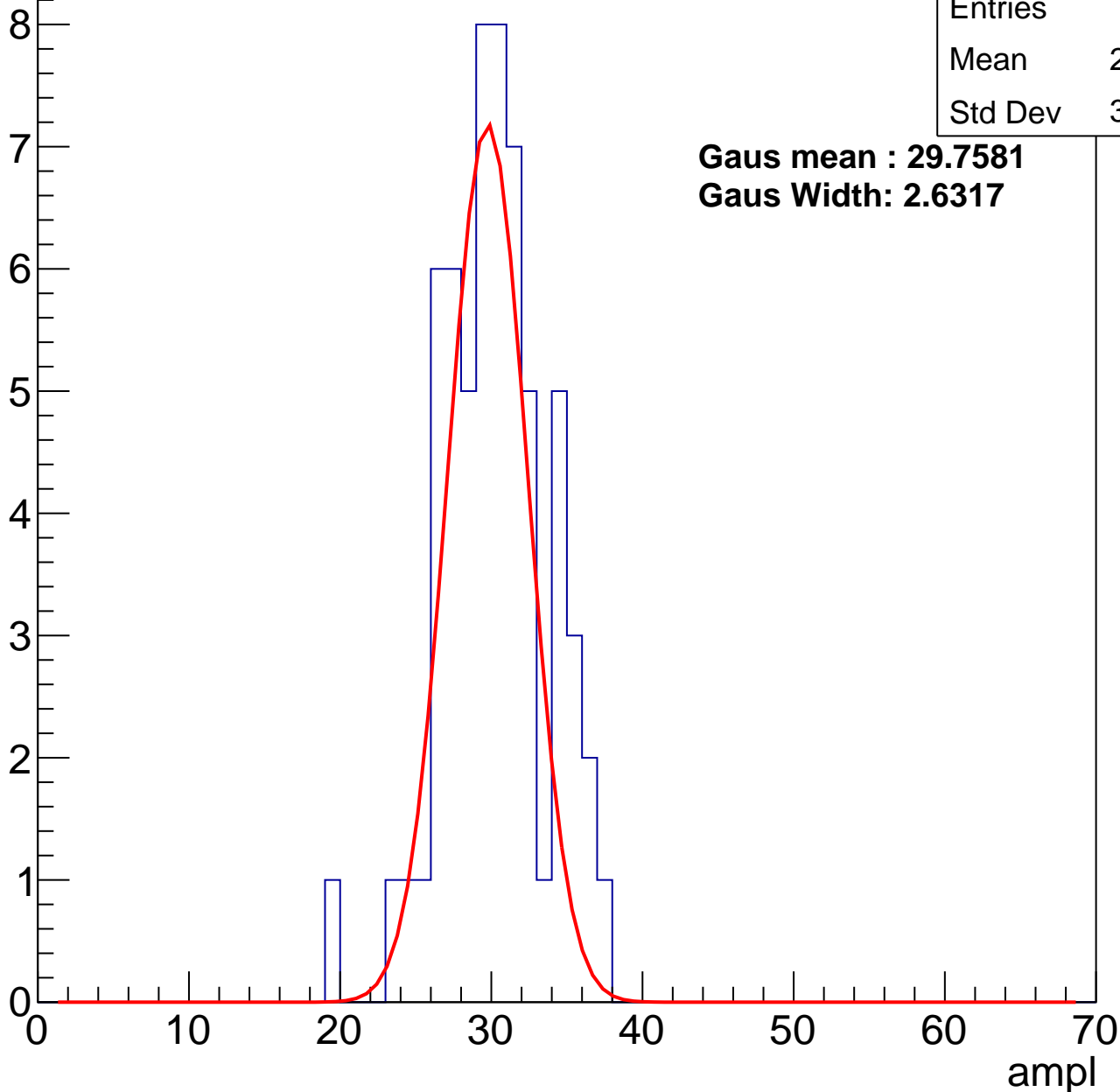
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	29.75
Std Dev	3.429

Gaus mean : 29.7581

Gaus Width: 2.6317



B1L102S, U22-ch83, adc1

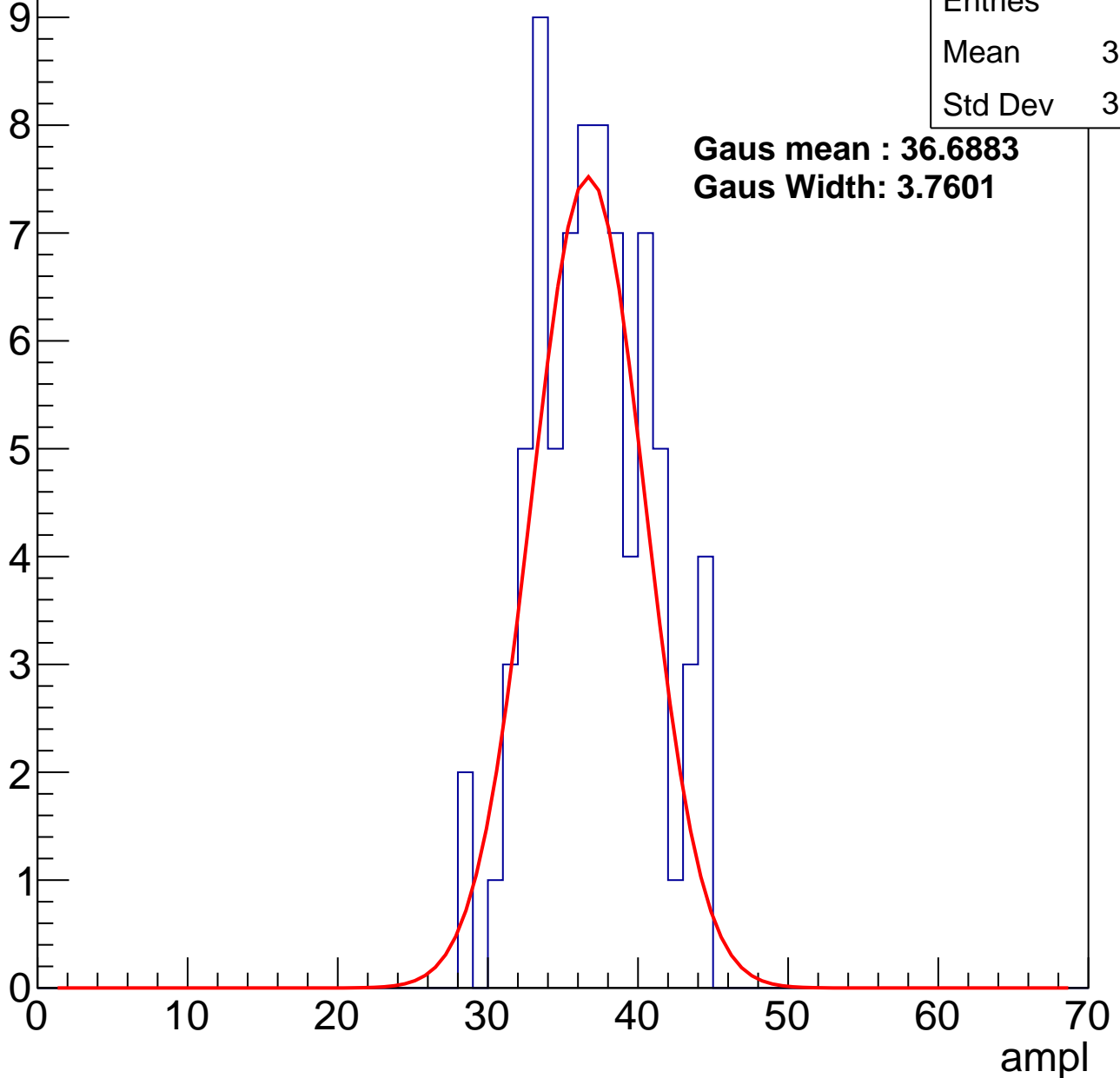
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.57
Std Dev	3.814

Gaus mean : 36.6883

Gaus Width: 3.7601



B1L102S, U22-ch83, adc2

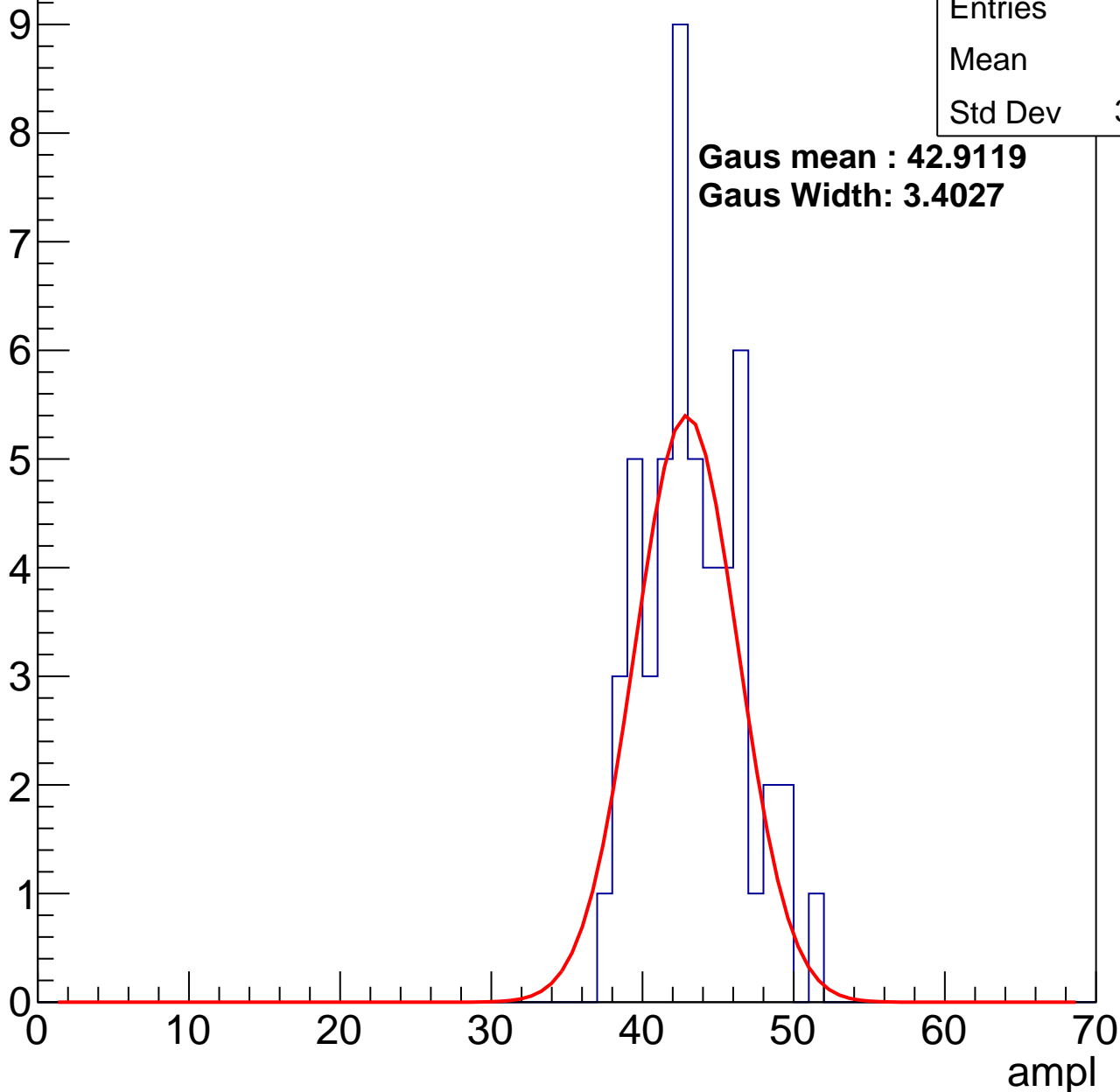
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.9
Std Dev	3.201

Gaus mean : 42.9119

Gaus Width: 3.4027

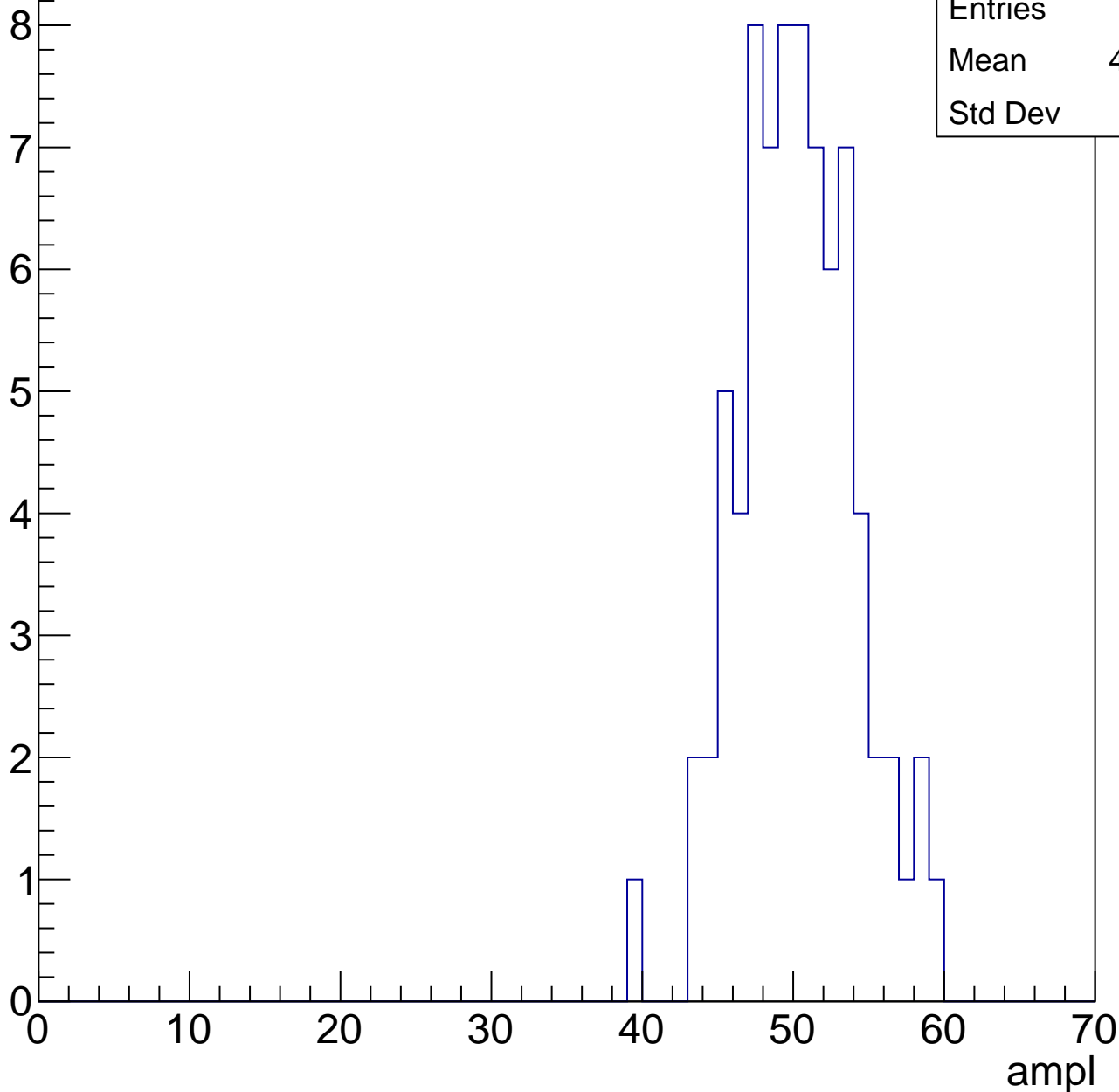


B1L102S, U22-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.82
Std Dev	3.82

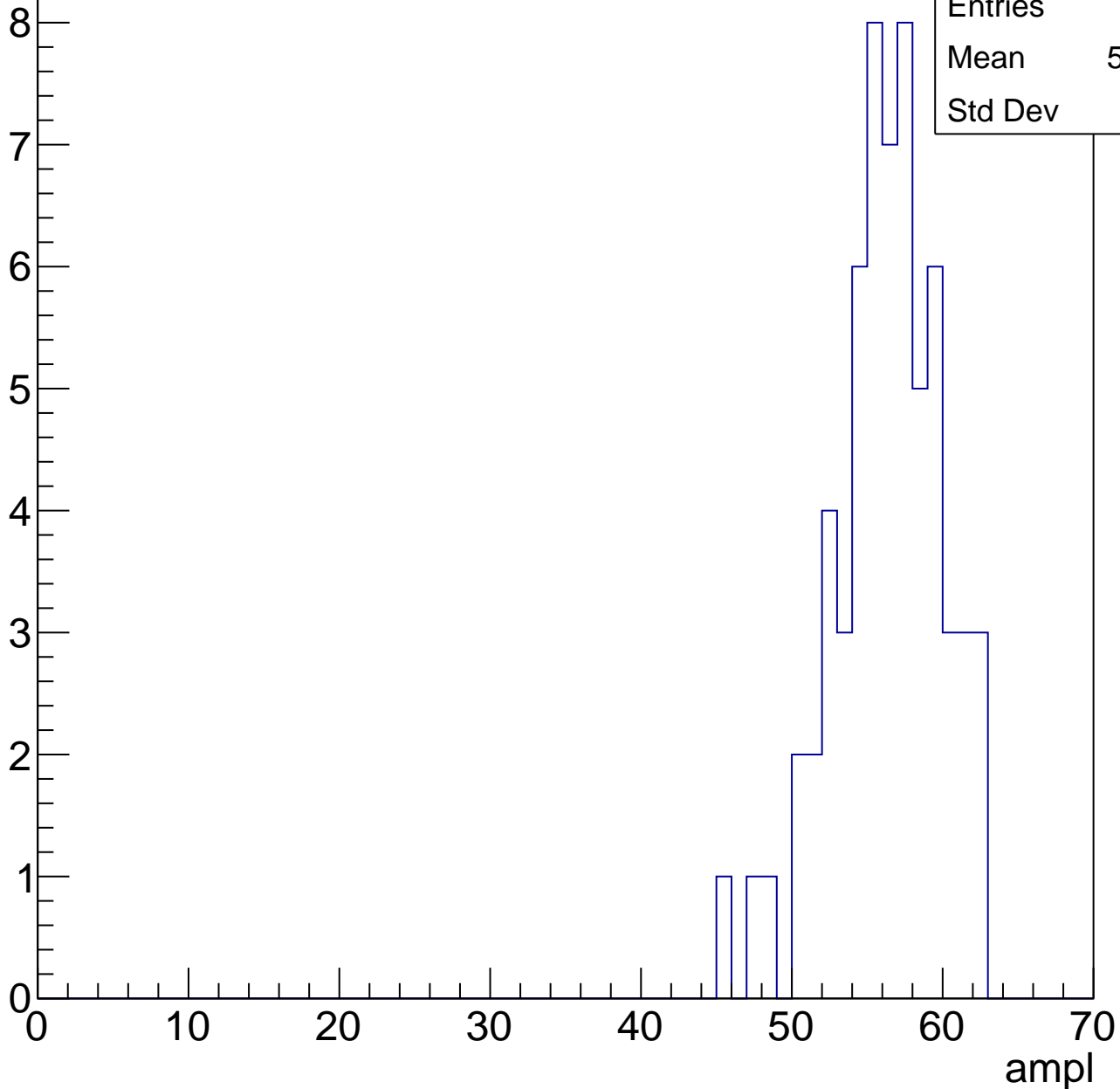


B1L102S, U22-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.78
Std Dev	3.61

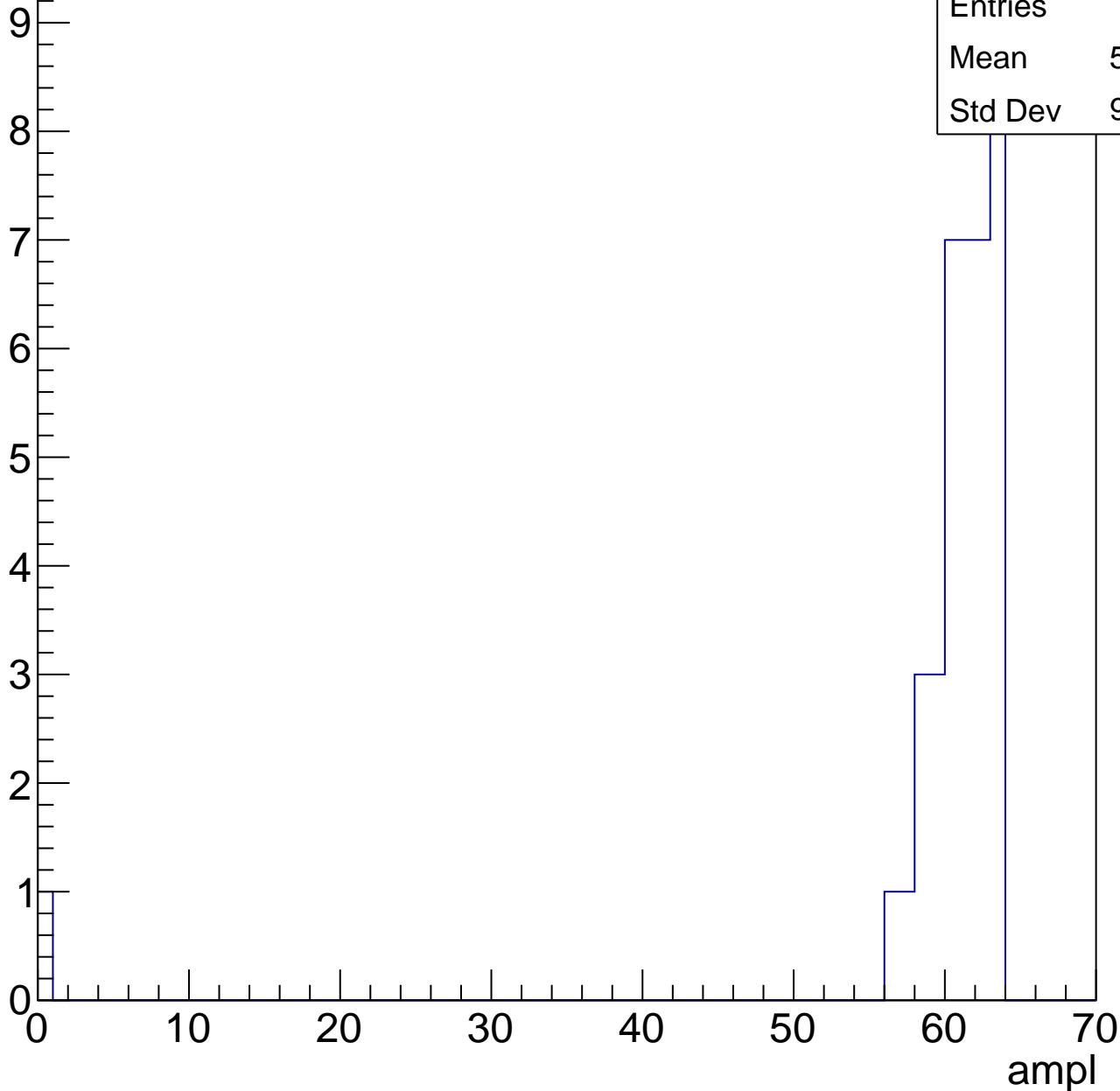


B1L102S, U22-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	59.28
Std Dev	9.787



B1L102S, U22-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch84, adc0

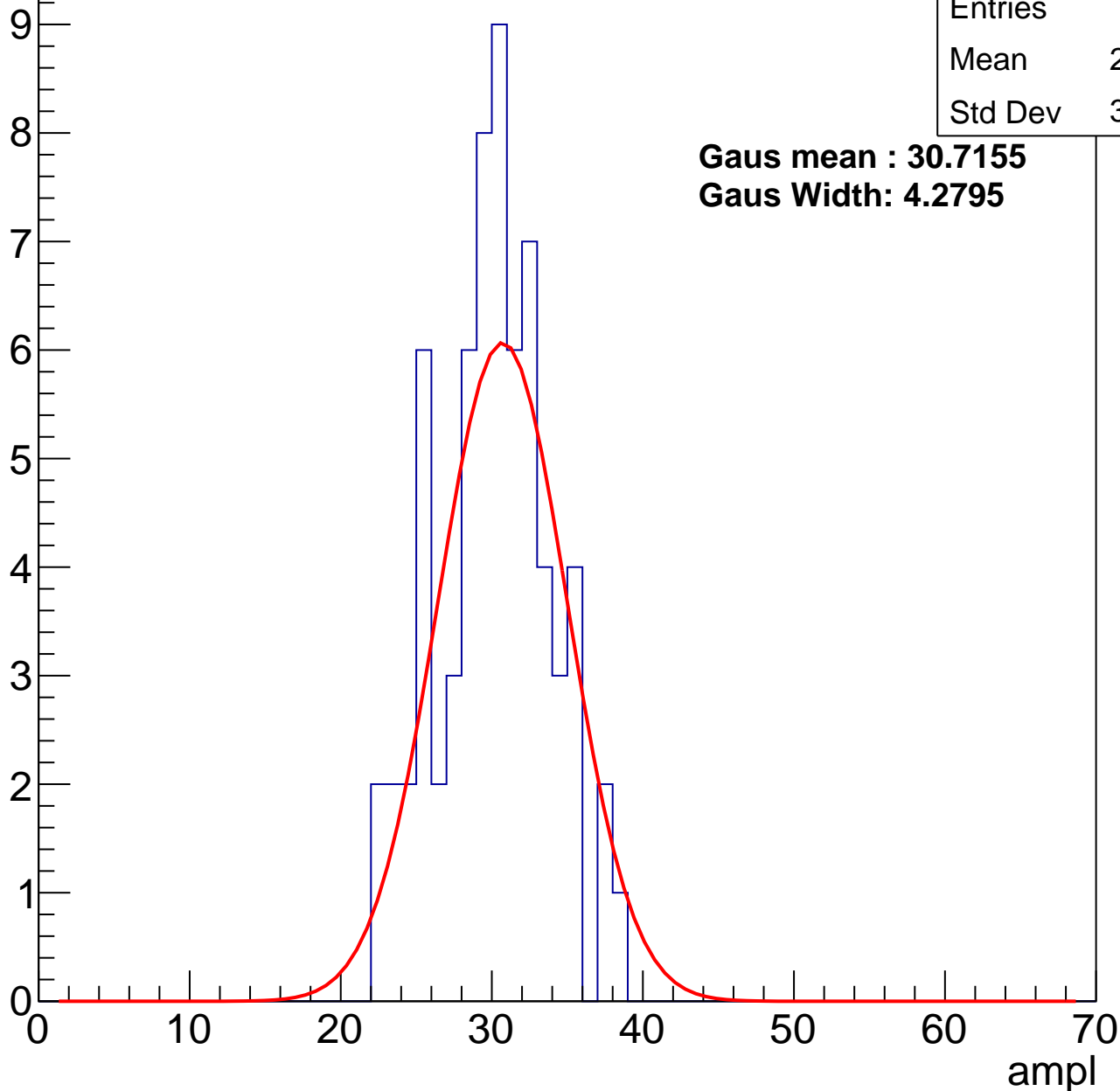
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	29.66
Std Dev	3.688

Gaus mean : 30.7155

Gaus Width: 4.2795



B1L102S, U22-ch84, adc1

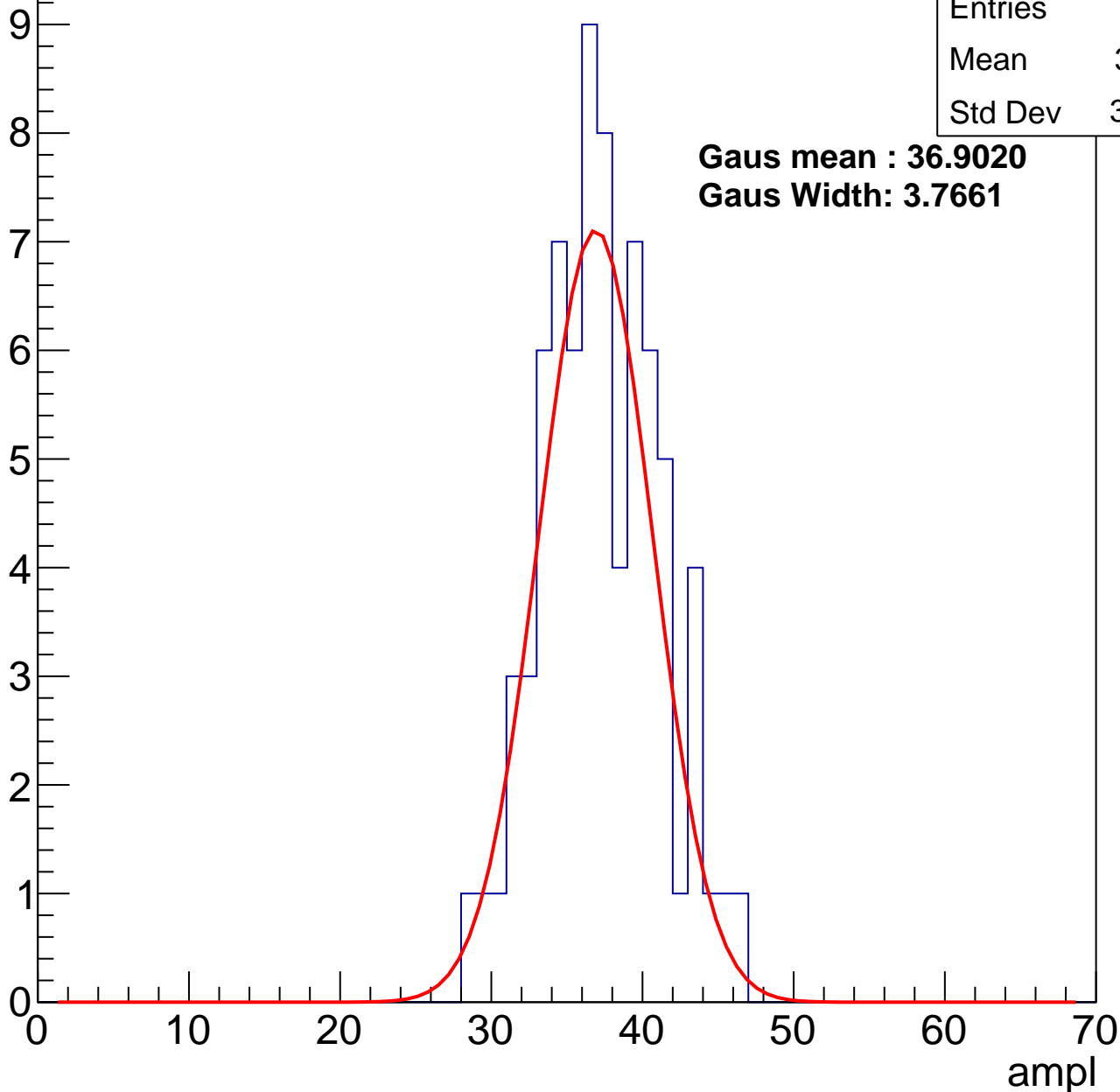
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.81
Std Dev	3.822

Gaus mean : 36.9020

Gaus Width: 3.7661



B1L102S, U22-ch84, adc2

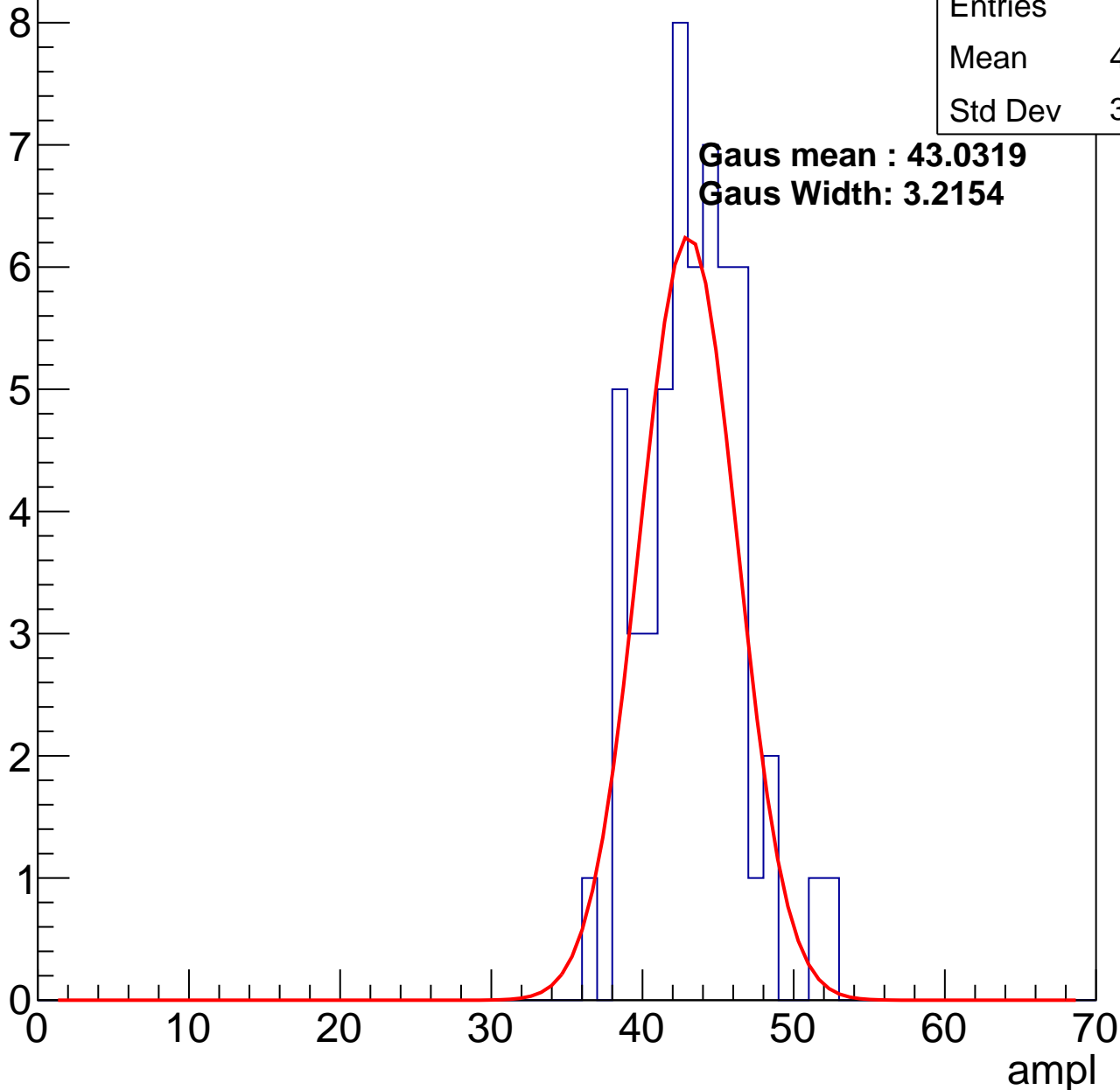
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.95
Std Dev	3.227

Gaus mean : 43.0319

Gaus Width: 3.2154

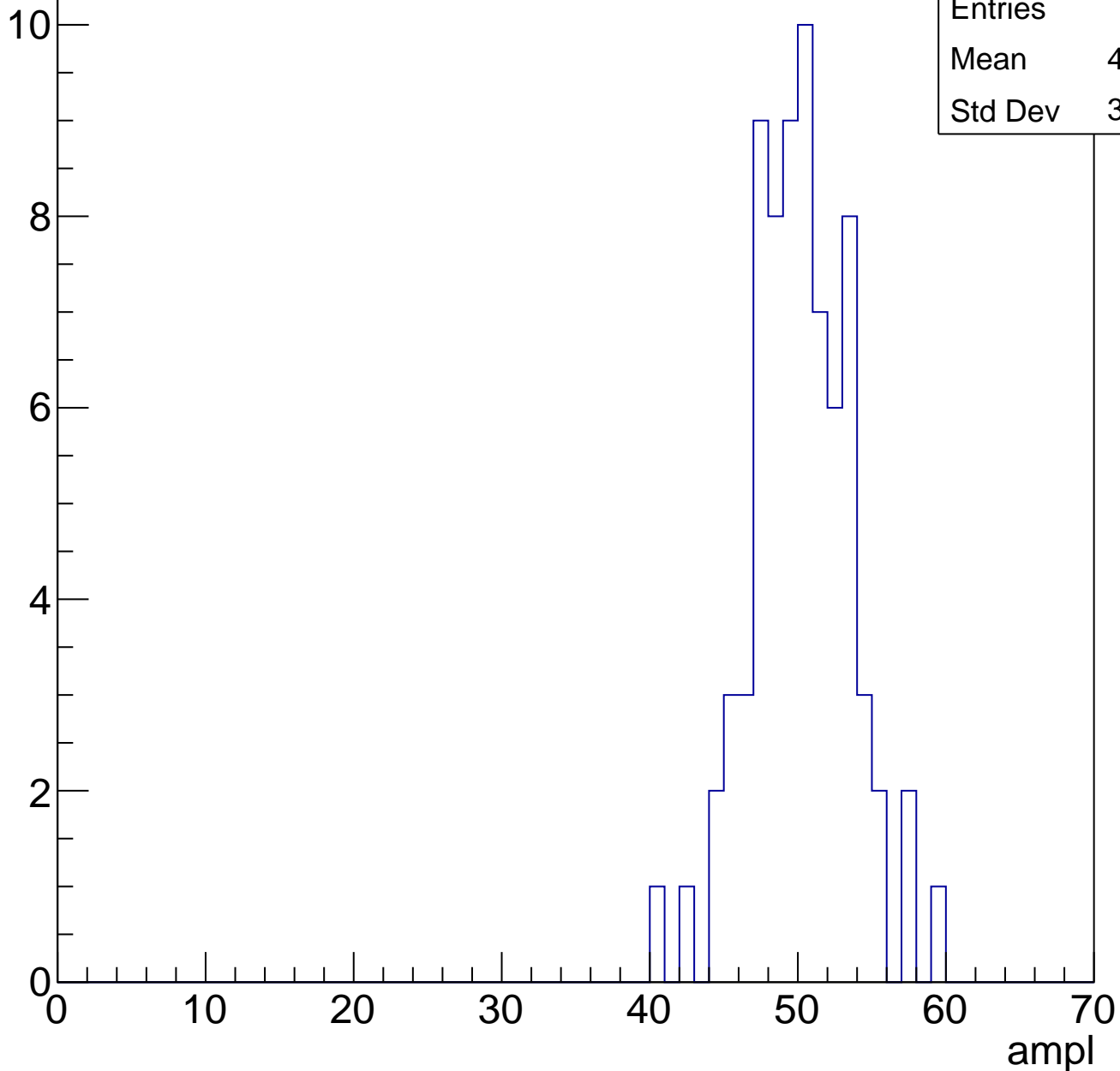


B1L102S, U22-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	49.72
Std Dev	3.377

Entry



B1L102S, U22-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	56.47
Std Dev	2.872

Entry

10

8

6

4

2

0

0

10

20

30

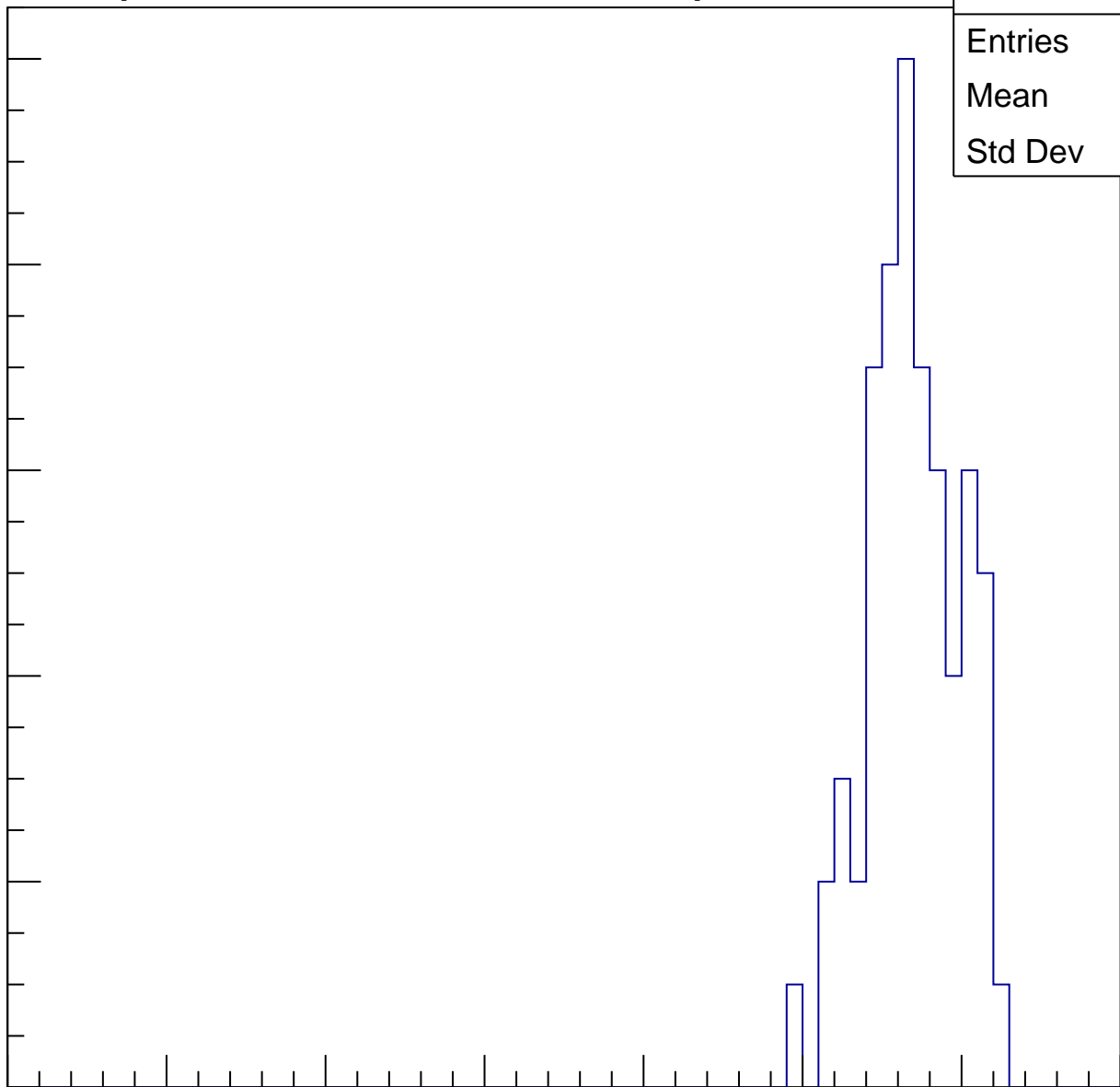
40

50

60

ampl

70

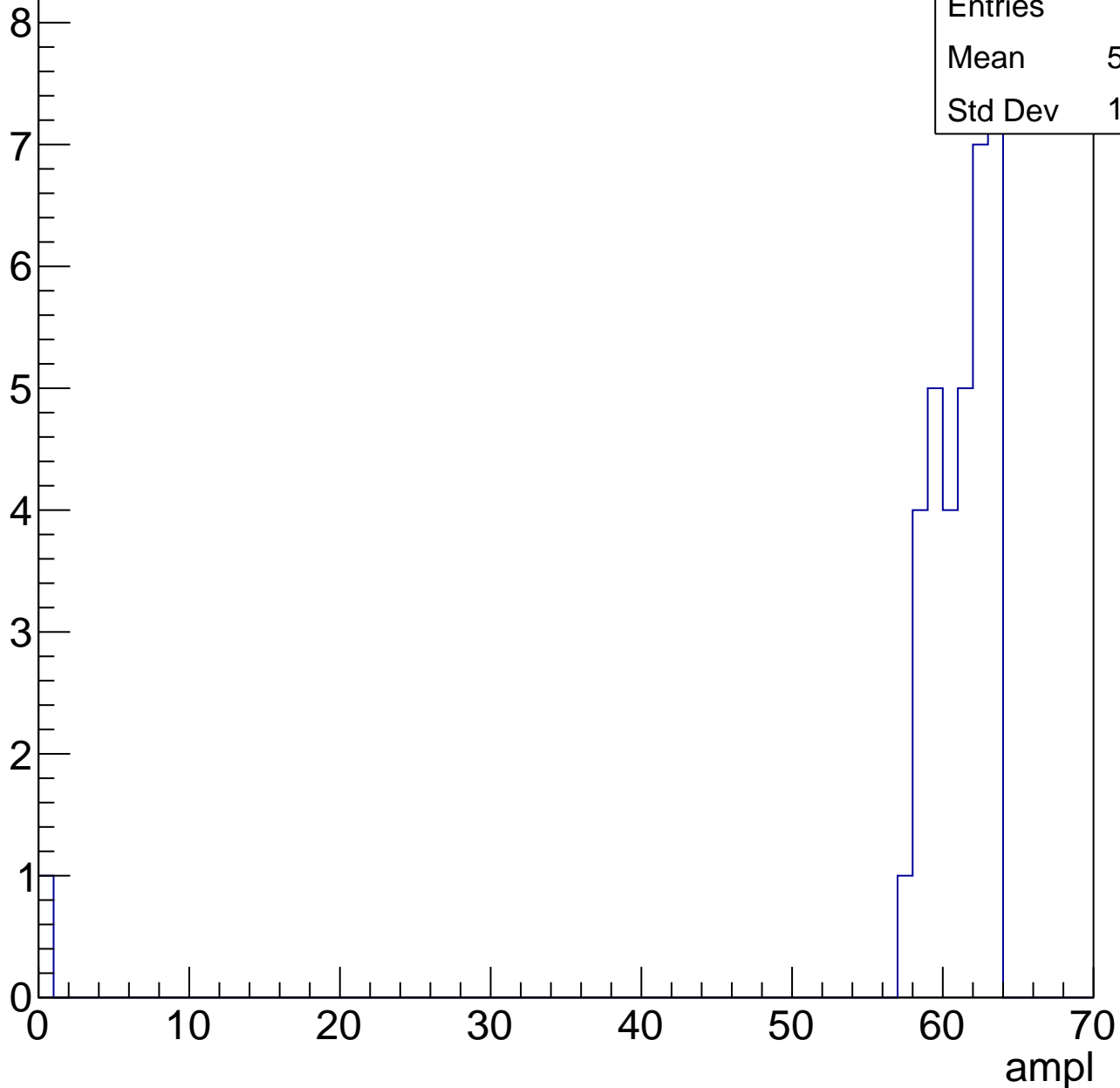


B1L102S, U22-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	59.06
Std Dev	10.29



B1L102S, U22-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch85, adc0

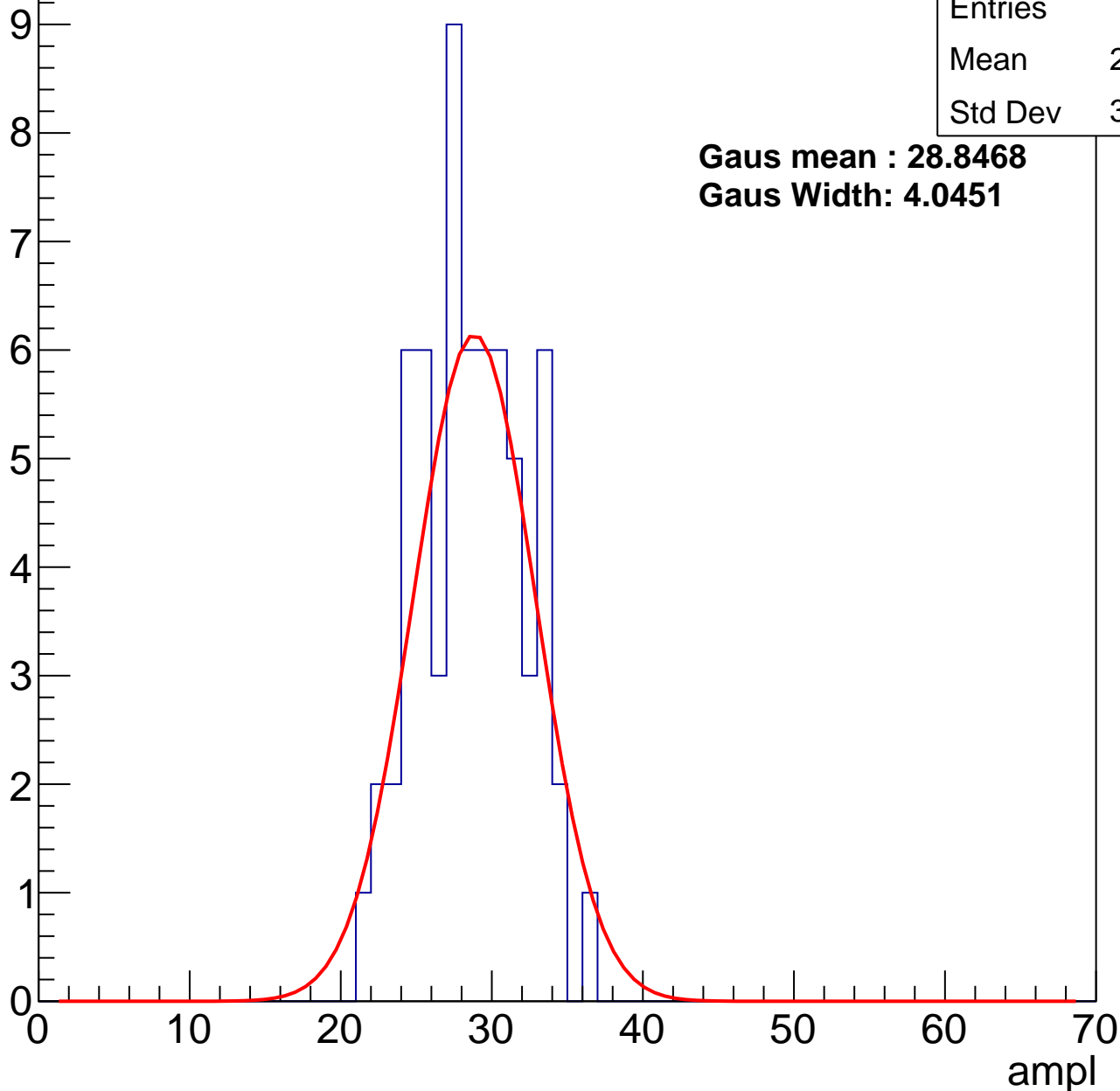
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.14
Std Dev	3.423

Gaus mean : 28.8468

Gaus Width: 4.0451



B1L102S, U22-ch85, adc1

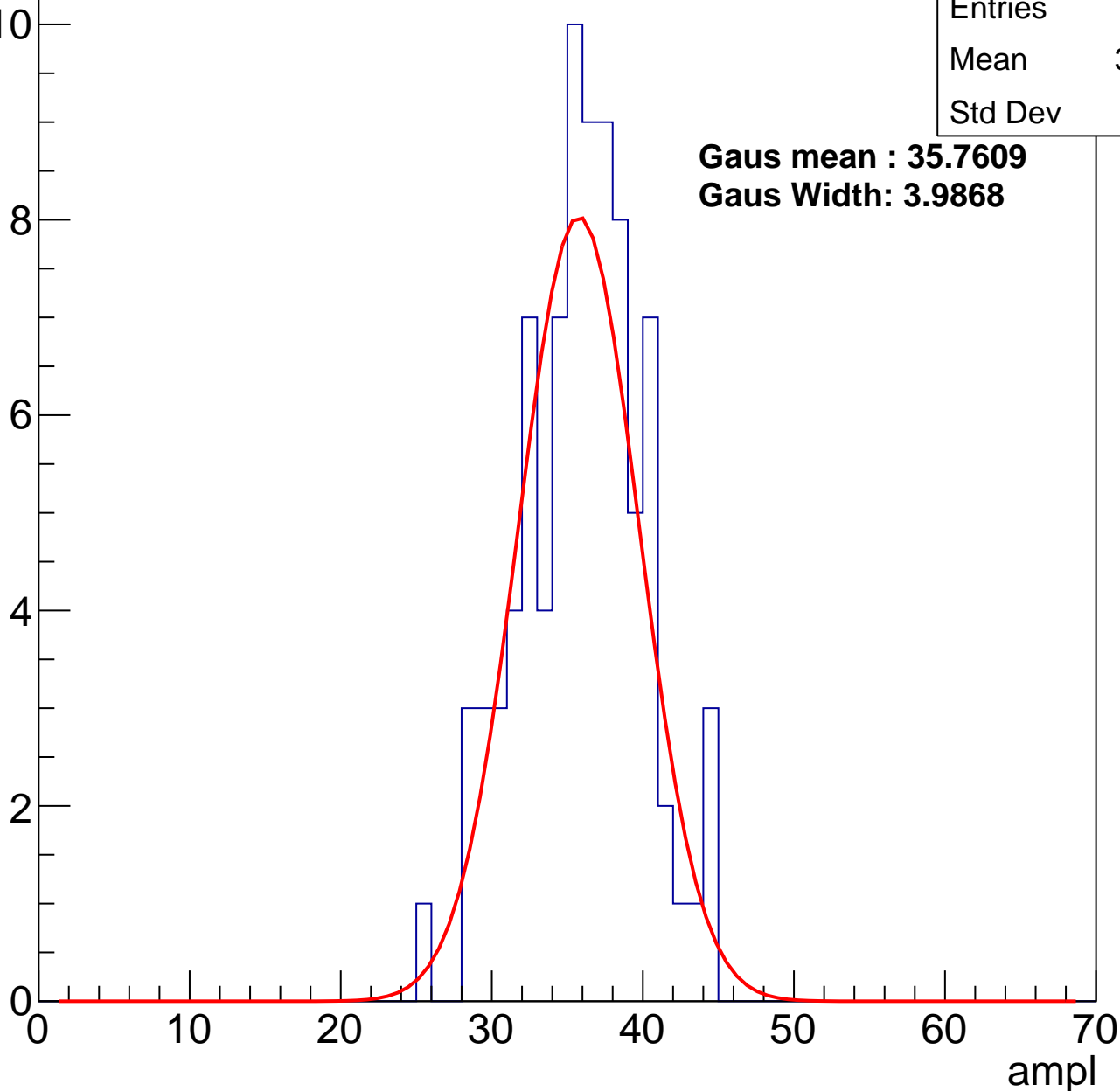
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	35.51
Std Dev	3.95

Gaus mean : 35.7609

Gaus Width: 3.9868



B1L102S, U22-ch85, adc2

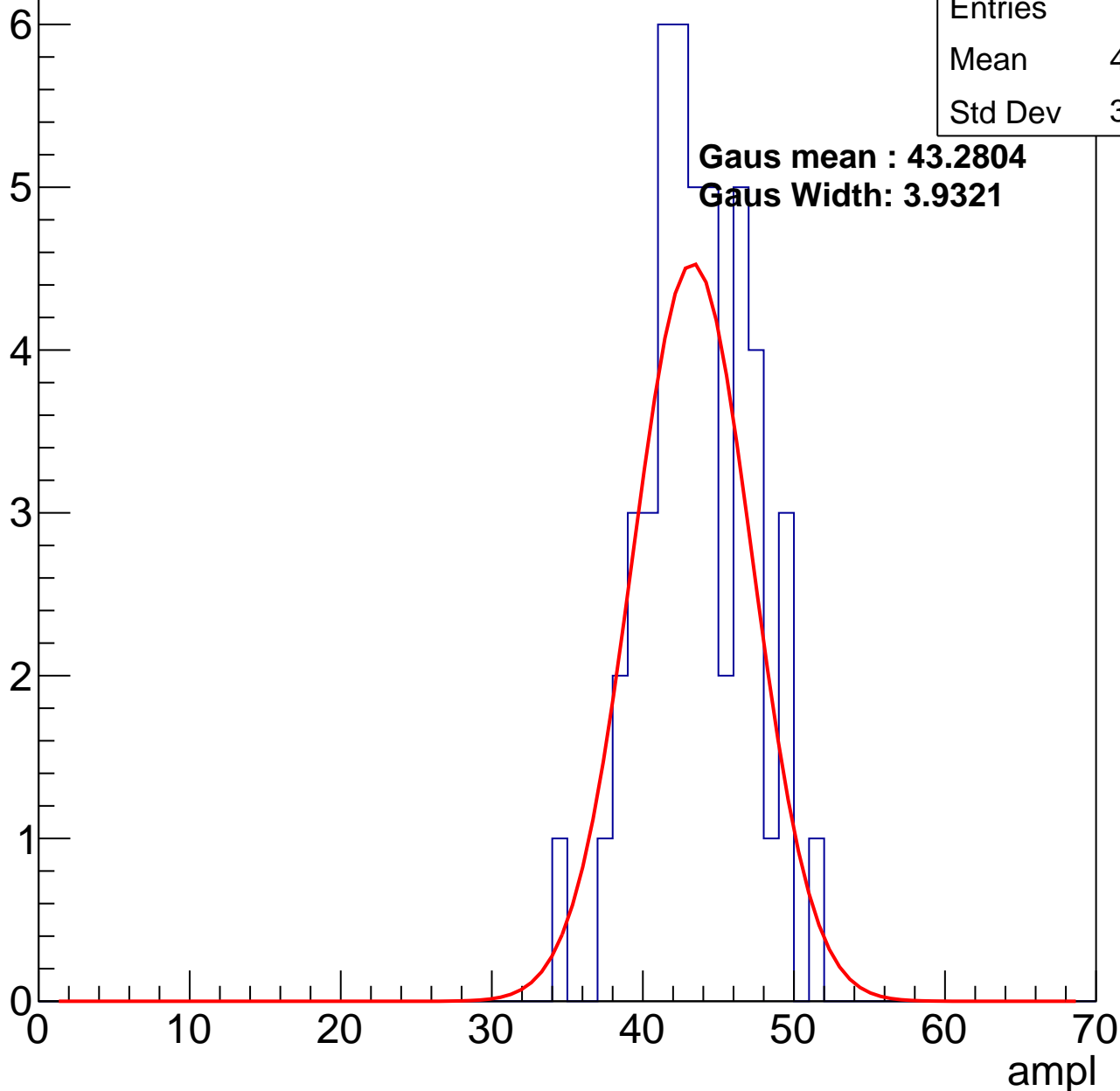
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	43.15
Std Dev	3.506

Gaus mean : 43.2804

Gaus Width: 3.9321

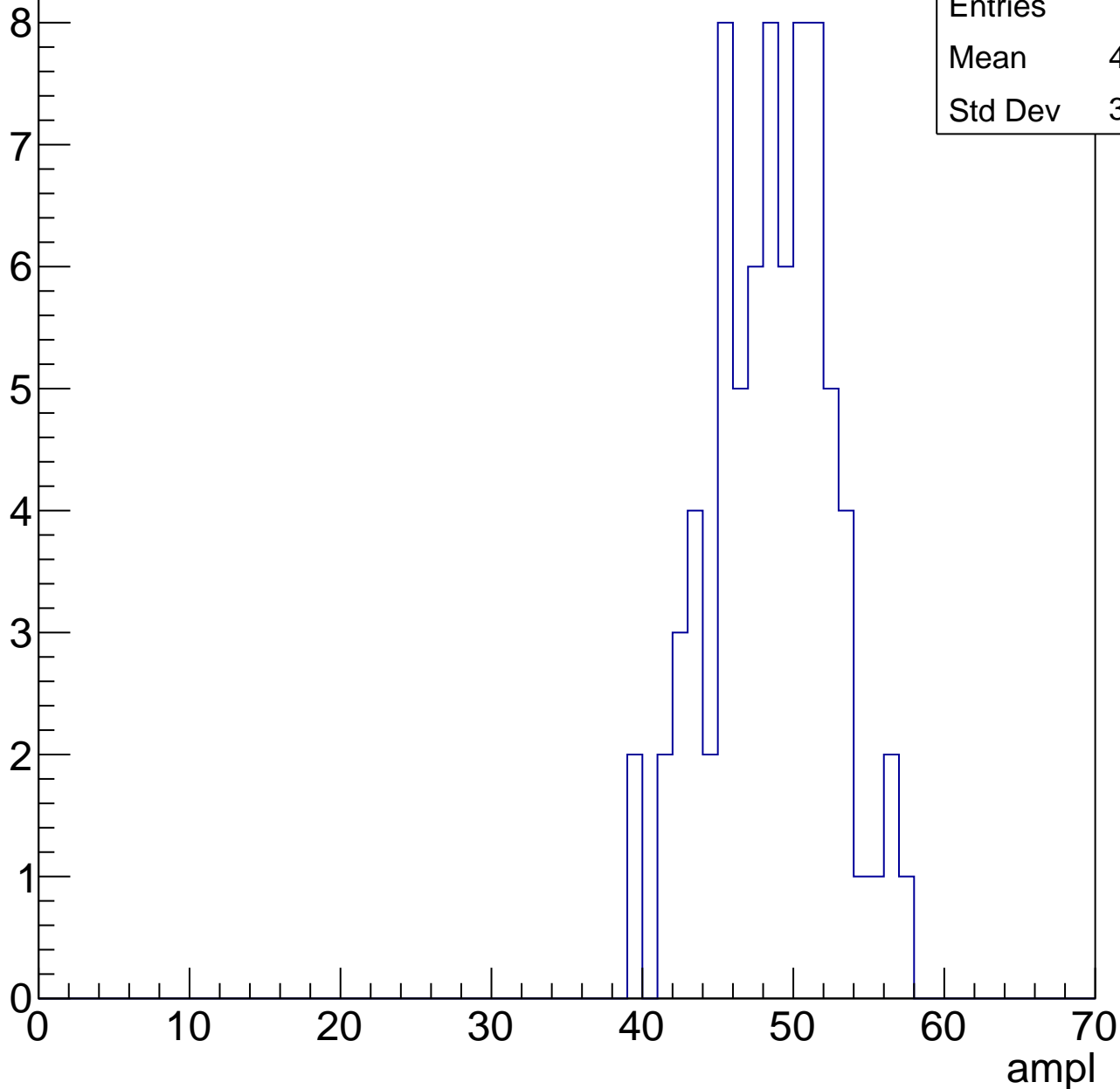


B1L102S, U22-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	48.08
Std Dev	3.943

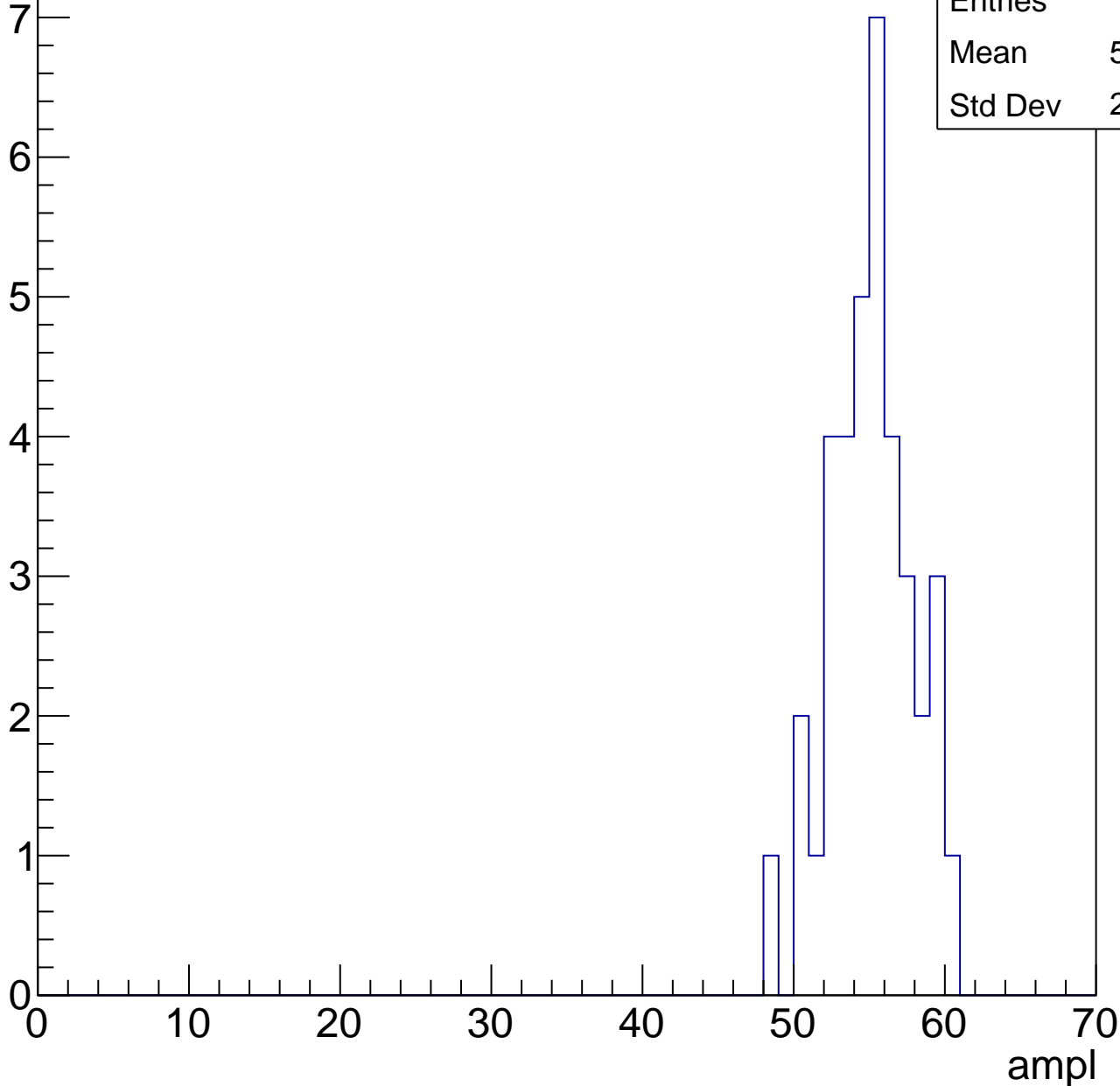


B1L102S, U22-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

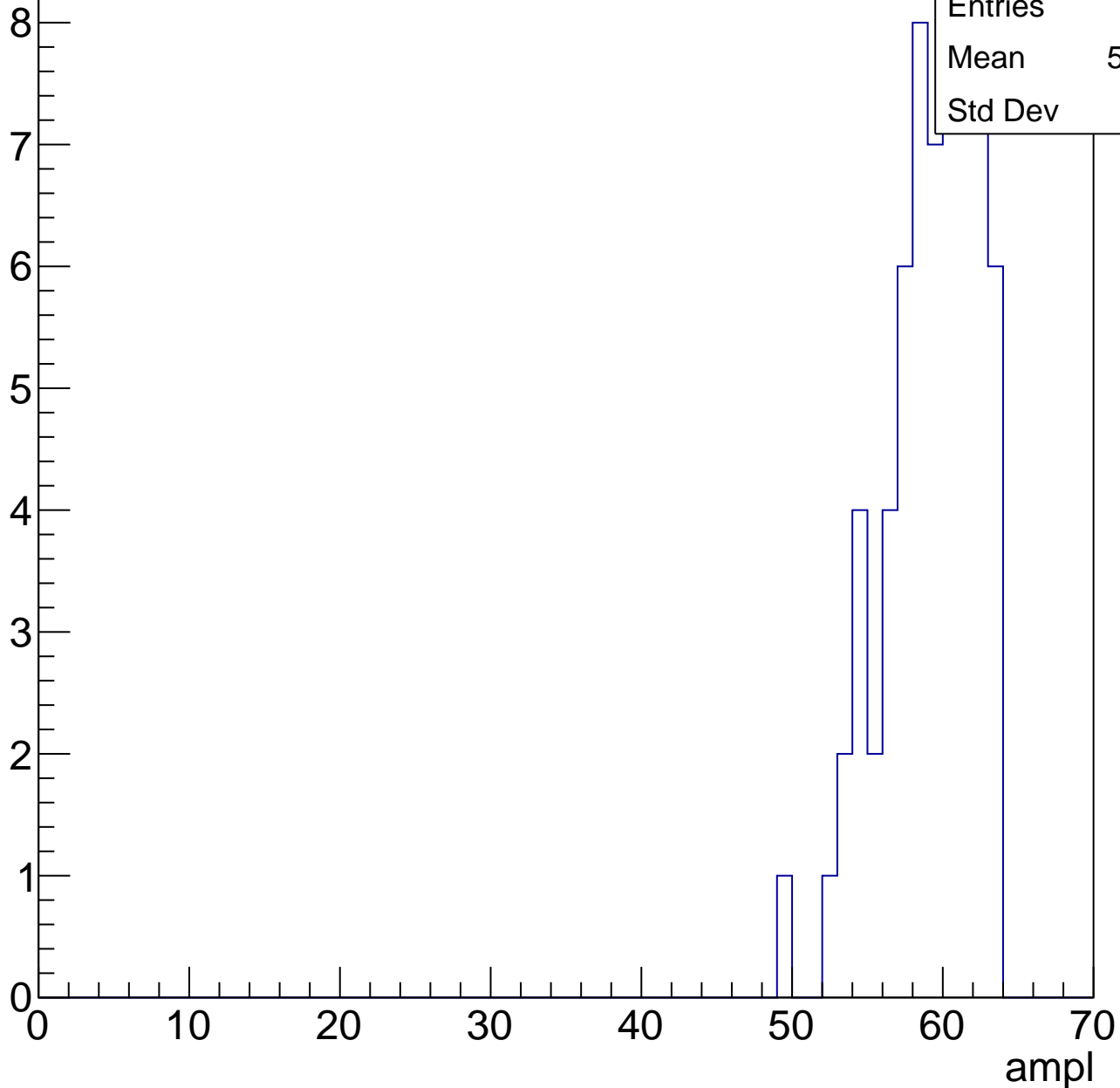
Entries	37
Mean	54.65
Std Dev	2.723



B1L102S, U22-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

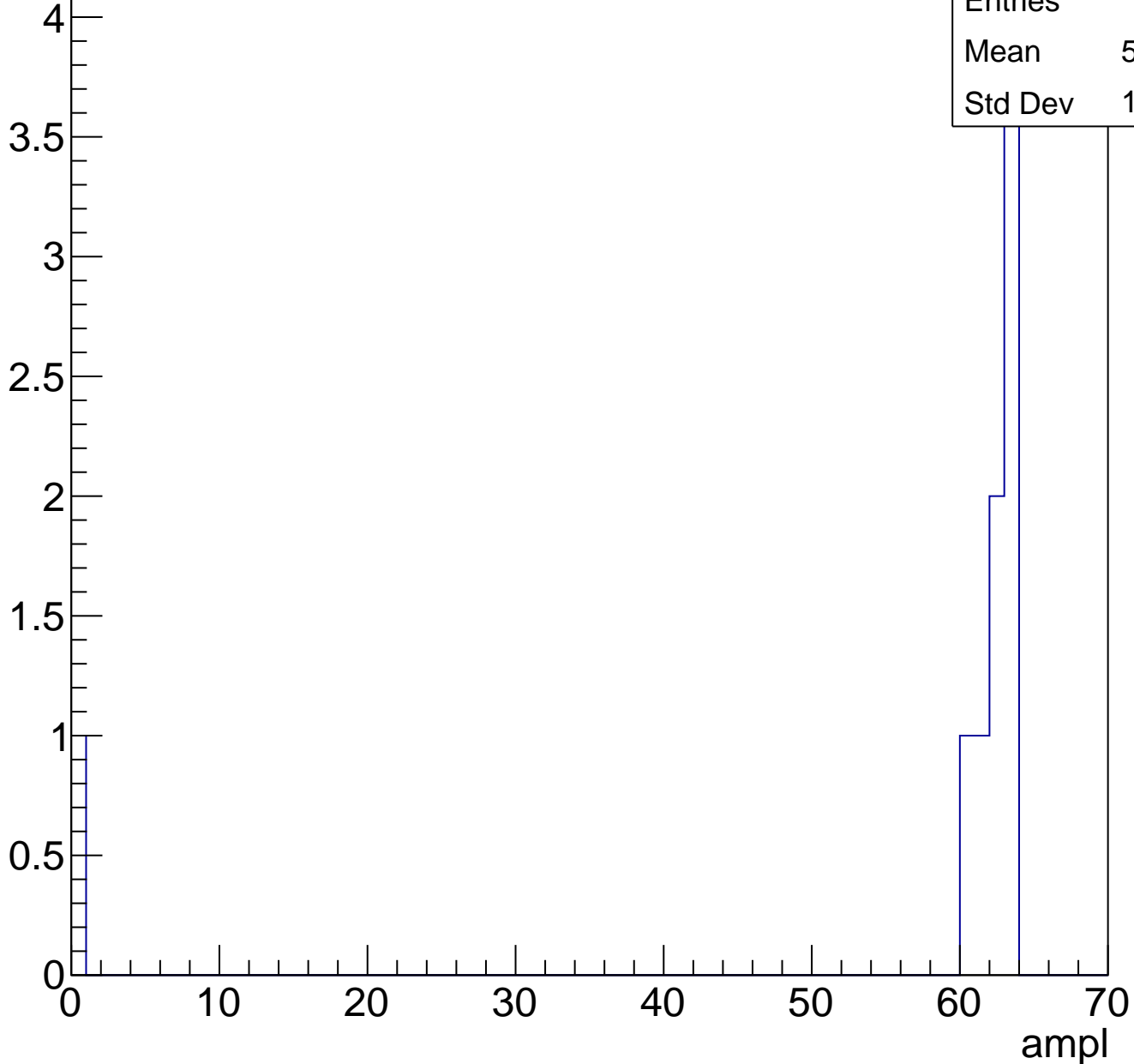
Entry



B1L102S, U22-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

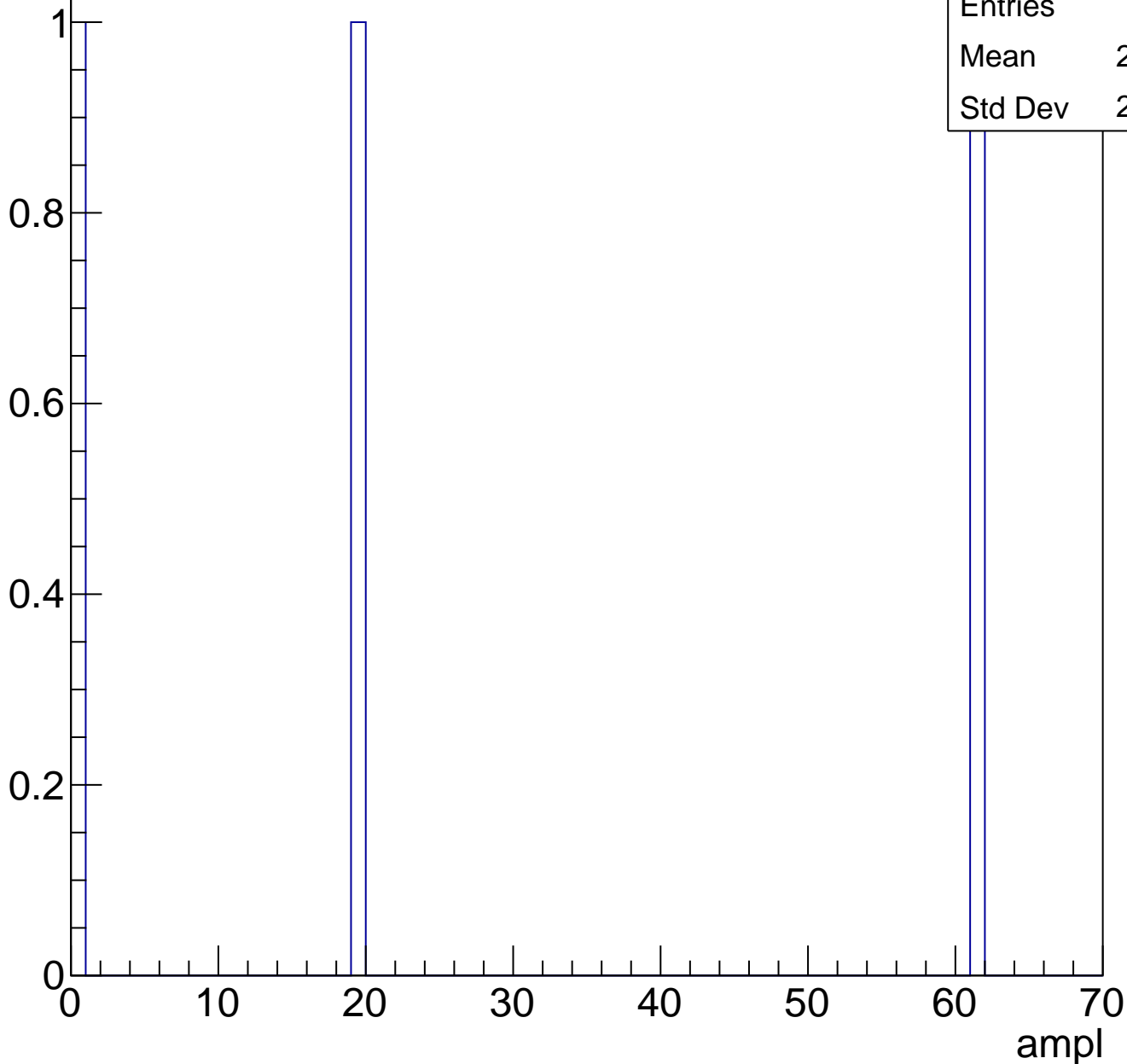
Entry



B1L102S, U22-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	26.67
Std Dev	25.49

B1L102S, U22-ch86, adc0

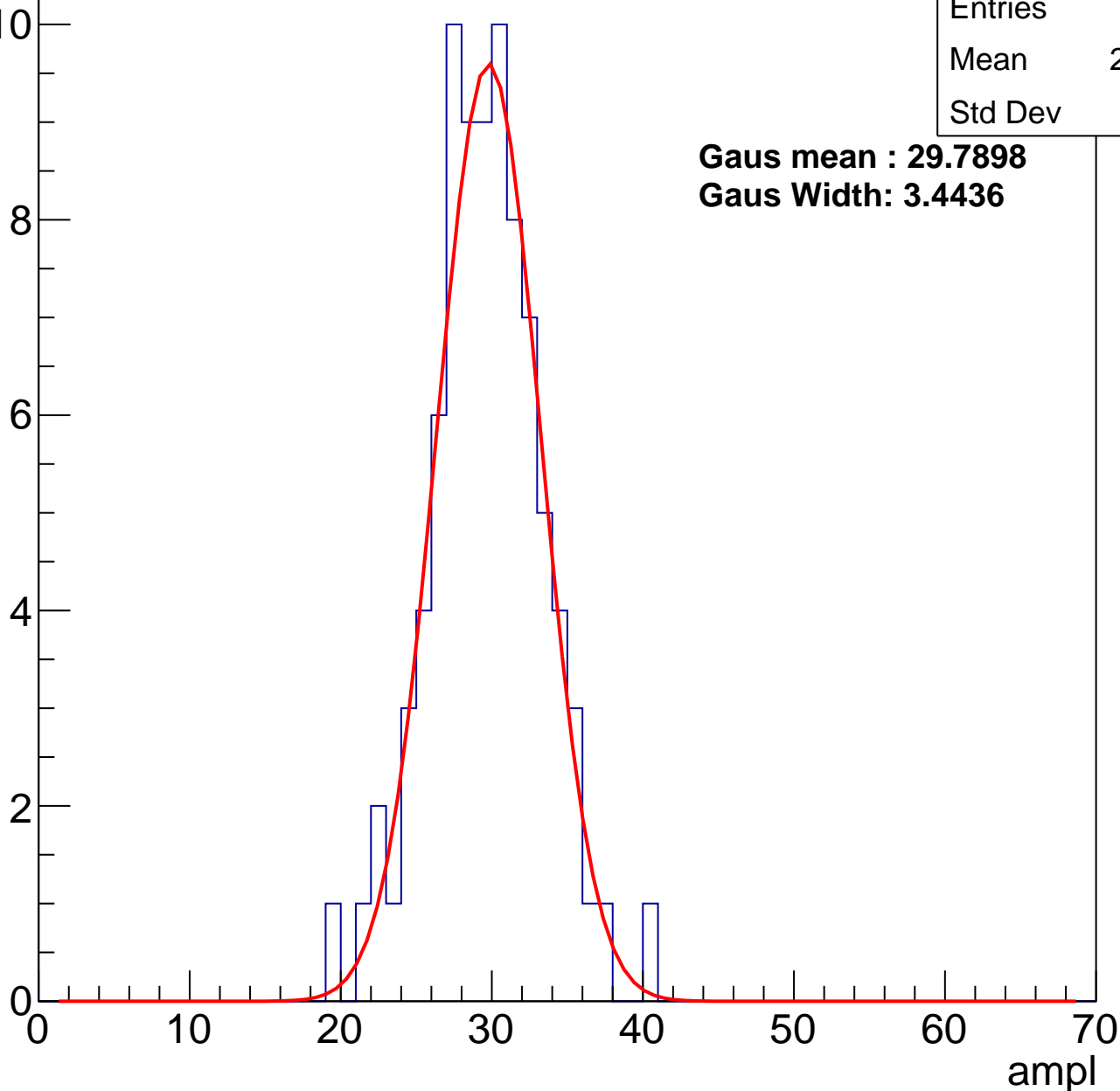
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	29.17
Std Dev	3.68

Gaus mean : 29.7898

Gaus Width: 3.4436



B1L102S, U22-ch86, adc1

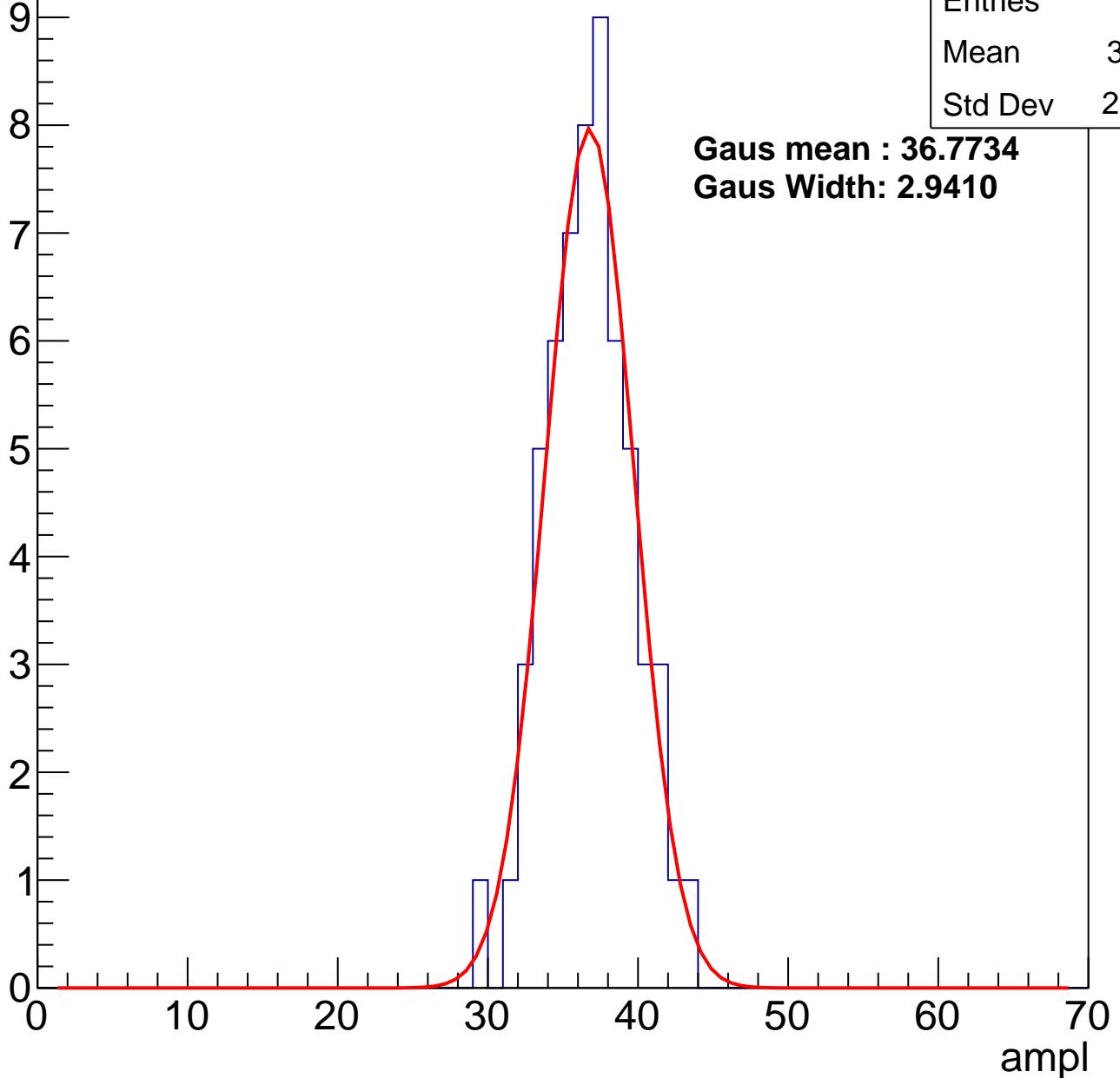
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.31
Std Dev	2.848

Gaus mean : 36.7734

Gaus Width: 2.9410



B1L102S, U22-ch86, adc2

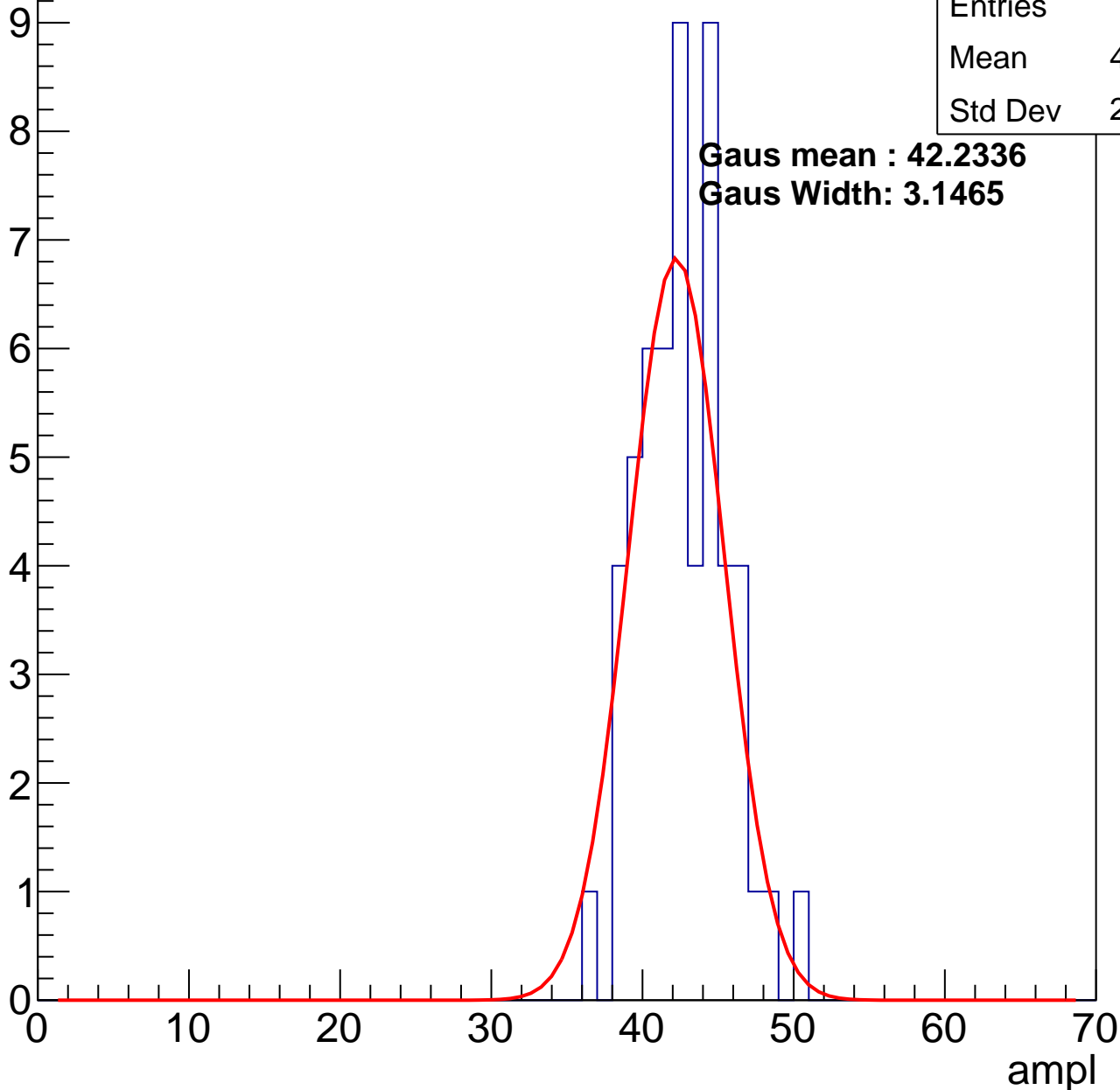
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.25
Std Dev	2.817

Gaus mean : 42.2336

Gaus Width: 3.1465

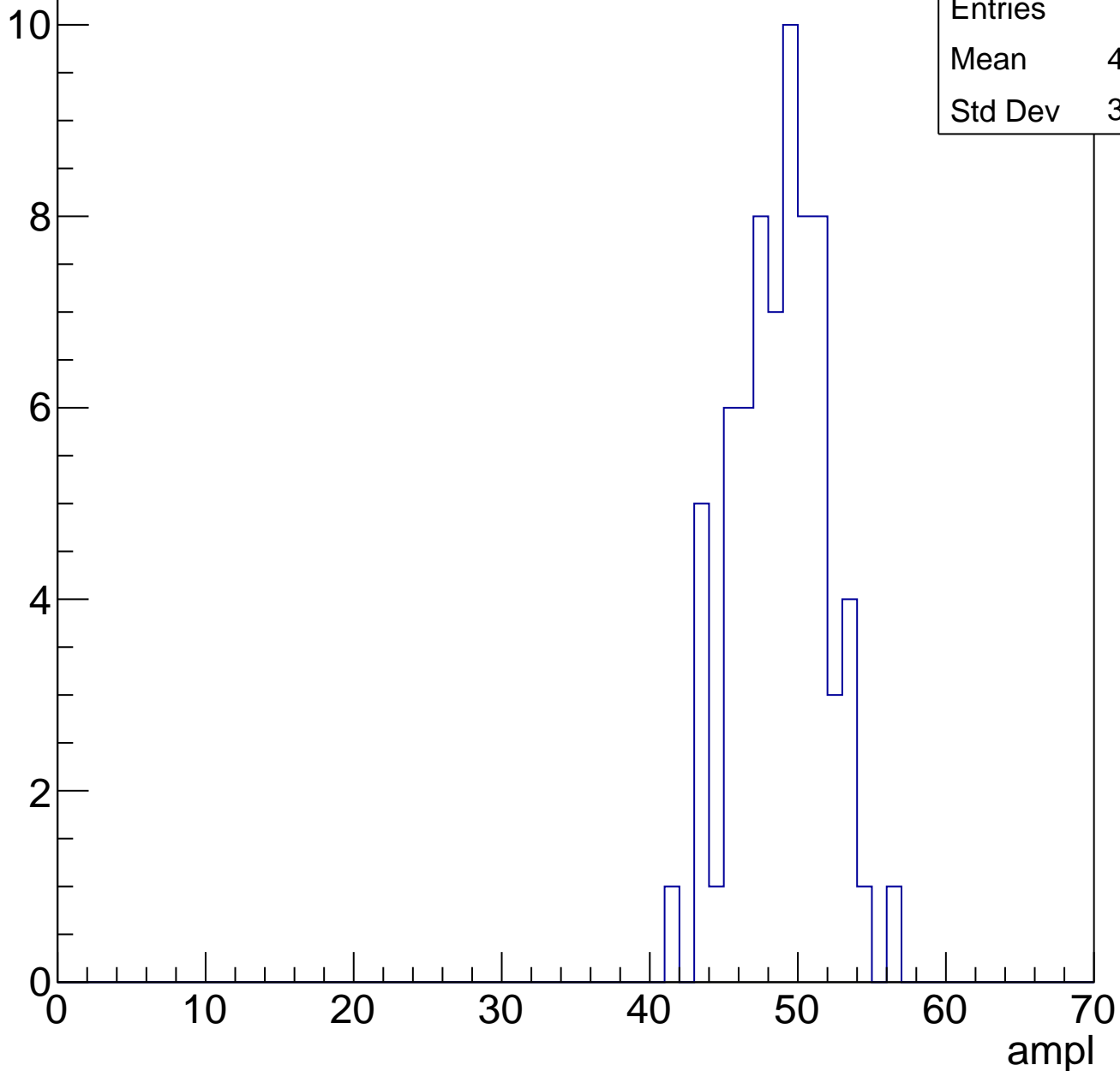


B1L102S, U22-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.32
Std Dev	3.024

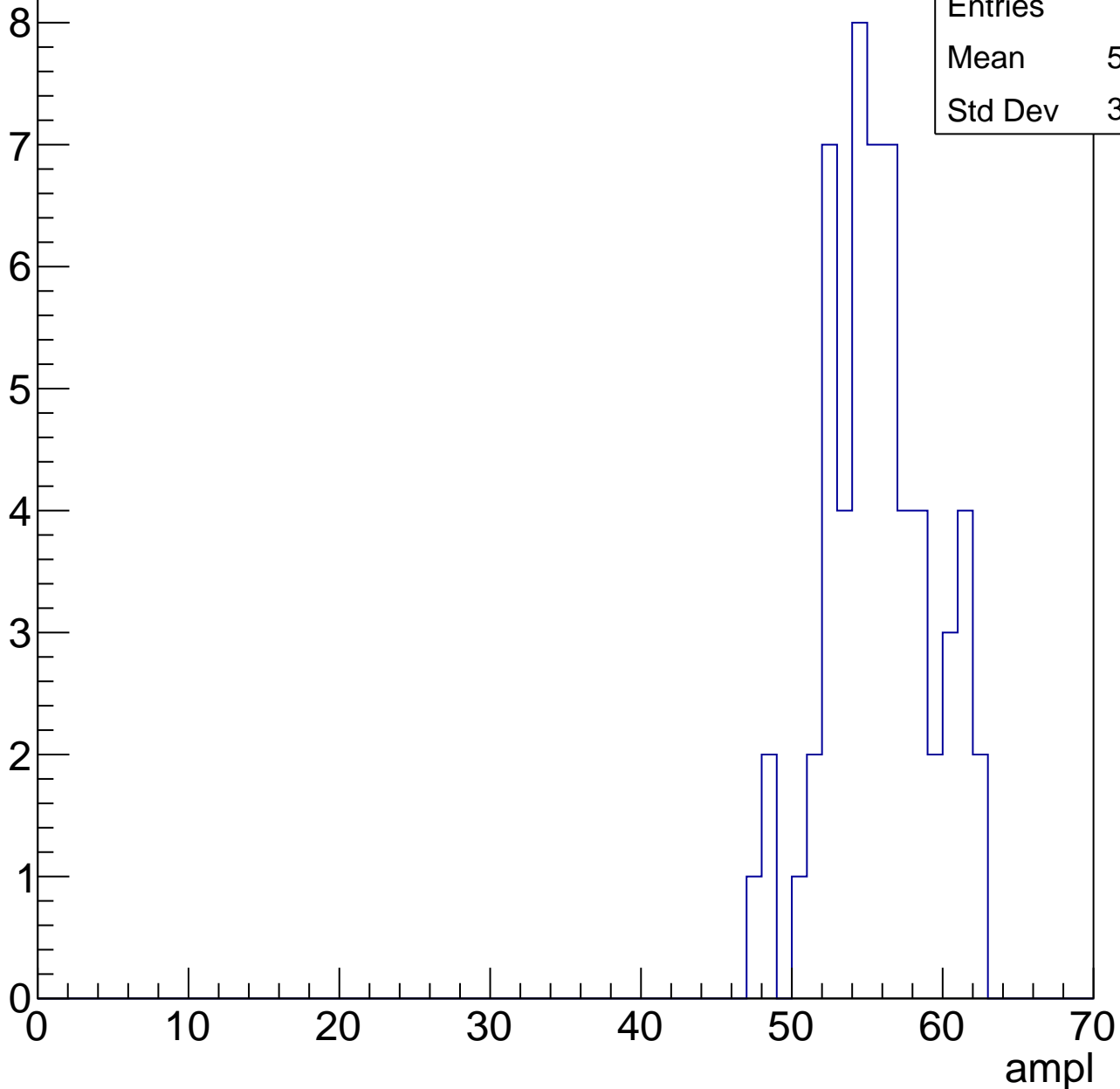


B1L102S, U22-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

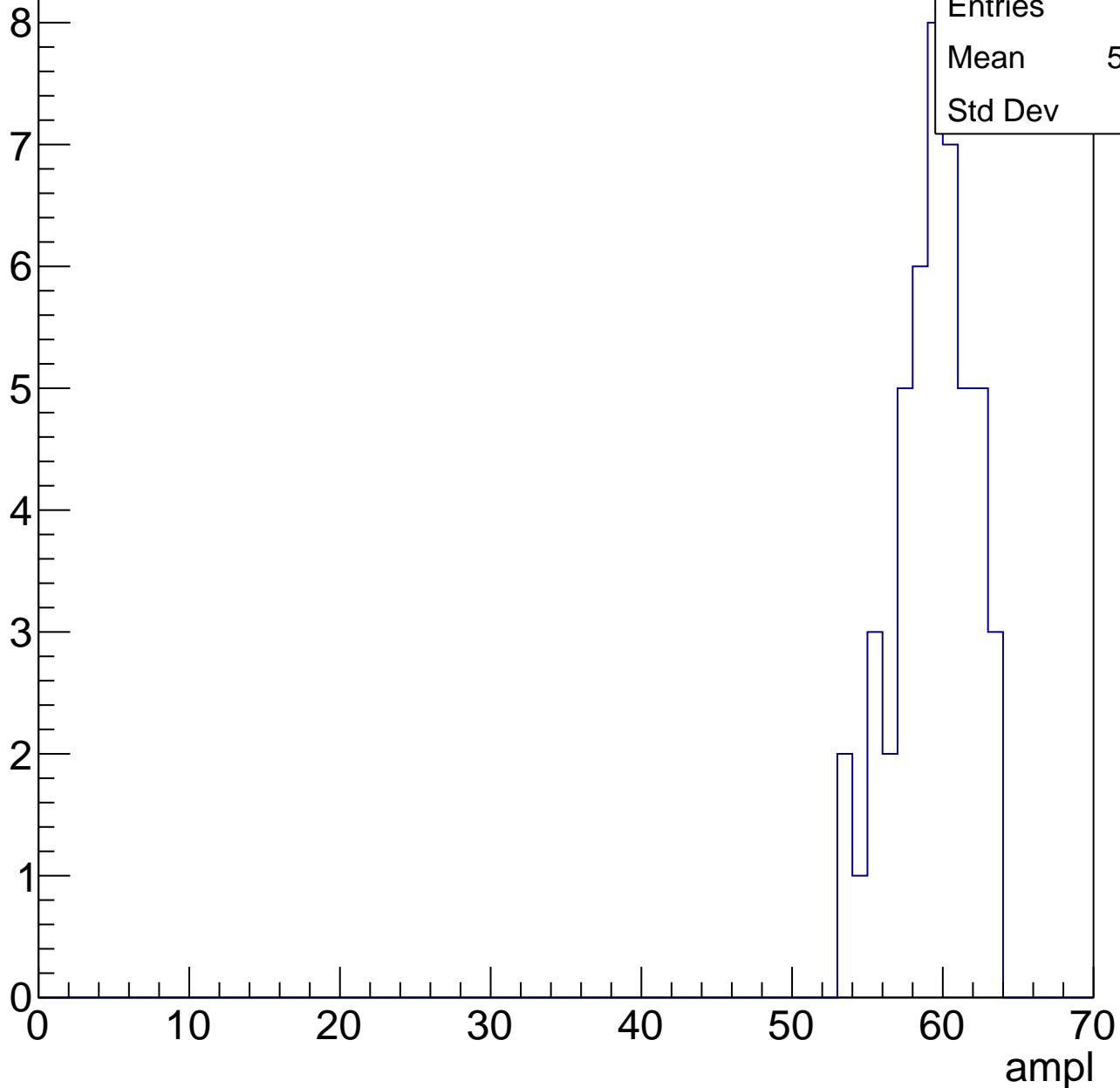
Entries	58
Mean	55.28
Std Dev	3.498



B1L102S, U22-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

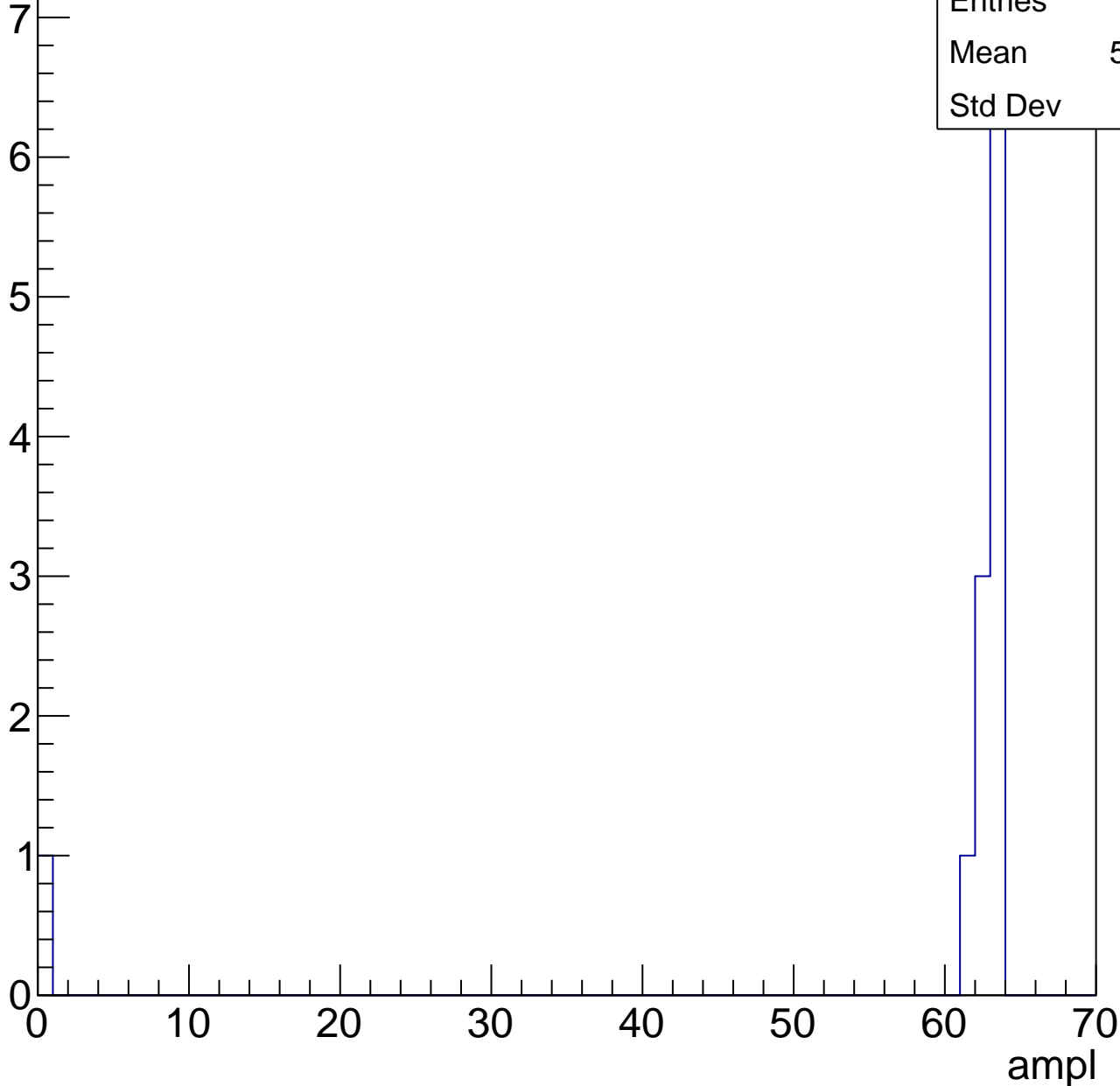


B1L102S, U22-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

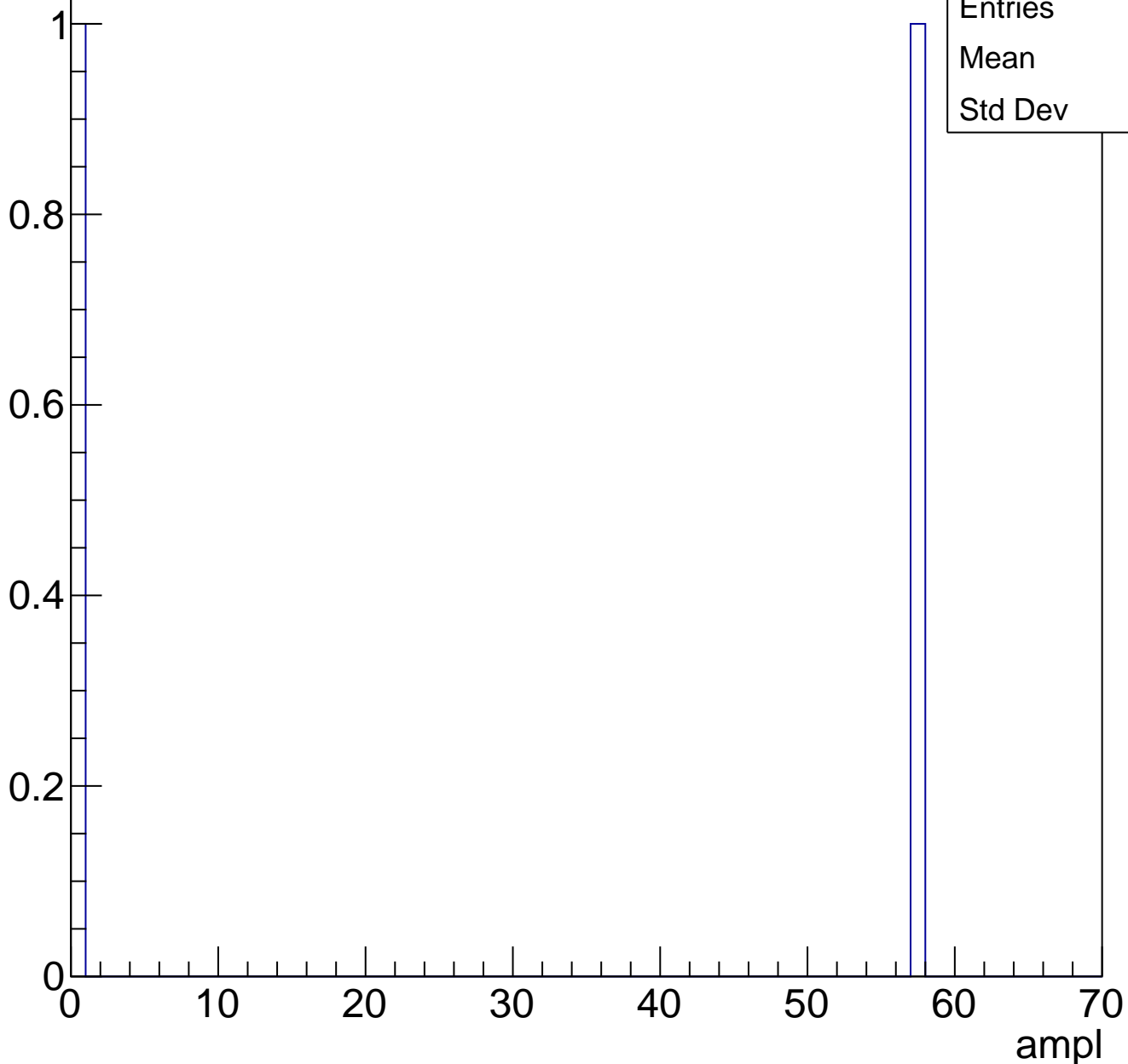
Entries	12
Mean	57.33
Std Dev	17.3



B1L102S, U22-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	28.5
Std Dev	28.5

B1L102S, U22-ch87, adc0

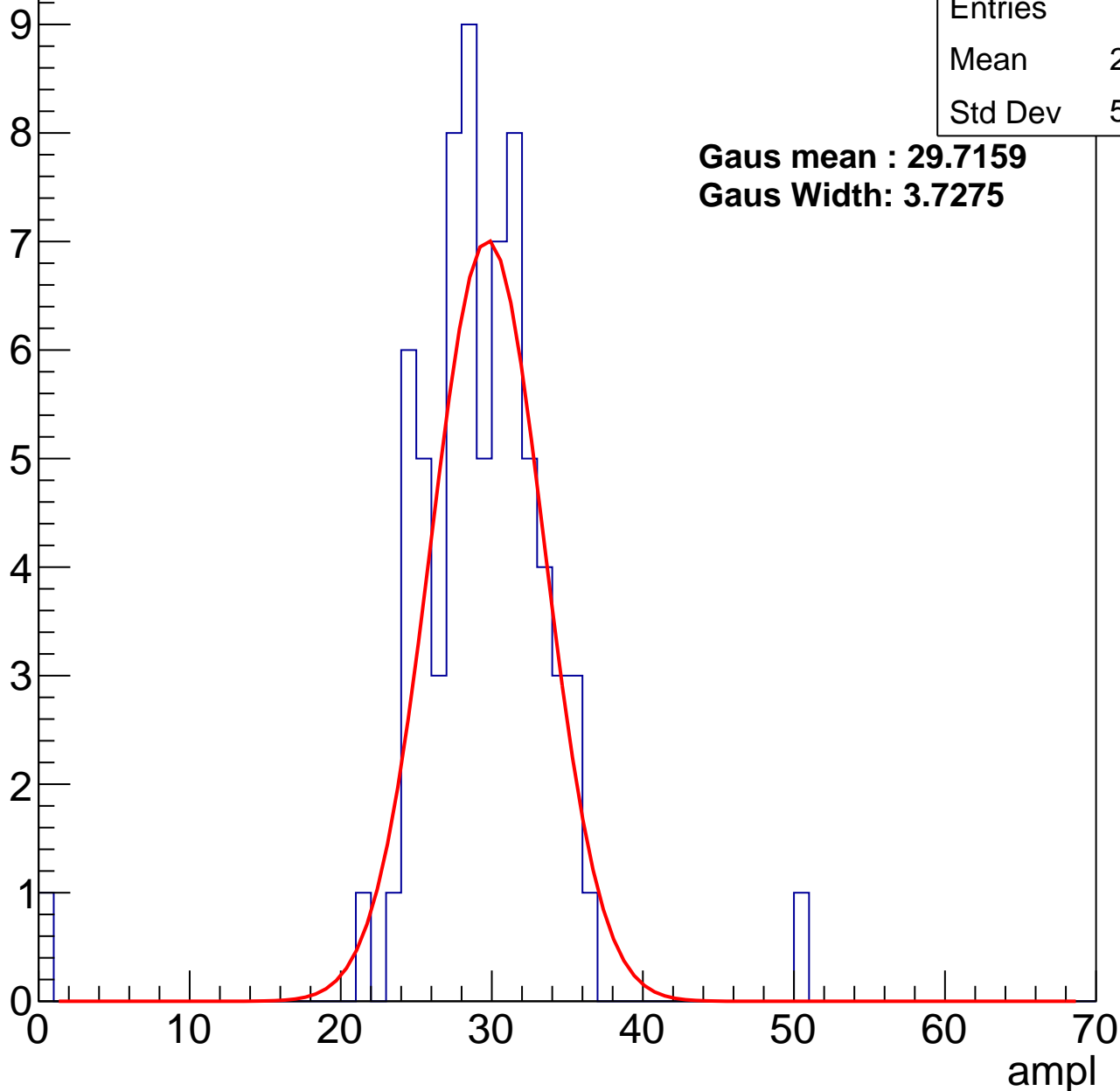
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.83
Std Dev	5.384

Gaus mean : 29.7159

Gaus Width: 3.7275



B1L102S, U22-ch87, adc1

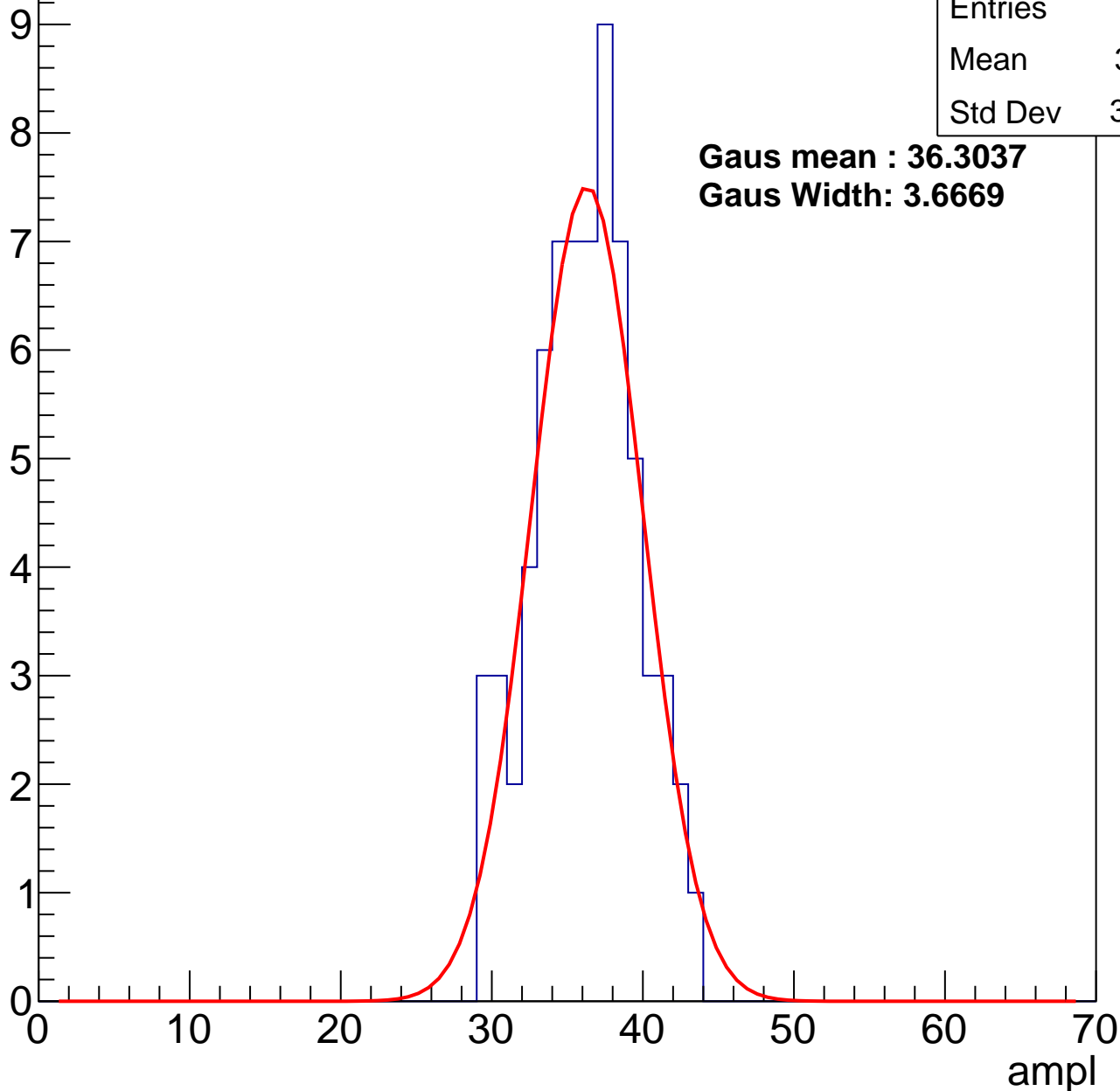
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.71
Std Dev	3.358

Gaus mean : 36.3037

Gaus Width: 3.6669



B1L102S, U22-ch87, adc2

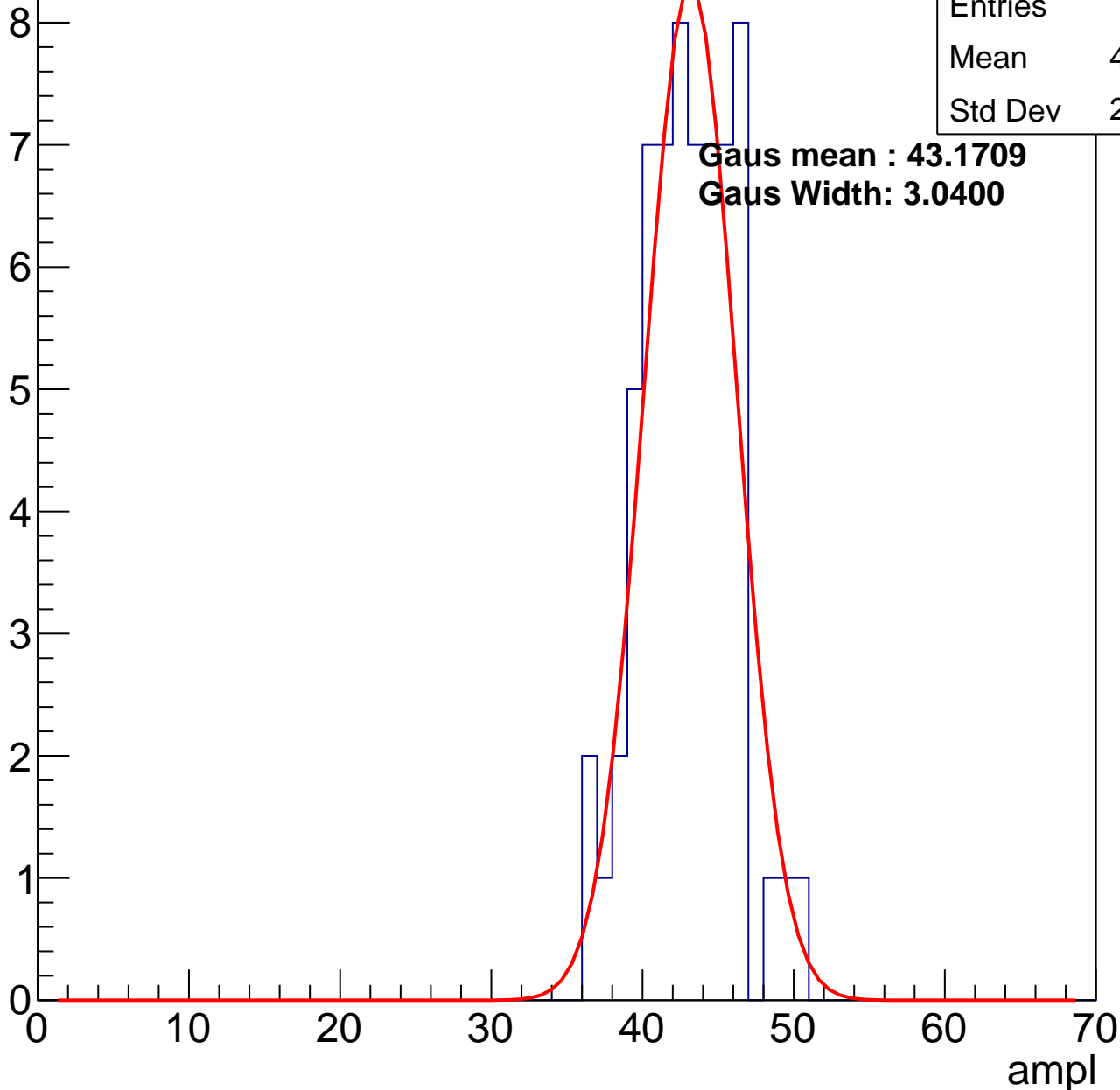
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.53
Std Dev	2.974

Gaus mean : 43.1709

Gaus Width: 3.0400

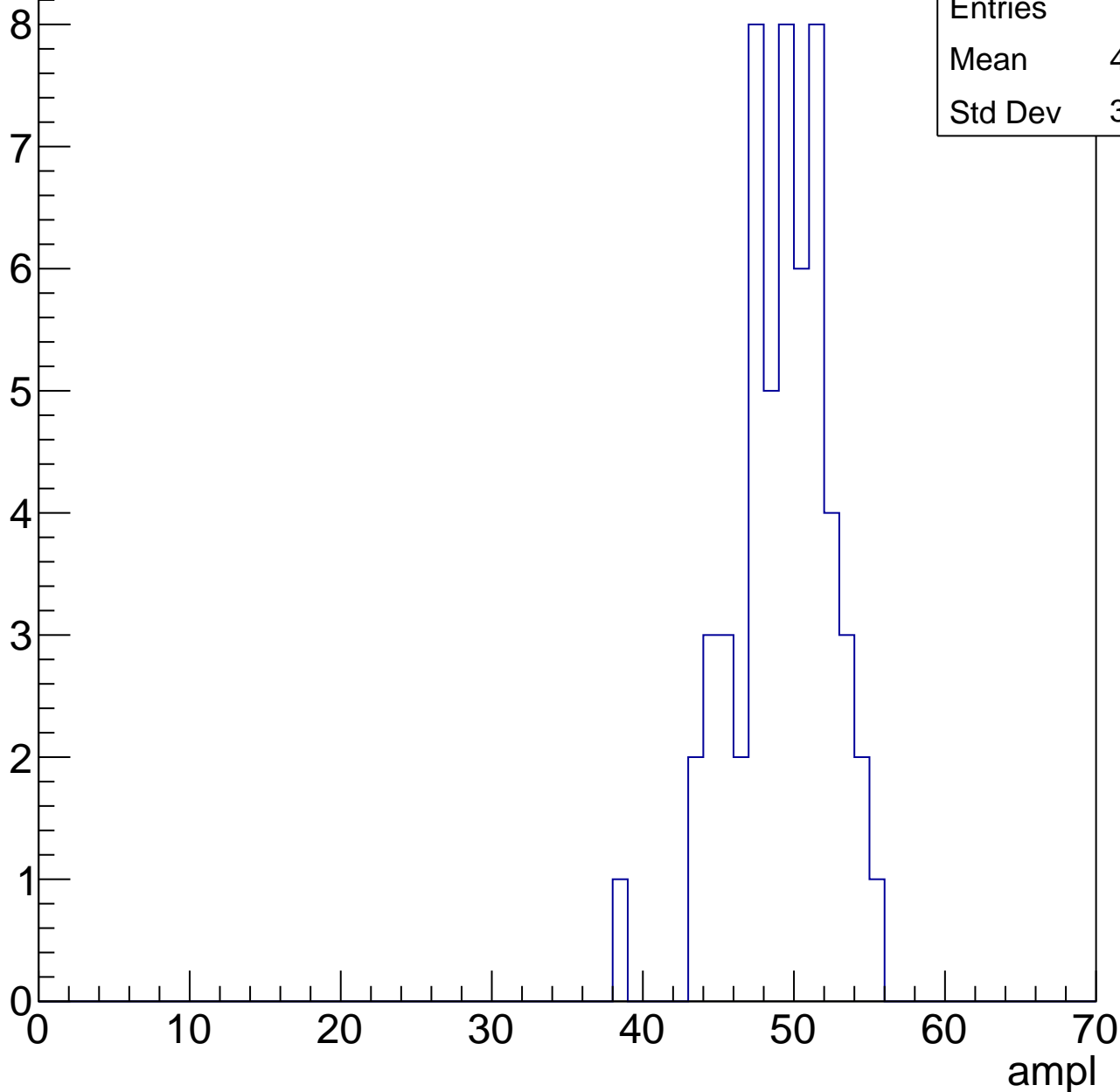


B1L102S, U22-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.73
Std Dev	3.204

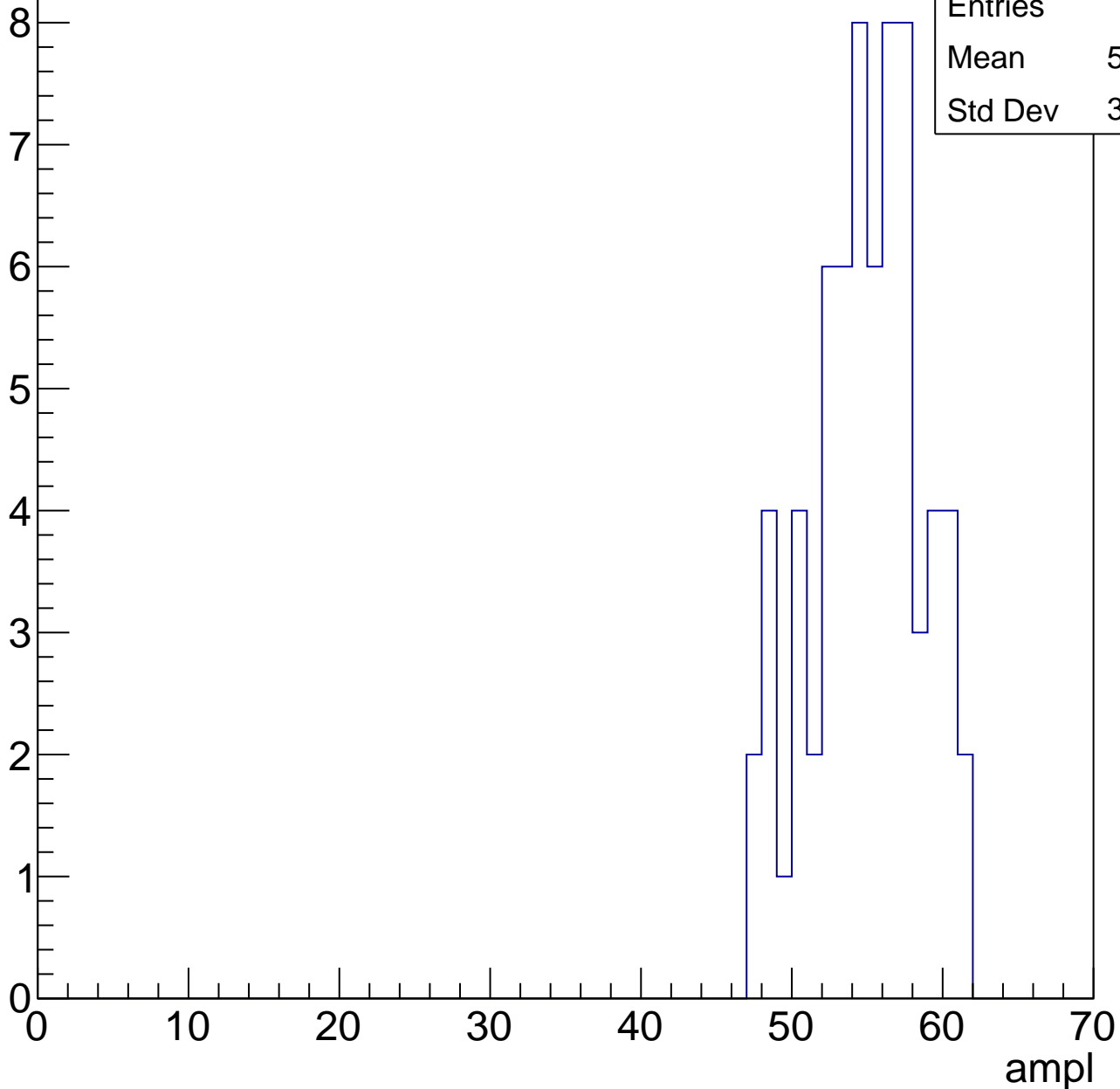


B1L102S, U22-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.49
Std Dev	3.562

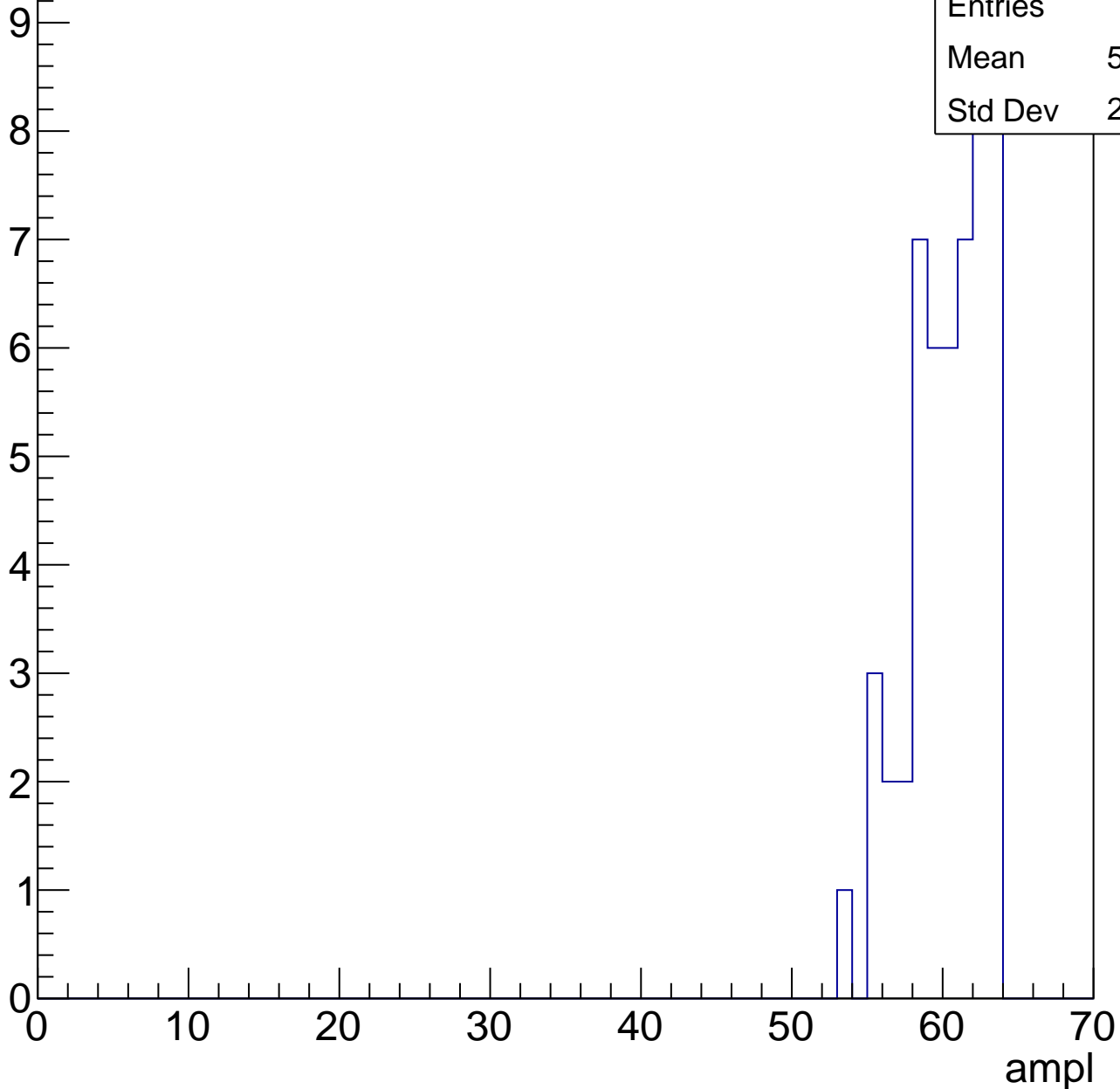


B1L102S, U22-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.86
Std Dev	2.513



B1L102S, U22-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L102S, U22-ch88, adc0

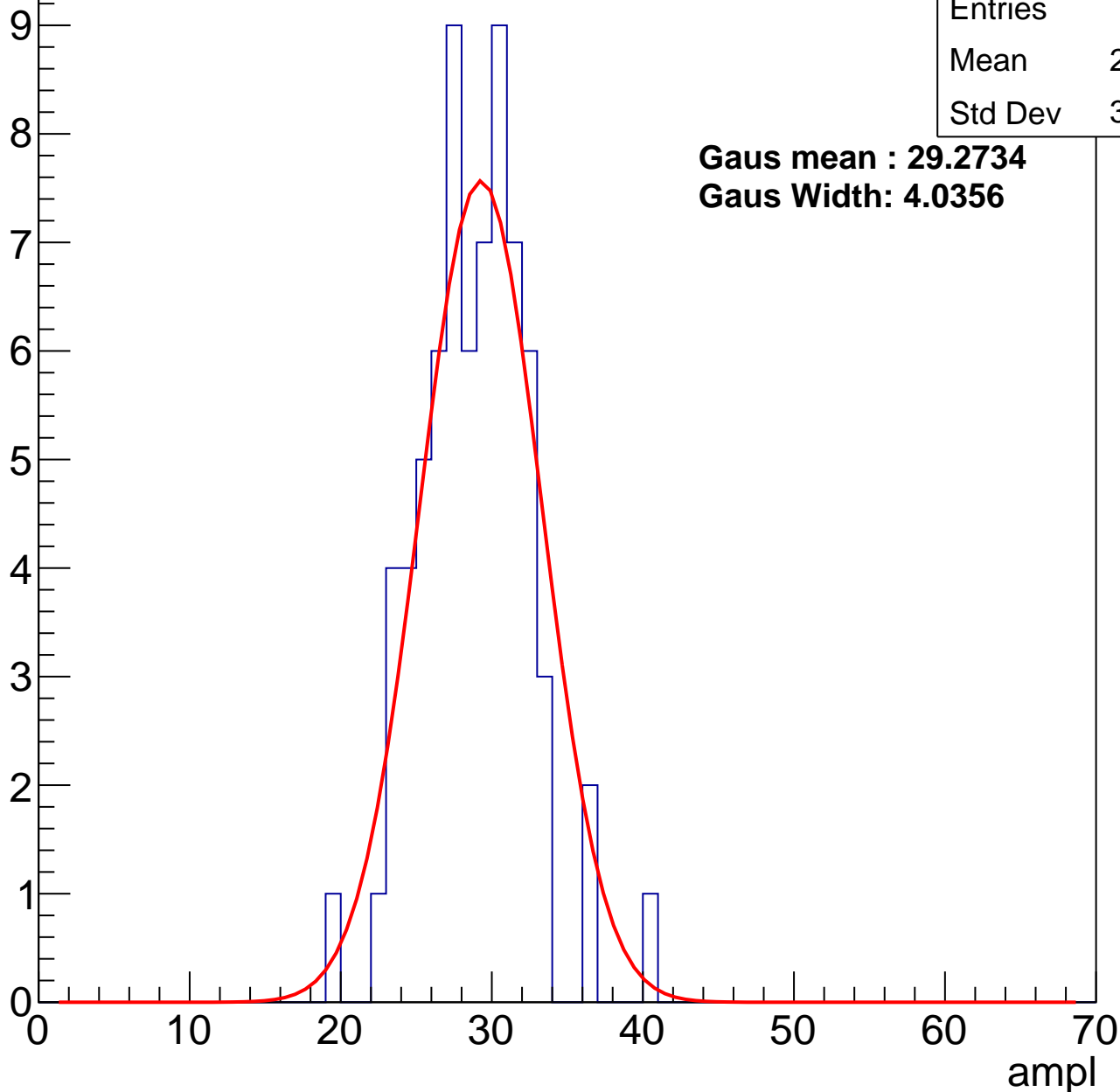
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.37
Std Dev	3.557

Gaus mean : 29.2734

Gaus Width: 4.0356



B1L102S, U22-ch88, adc1

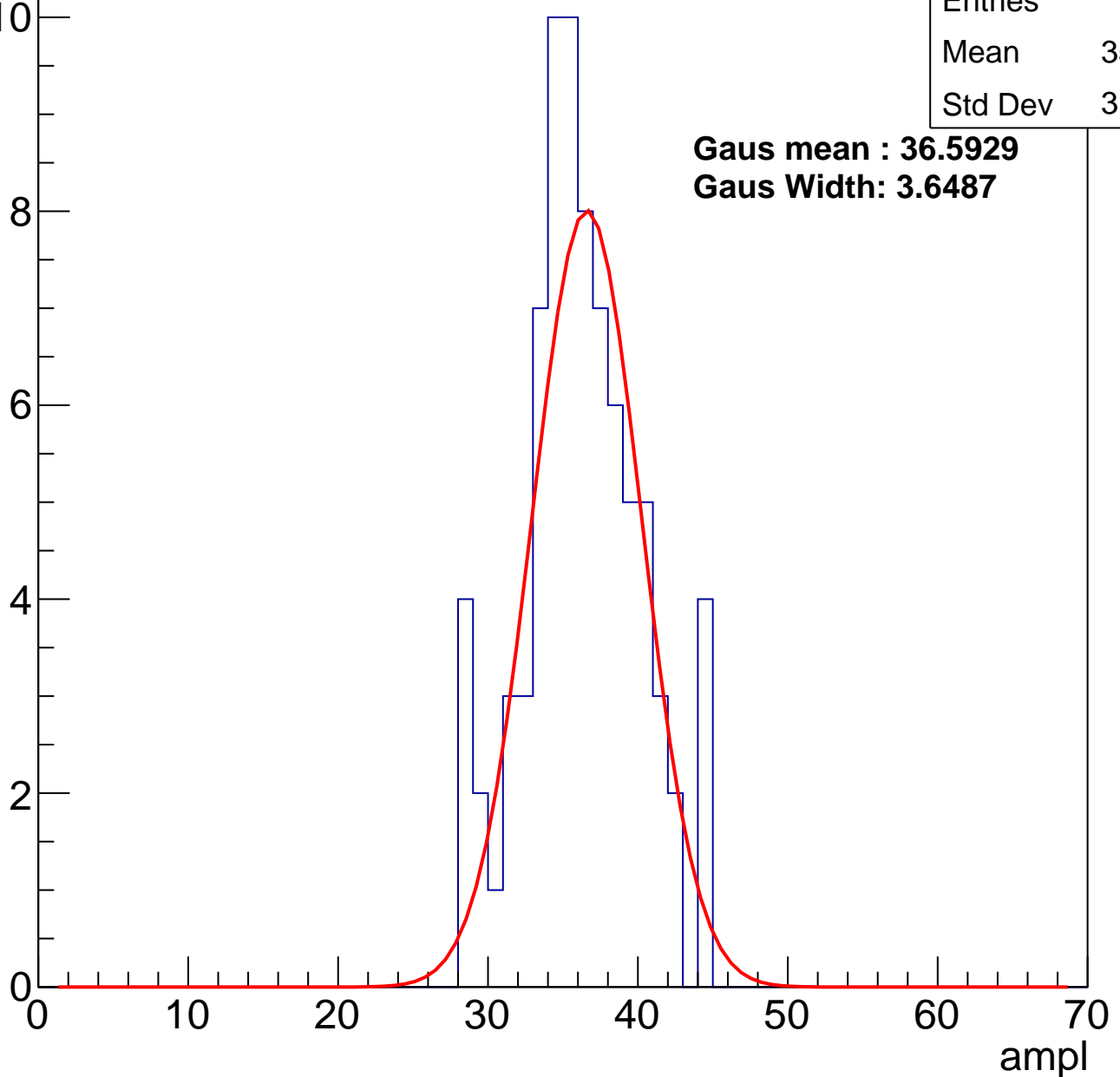
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.79
Std Dev	3.843

Gaus mean : 36.5929

Gaus Width: 3.6487



B1L102S, U22-ch88, adc2

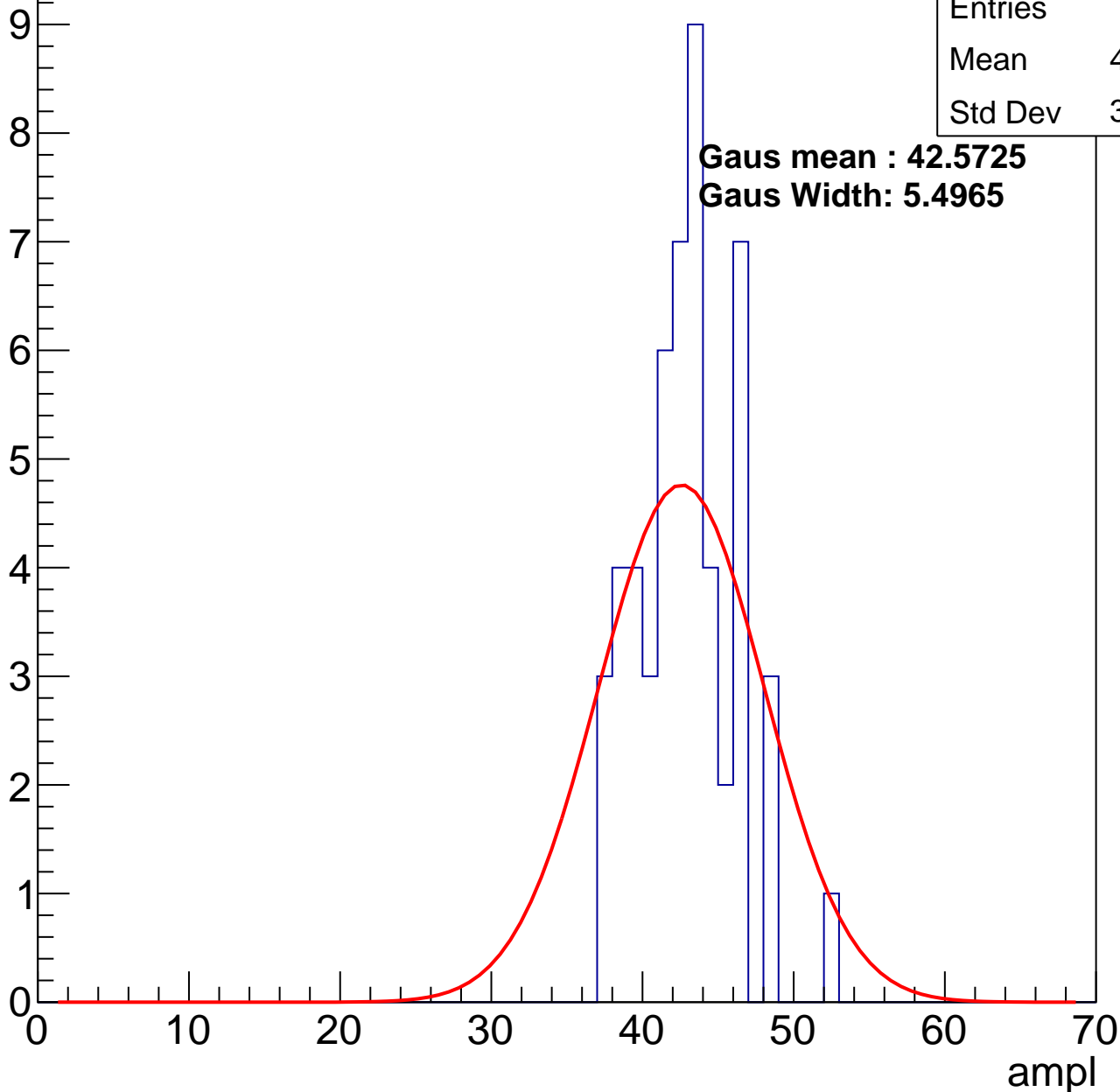
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.45
Std Dev	3.207

Gaus mean : 42.5725

Gaus Width: 5.4965



B1L102S, U22-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	48.58
Std Dev	3.55

Entry

10

8

6

4

2

0

0

10

20

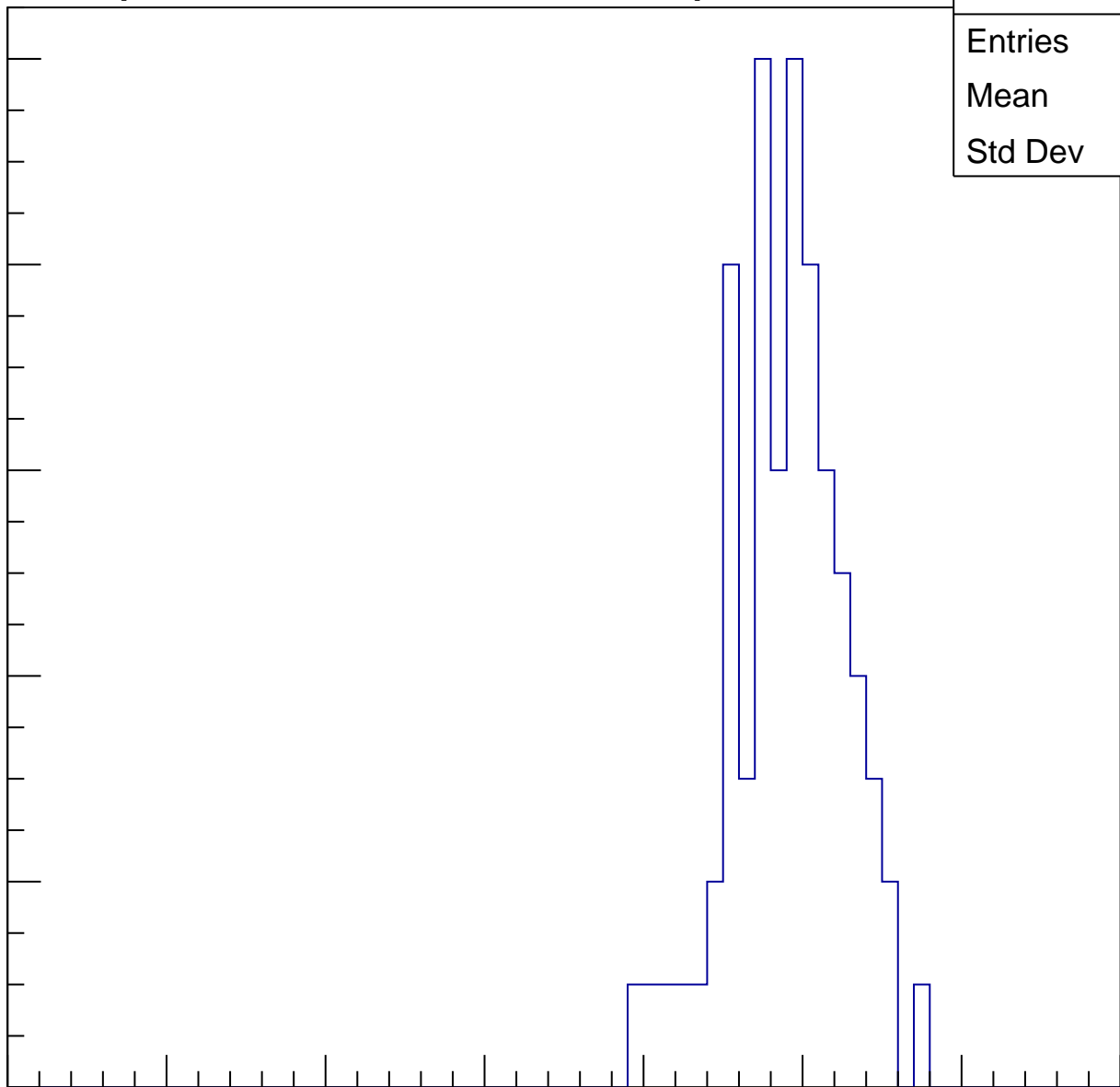
30

40

50

60

ampl

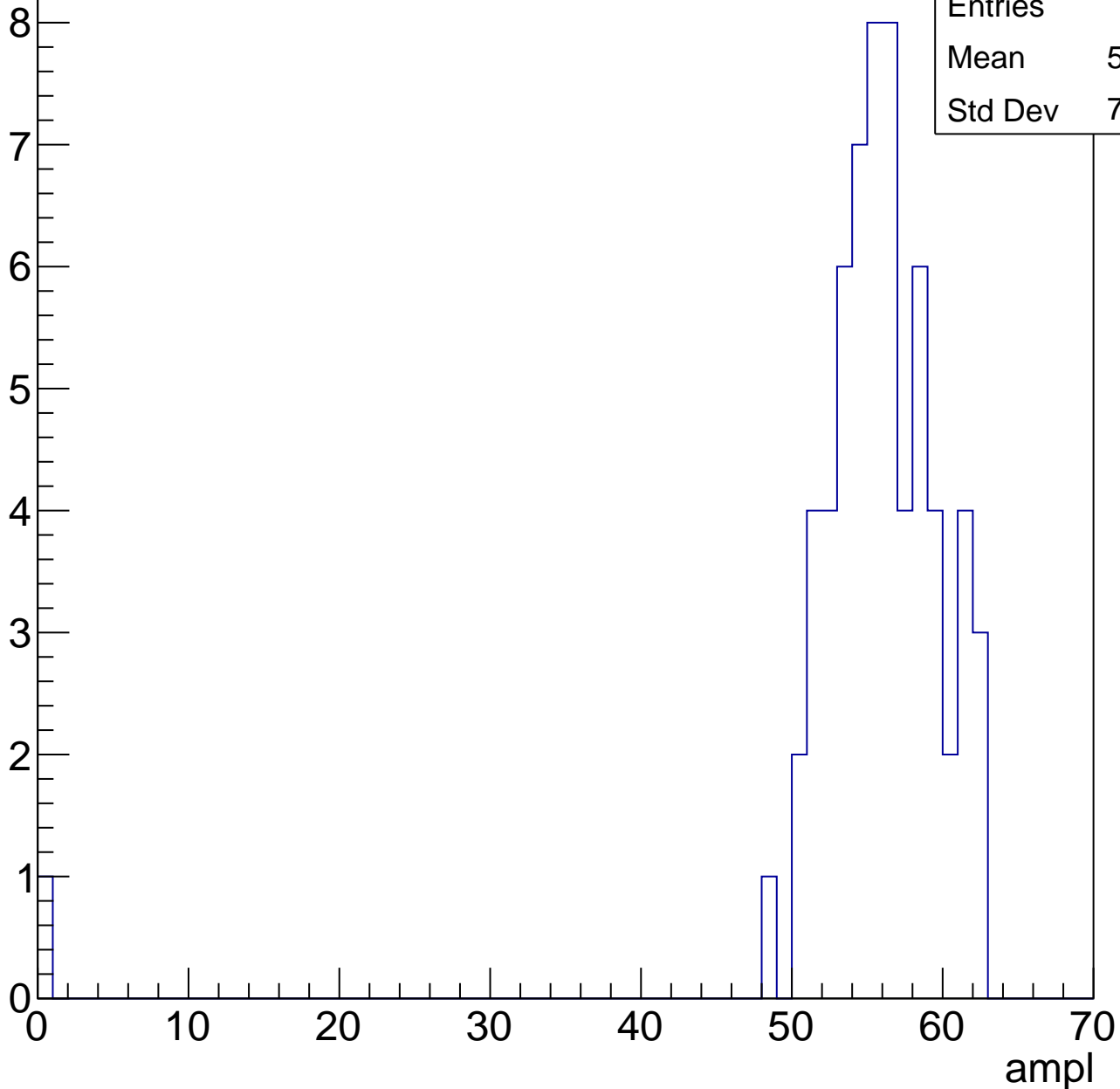


B1L102S, U22-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.78
Std Dev	7.639



B1L102S, U22-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

40

Mean

59.92

Std Dev

2.126

ampl

0

10

20

30

40

50

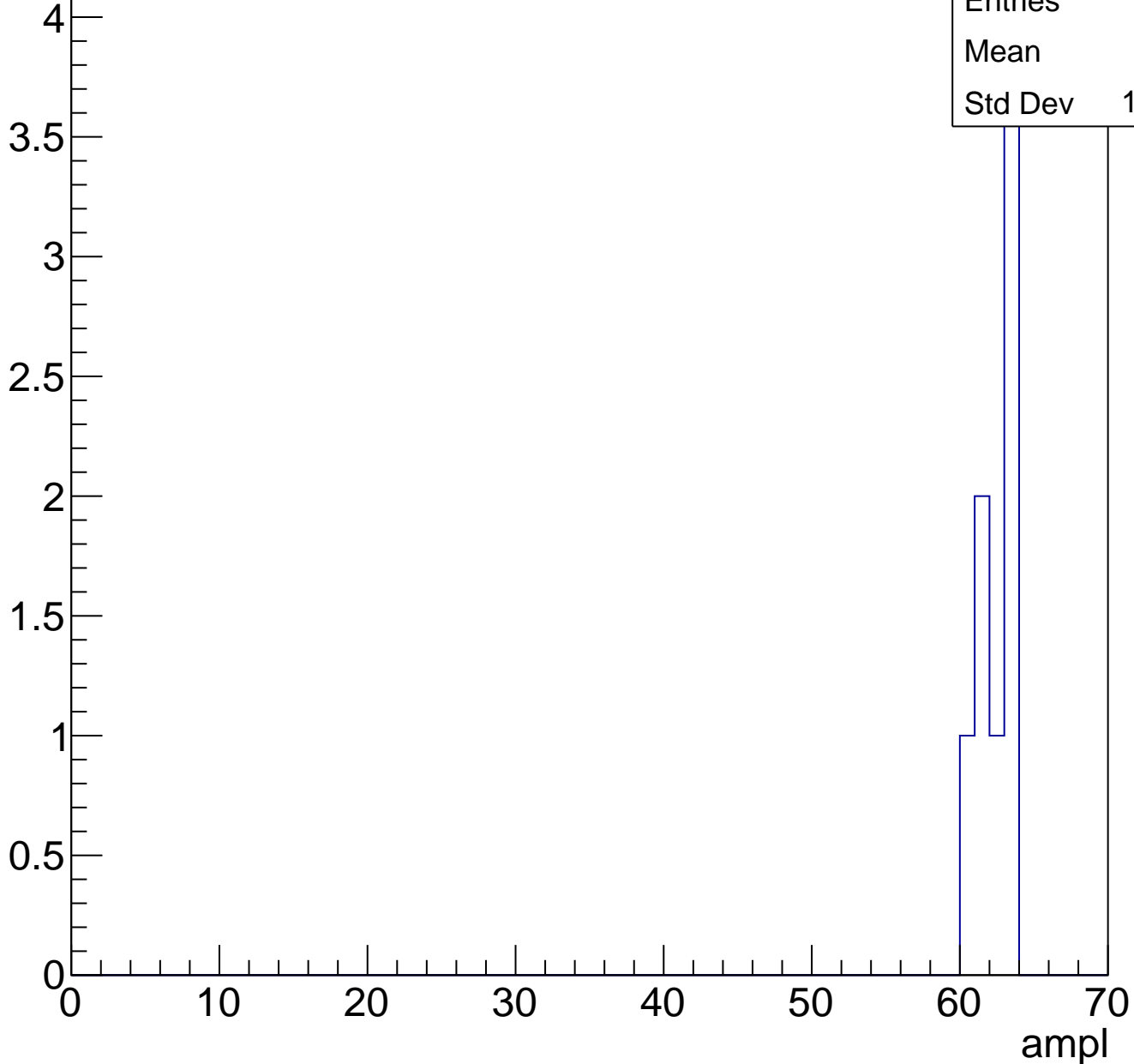
60

70

B1L102S, U22-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch89, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	29.58
Std Dev	3.631

Gaus mean : 30.0734

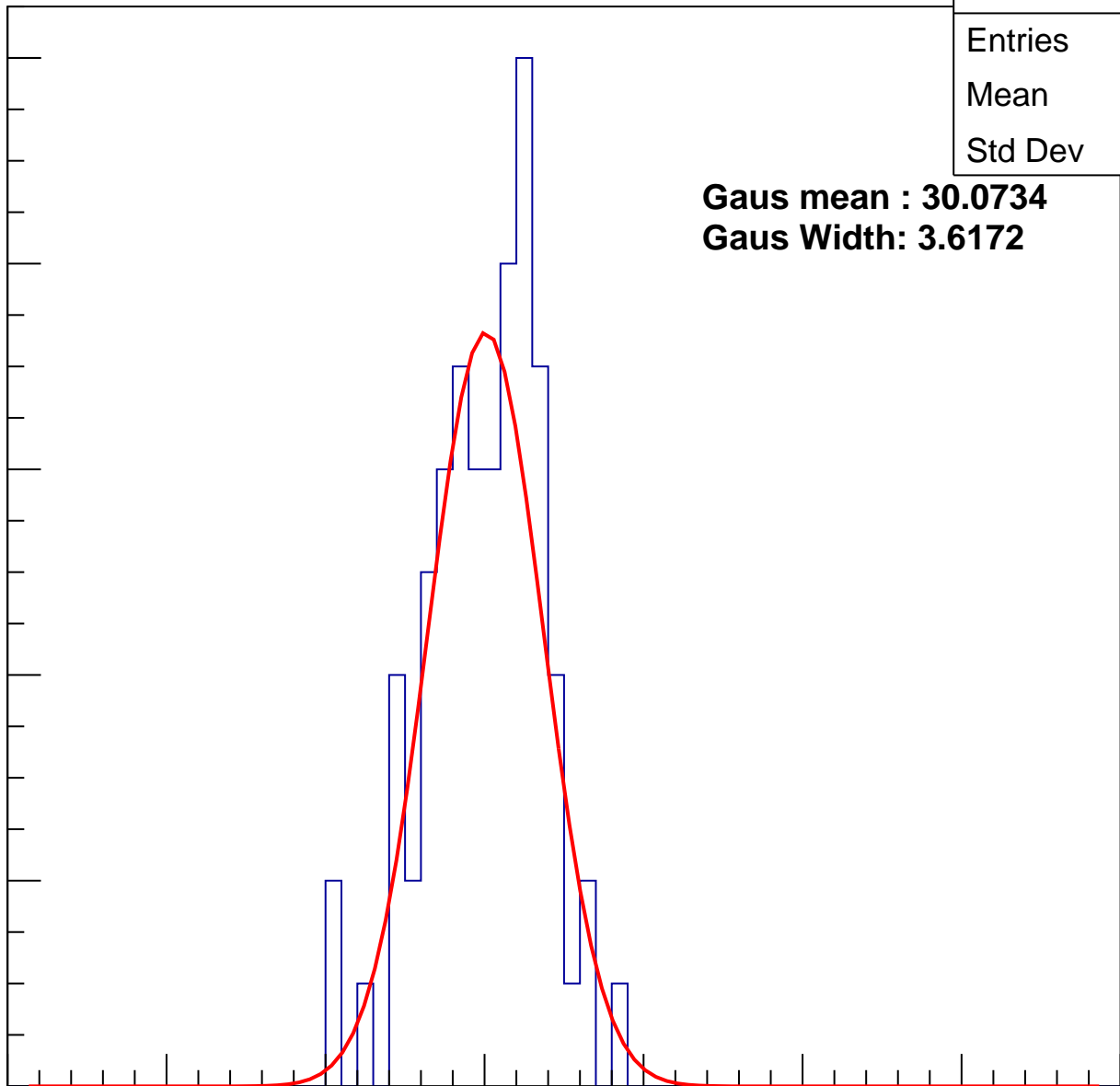
Gaus Width: 3.6172

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch89, adc1

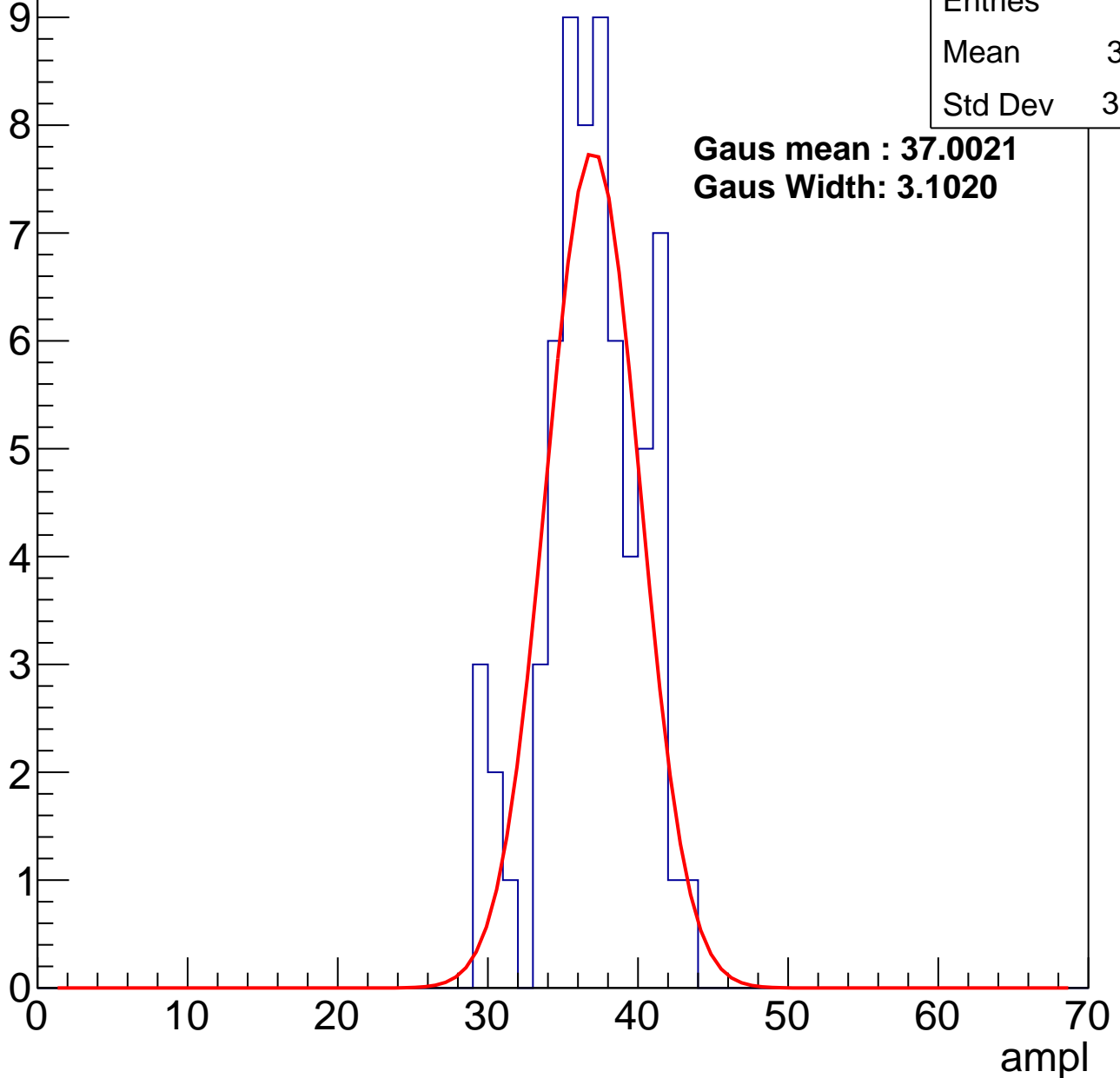
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.51
Std Dev	3.273

Gaus mean : 37.0021

Gaus Width: 3.1020



B1L102S, U22-ch89, adc2

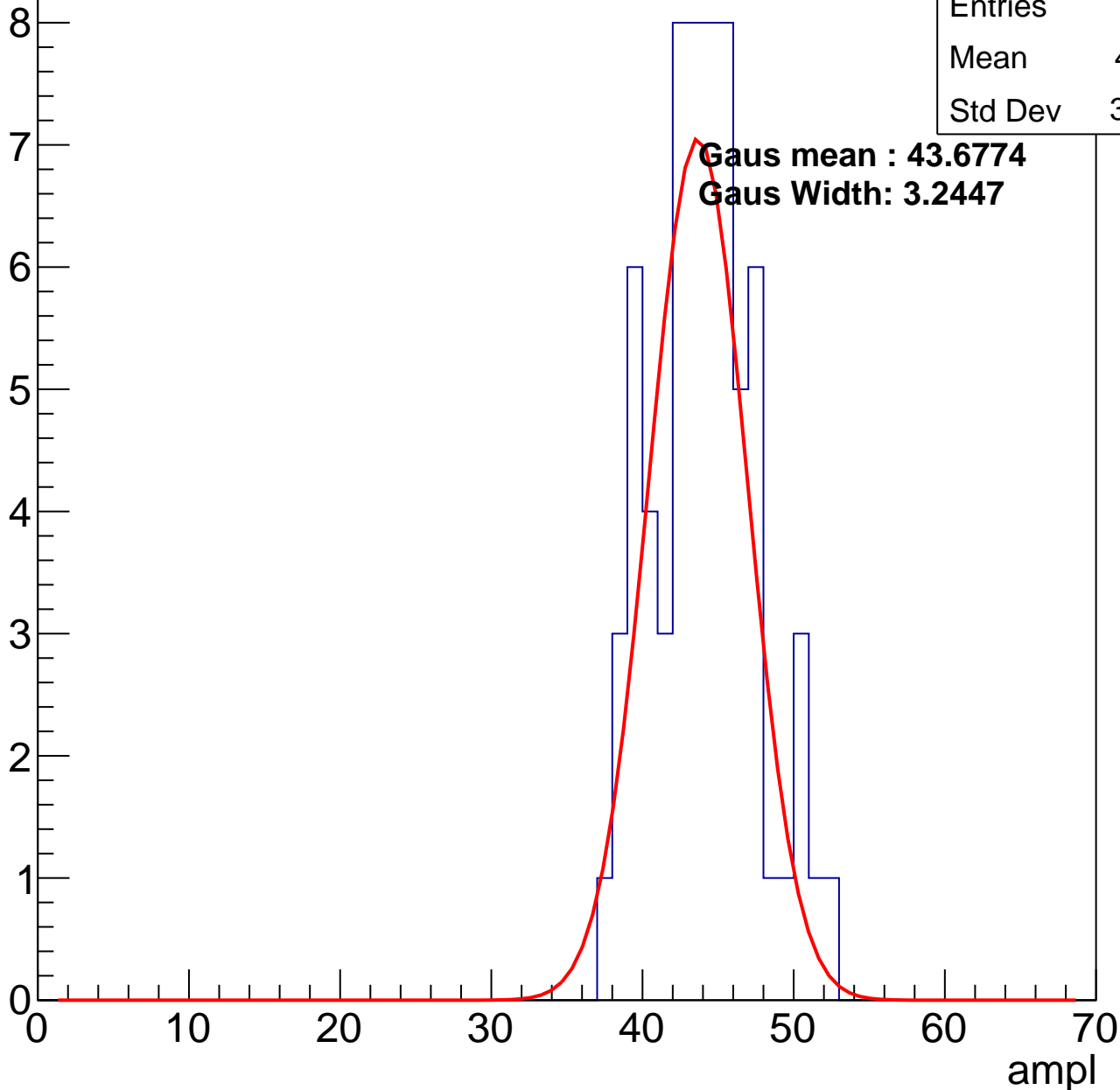
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	43.61
Std Dev	3.394

Gaus mean : 43.6774

Gaus Width: 3.2447

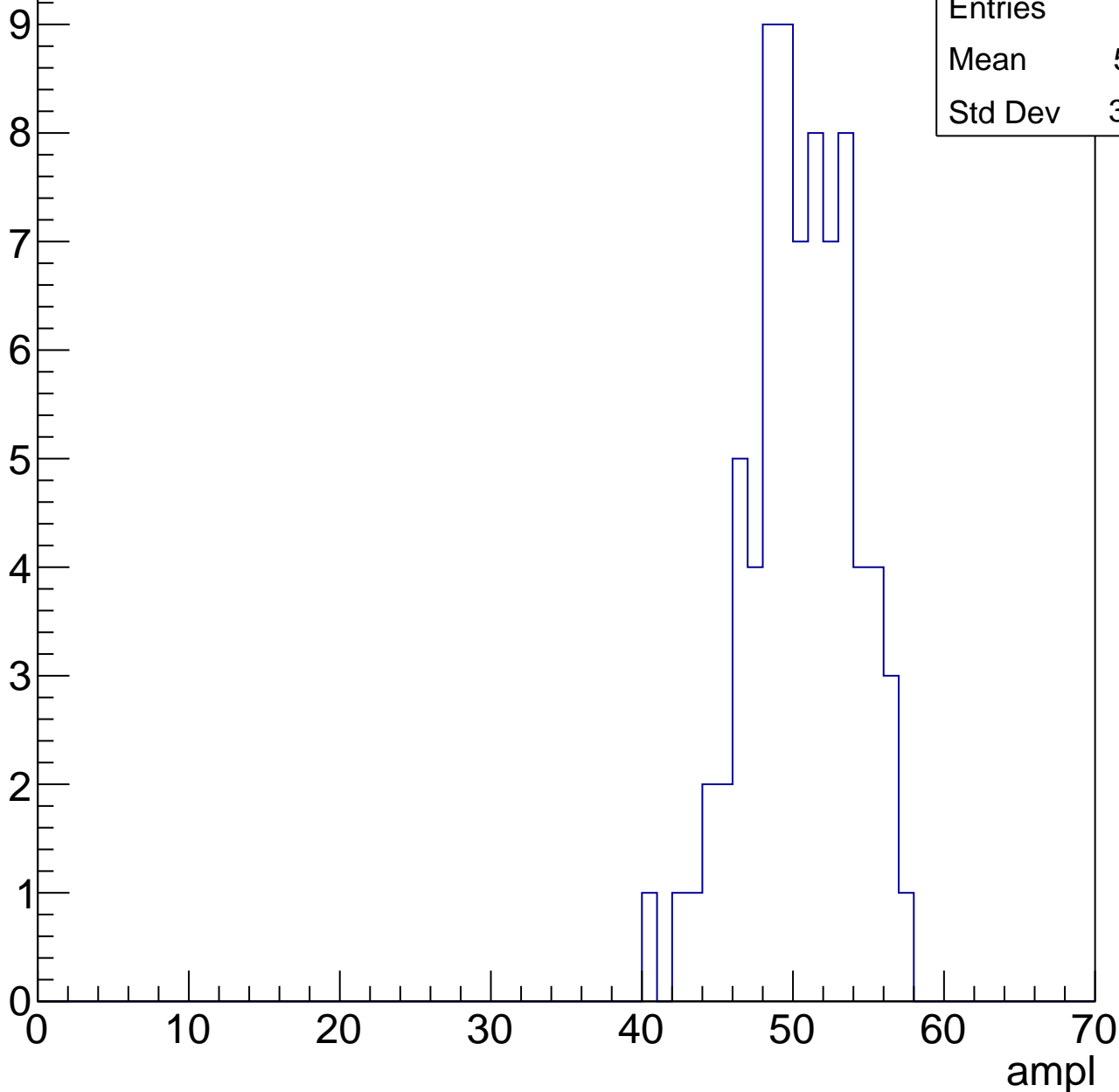


B1L102S, U22-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	50.01
Std Dev	3.489

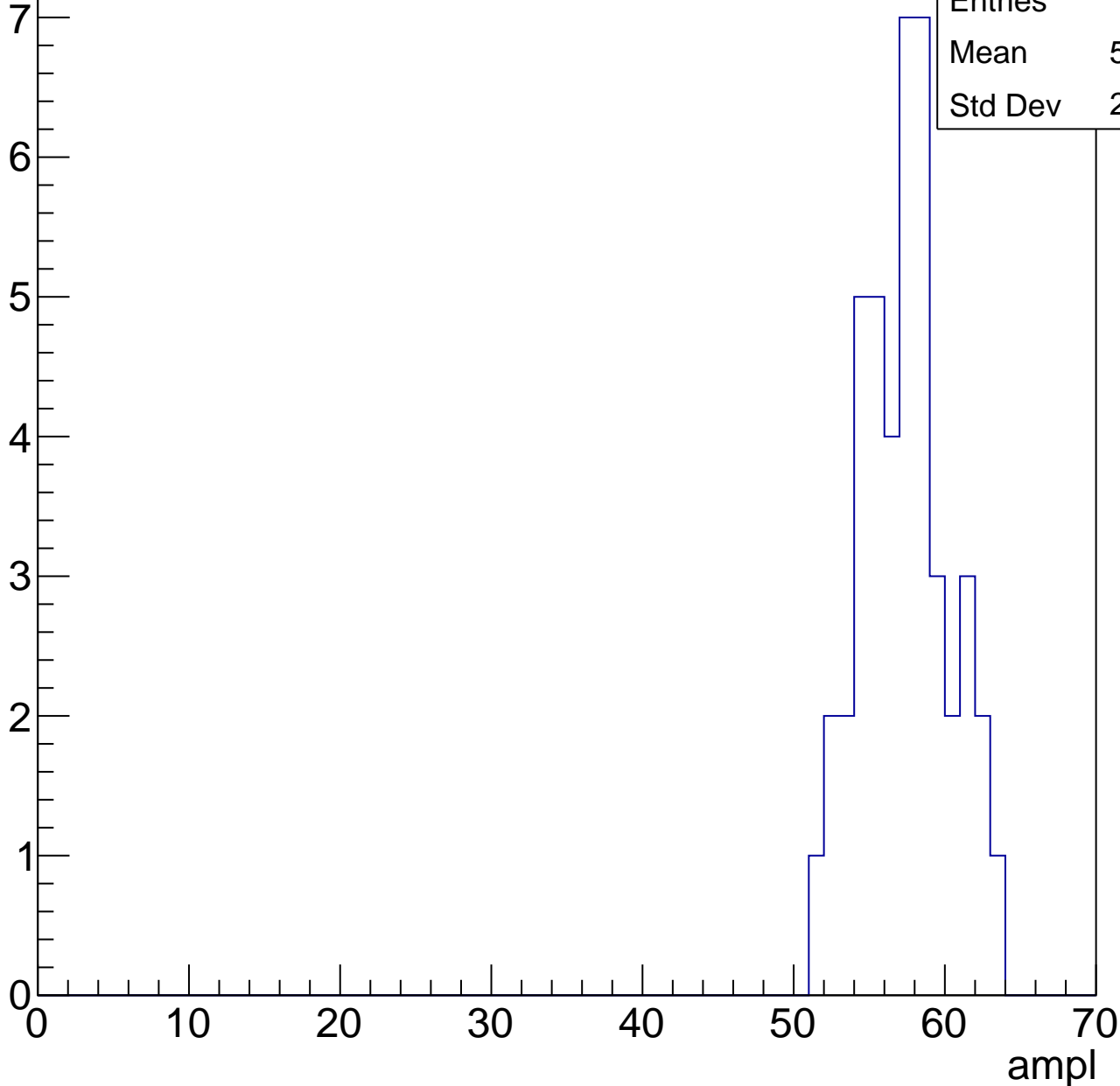


B1L102S, U22-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

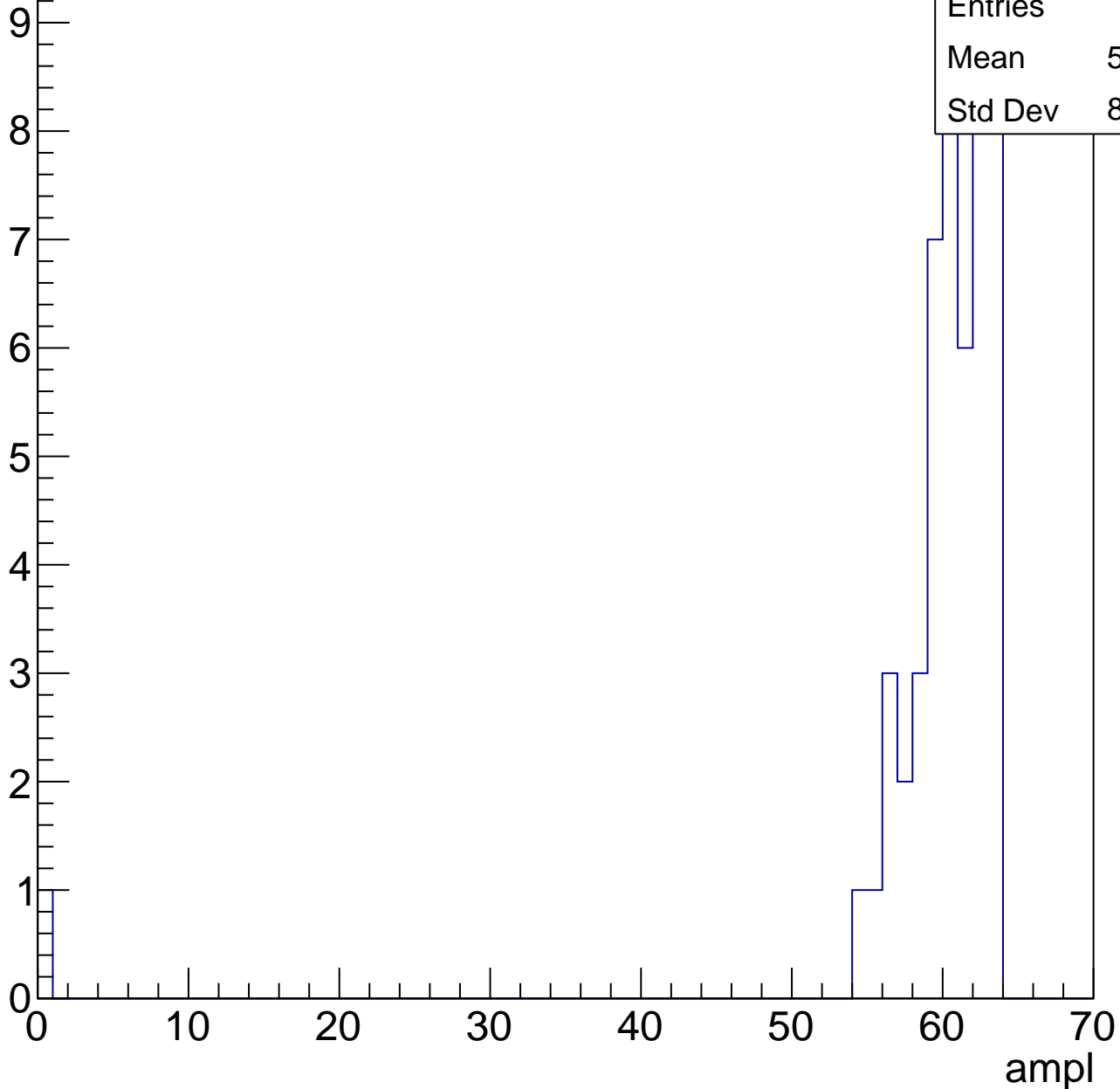
Entries	44
Mean	56.86
Std Dev	2.849



B1L102S, U22-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch90, adc0

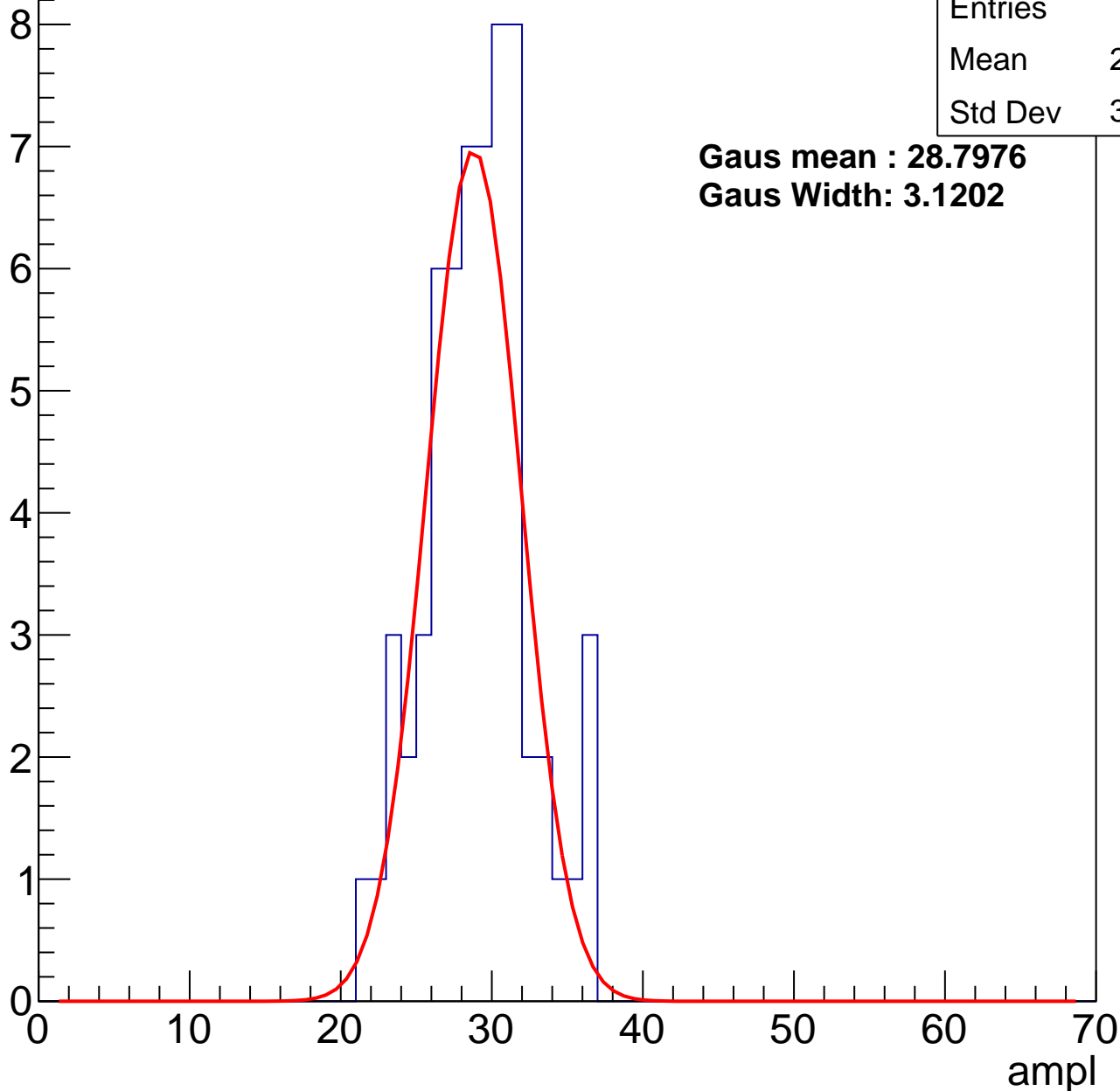
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.64
Std Dev	3.373

Gaus mean : 28.7976

Gaus Width: 3.1202



B1L102S, U22-ch90, adc1

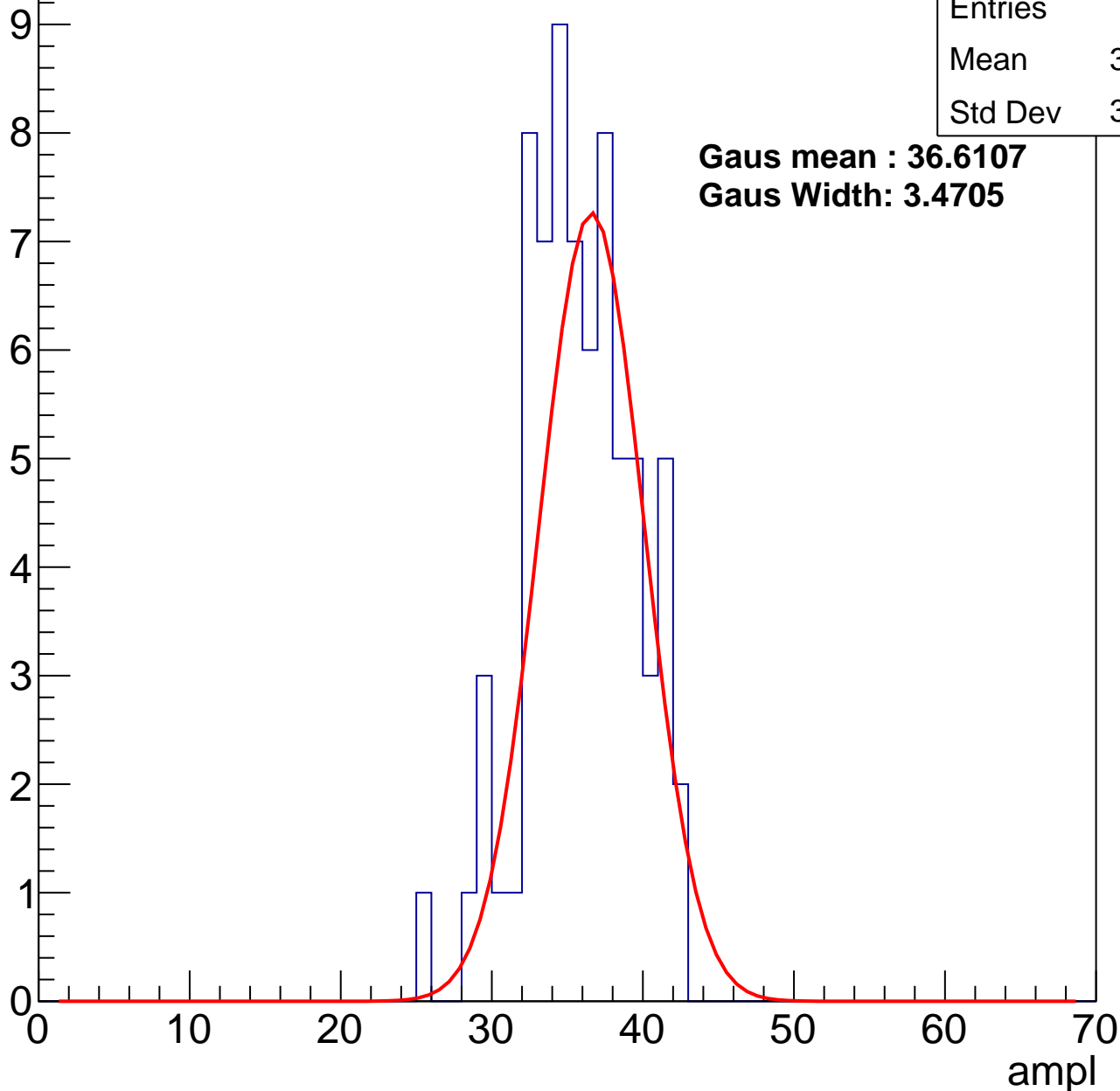
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.35
Std Dev	3.564

Gaus mean : 36.6107

Gaus Width: 3.4705



B1L102S, U22-ch90, adc2

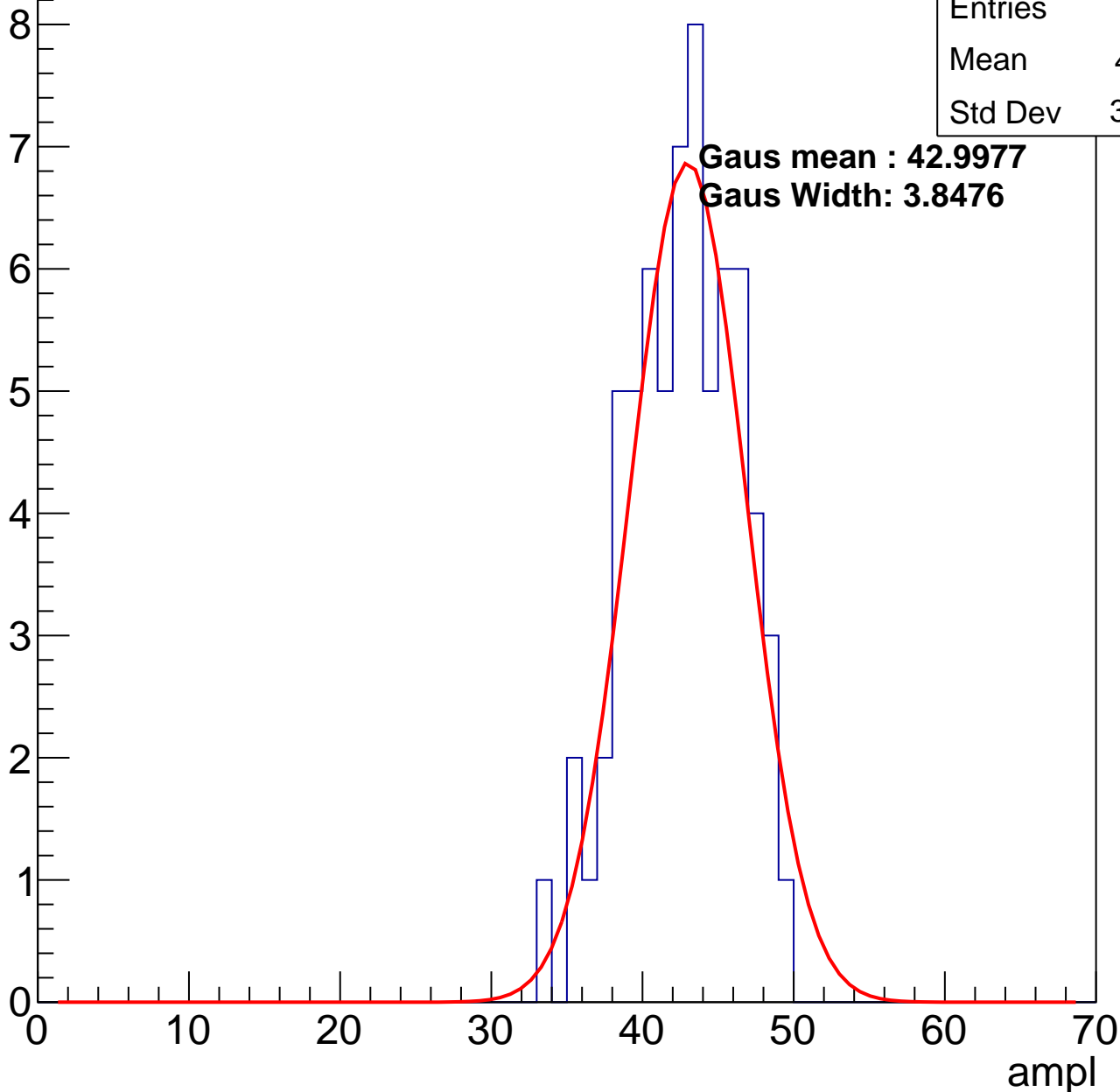
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.21
Std Dev	3.564

Gaus mean : 42.9977

Gaus Width: 3.8476

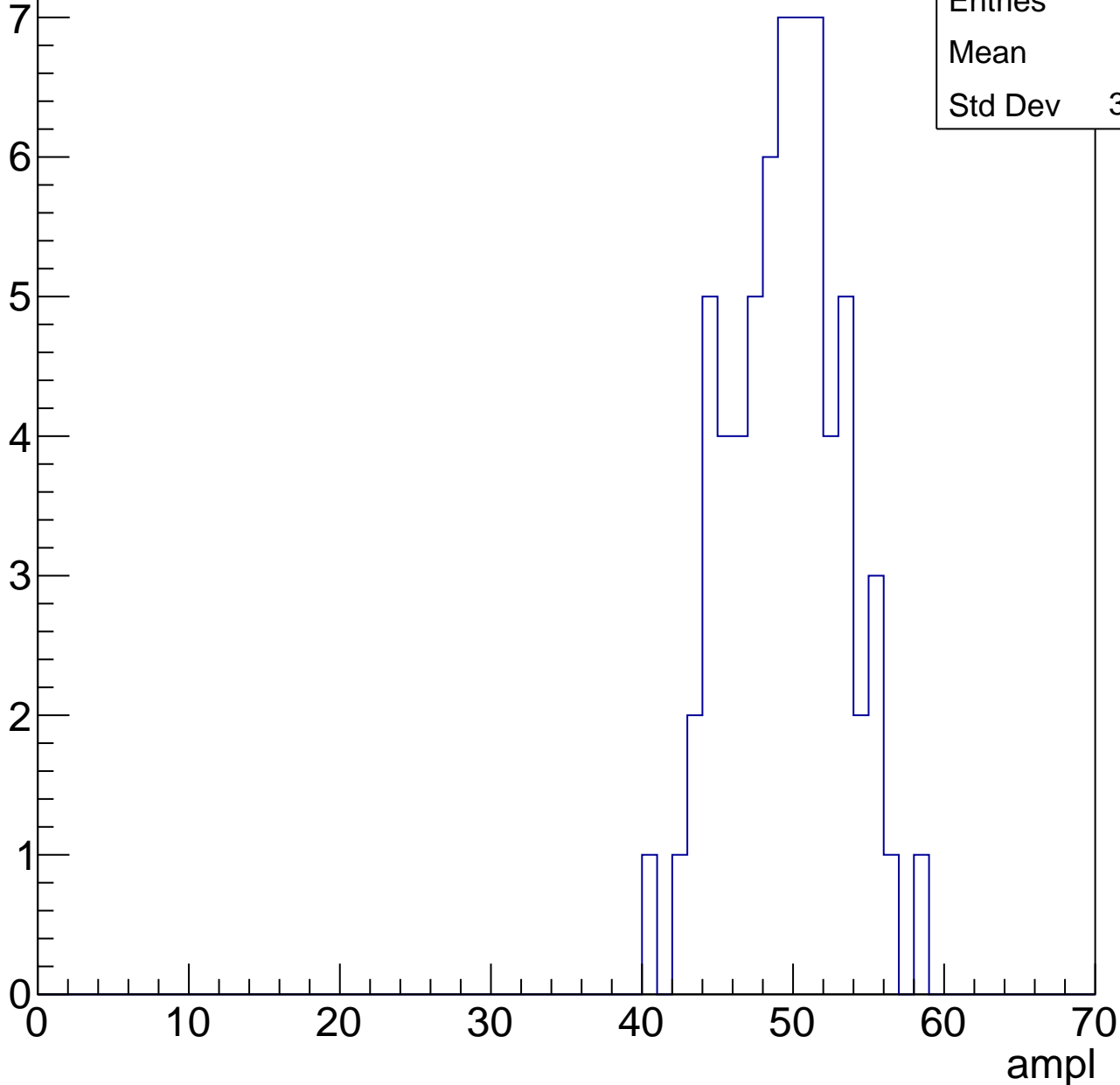


B1L102S, U22-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49
Std Dev	3.704

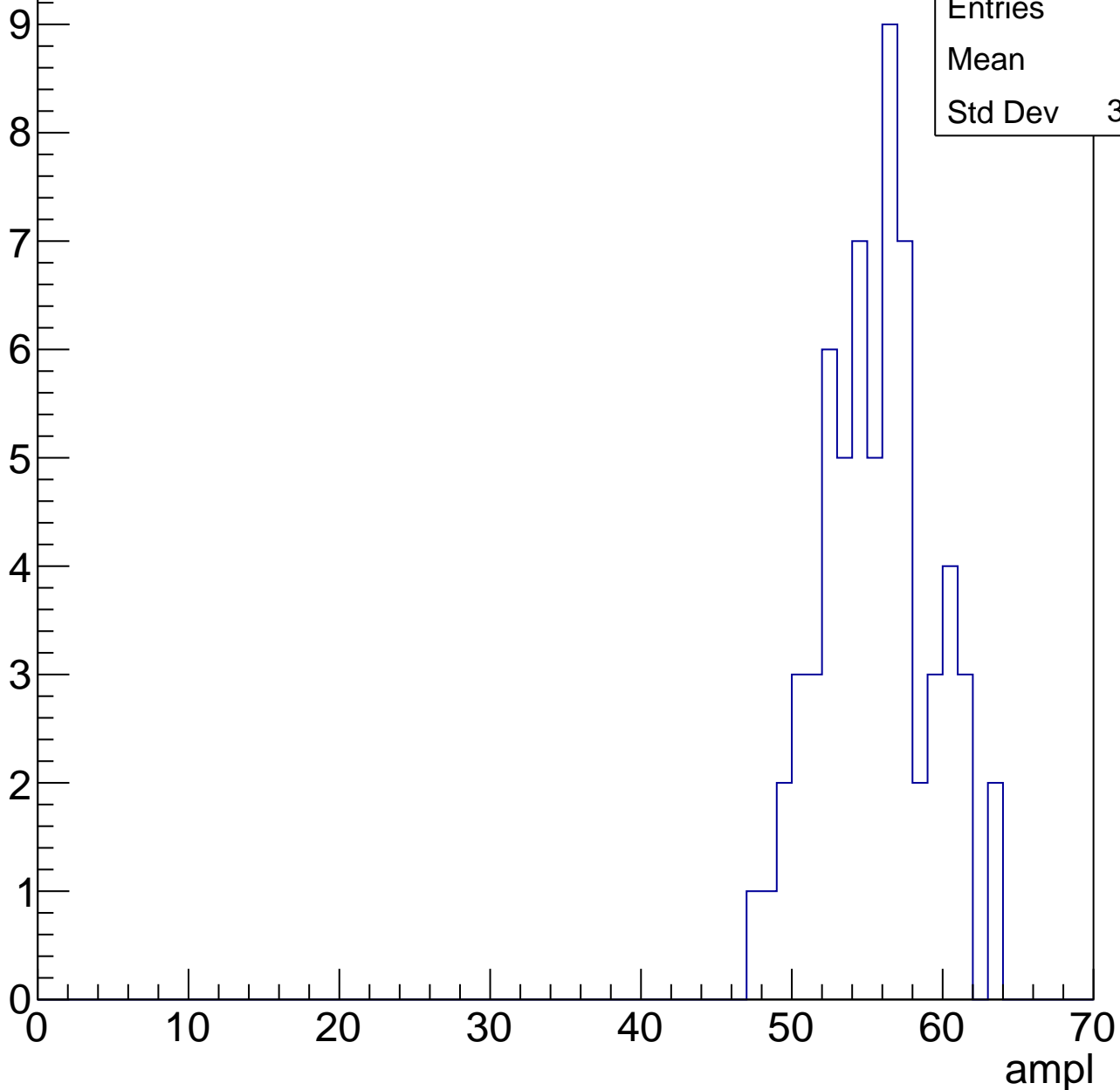


B1L102S, U22-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.1
Std Dev	3.624



B1L102S, U22-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	41
Mean	59.83
Std Dev	2.367

ampl

0

10

20

30

40

50

60

70

B1L102S, U22-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

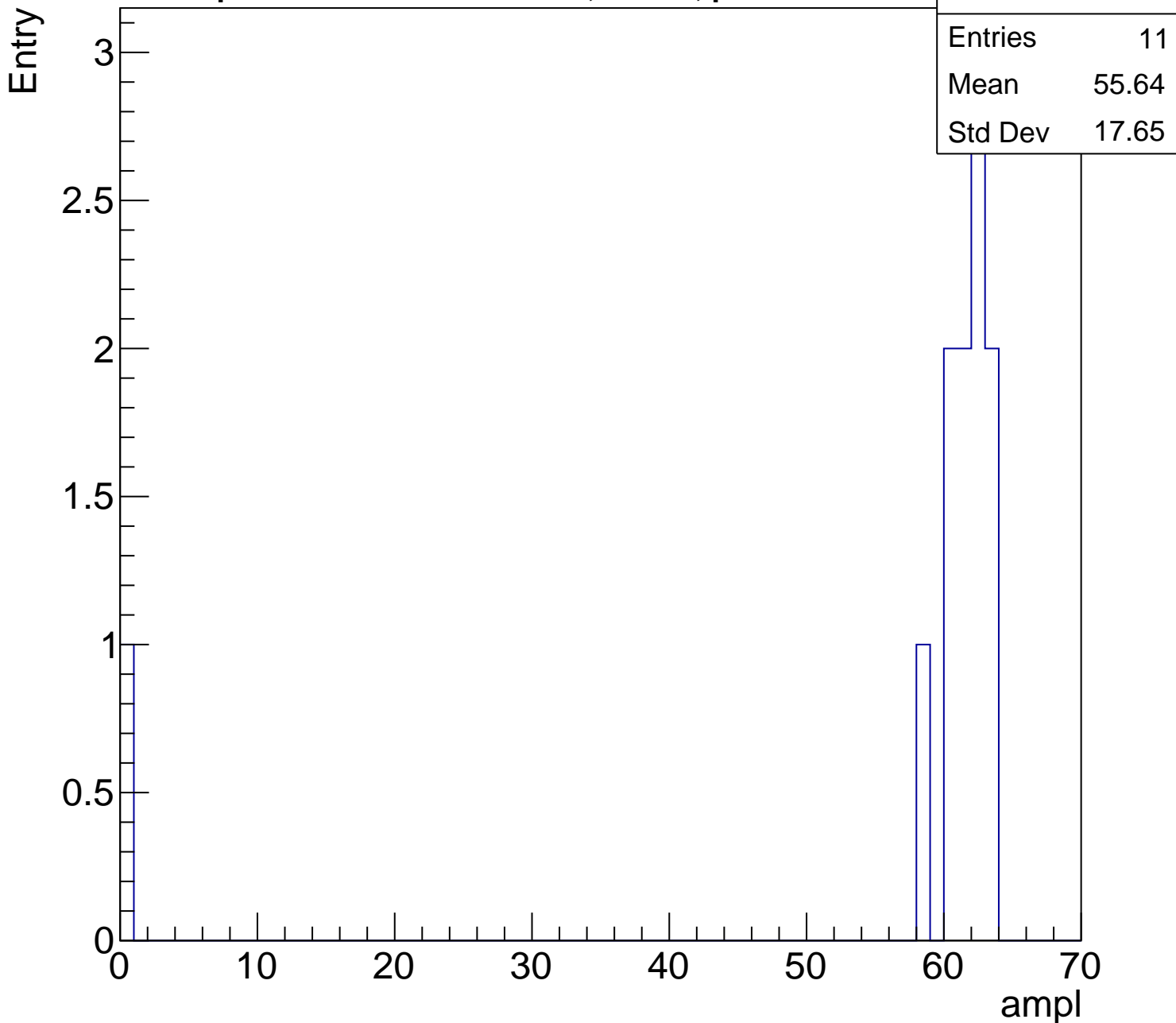
Mean

55.64

Std Dev

17.65

ampl



B1L102S, U22-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U22-ch91, adc0

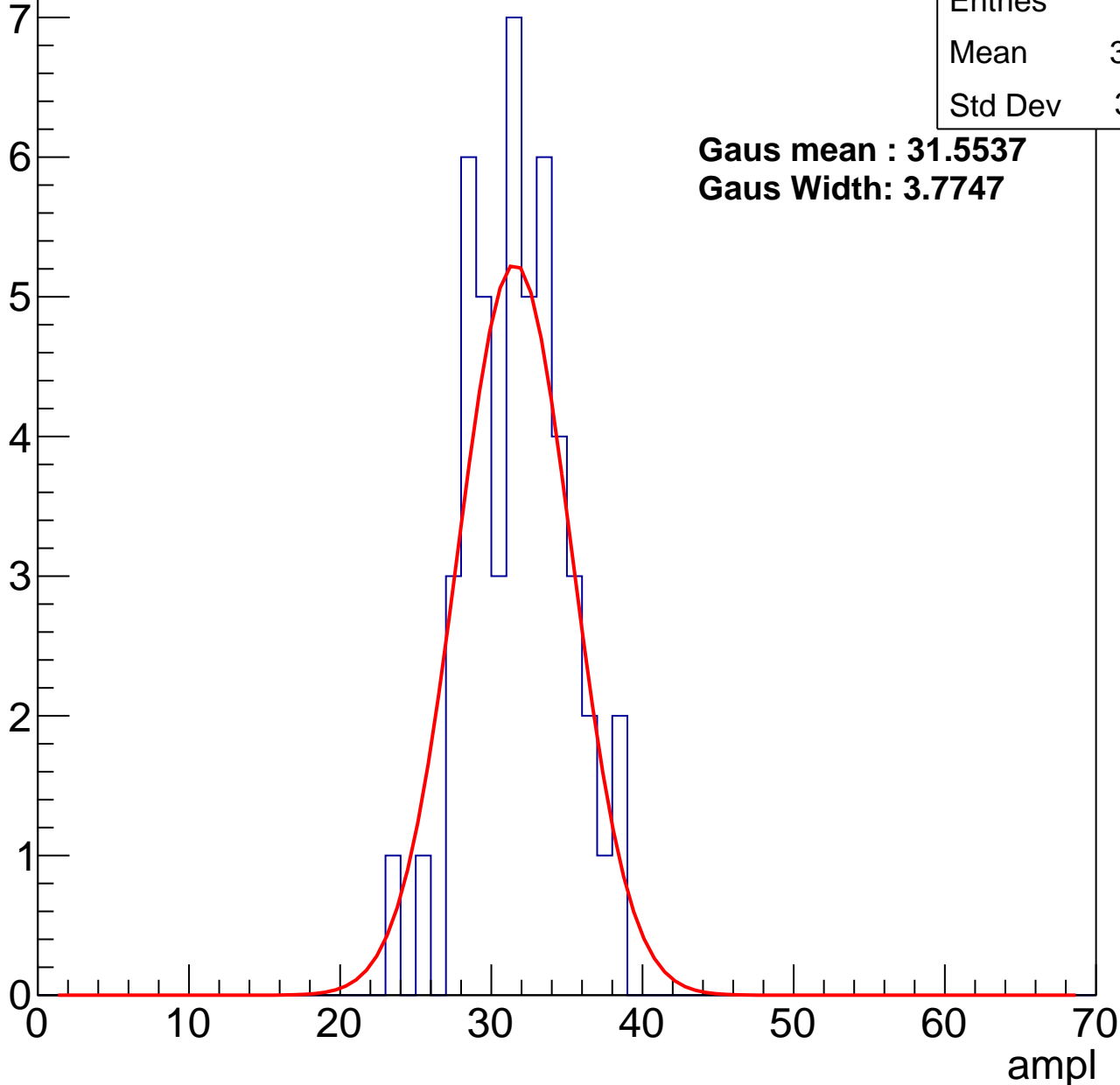
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	31.29
Std Dev	3.251

Gaus mean : 31.5537

Gaus Width: 3.7747



B1L102S, U22-ch91, adc1

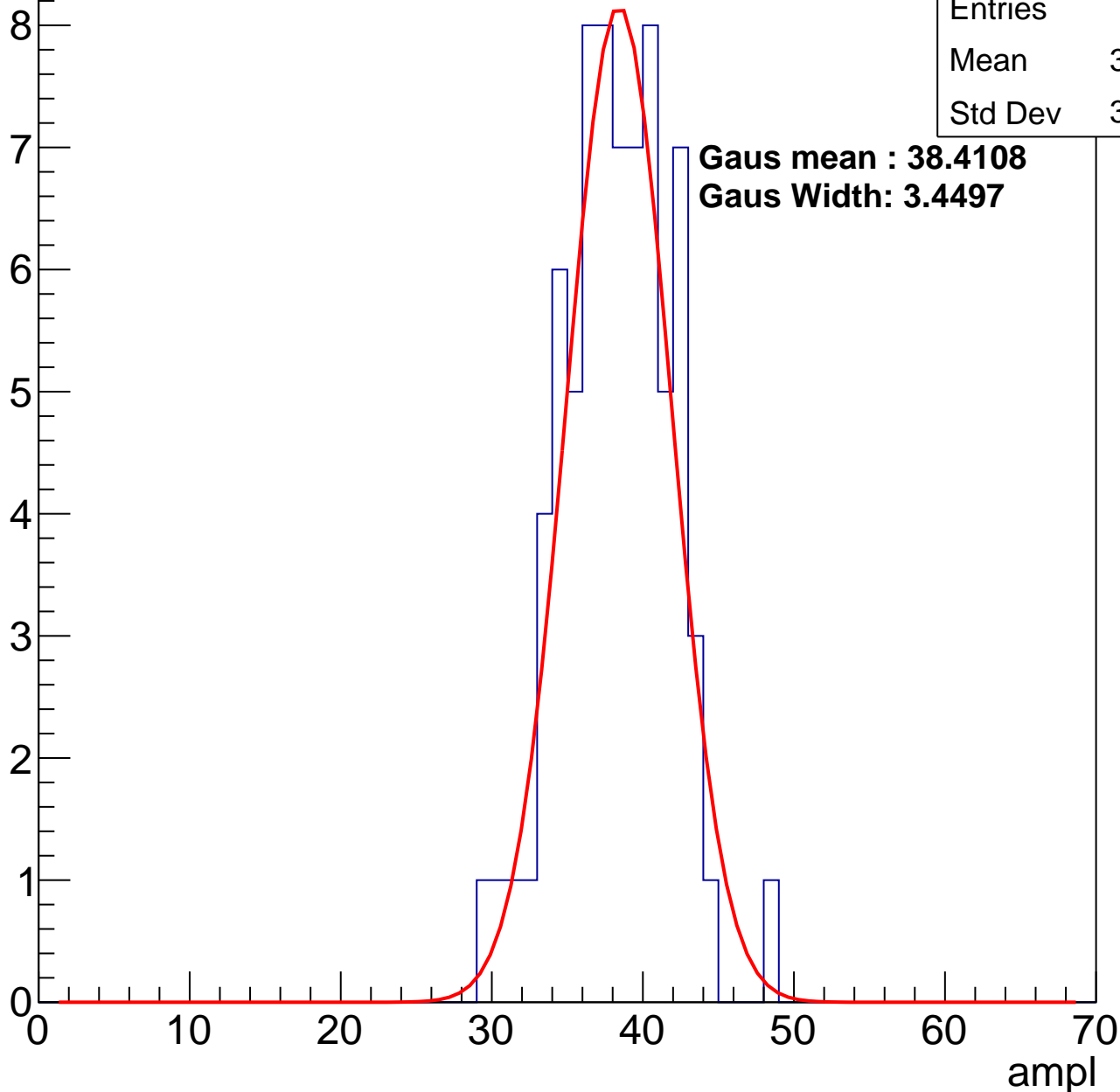
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	37.78
Std Dev	3.515

Gaus mean : 38.4108

Gaus Width: 3.4497



B1L102S, U22-ch91, adc2

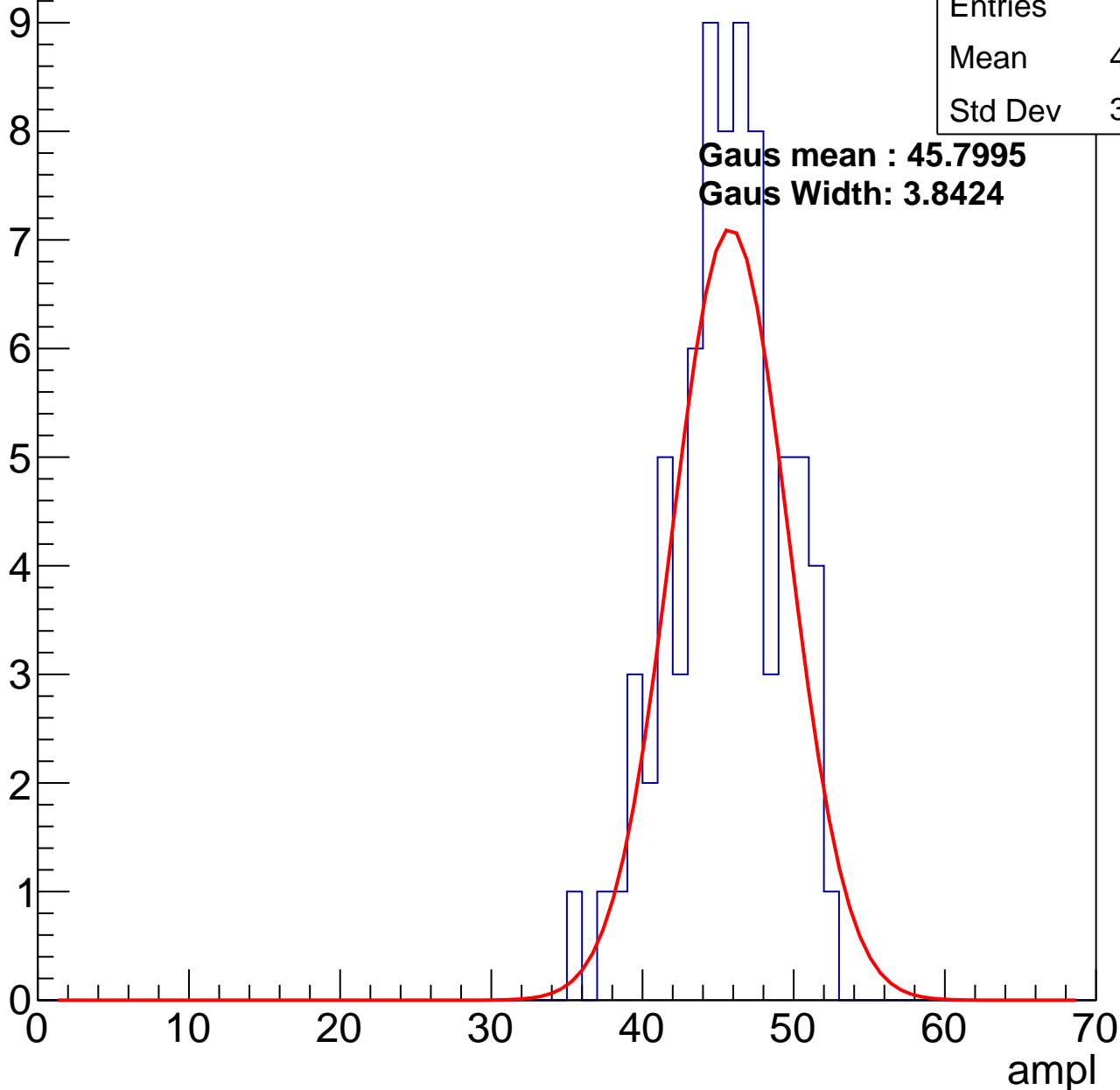
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	45.09
Std Dev	3.632

Gaus mean : 45.7995

Gaus Width: 3.8424

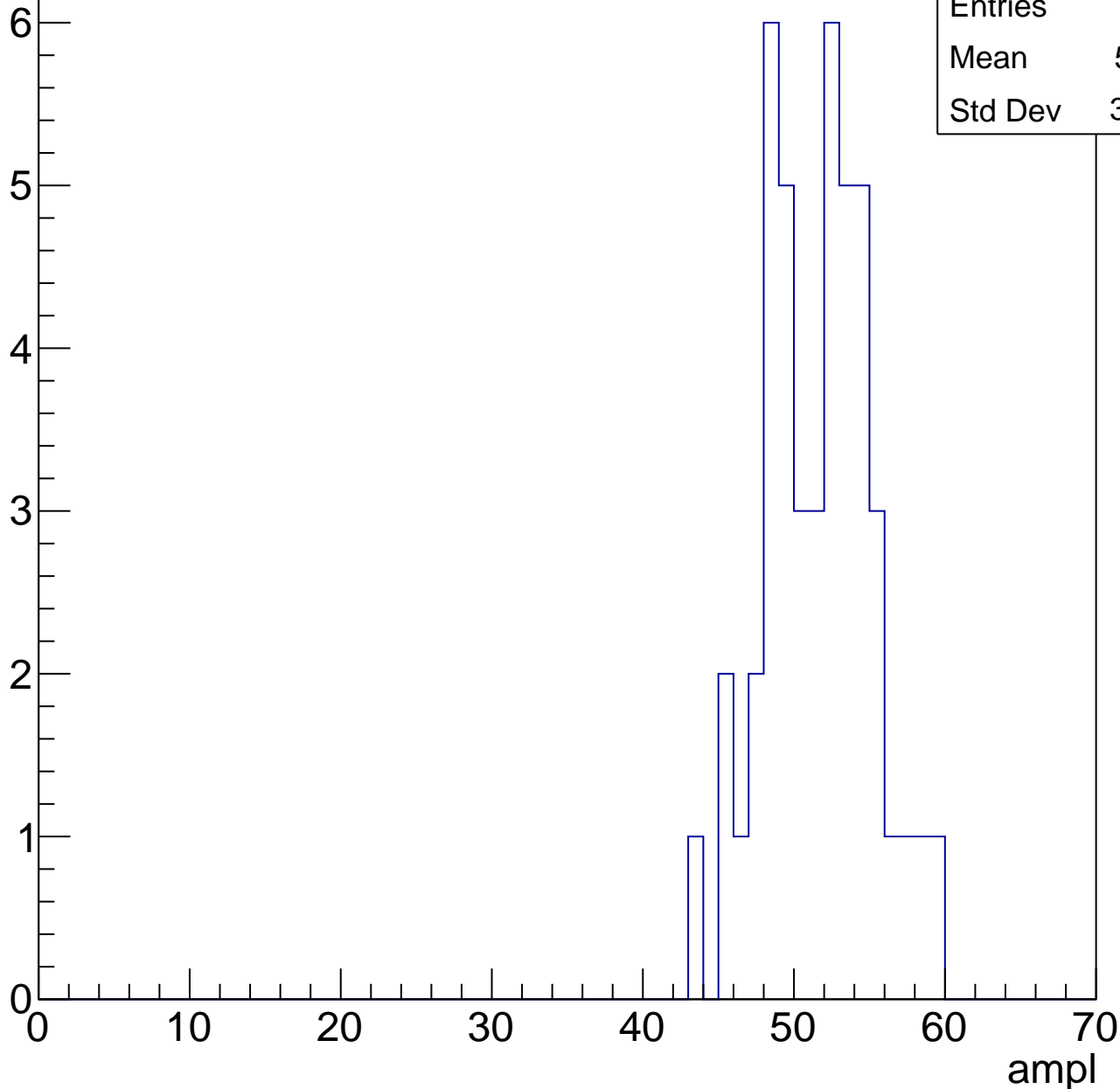


B1L102S, U22-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

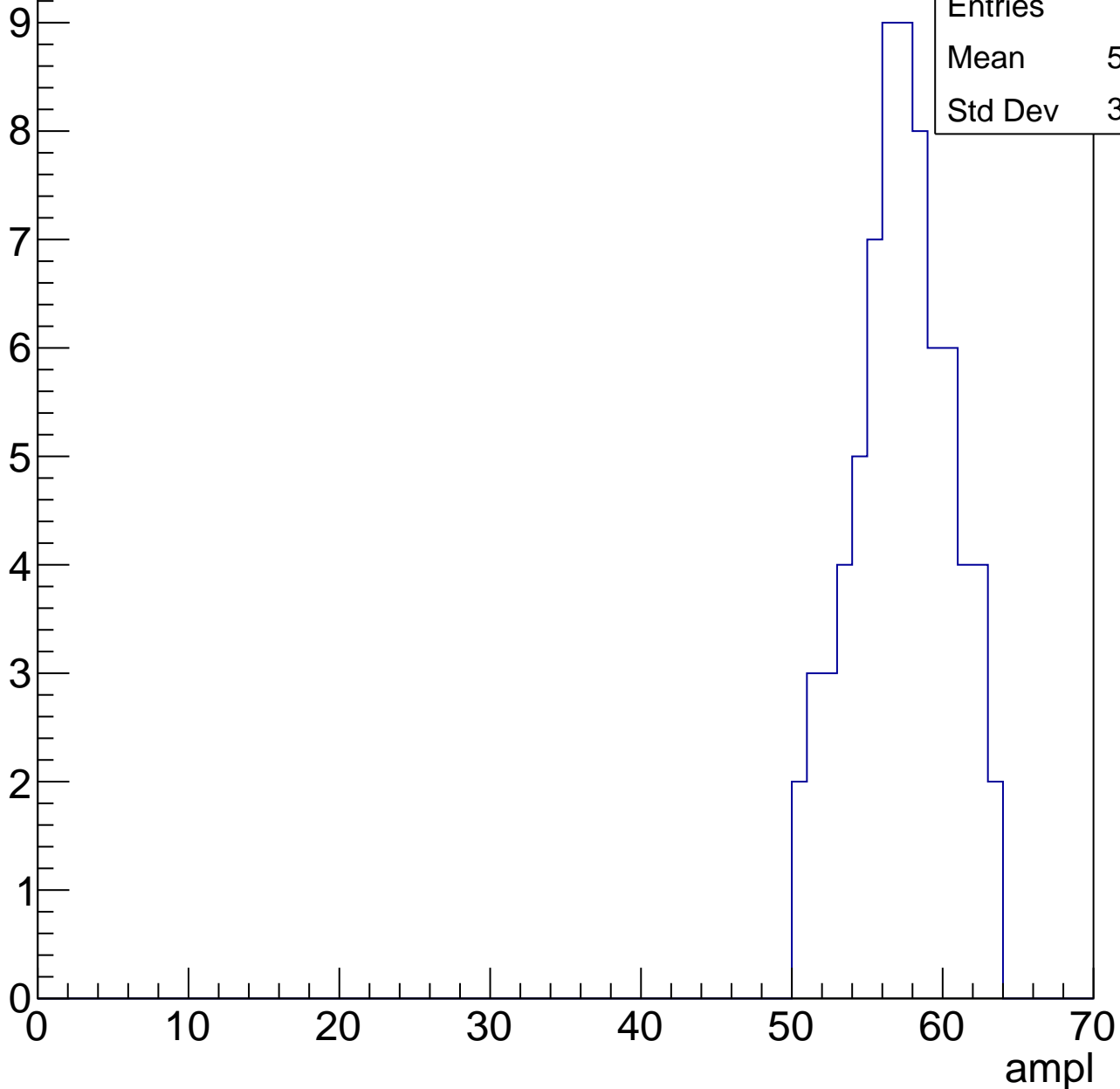
Entries	46
Mean	51.11
Std Dev	3.497



B1L102S, U22-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



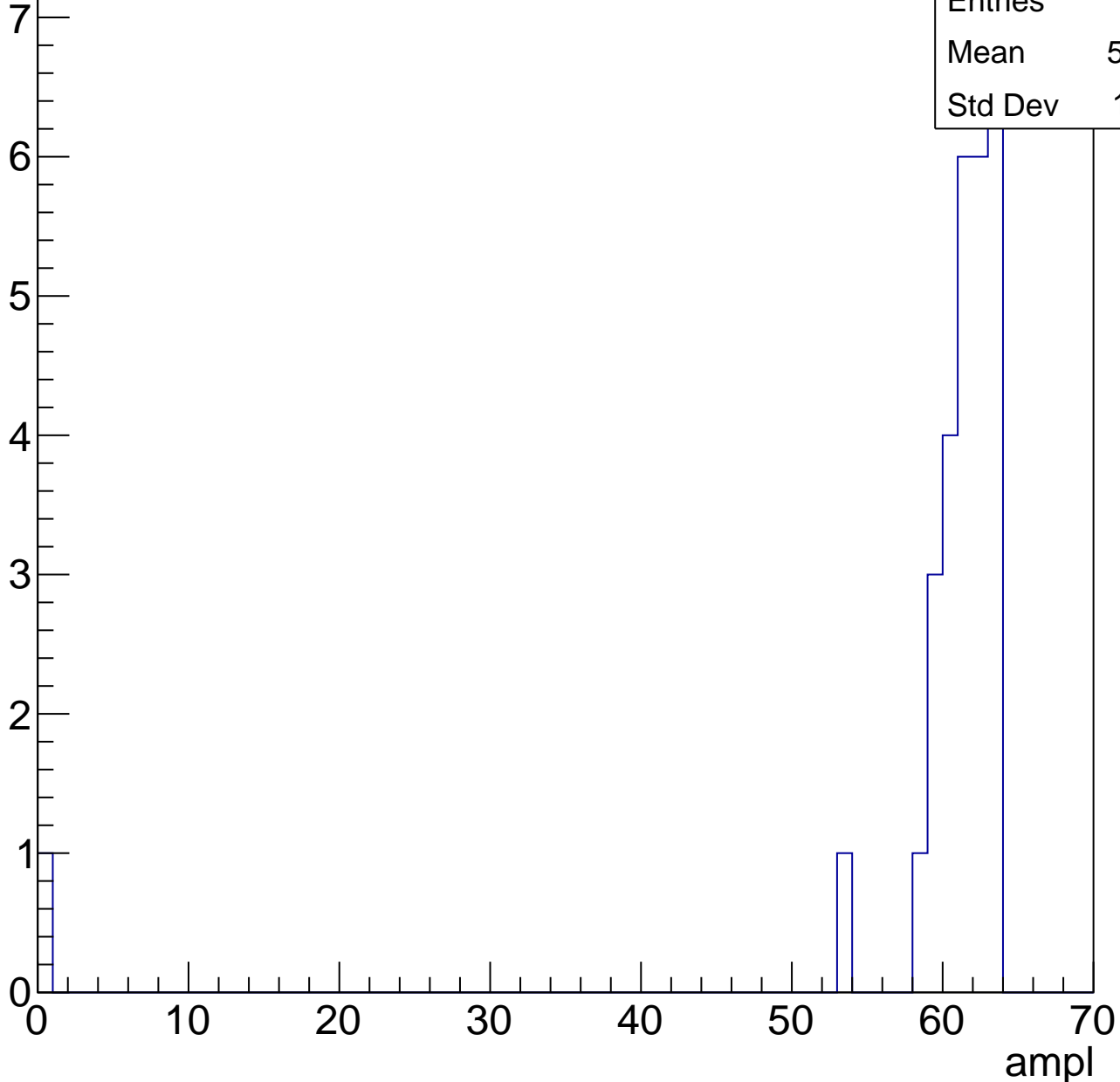
Entries	72
Mean	56.79
Std Dev	3.219

B1L102S, U22-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	29
Mean	58.86
Std Dev	11.31



B1L102S, U22-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch92, adc0

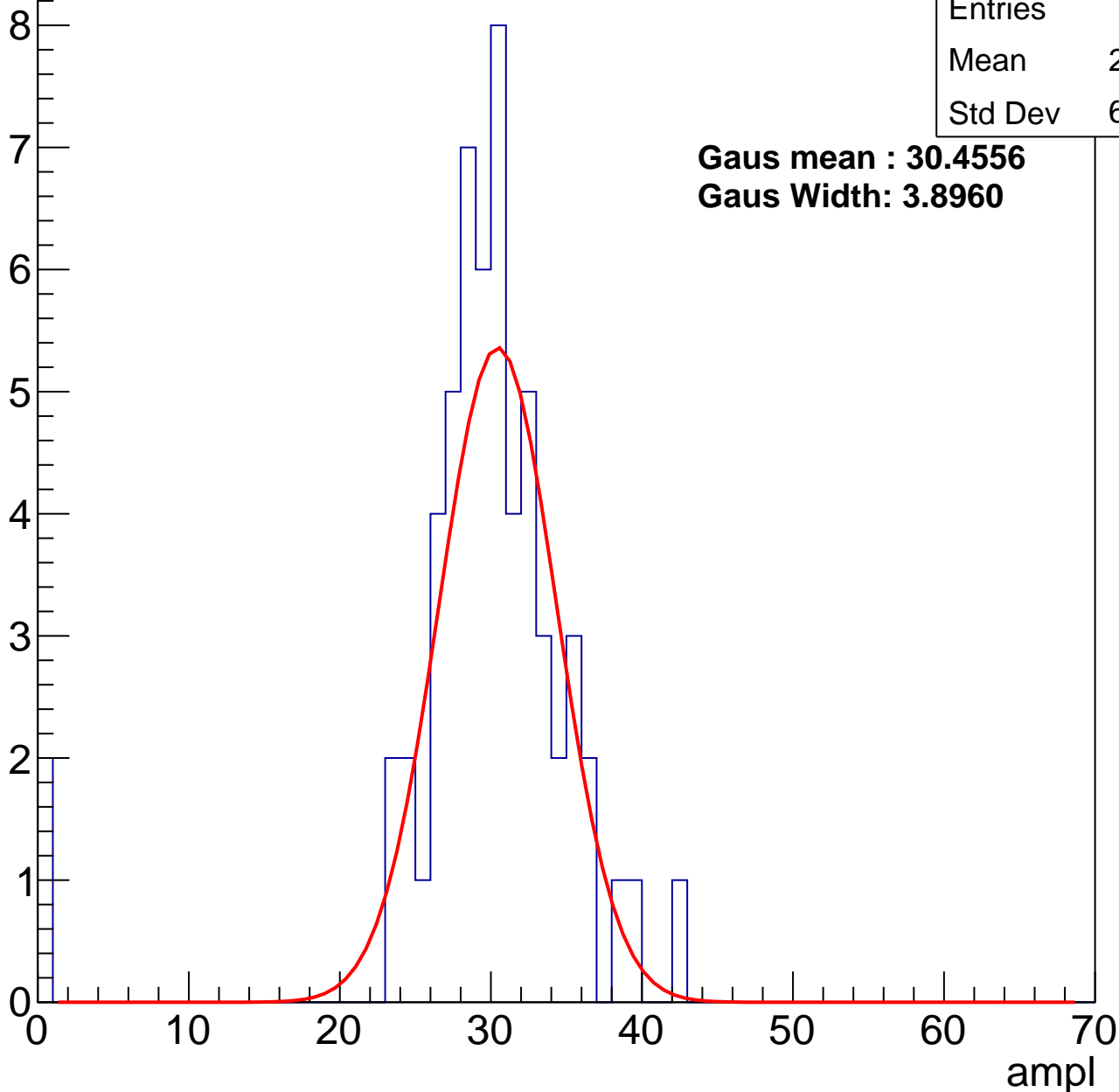
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.07
Std Dev	6.646

Gaus mean : 30.4556

Gaus Width: 3.8960



B1L102S, U22-ch92, adc1

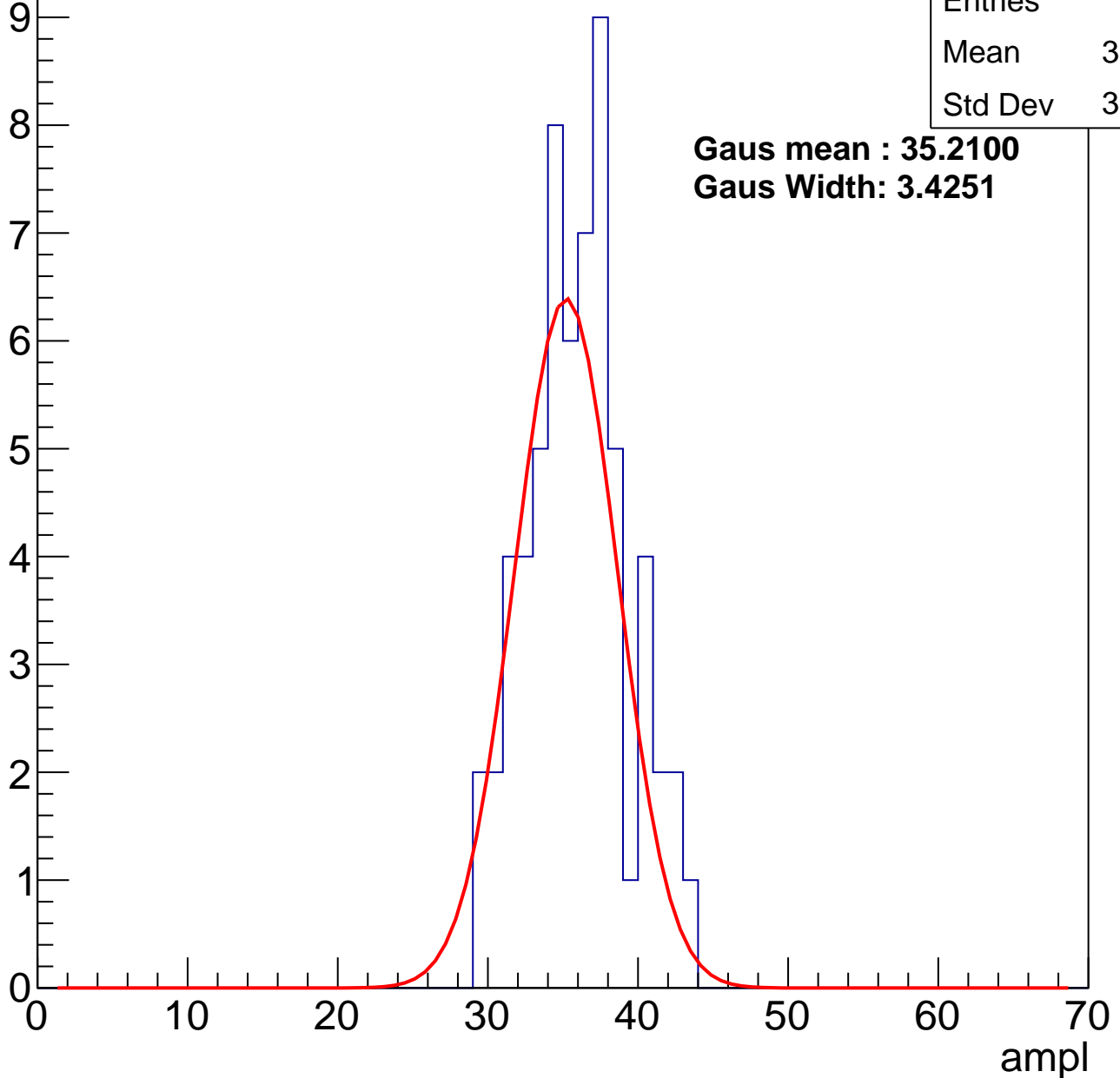
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.48
Std Dev	3.296

Gaus mean : 35.2100

Gaus Width: 3.4251



B1L102S, U22-ch92, adc2

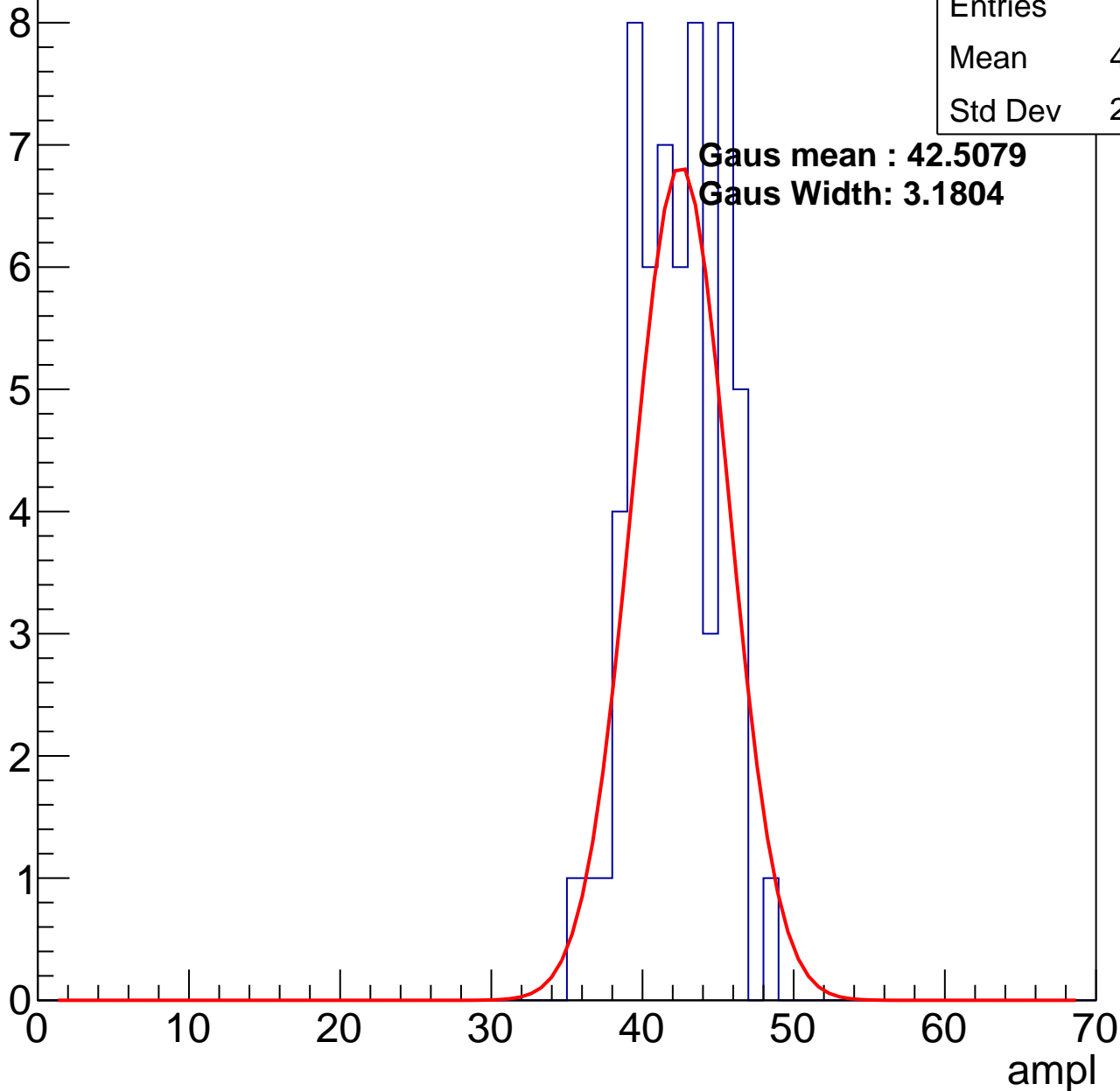
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.78
Std Dev	2.859

Gaus mean : 42.5079

Gaus Width: 3.1804

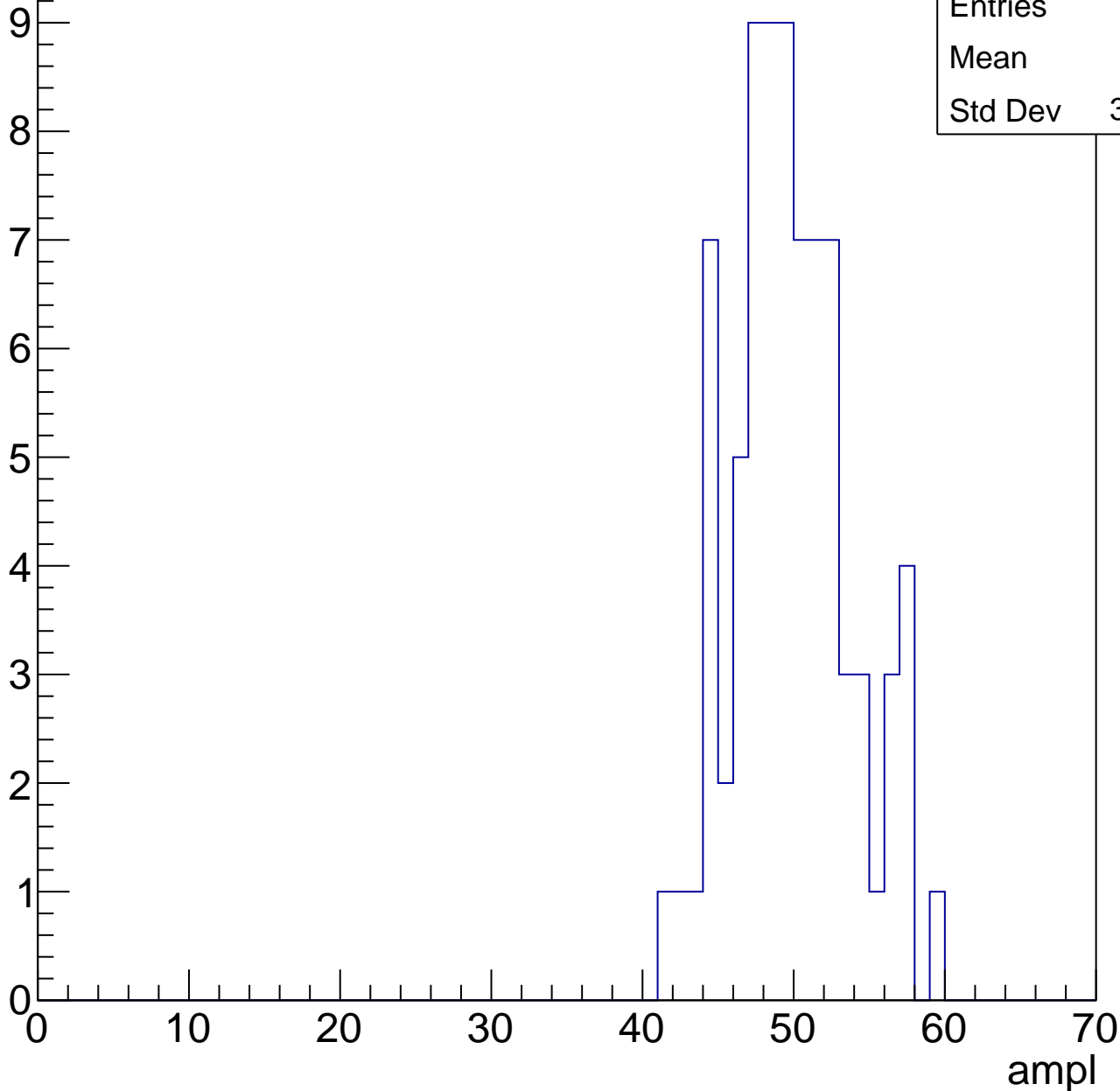


B1L102S, U22-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	49.4
Std Dev	3.862



B1L102S, U22-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

6
5
4
3
2
1
0

Entries	49
Mean	55.1
Std Dev	3.471

0
1
2
3
4
5
6

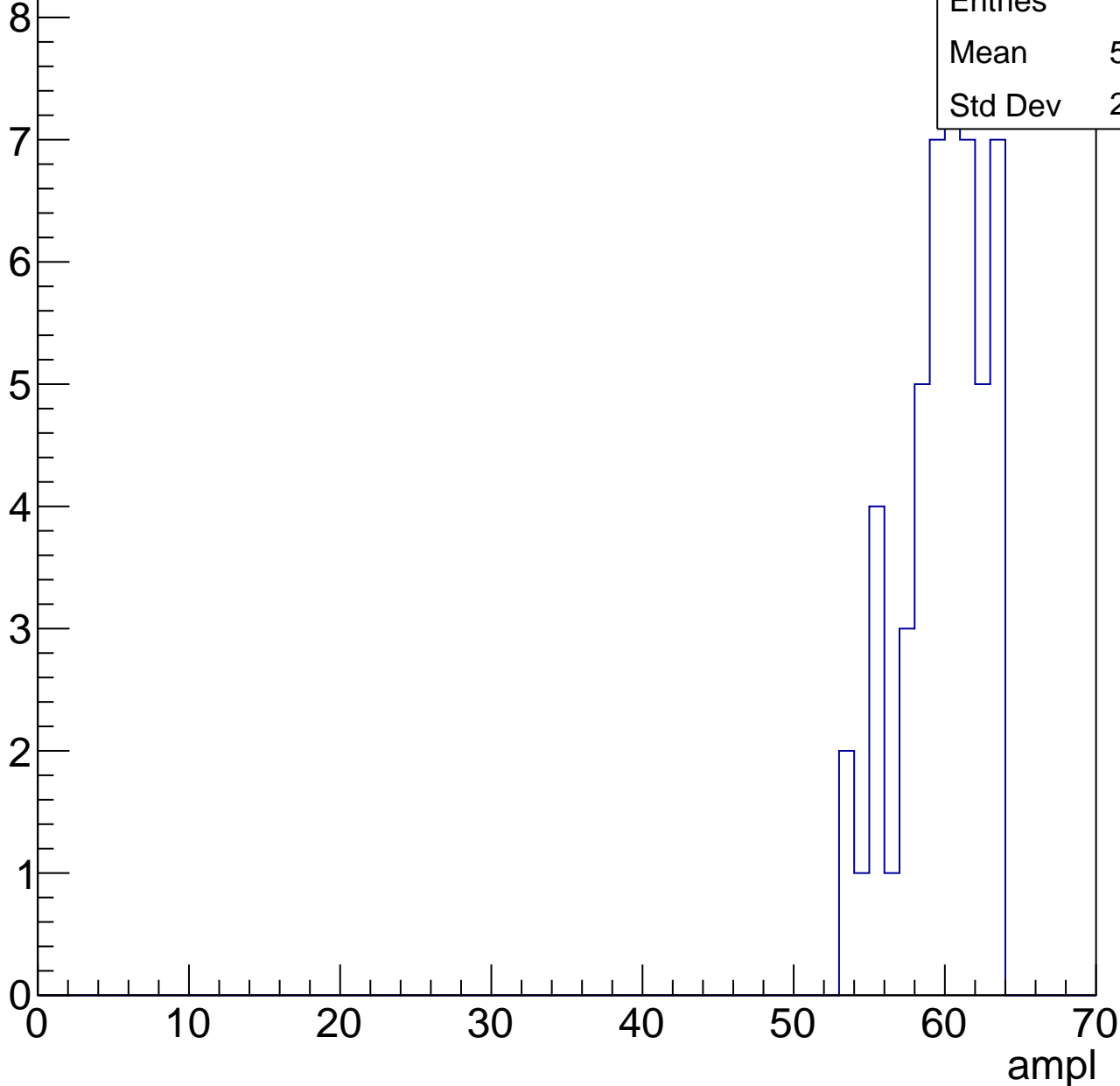
ampl

B1L102S, U22-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

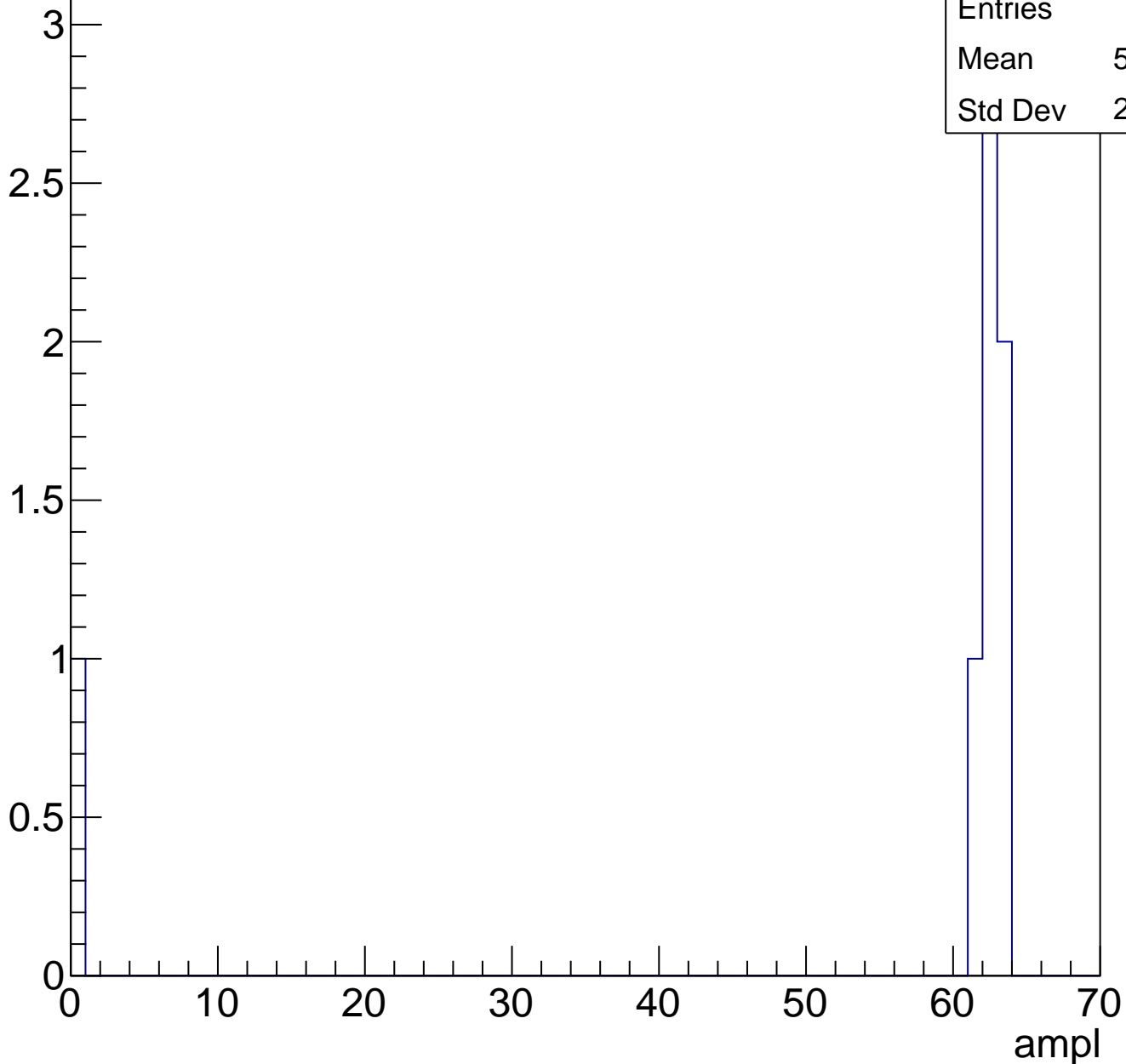
Entries	50
Mean	59.36
Std Dev	2.733



B1L102S, U22-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch93, adc0

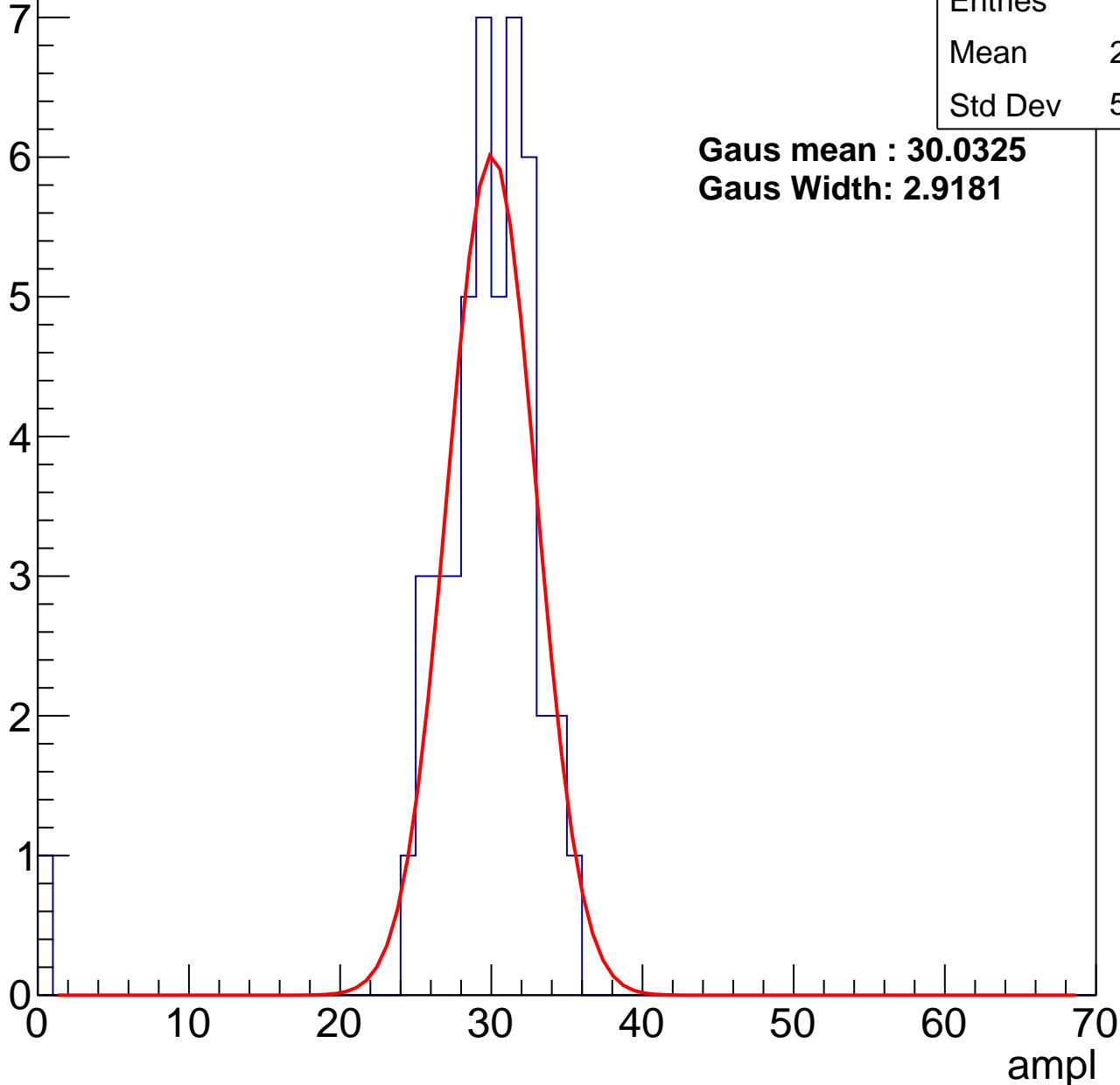
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	28.89
Std Dev	5.027

Gaus mean : 30.0325

Gaus Width: 2.9181



B1L102S, U22-ch93, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	35.26
Std Dev	3.447

Gaus mean : 35.7519

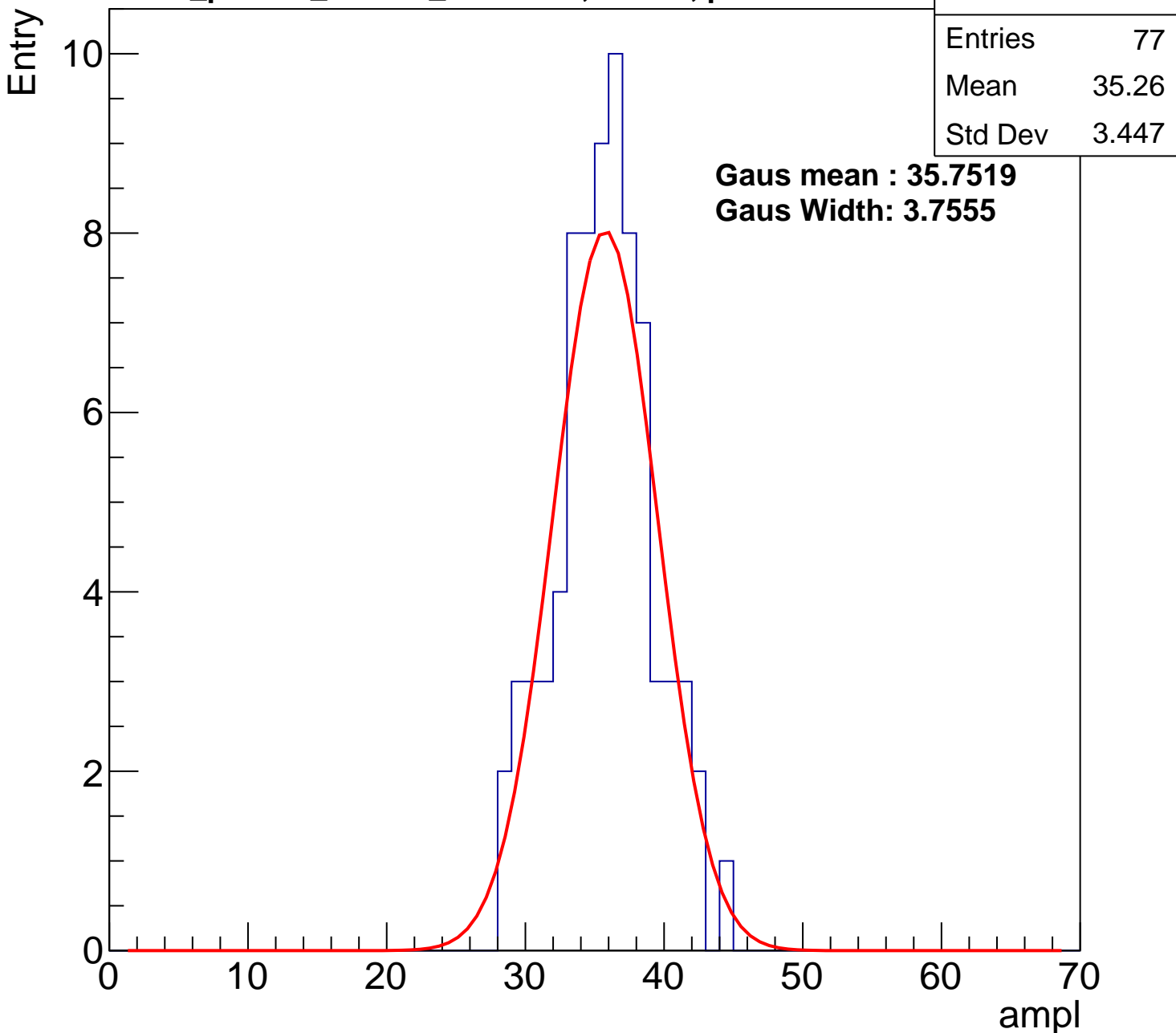
Gaus Width: 3.7555

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

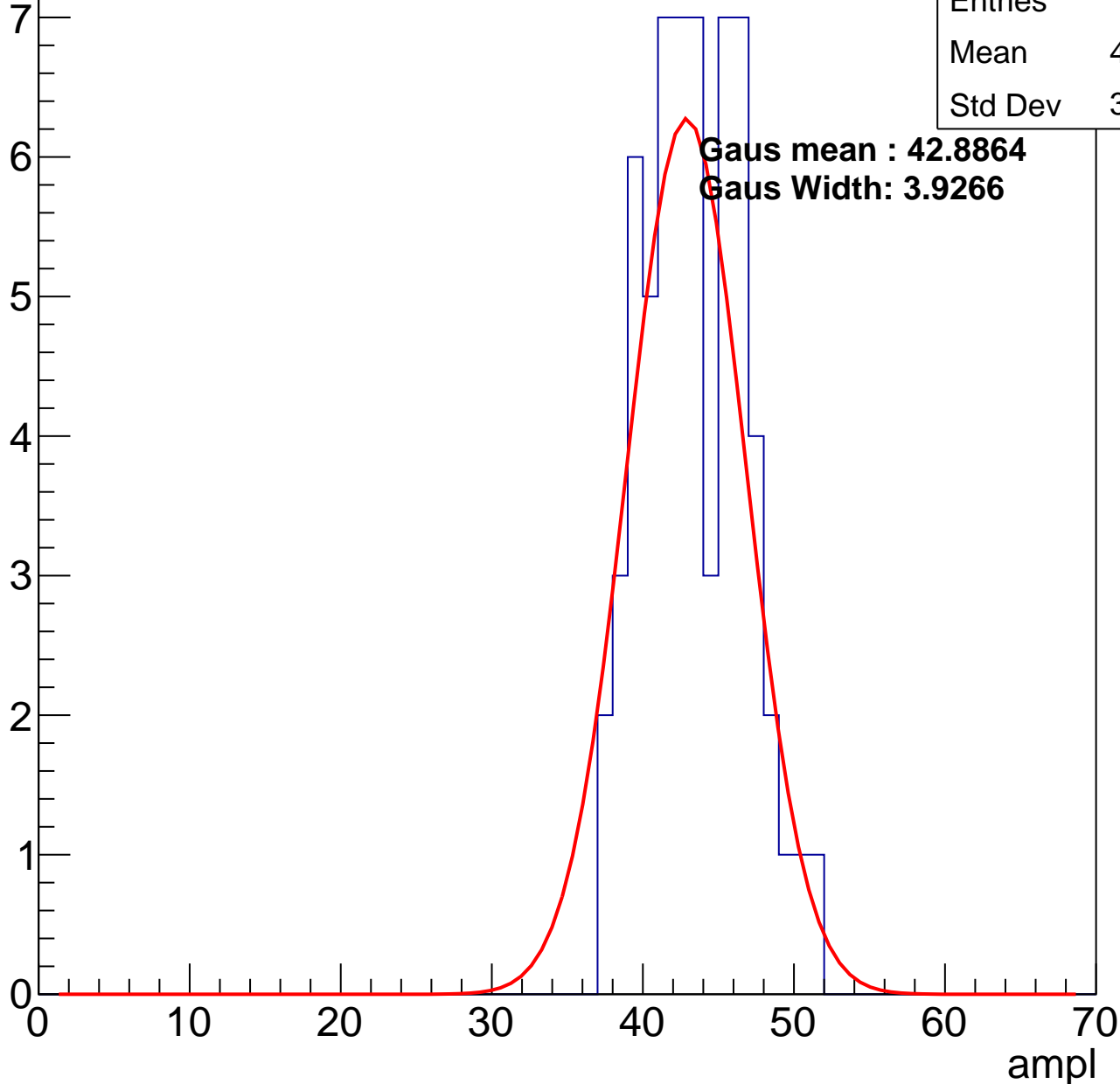


B1L102S, U22-ch93, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.97
Std Dev	3.285

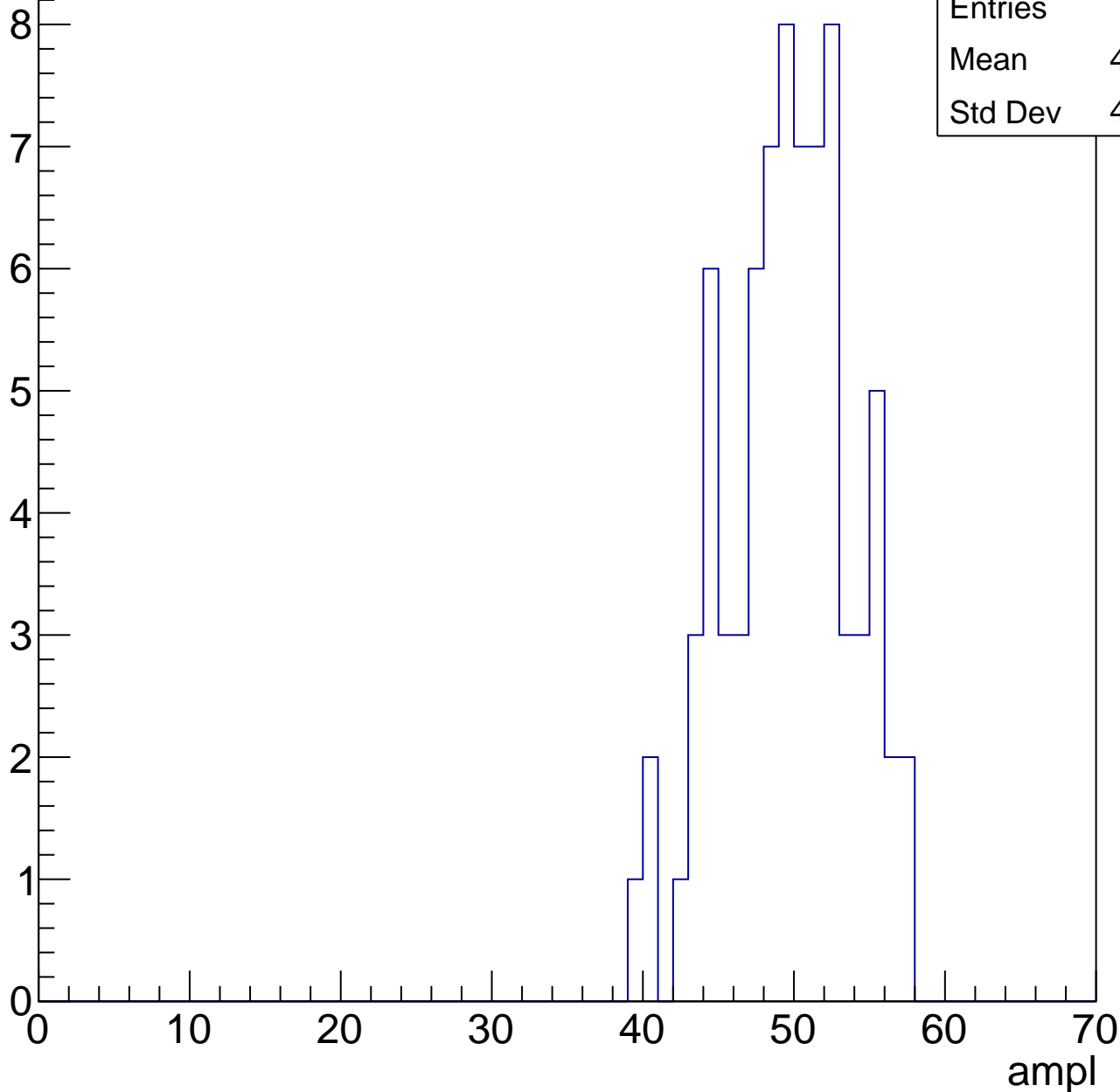


B1L102S, U22-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.12
Std Dev	4.125

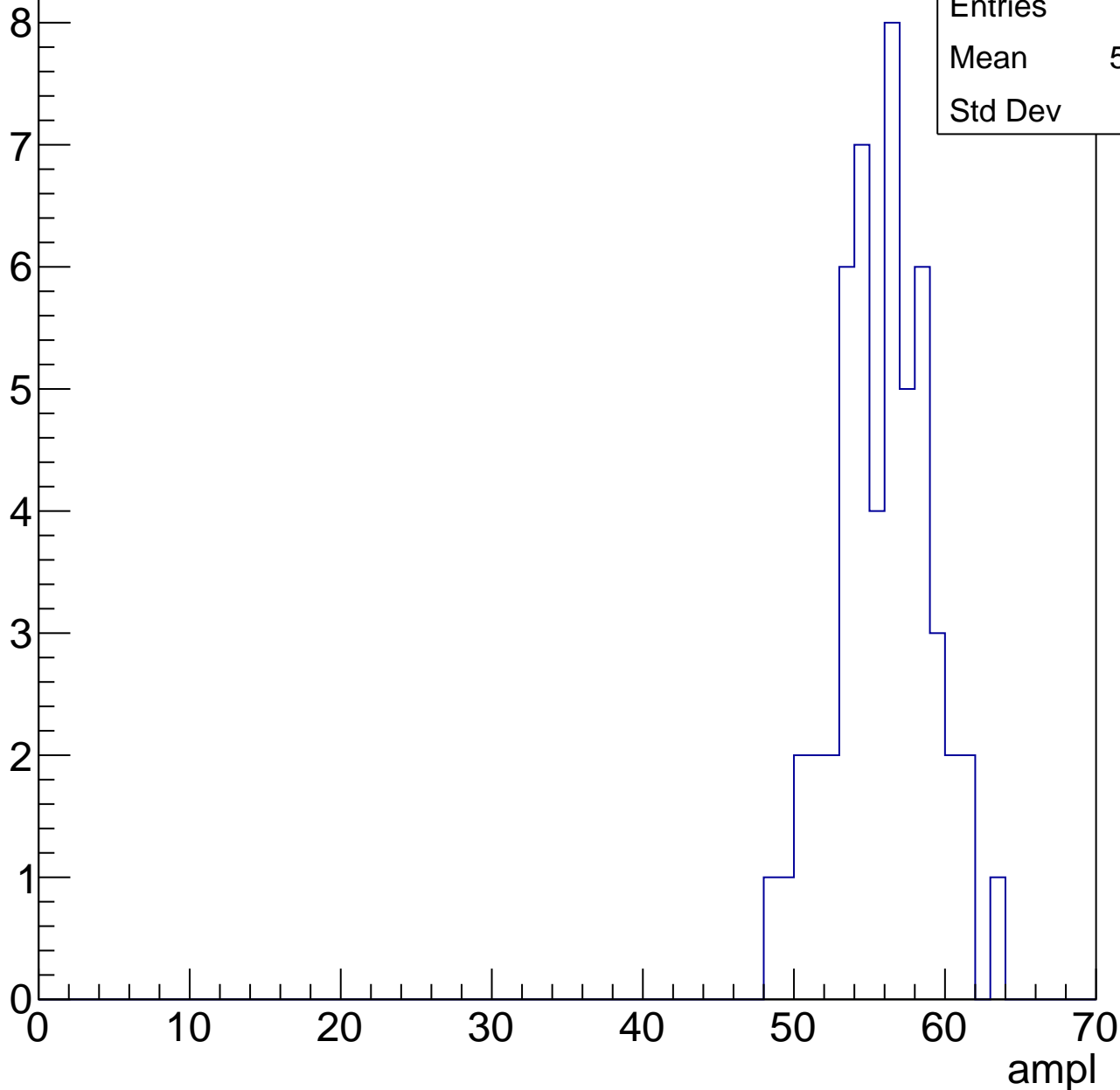


B1L102S, U22-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

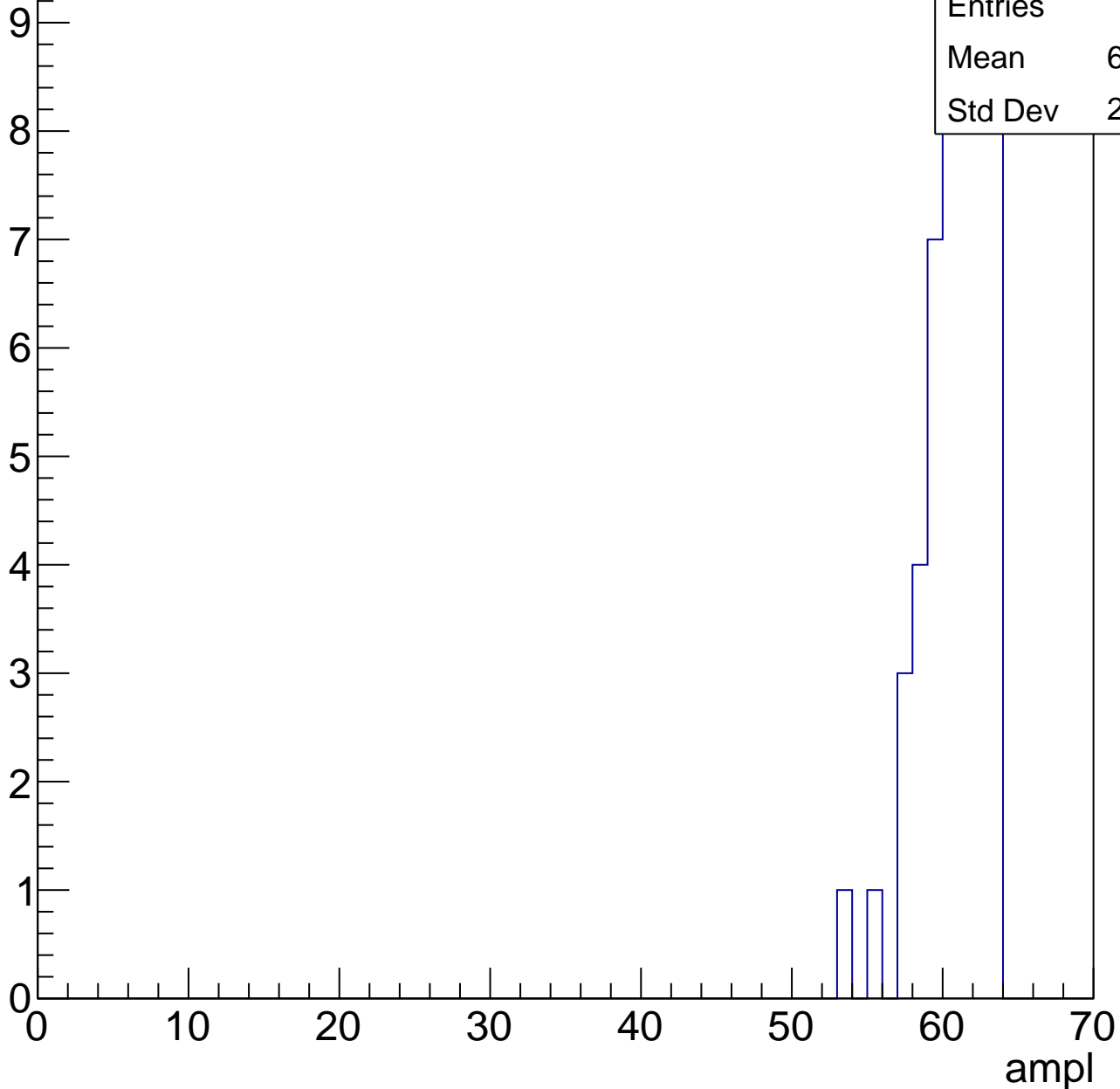
Entries	52
Mean	55.42
Std Dev	3.17



B1L102S, U22-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch94, adc0

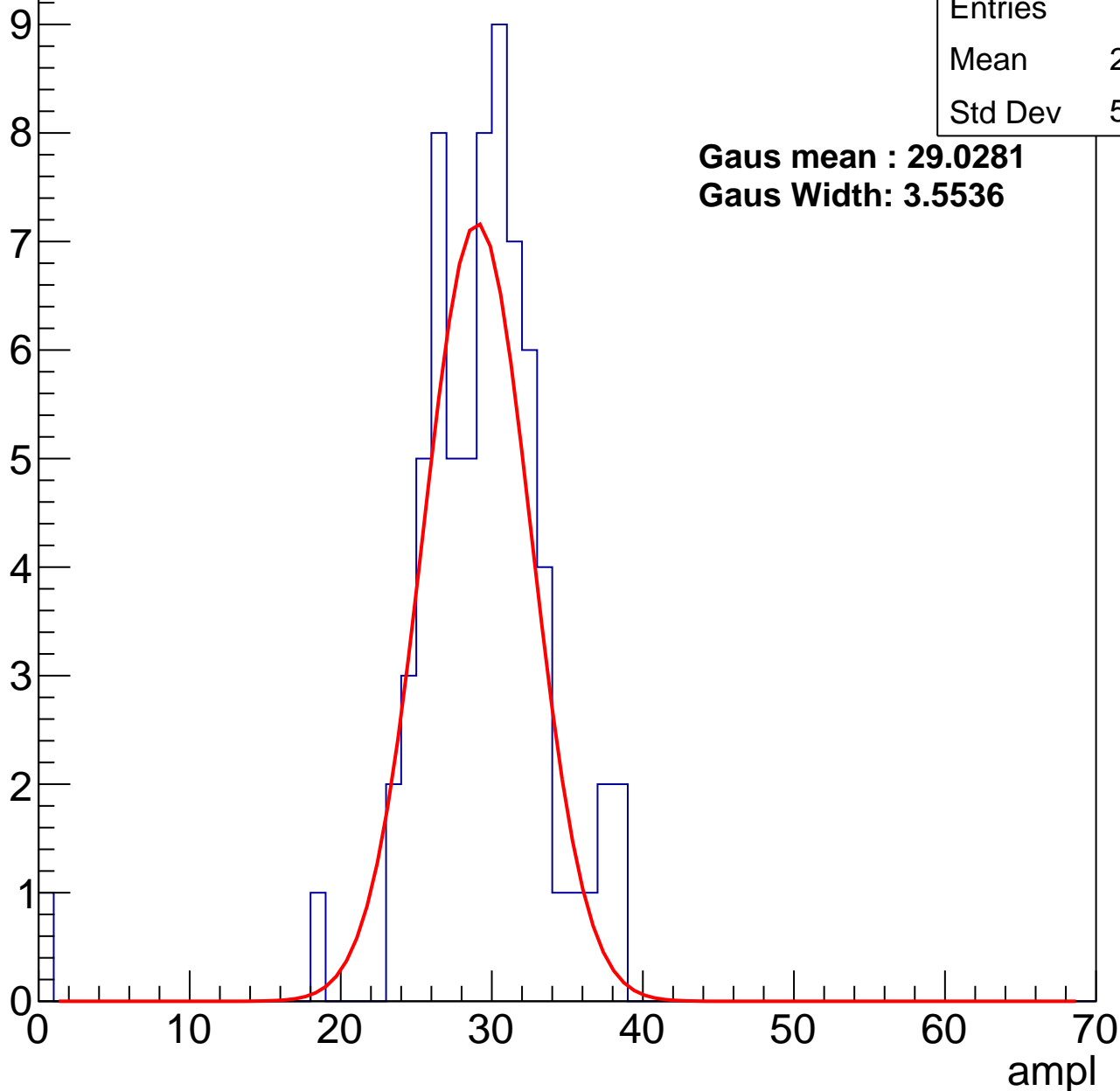
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.76
Std Dev	5.086

Gaus mean : 29.0281

Gaus Width: 3.5536



B1L102S, U22-ch94, adc1

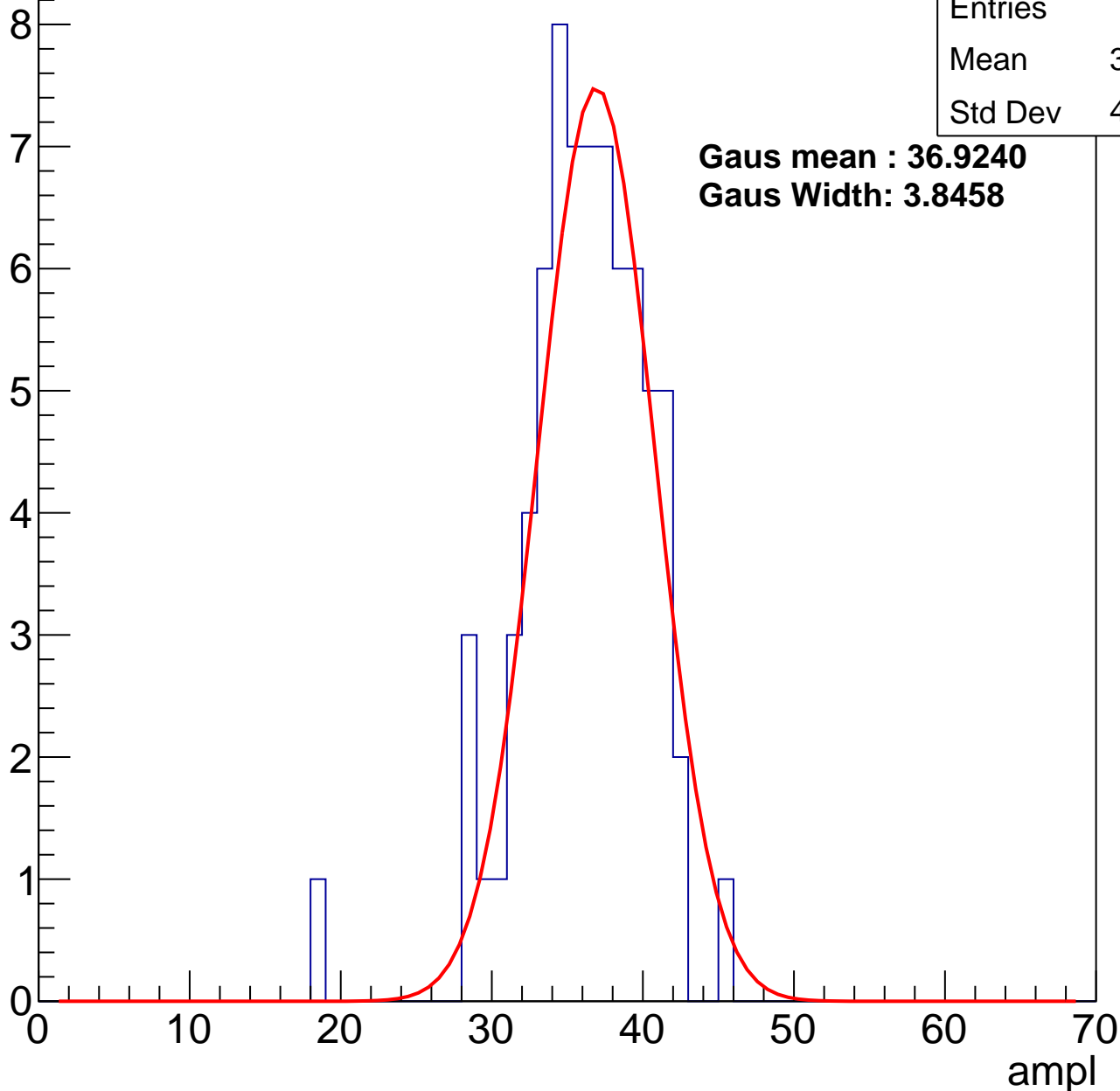
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.67
Std Dev	4.168

Gaus mean : 36.9240

Gaus Width: 3.8458



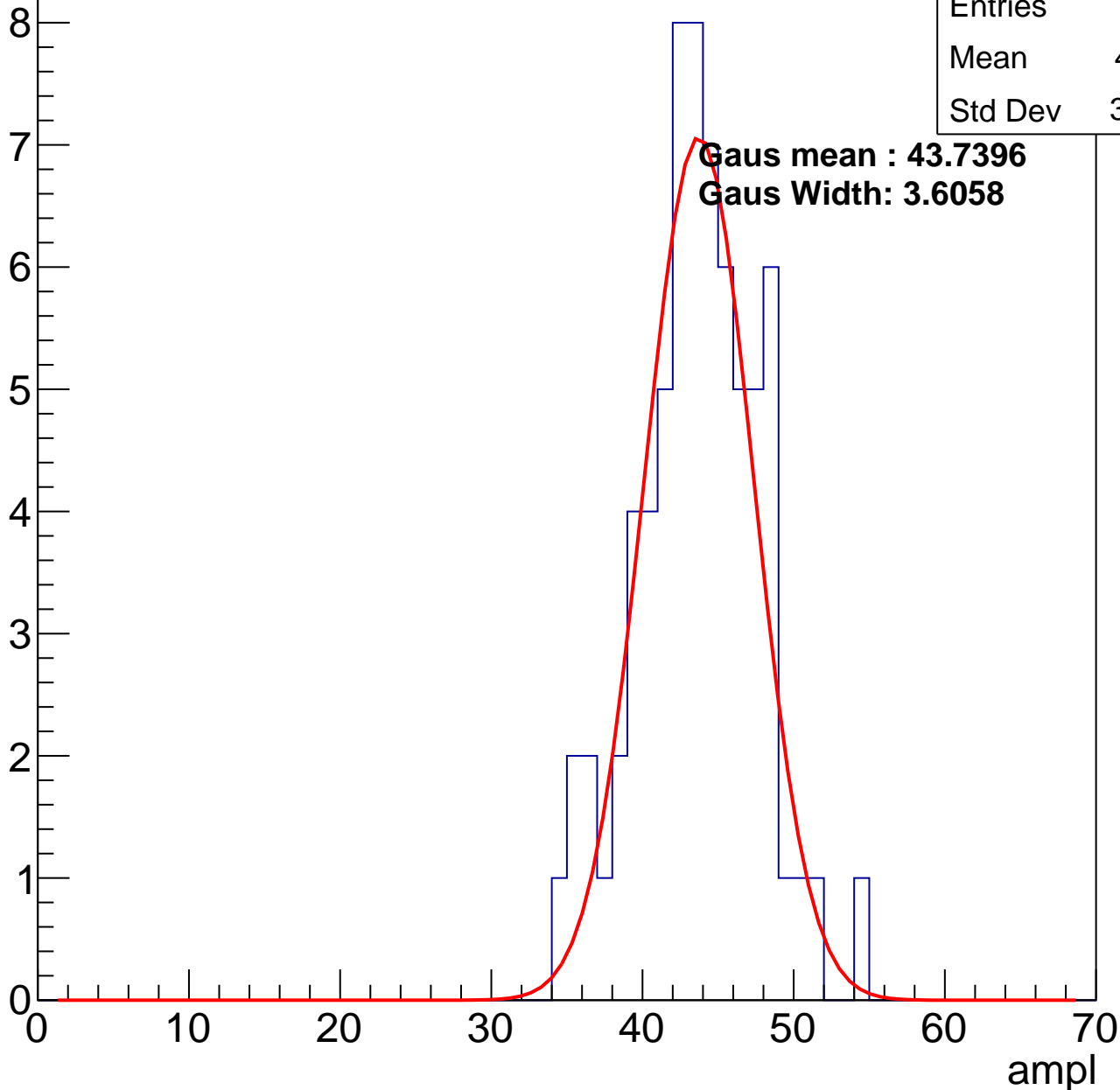
B1L102S, U22-ch94, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.21
Std Dev	3.953

Gaus mean : 43.7396
Gaus Width: 3.6058

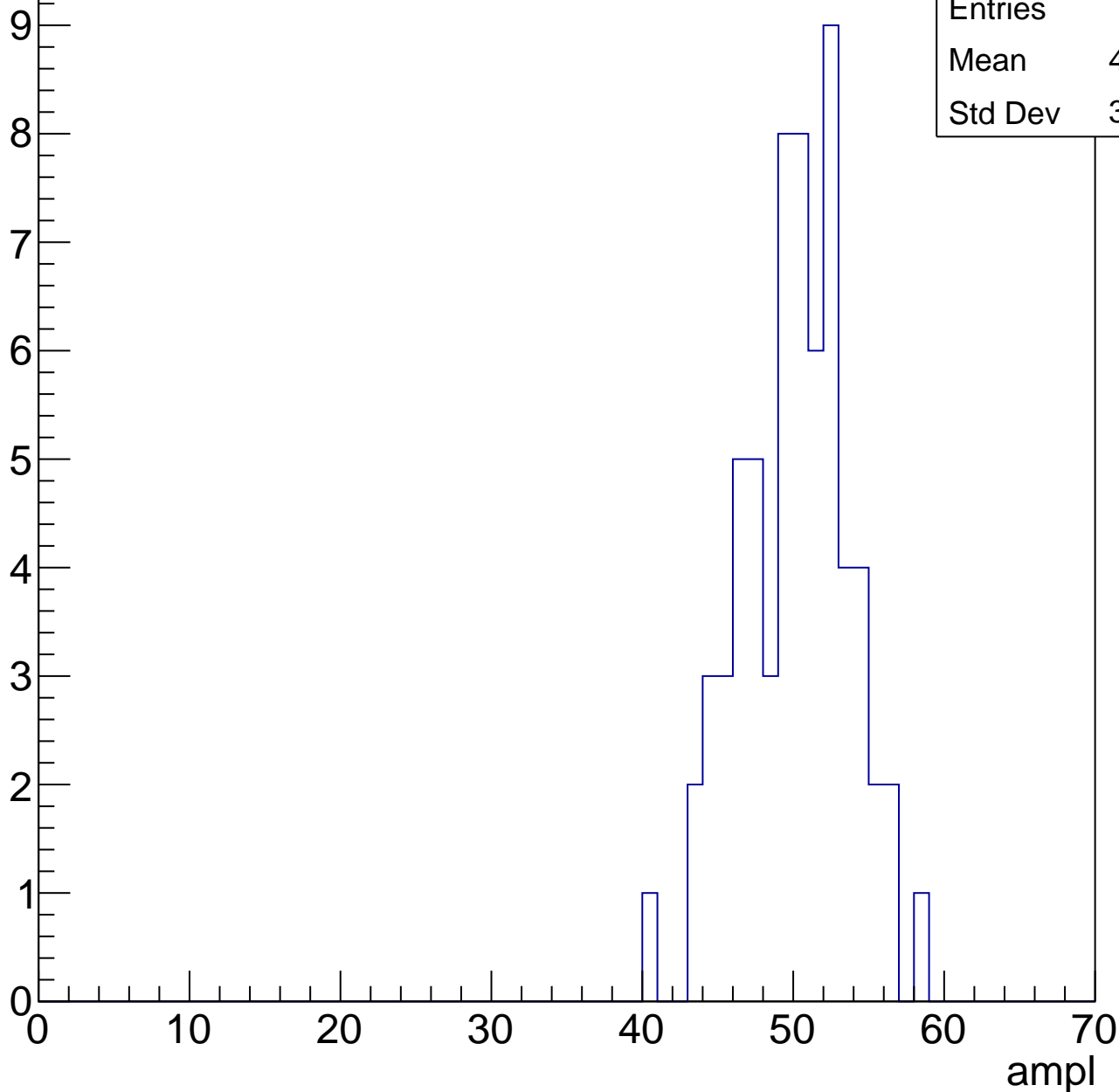


B1L102S, U22-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.64
Std Dev	3.579

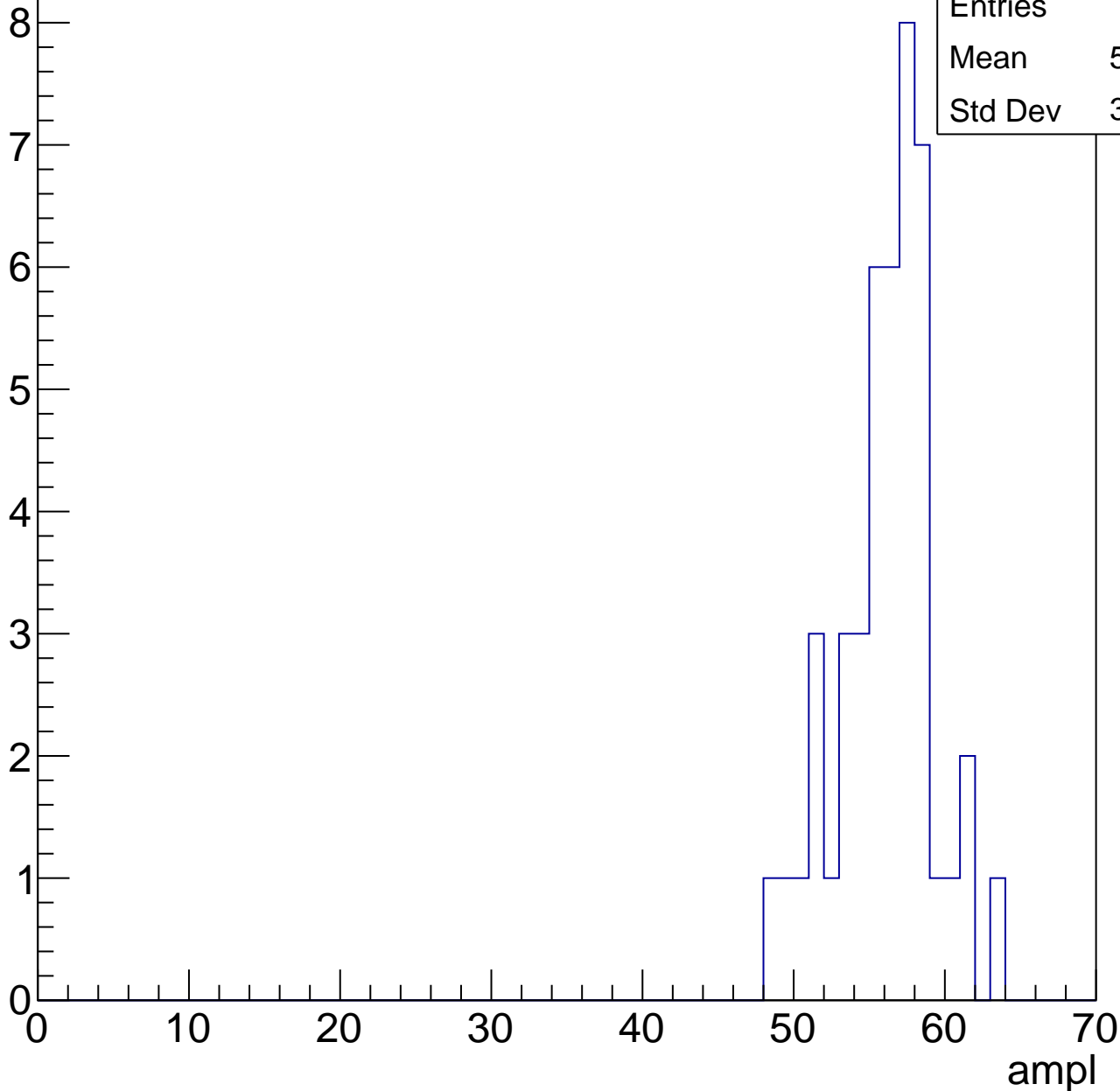


B1L102S, U22-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

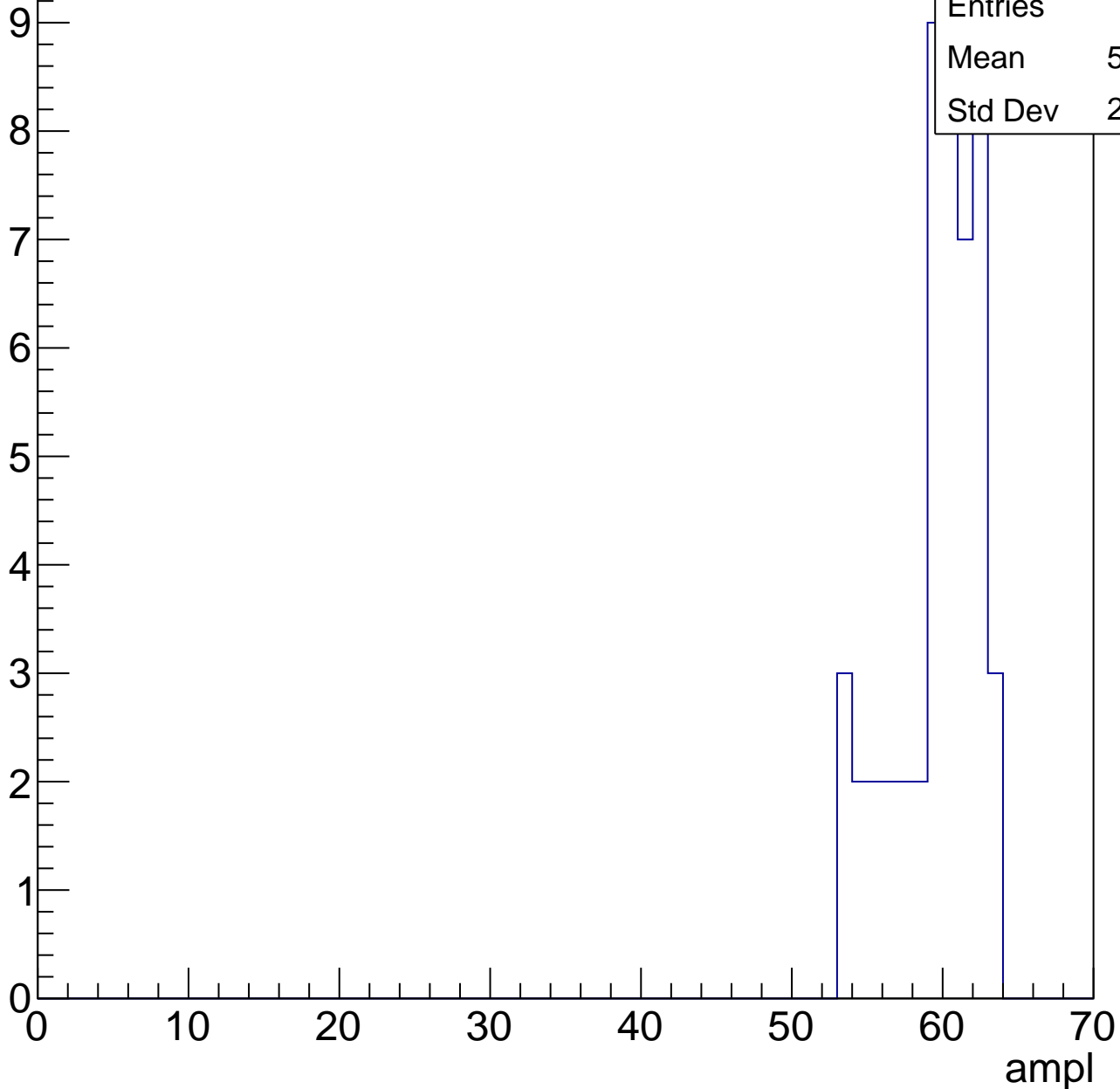
Entries	45
Mean	55.67
Std Dev	3.127



B1L102S, U22-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

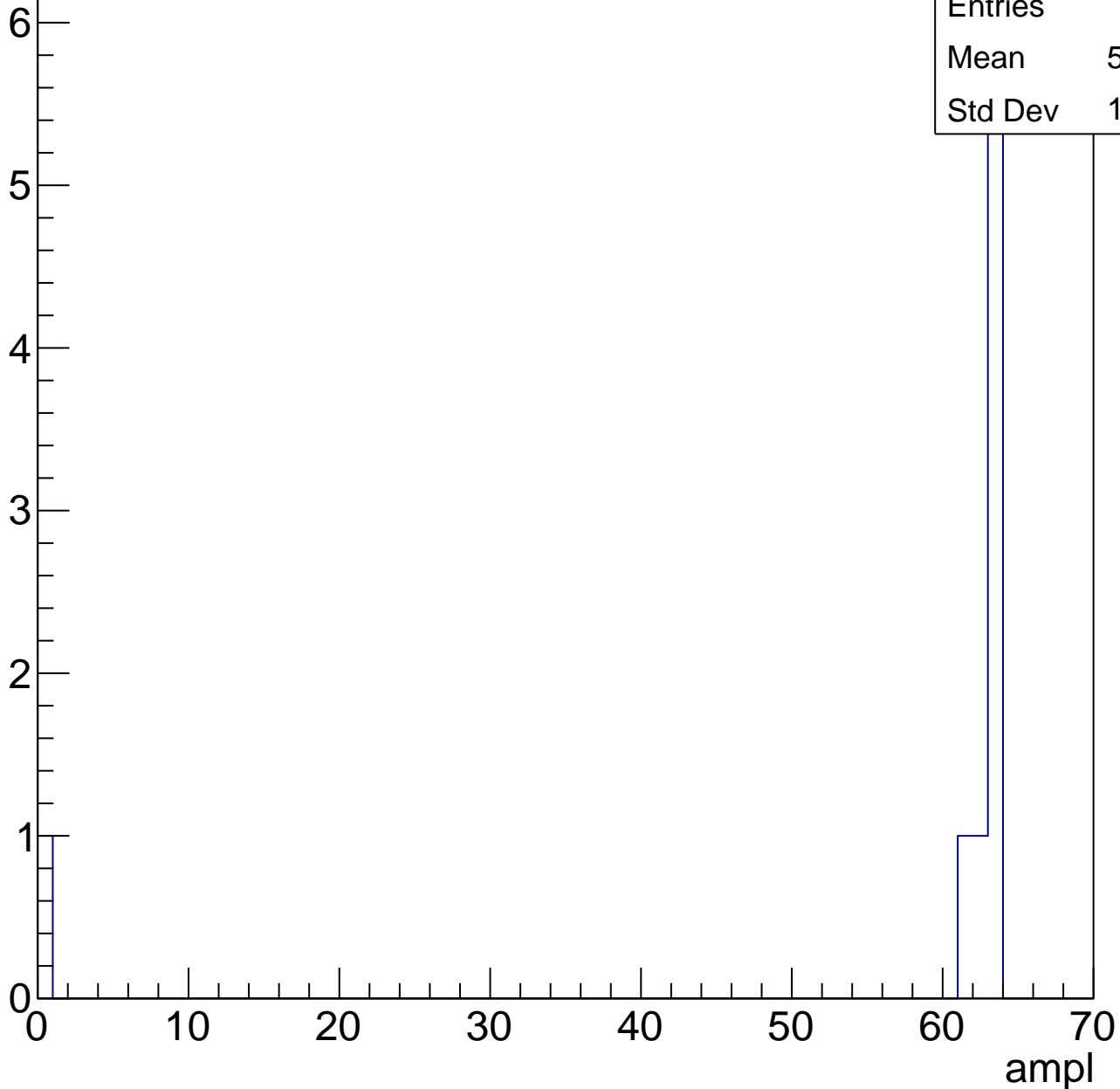


B1L102S, U22-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	55.67
Std Dev	19.69



B1L102S, U22-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U22-ch95, adc0

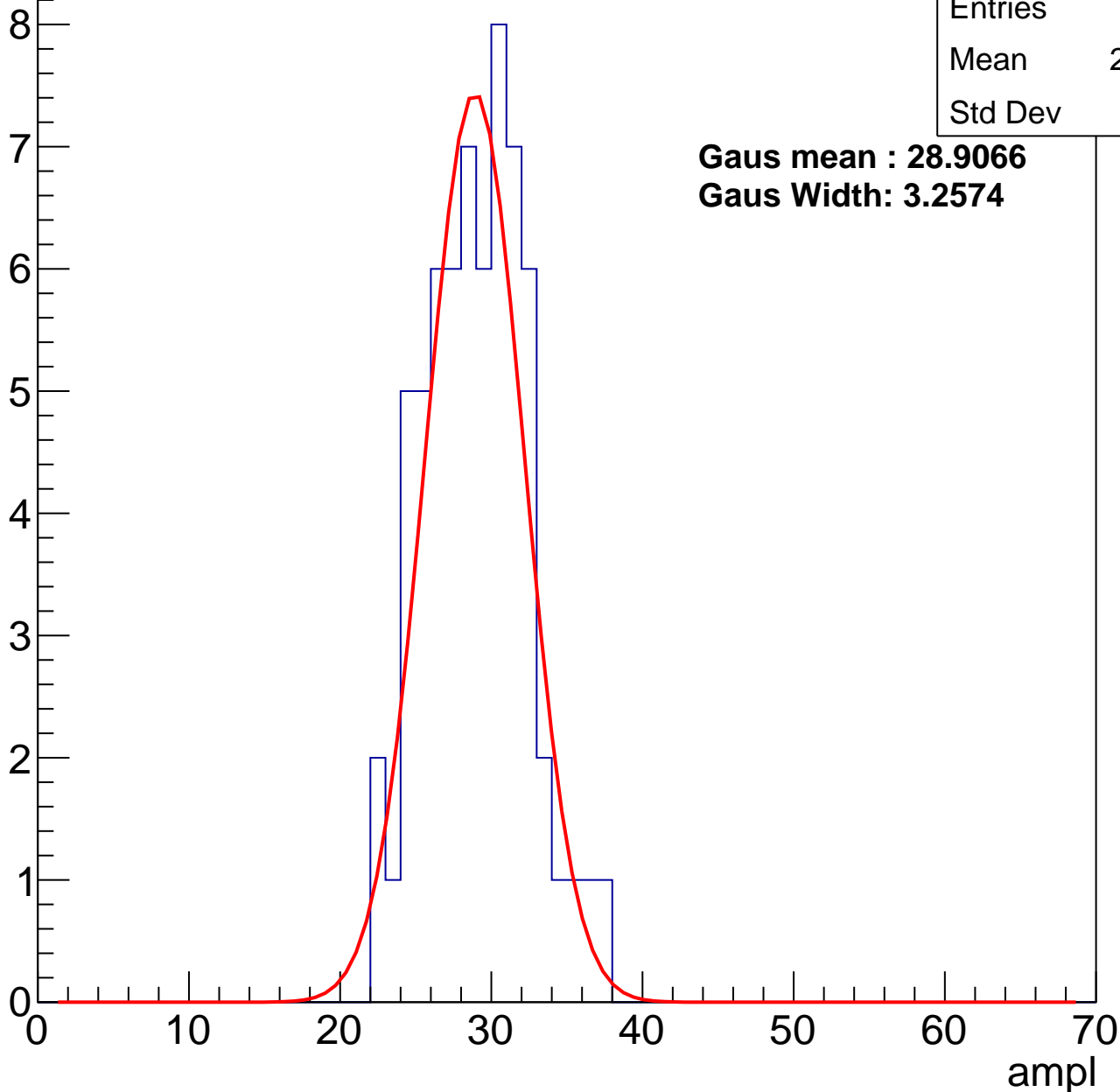
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.57
Std Dev	3.3

Gaus mean : 28.9066

Gaus Width: 3.2574



B1L102S, U22-ch95, adc1

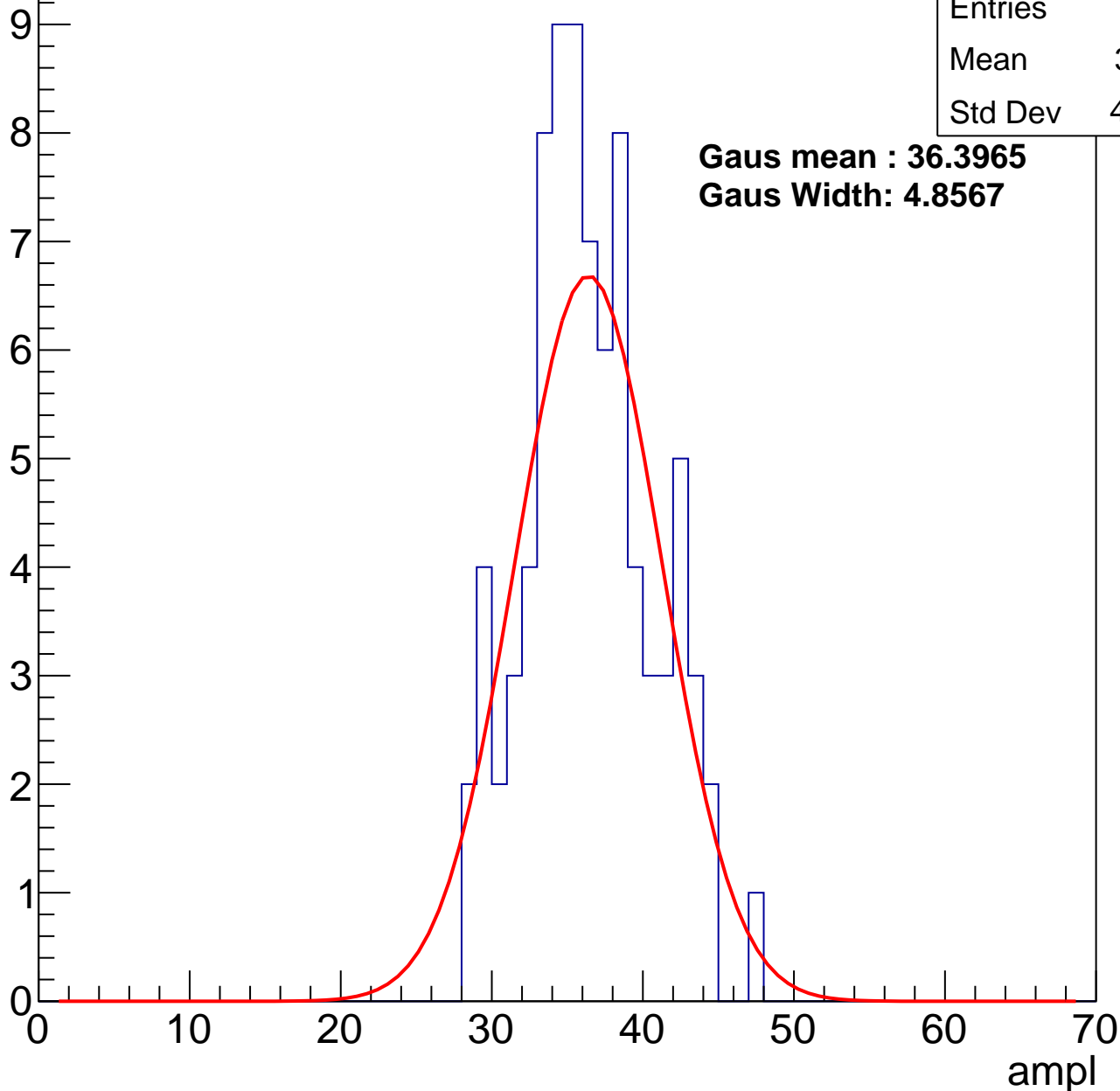
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	36.01
Std Dev	4.143

Gaus mean : 36.3965

Gaus Width: 4.8567



B1L102S, U22-ch95, adc2

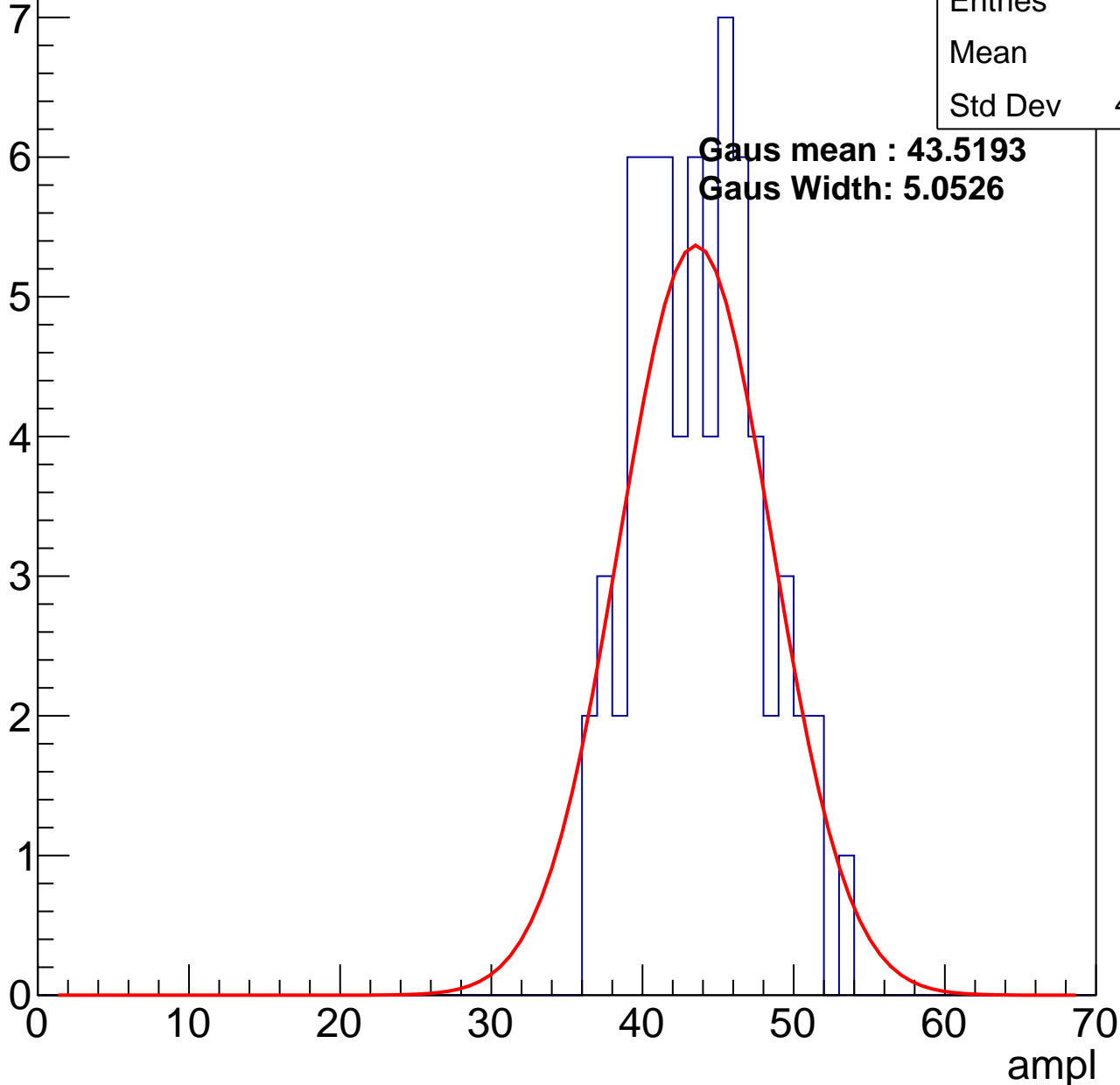
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.3
Std Dev	4.011

Gaus mean : 43.5193

Gaus Width: 5.0526

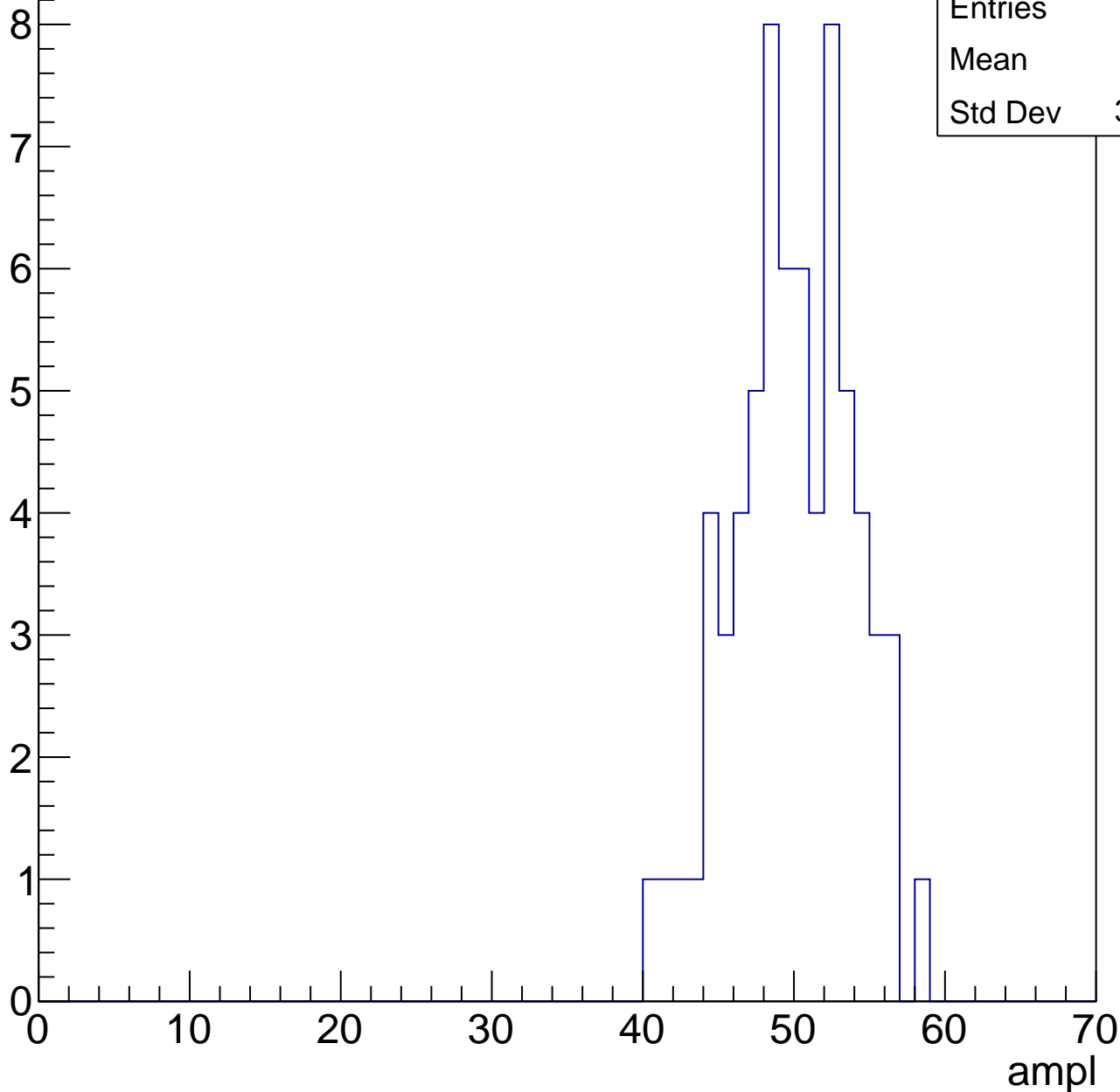


B1L102S, U22-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.5
Std Dev	3.901

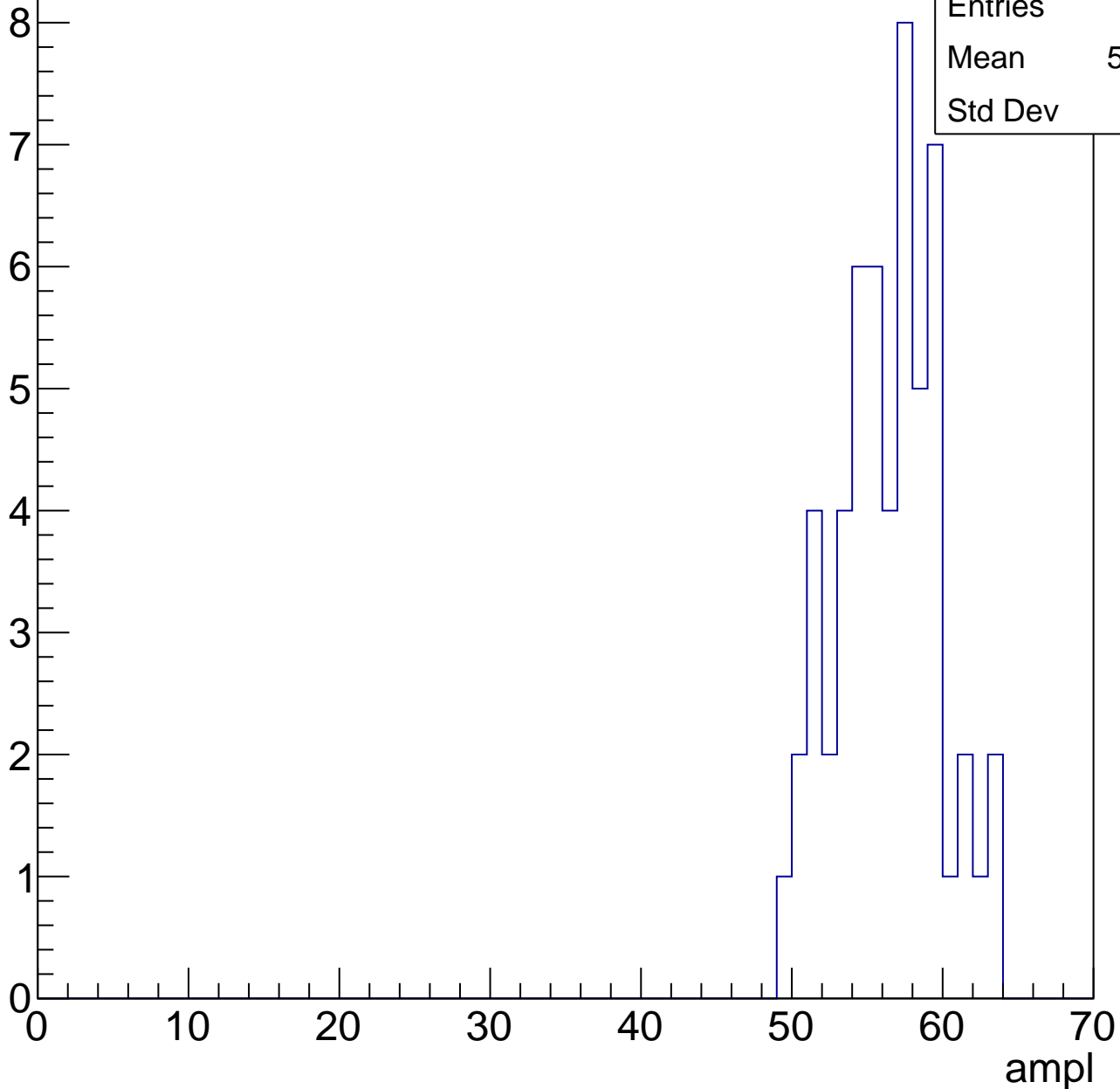


B1L102S, U22-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.93
Std Dev	3.33



B1L102S, U22-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

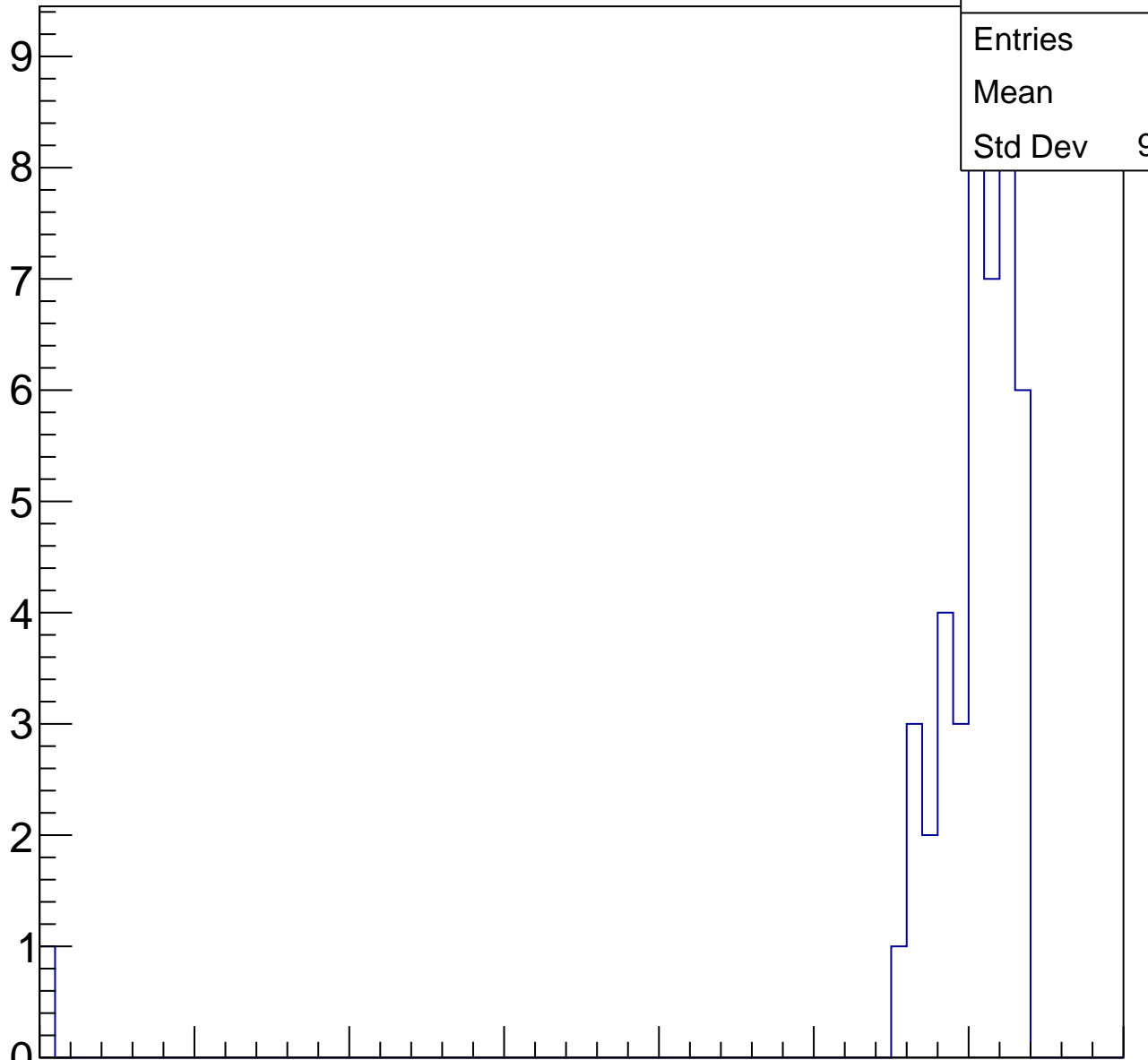
Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	58.8
Std Dev	9.219

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

B1L102S, U22-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch96, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	26.54
Std Dev	3.356

Gaus mean : 26.9264

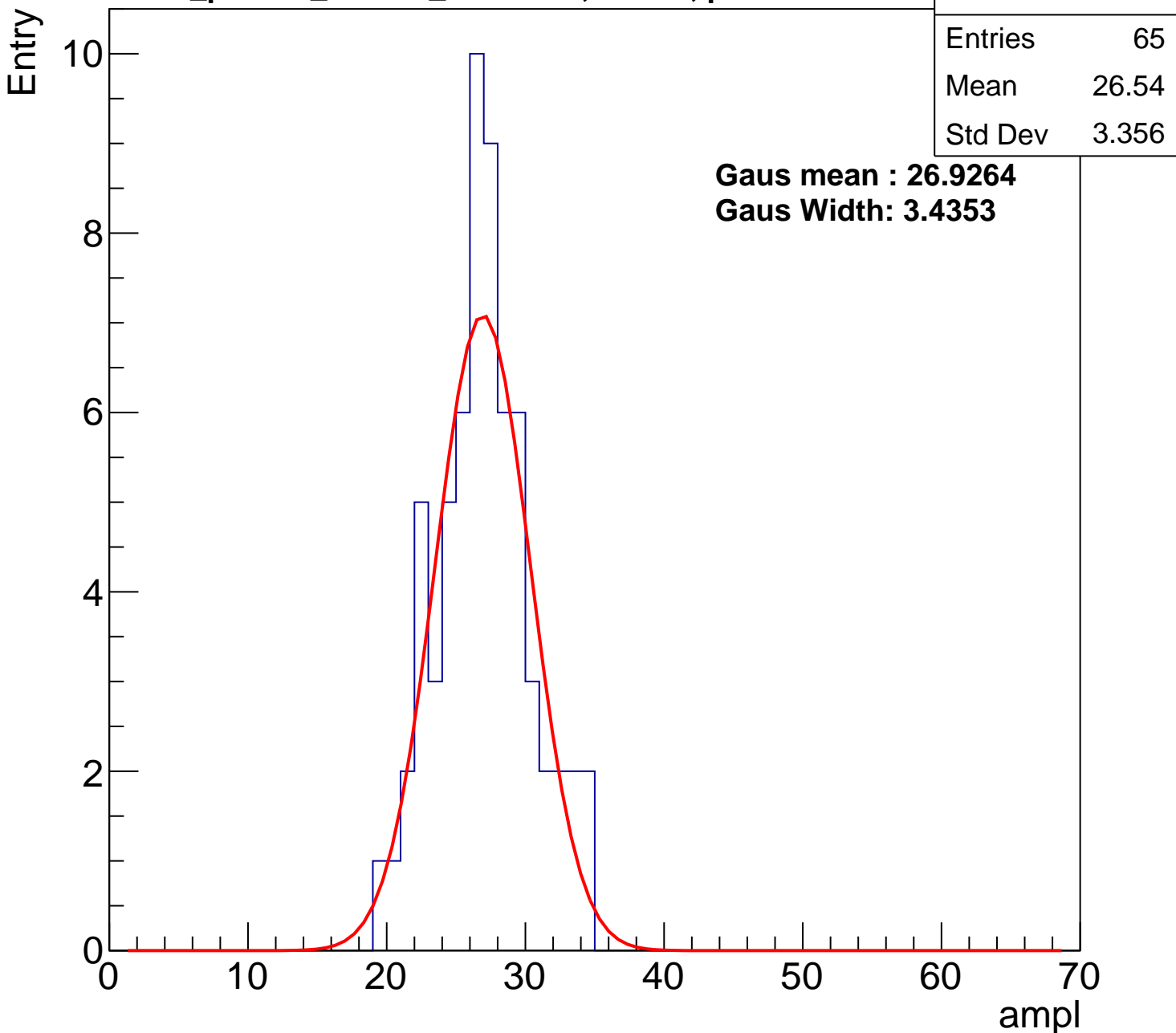
Gaus Width: 3.4353

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch96, adc1

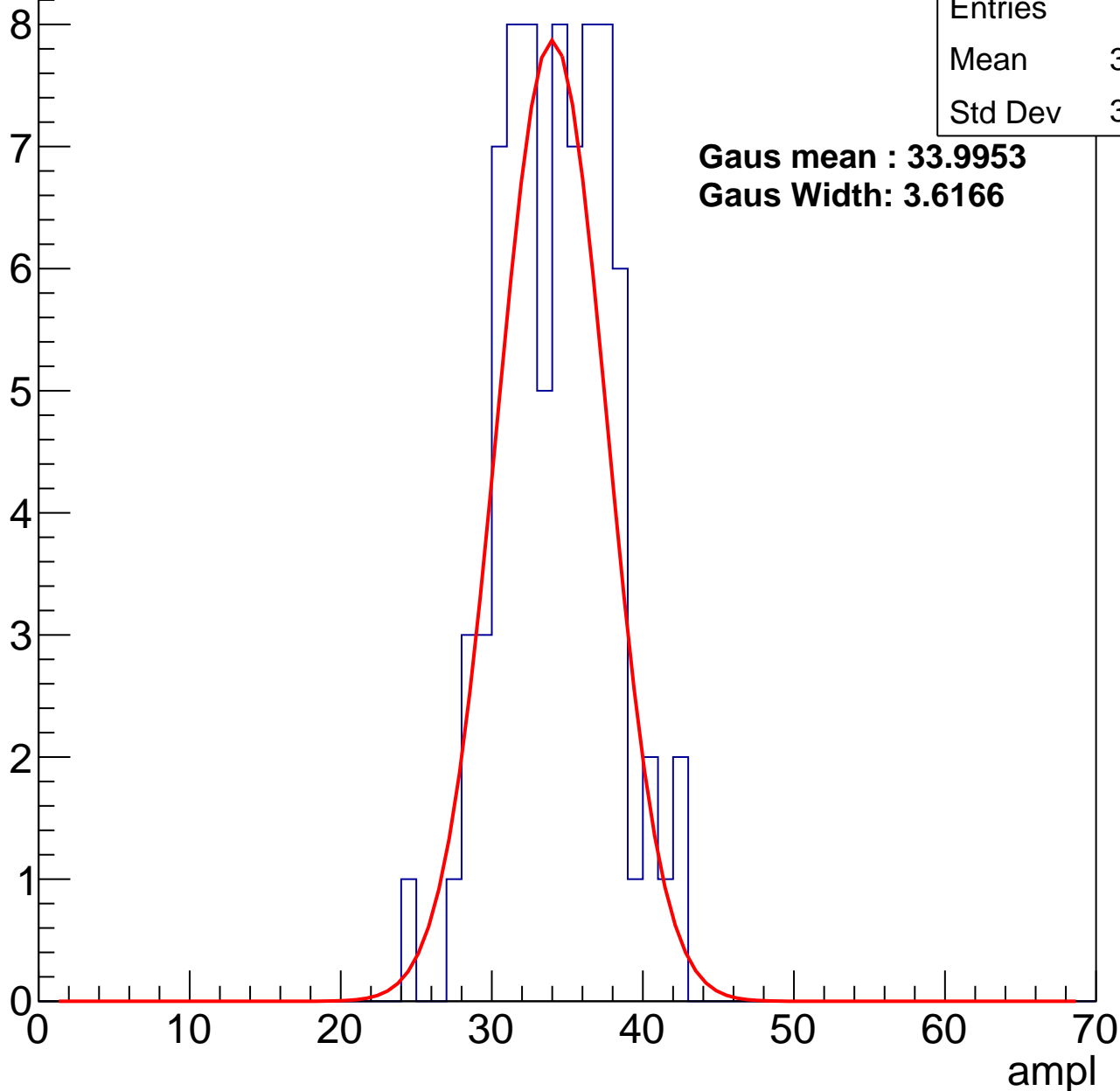
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	33.85
Std Dev	3.615

Gaus mean : 33.9953

Gaus Width: 3.6166



B1L102S, U22-ch96, adc2

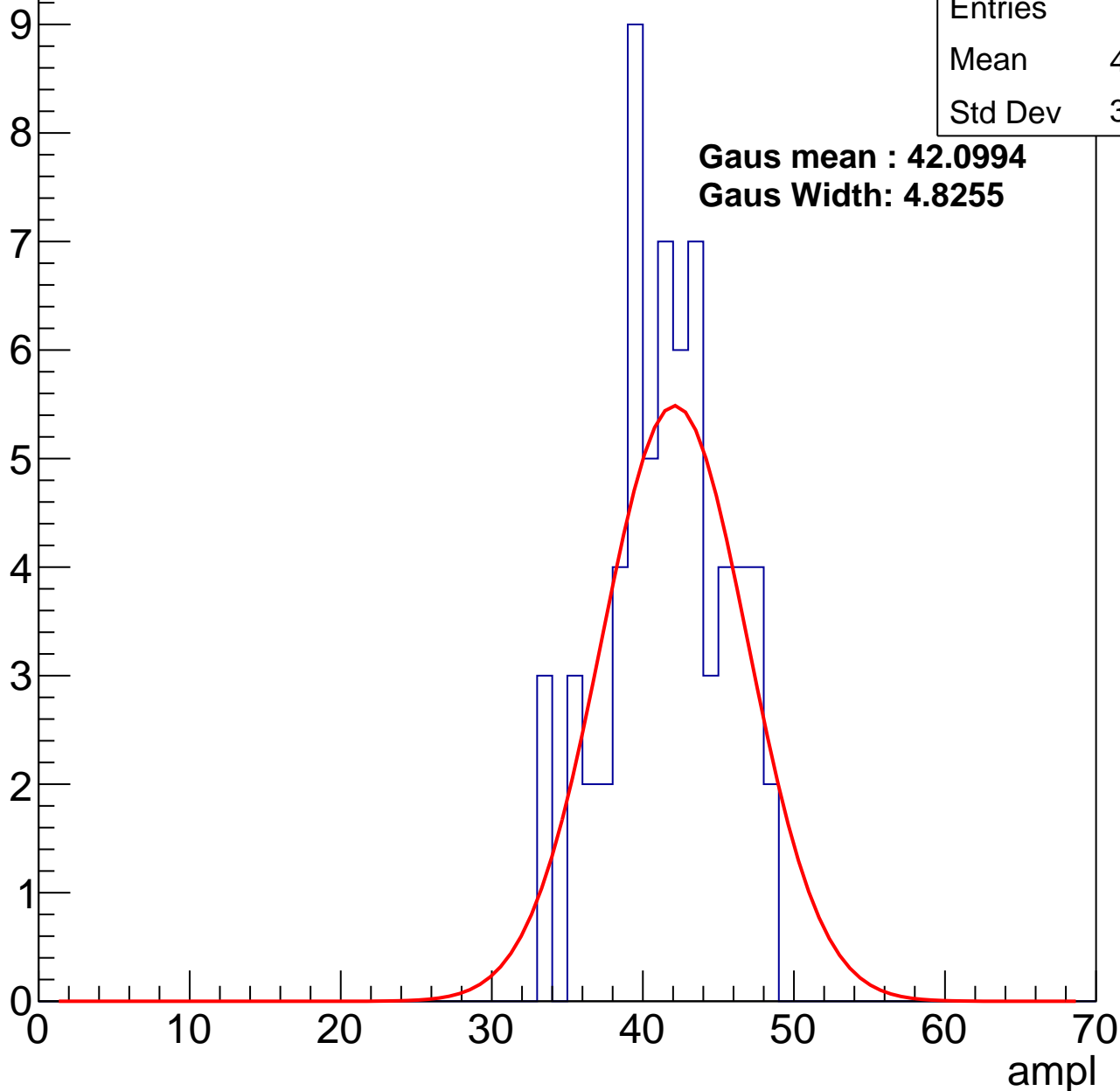
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.12
Std Dev	3.772

Gaus mean : 42.0994

Gaus Width: 4.8255

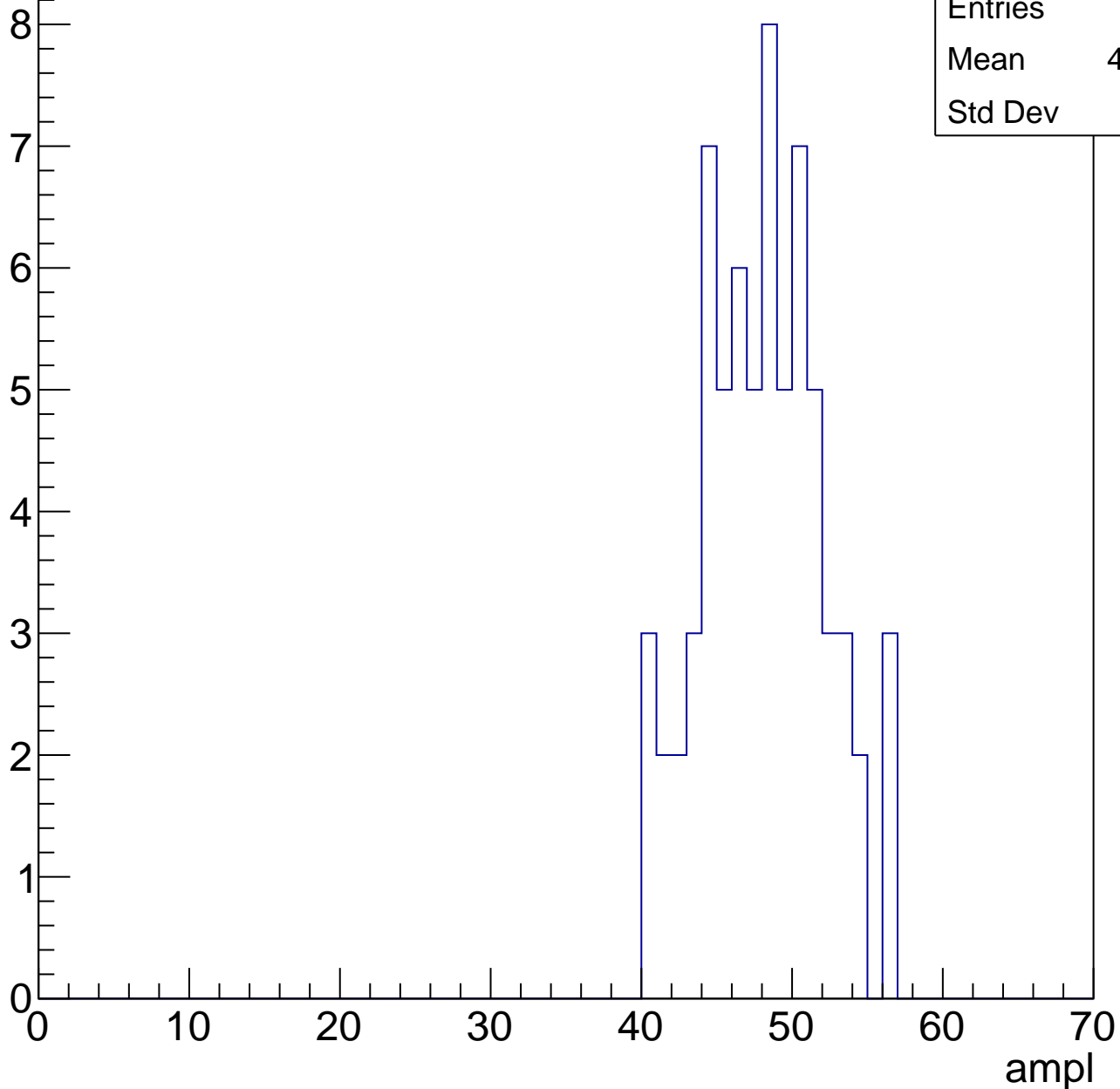


B1L102S, U22-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.59
Std Dev	3.95

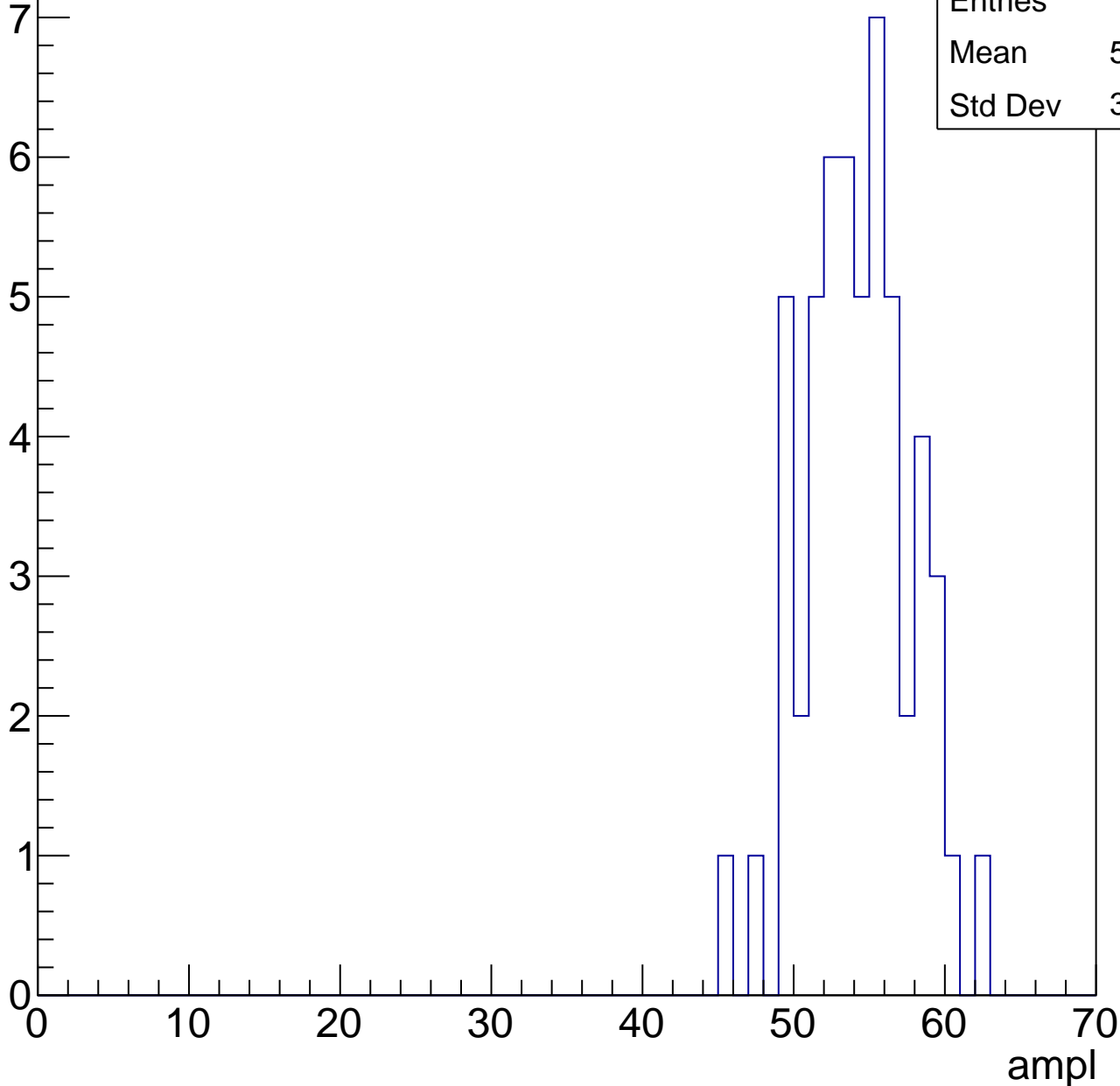


B1L102S, U22-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.74
Std Dev	3.449

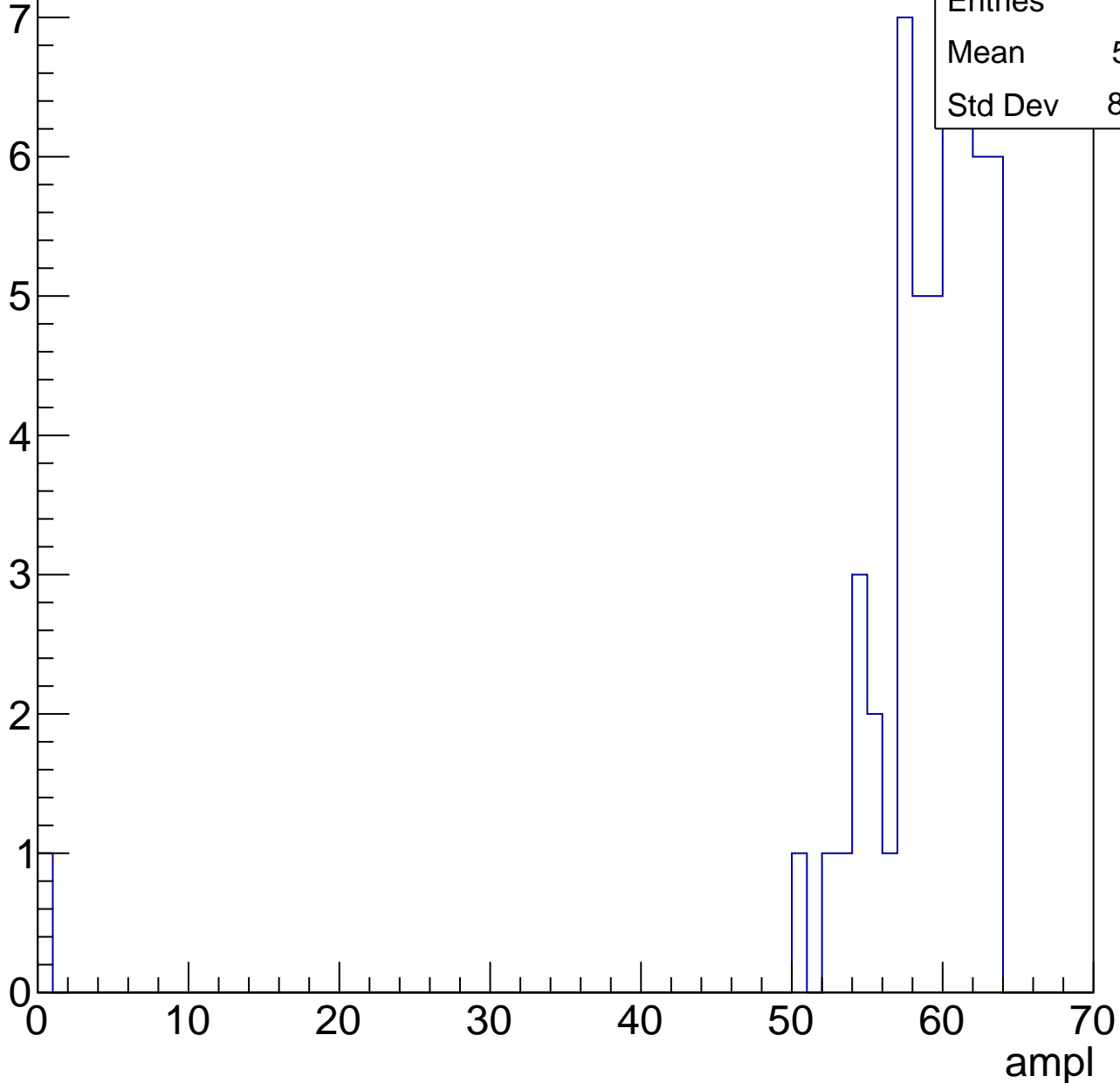


B1L102S, U22-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

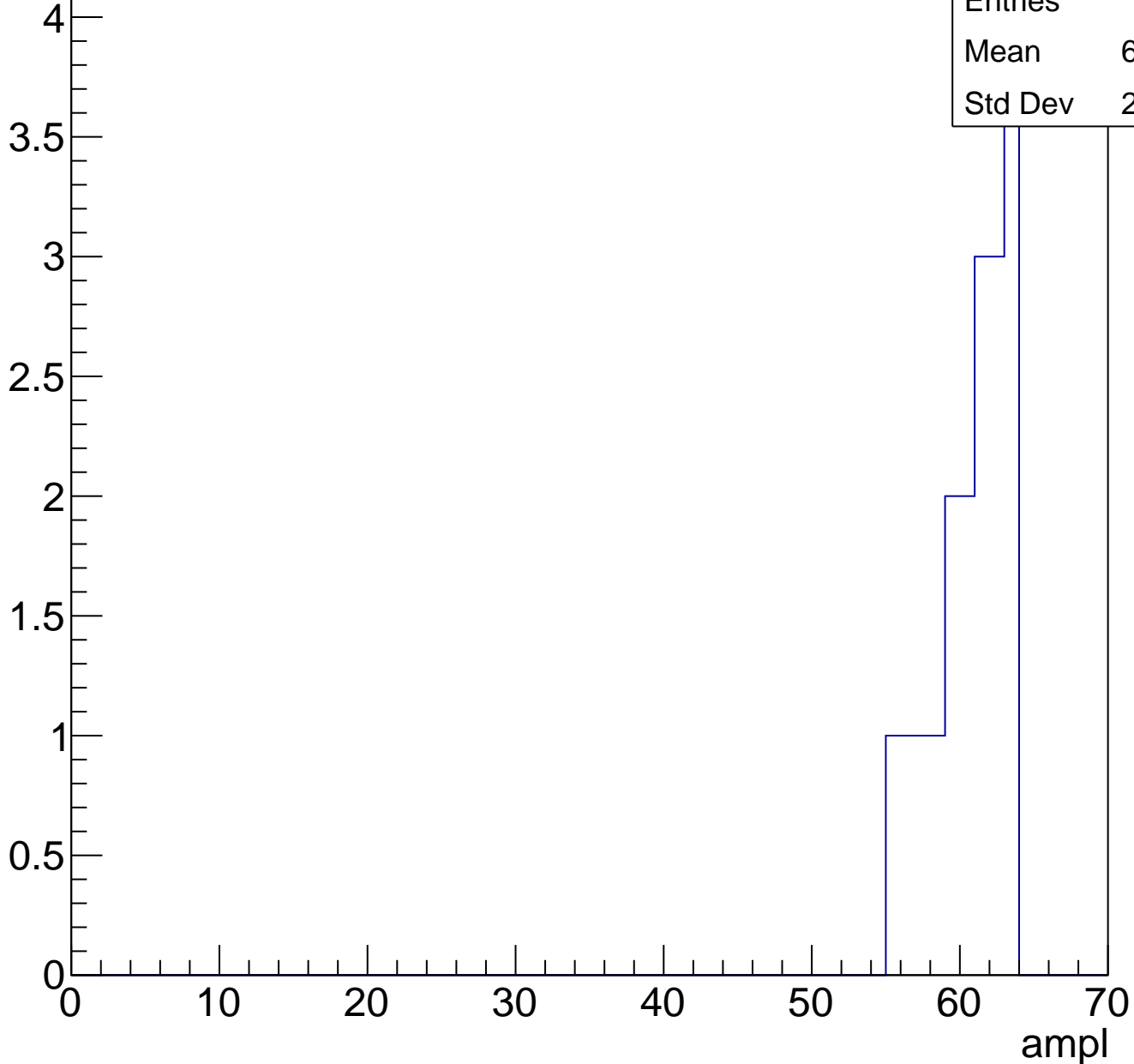
Entries	53
Mean	57.81
Std Dev	8.585



B1L102S, U22-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	18
Mean	60.28
Std Dev	2.422

B1L102S, U22-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U22-ch97, adc0

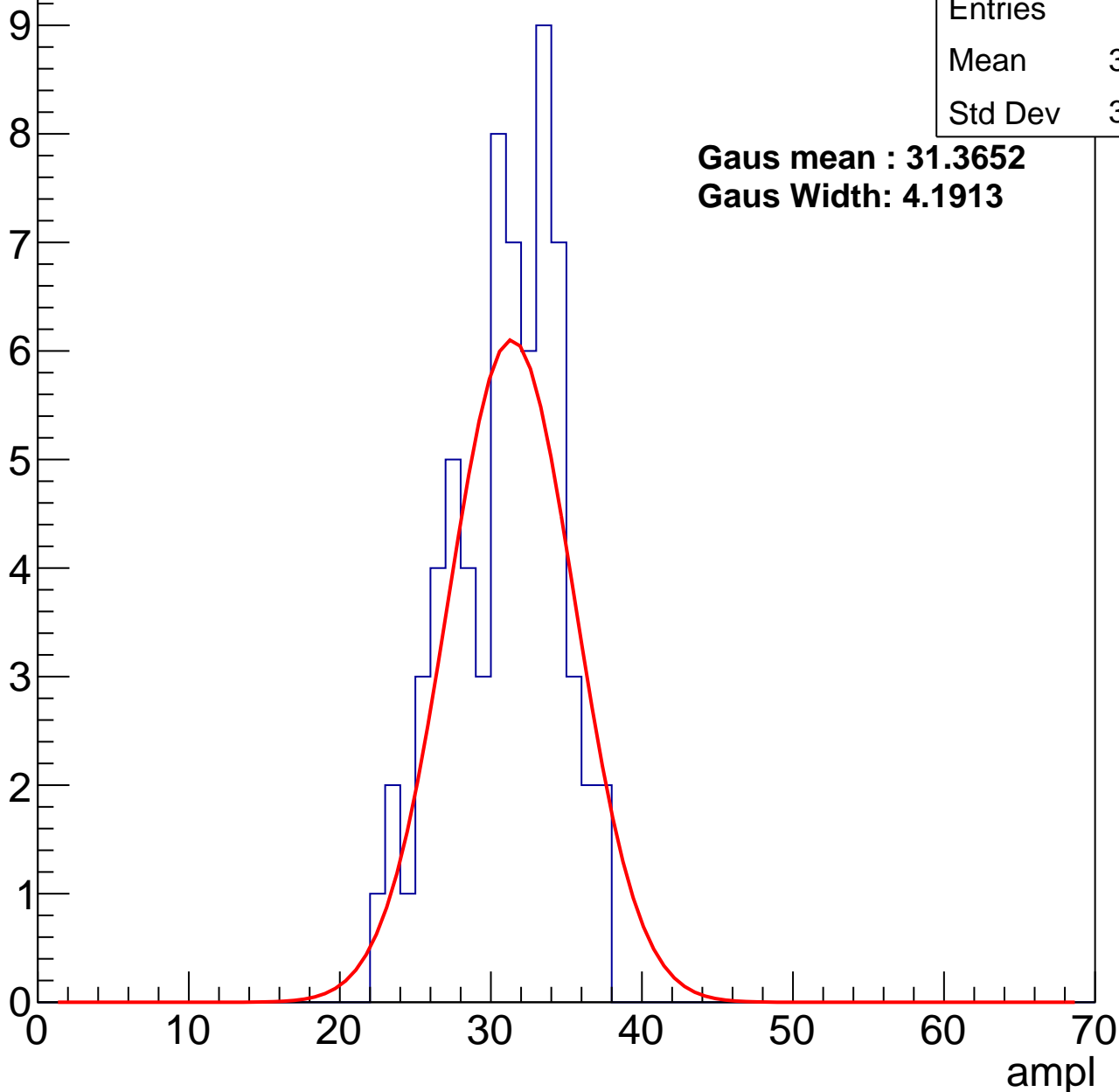
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	30.45
Std Dev	3.576

Gaus mean : 31.3652

Gaus Width: 4.1913



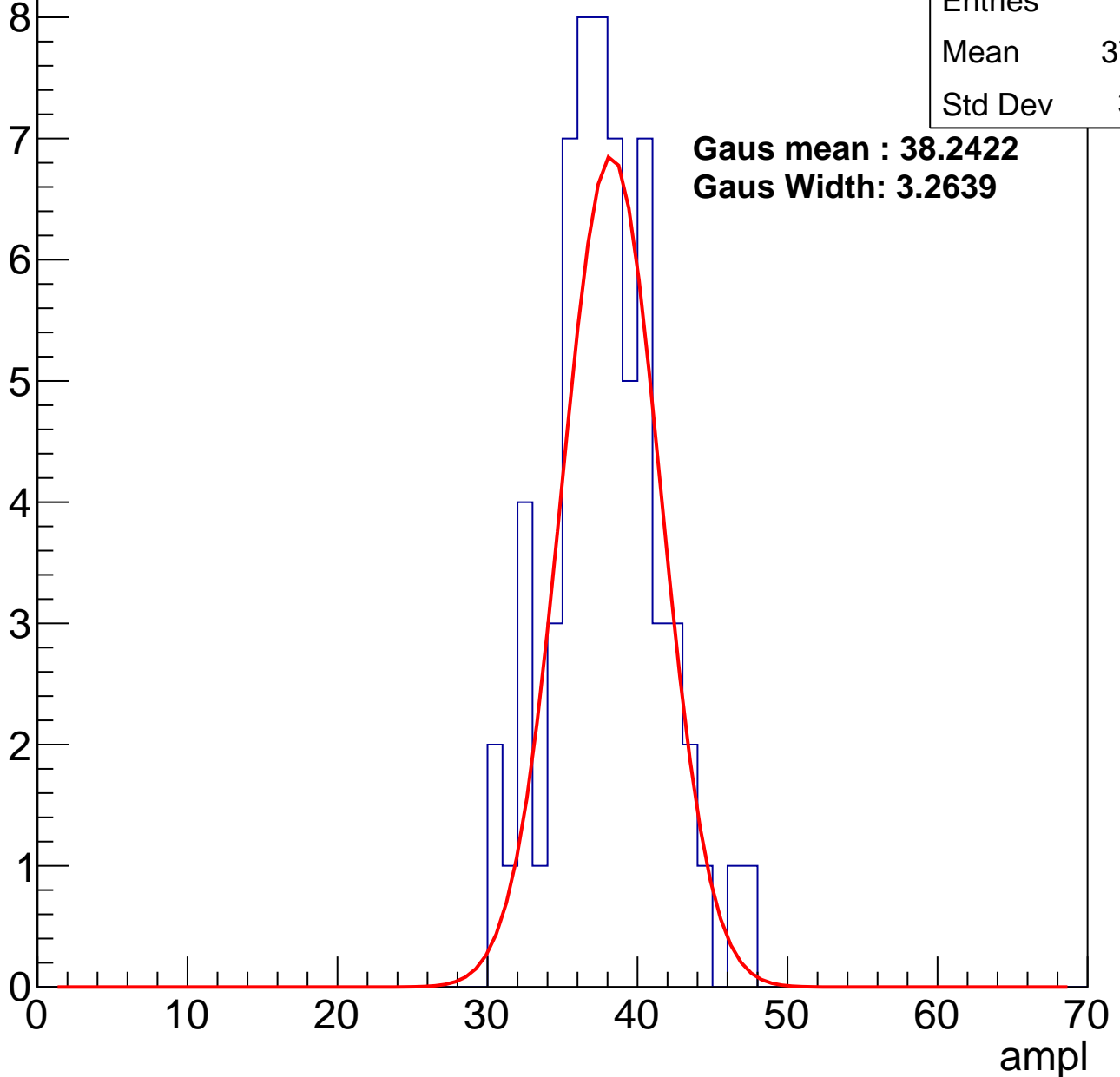
B1L102S, U22-ch97, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.44
Std Dev	3.57

Gaus mean : 38.2422
Gaus Width: 3.2639



B1L102S, U22-ch97, adc2

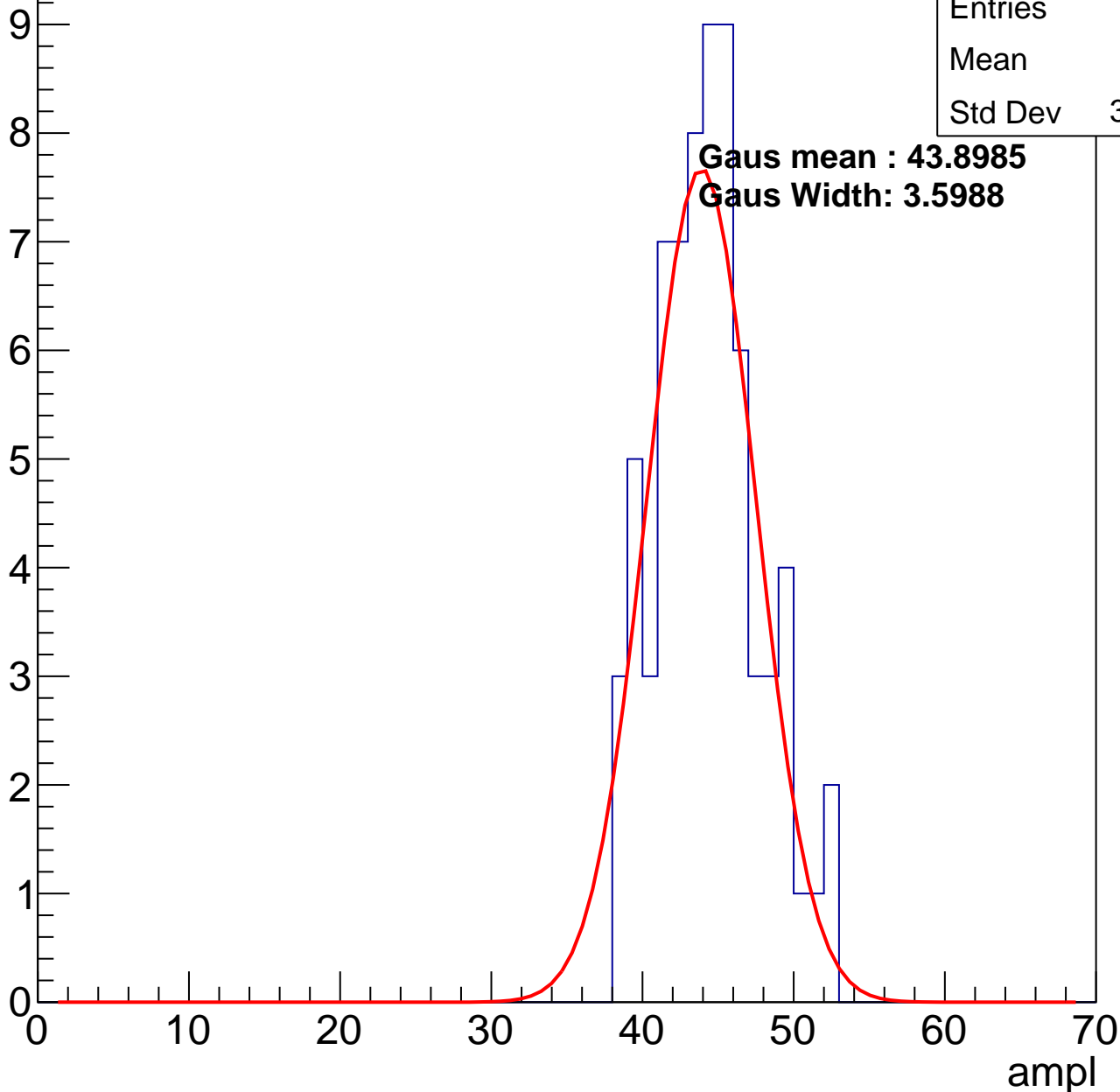
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.9
Std Dev	3.357

Gaus mean : 43.8985

Gaus Width: 3.5988

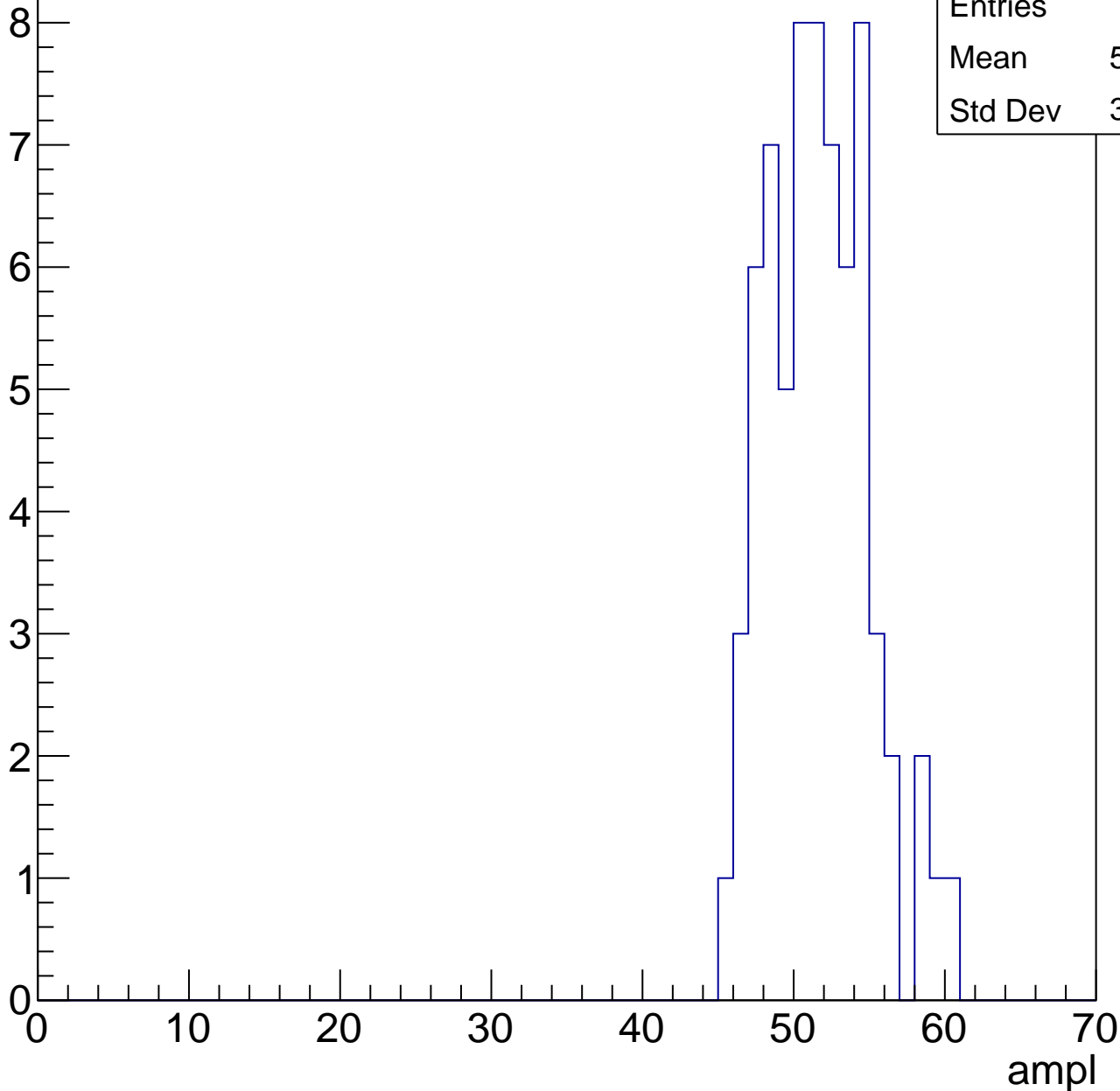


B1L102S, U22-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	51.18
Std Dev	3.299

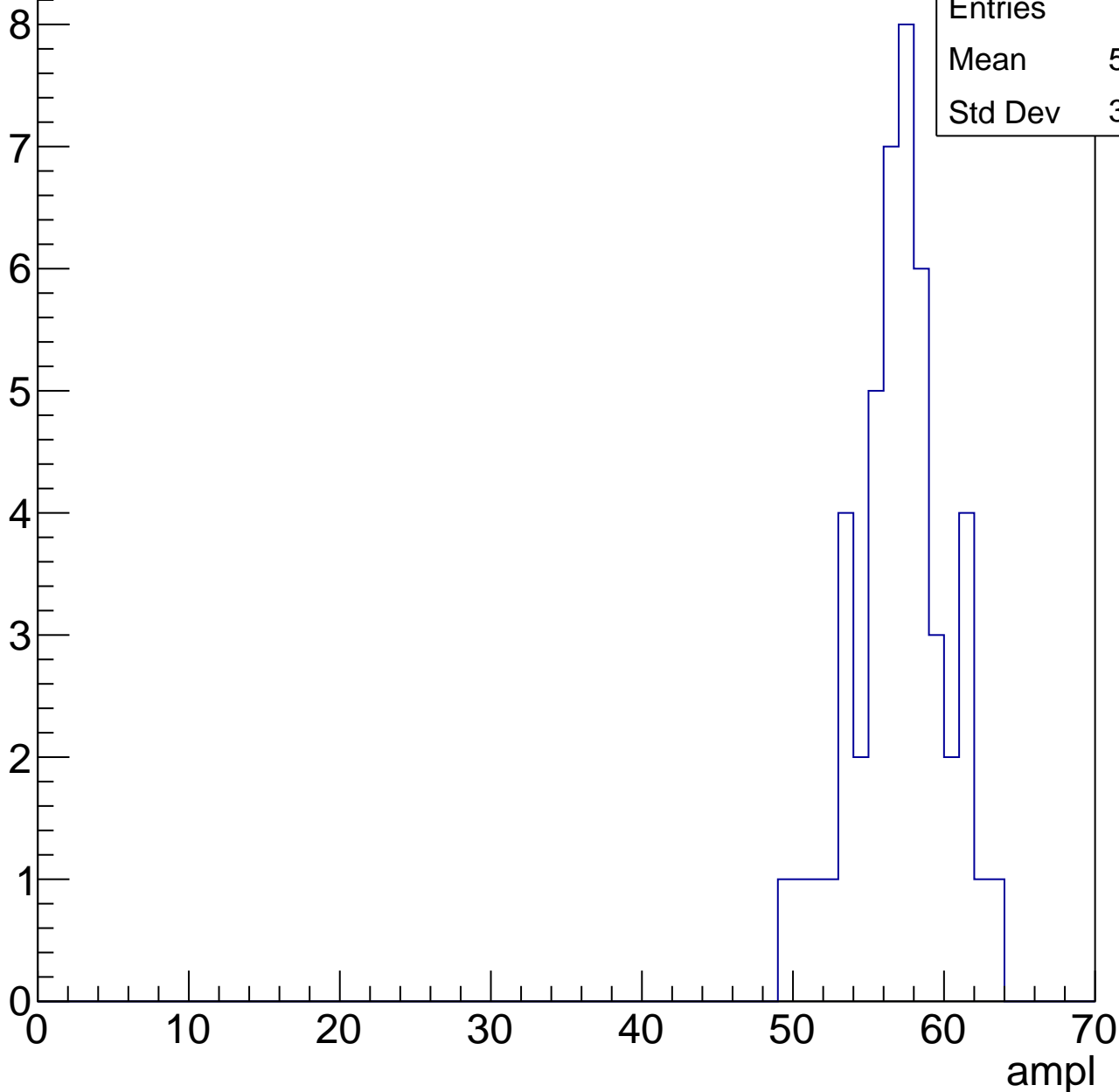


B1L102S, U22-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	56.57
Std Dev	3.044

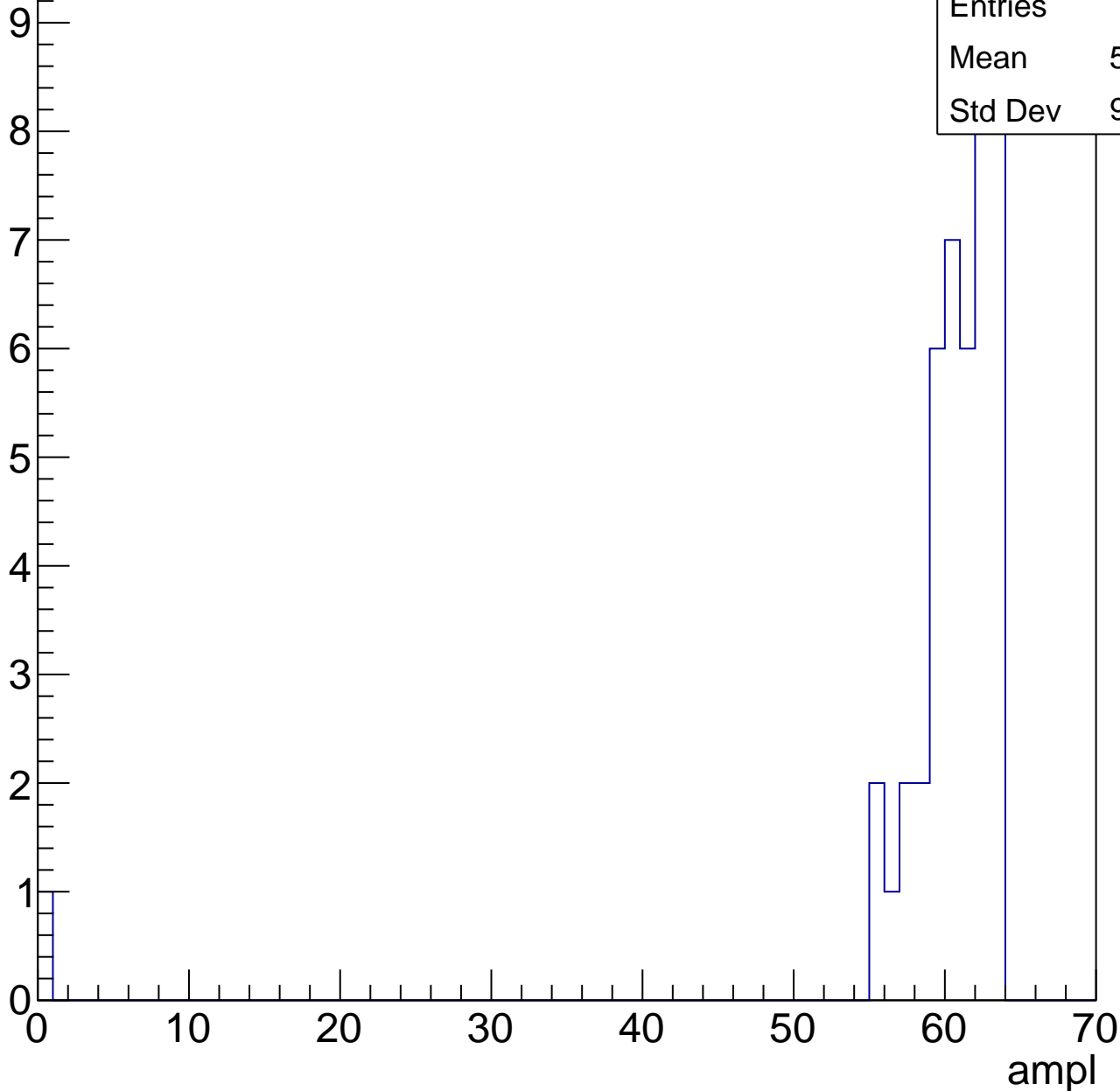


B1L102S, U22-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	59.05
Std Dev	9.259



B1L102S, U22-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U22-ch98, adc0

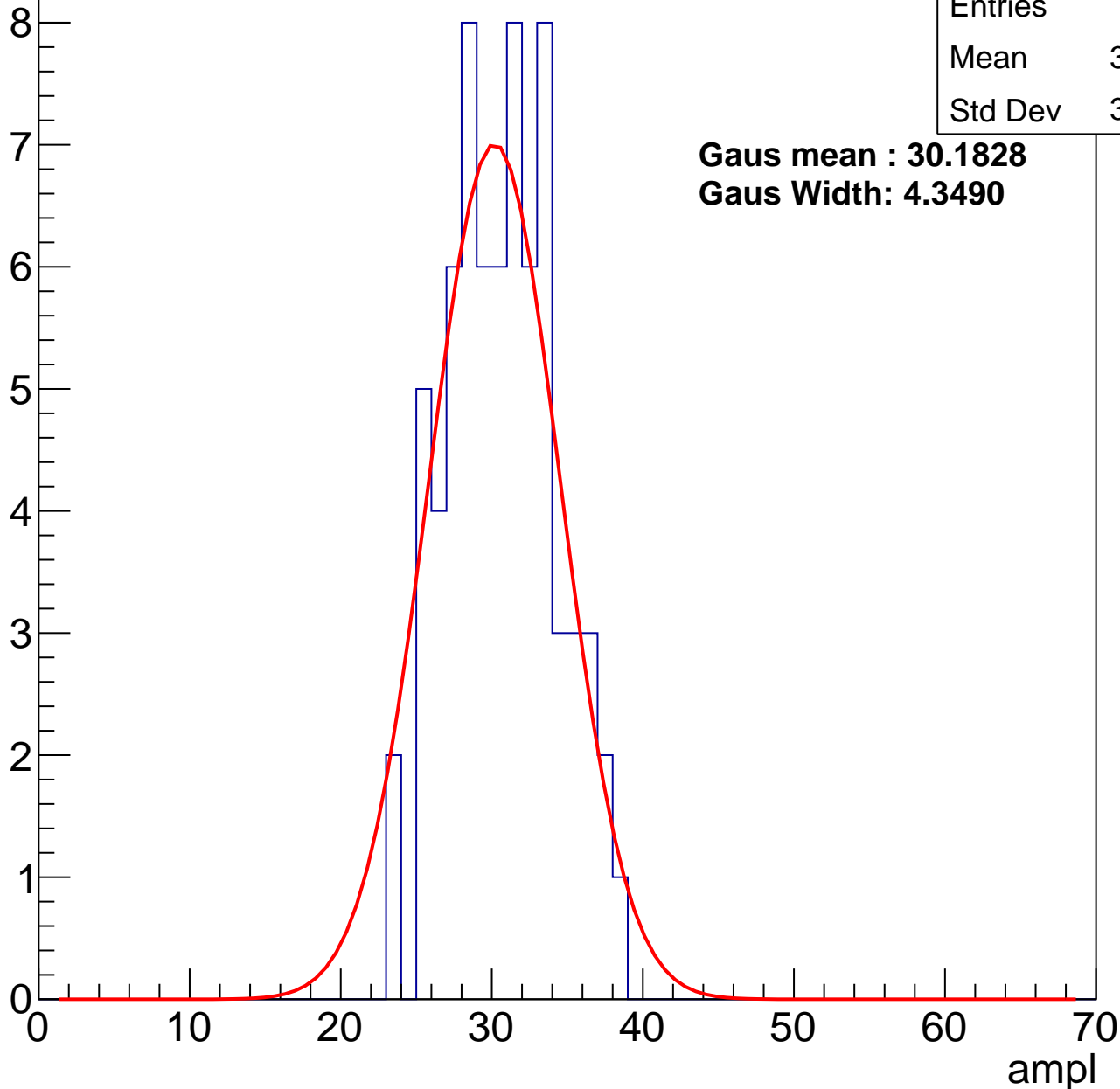
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	30.23
Std Dev	3.505

Gaus mean : 30.1828

Gaus Width: 4.3490



B1L102S, U22-ch98, adc1

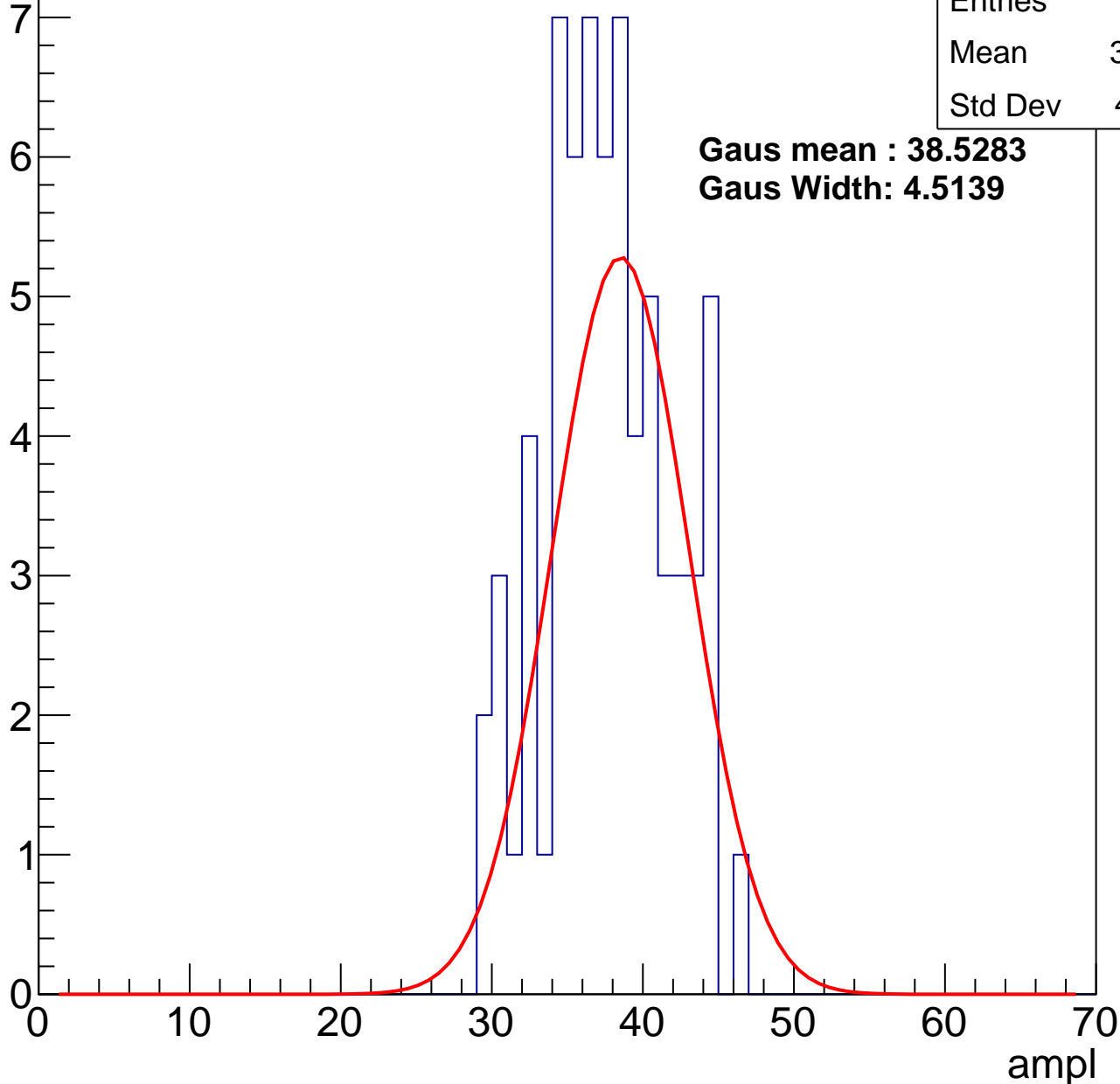
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	37.18
Std Dev	4.101

Gaus mean : 38.5283

Gaus Width: 4.5139



B1L102S, U22-ch98, adc2

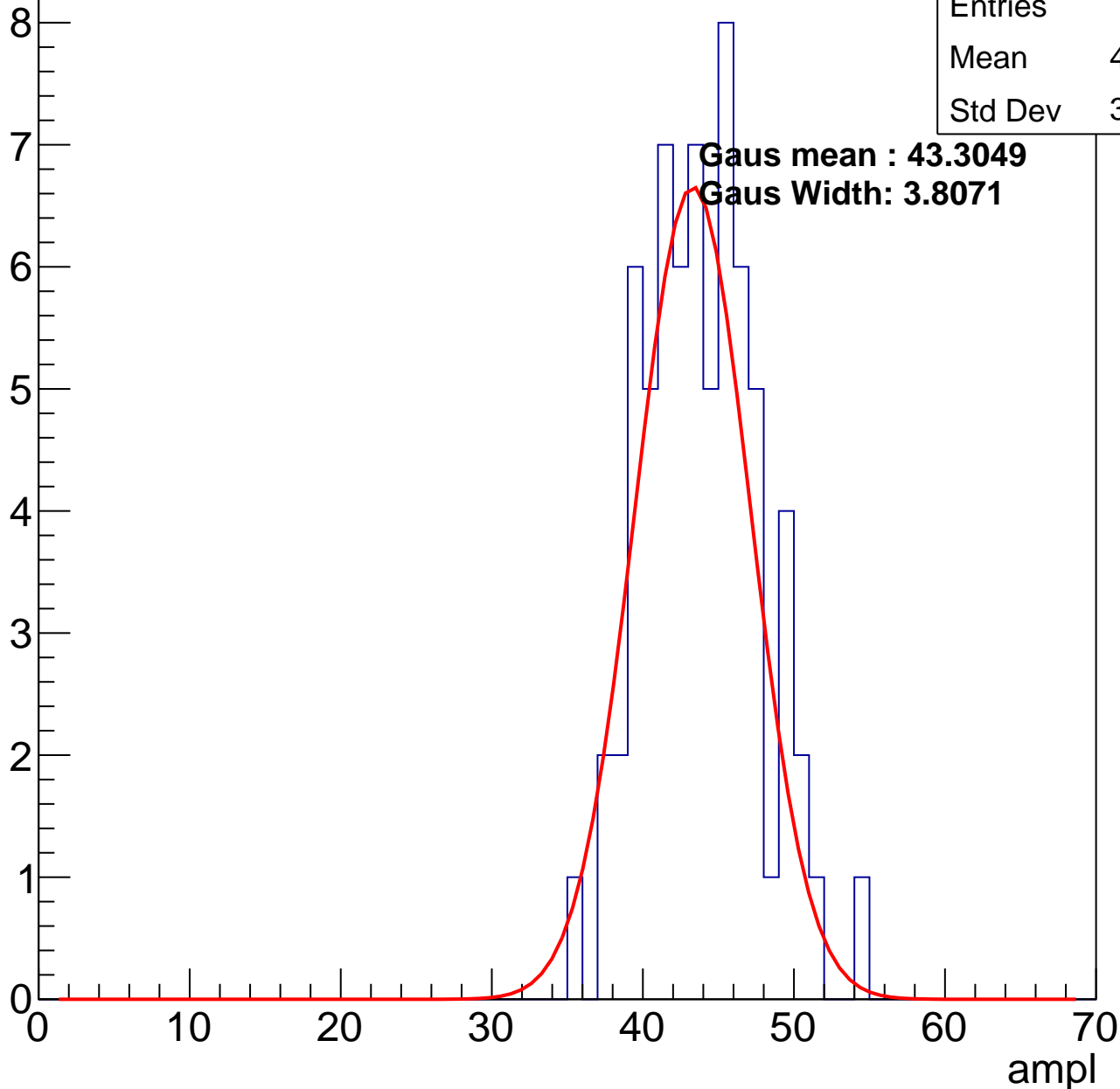
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.46
Std Dev	3.748

Gaus mean : 43.3049

Gaus Width: 3.8071

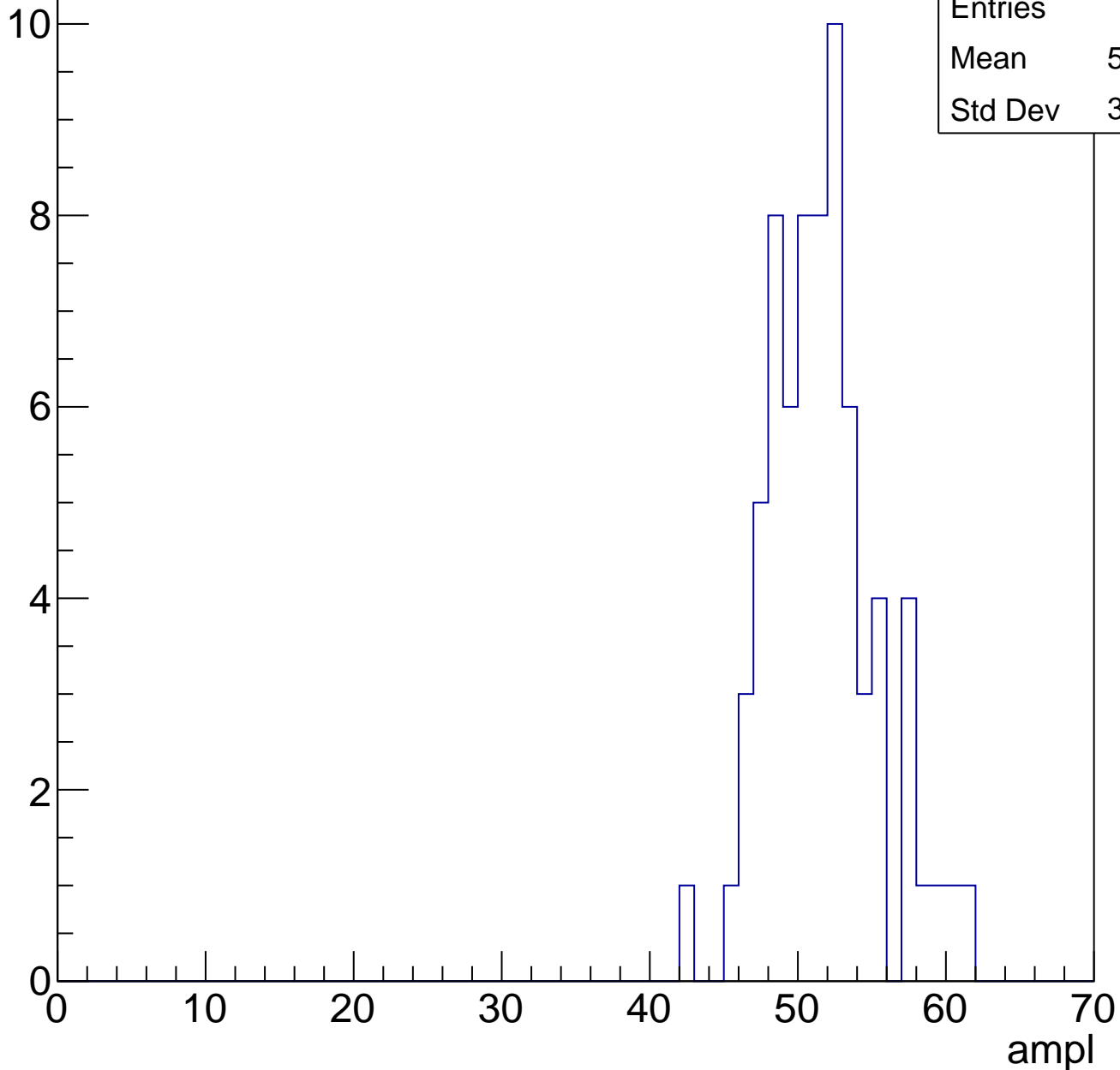


B1L102S, U22-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	51.15
Std Dev	3.633

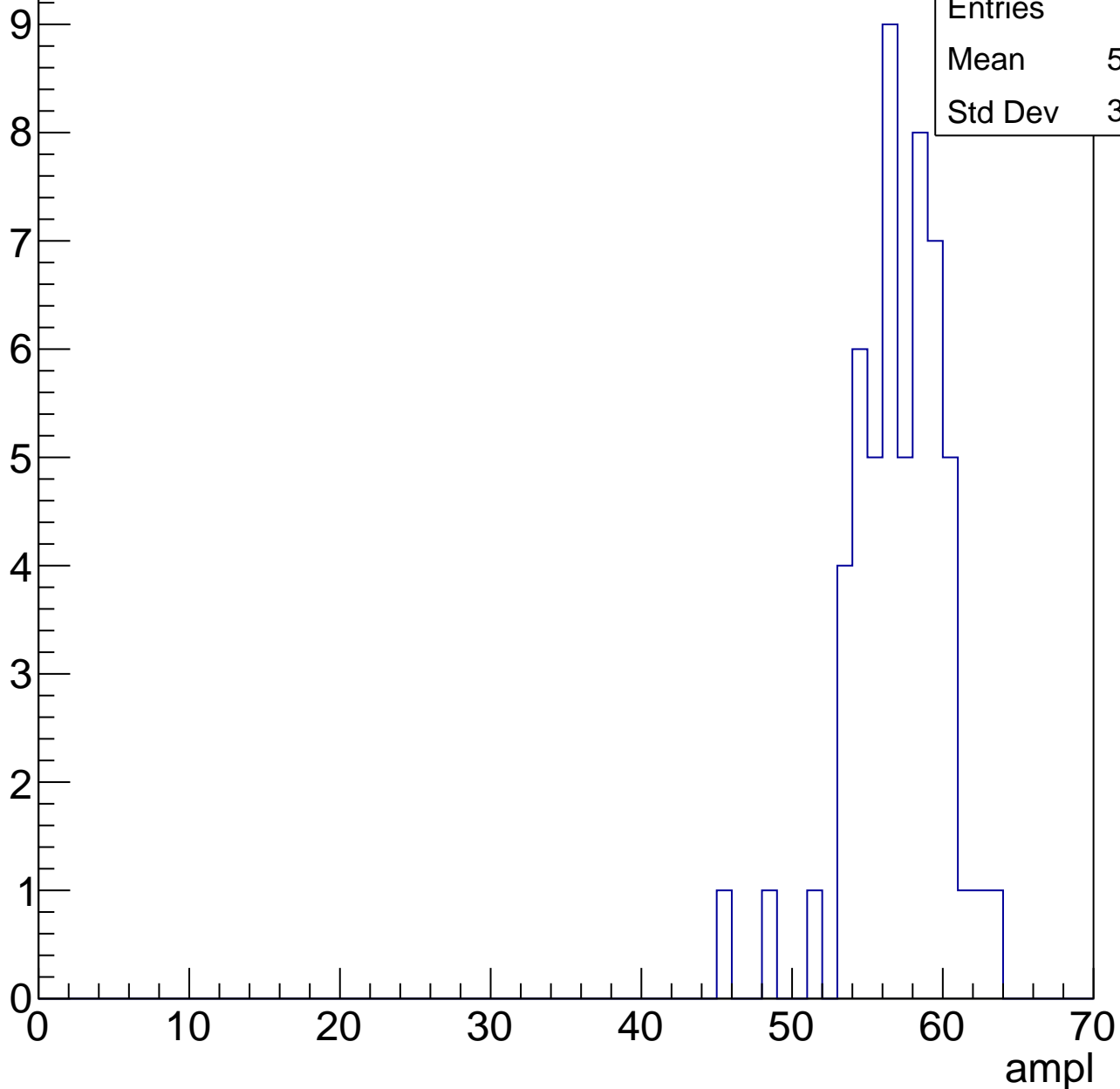
Entry



B1L102S, U22-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

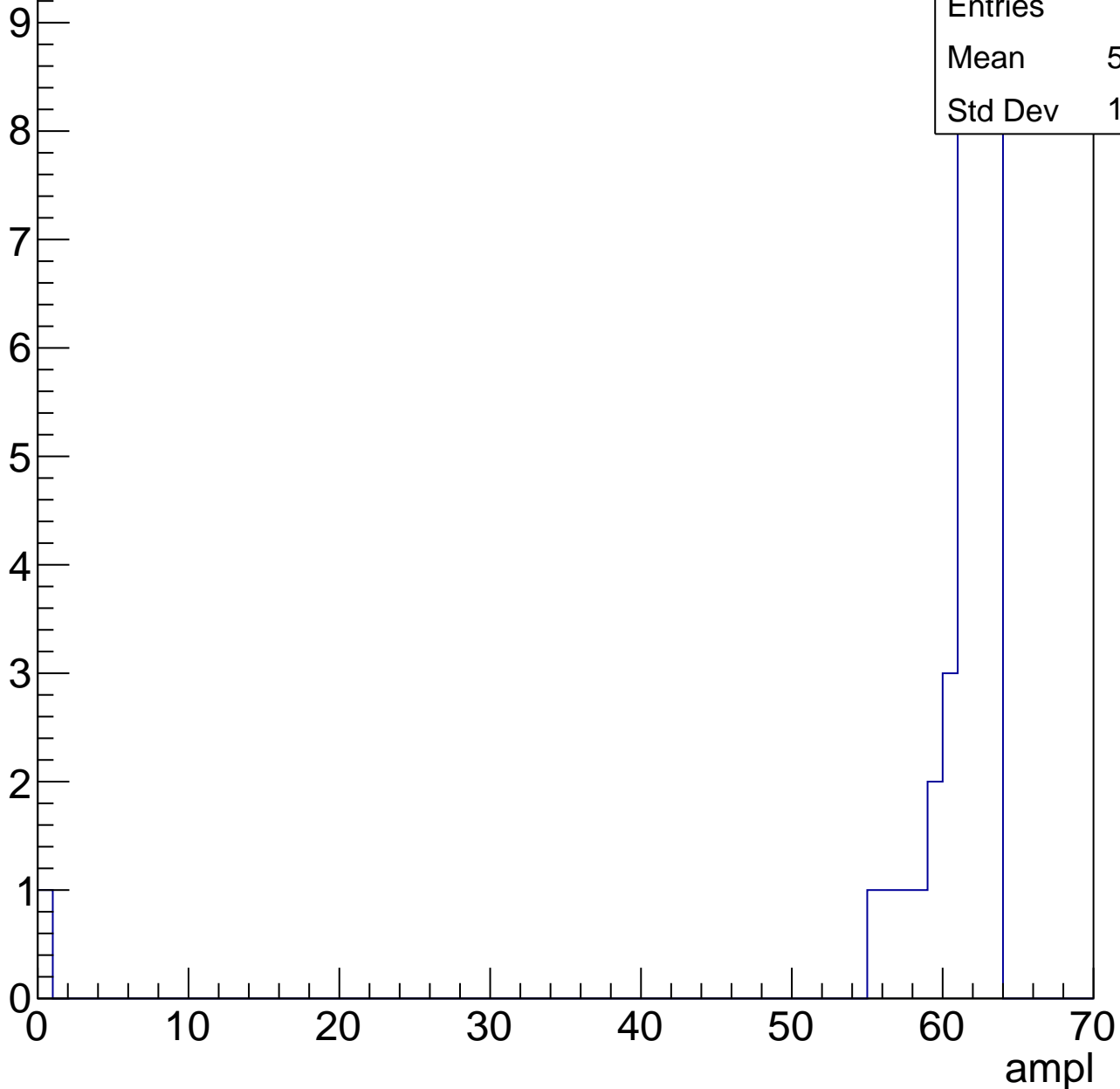
Entry



B1L102S, U22-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U22-ch99, adc0

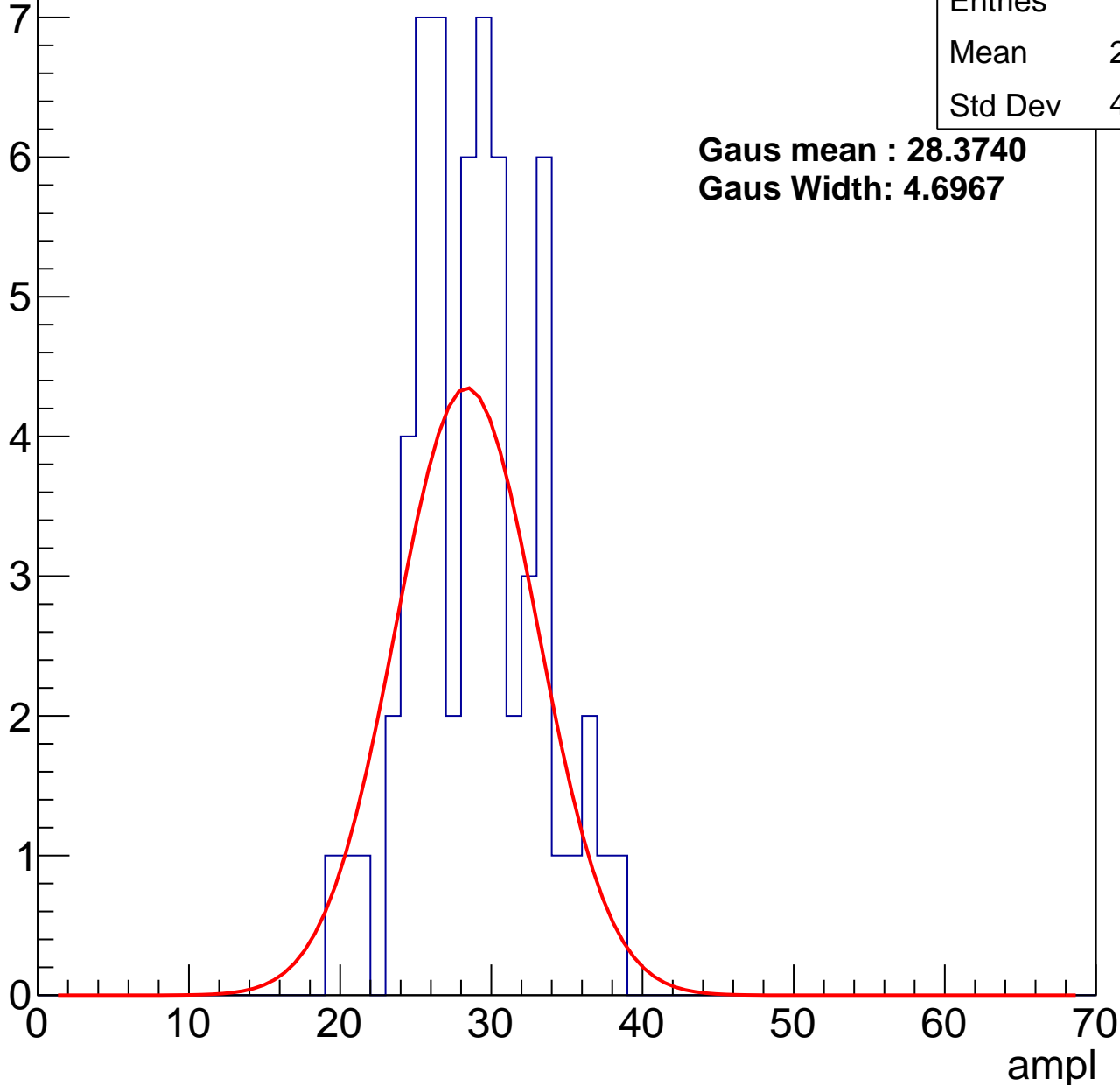
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.46
Std Dev	4.107

Gaus mean : 28.3740

Gaus Width: 4.6967



B1L102S, U22-ch99, adc1

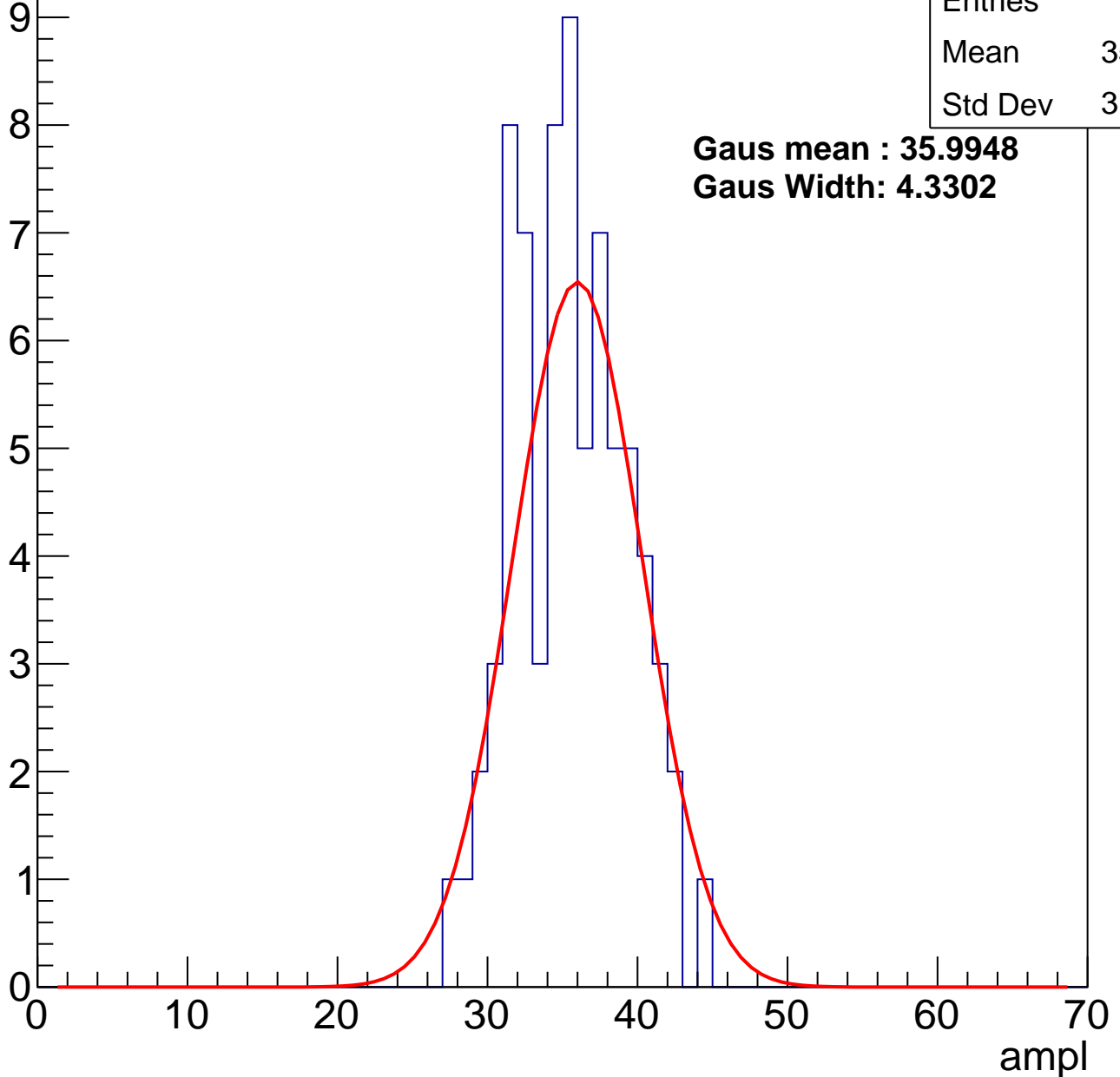
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.08
Std Dev	3.704

Gaus mean : 35.9948

Gaus Width: 4.3302



B1L102S, U22-ch99, adc2

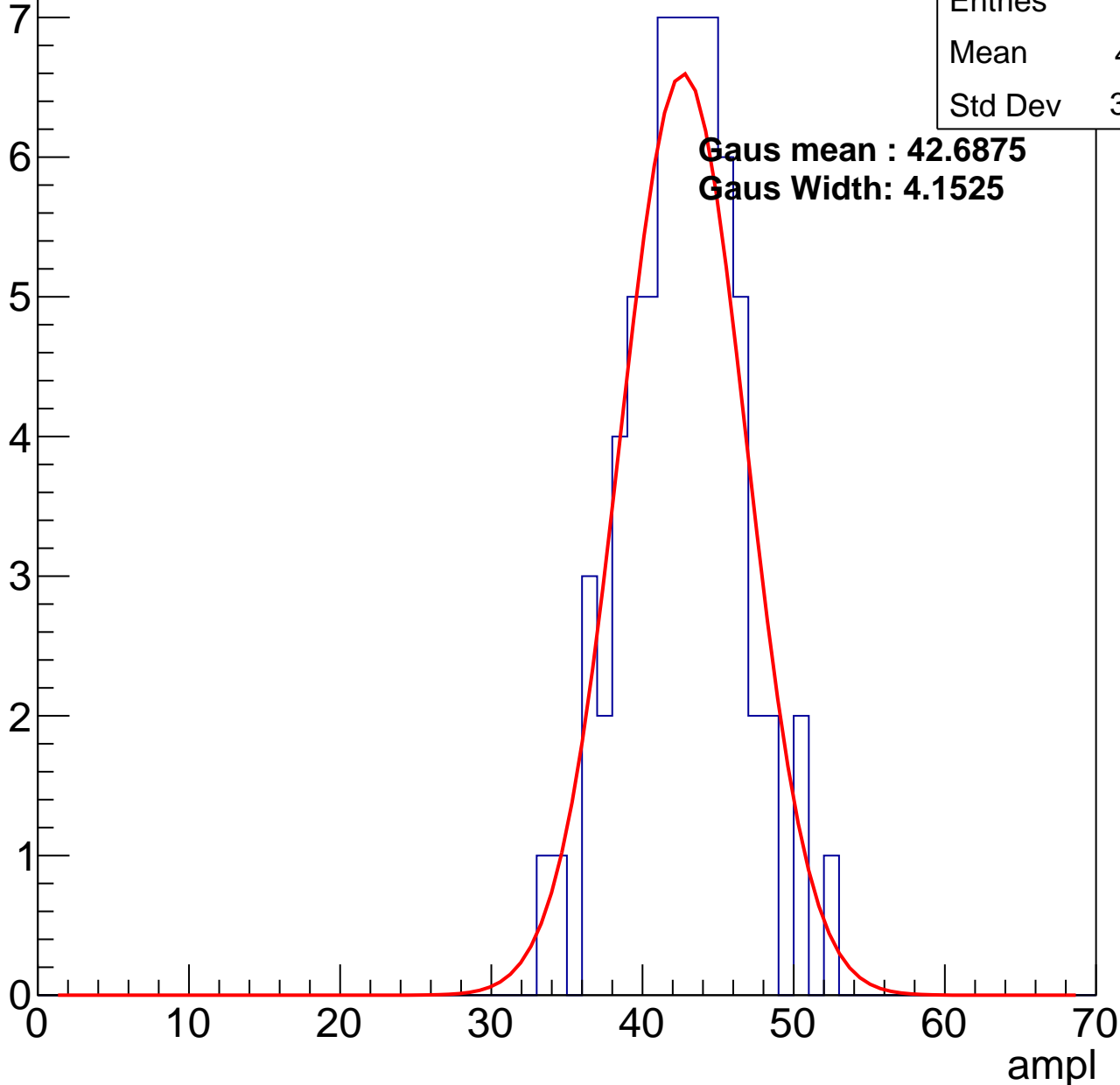
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.21
Std Dev	3.776

Gaus mean : 42.6875

Gaus Width: 4.1525

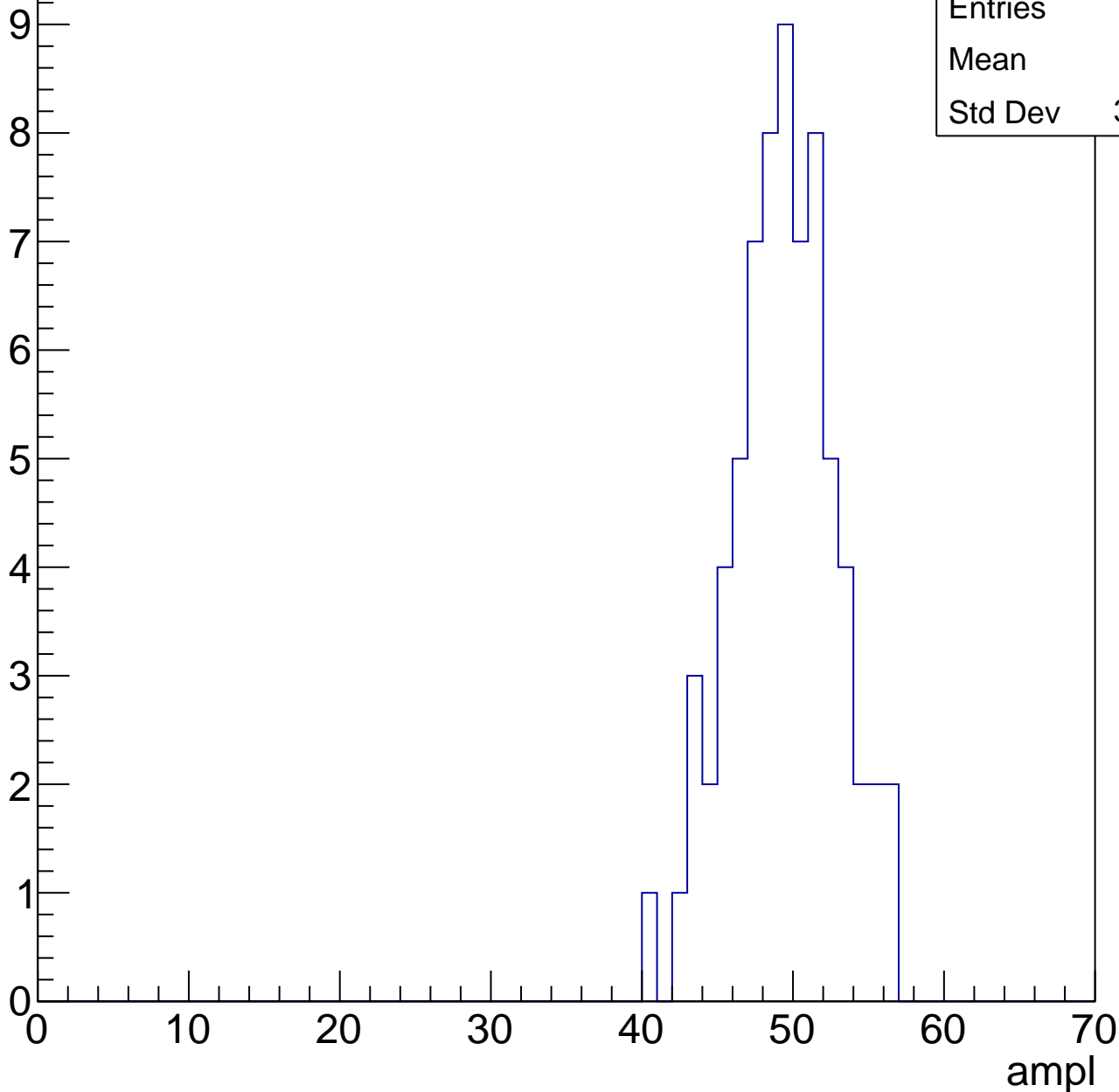


B1L102S, U22-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.9
Std Dev	3.381

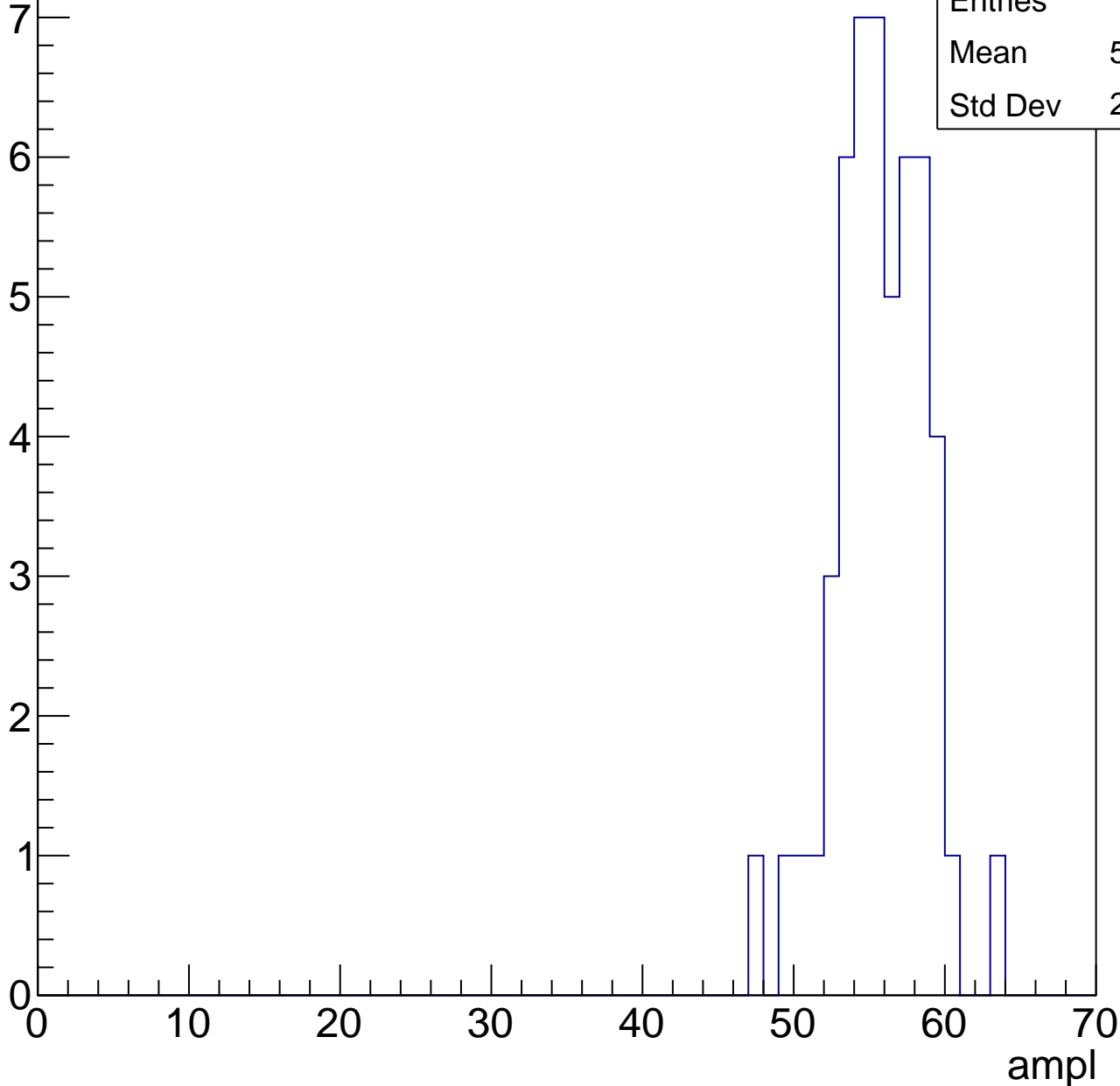


B1L102S, U22-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	55.26
Std Dev	2.938

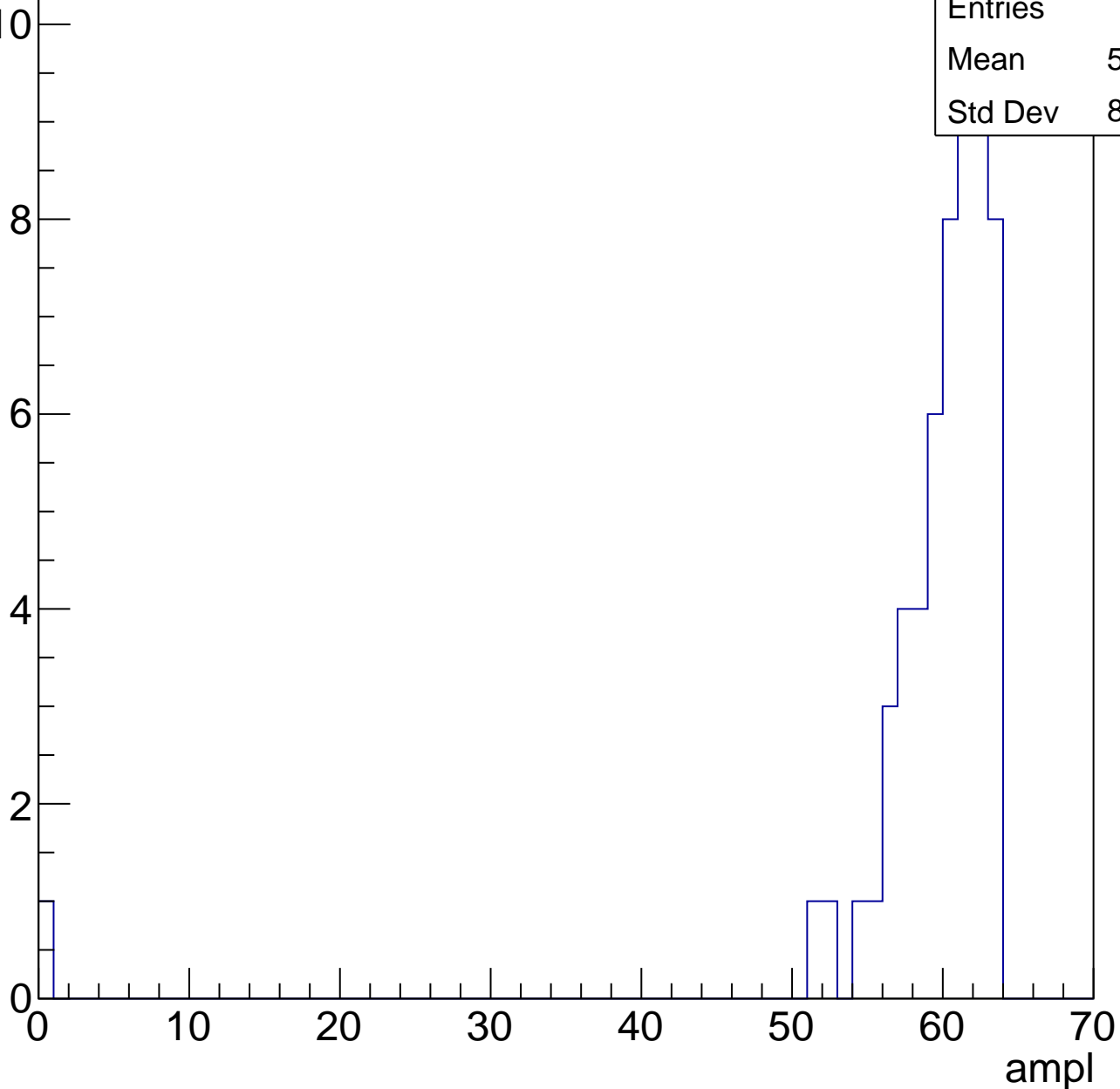


B1L102S, U22-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.76
Std Dev	8.247



B1L102S, U22-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch100, adc0

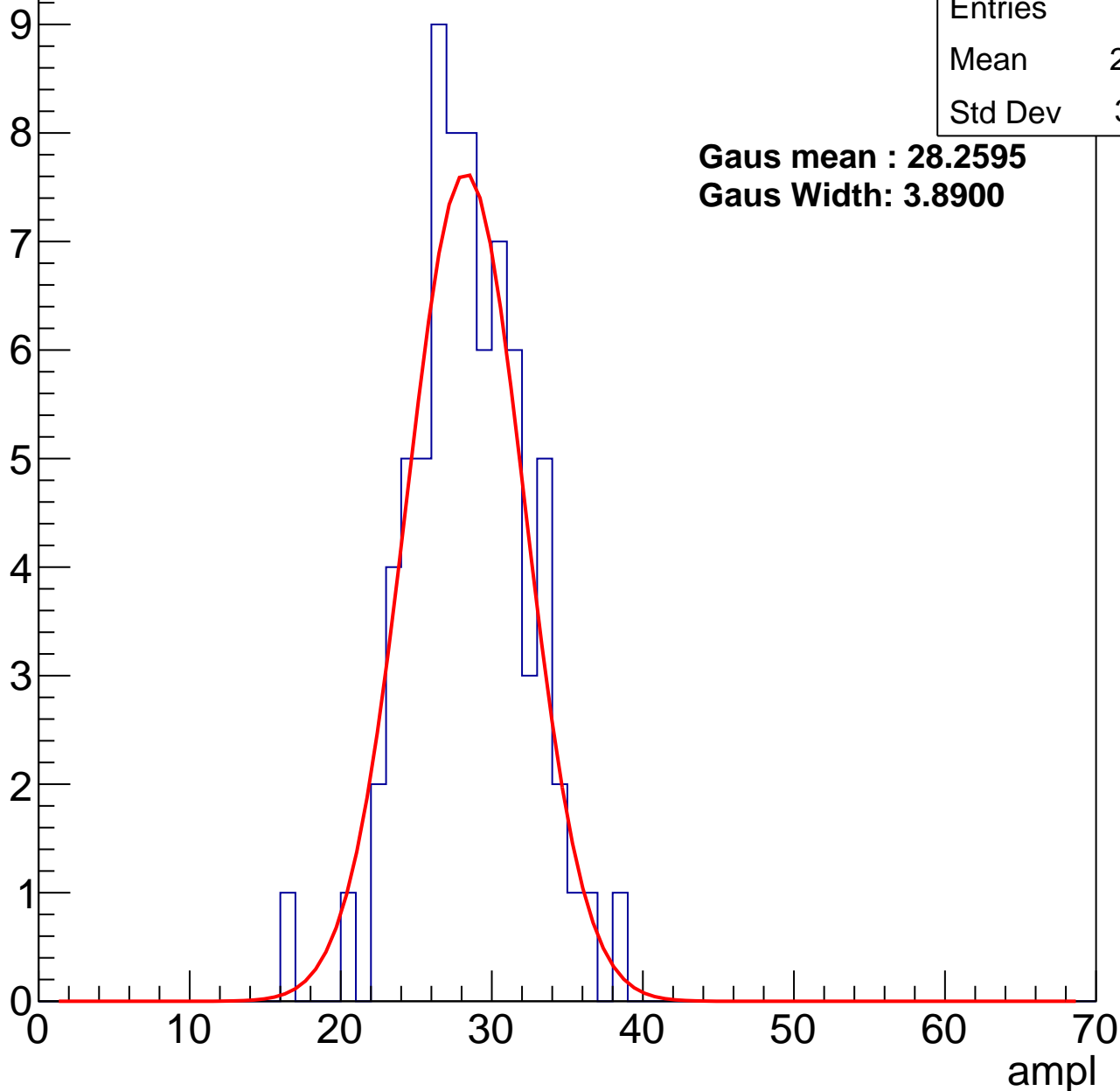
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.99
Std Dev	3.821

Gaus mean : 28.2595

Gaus Width: 3.8900



B1L102S, U22-ch100, adc1

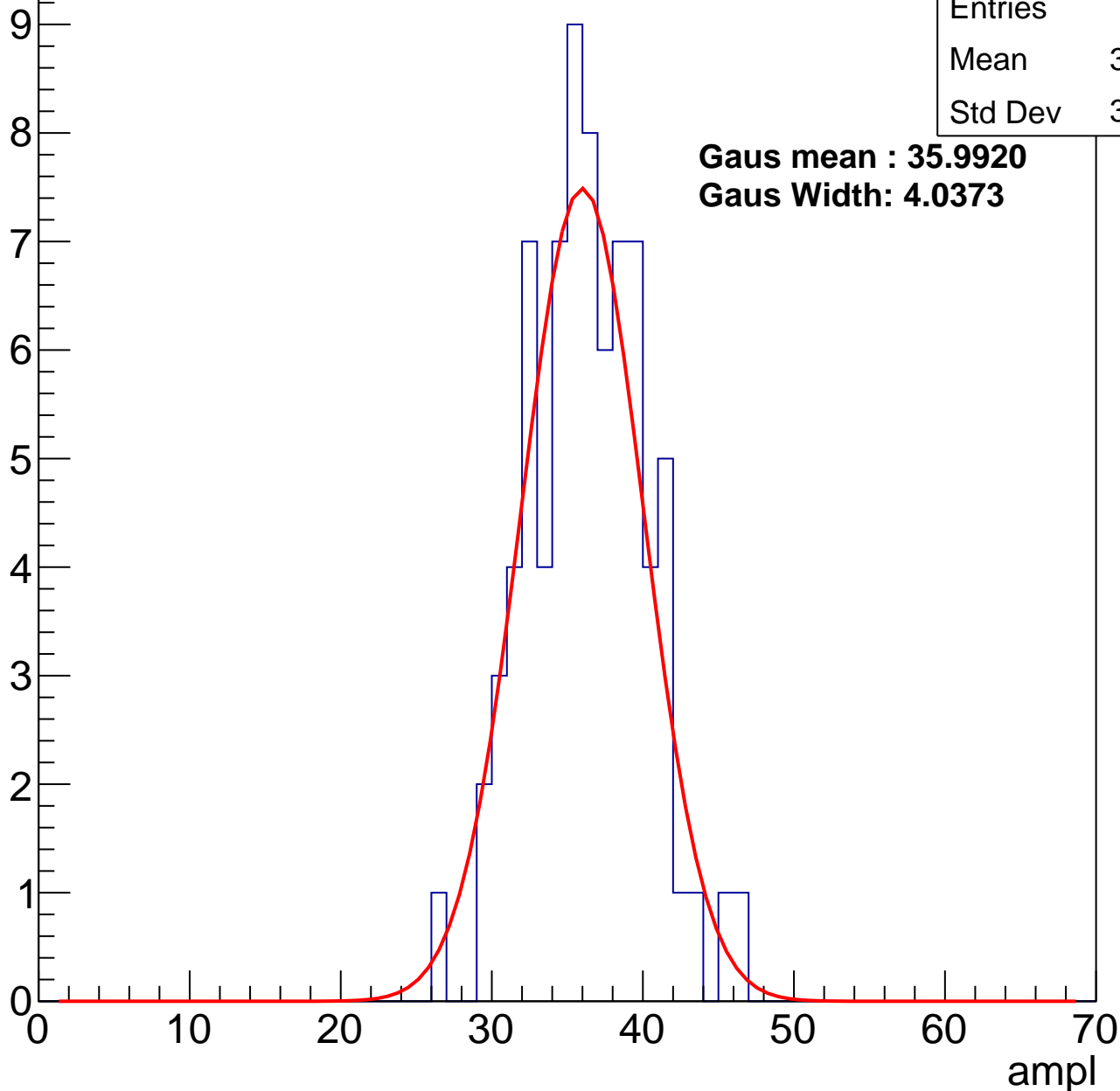
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.86
Std Dev	3.825

Gaus mean : 35.9920

Gaus Width: 4.0373



B1L102S, U22-ch100, adc2

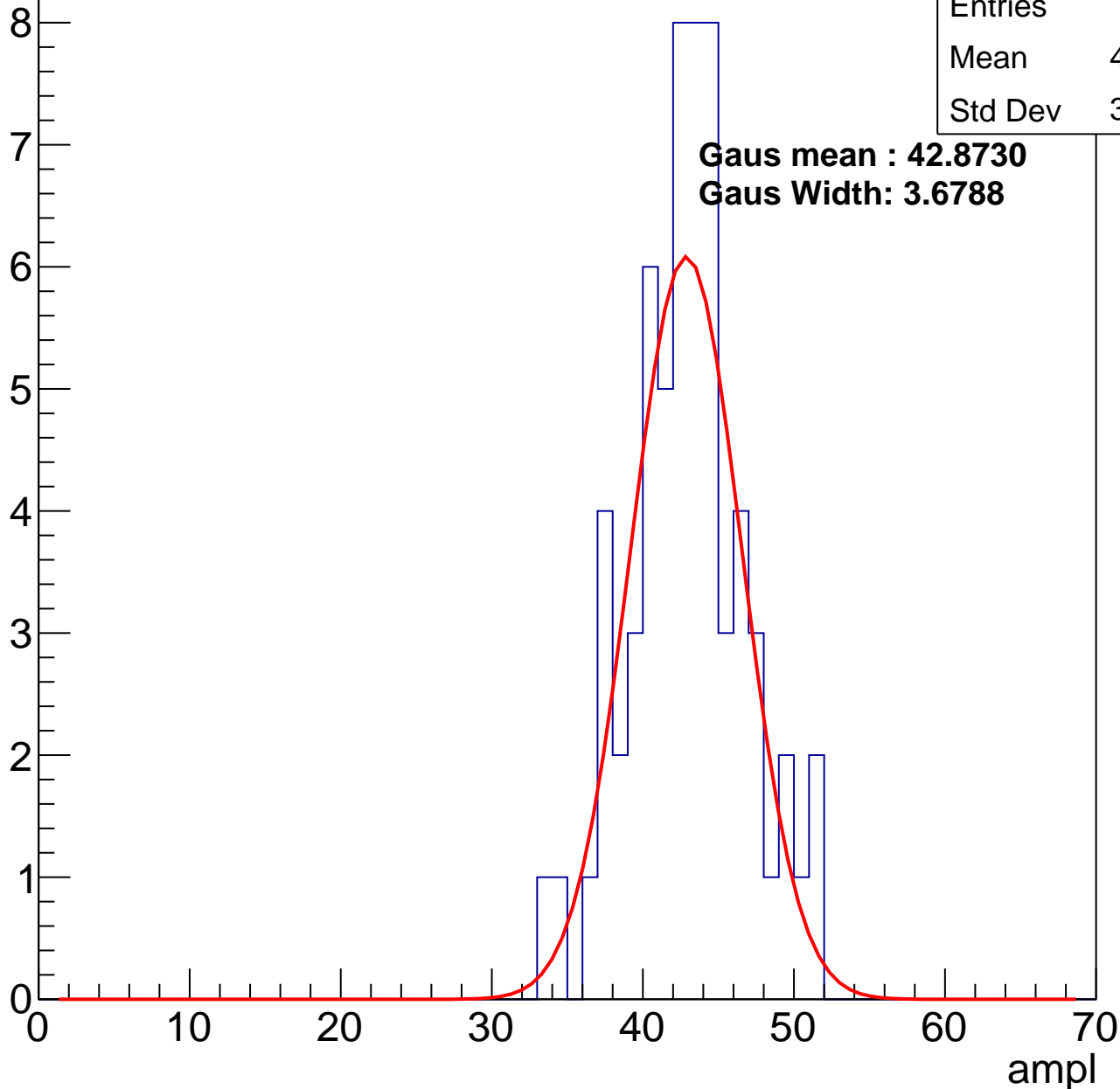
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.52
Std Dev	3.817

Gaus mean : 42.8730

Gaus Width: 3.6788

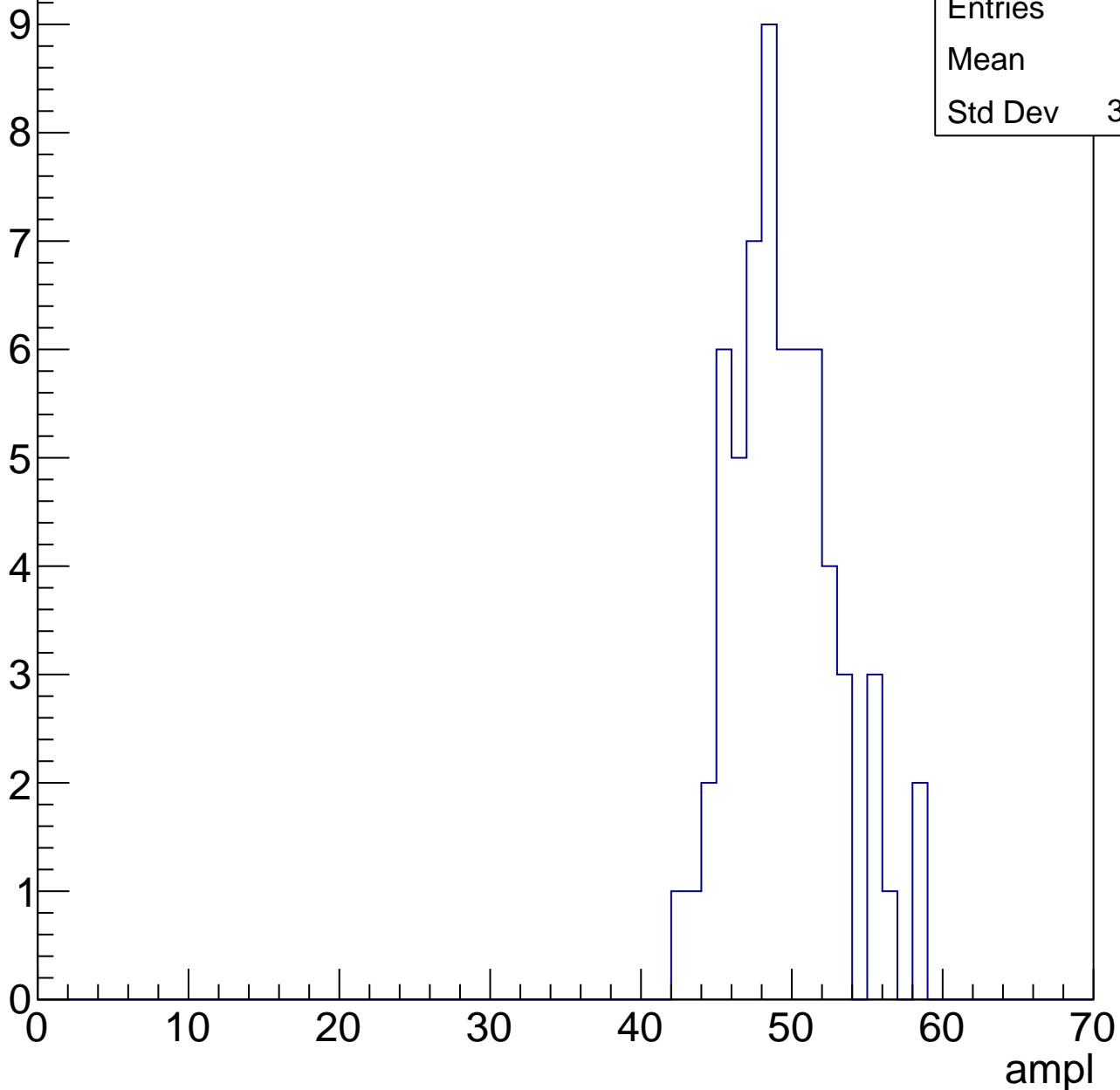


B1L102S, U22-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

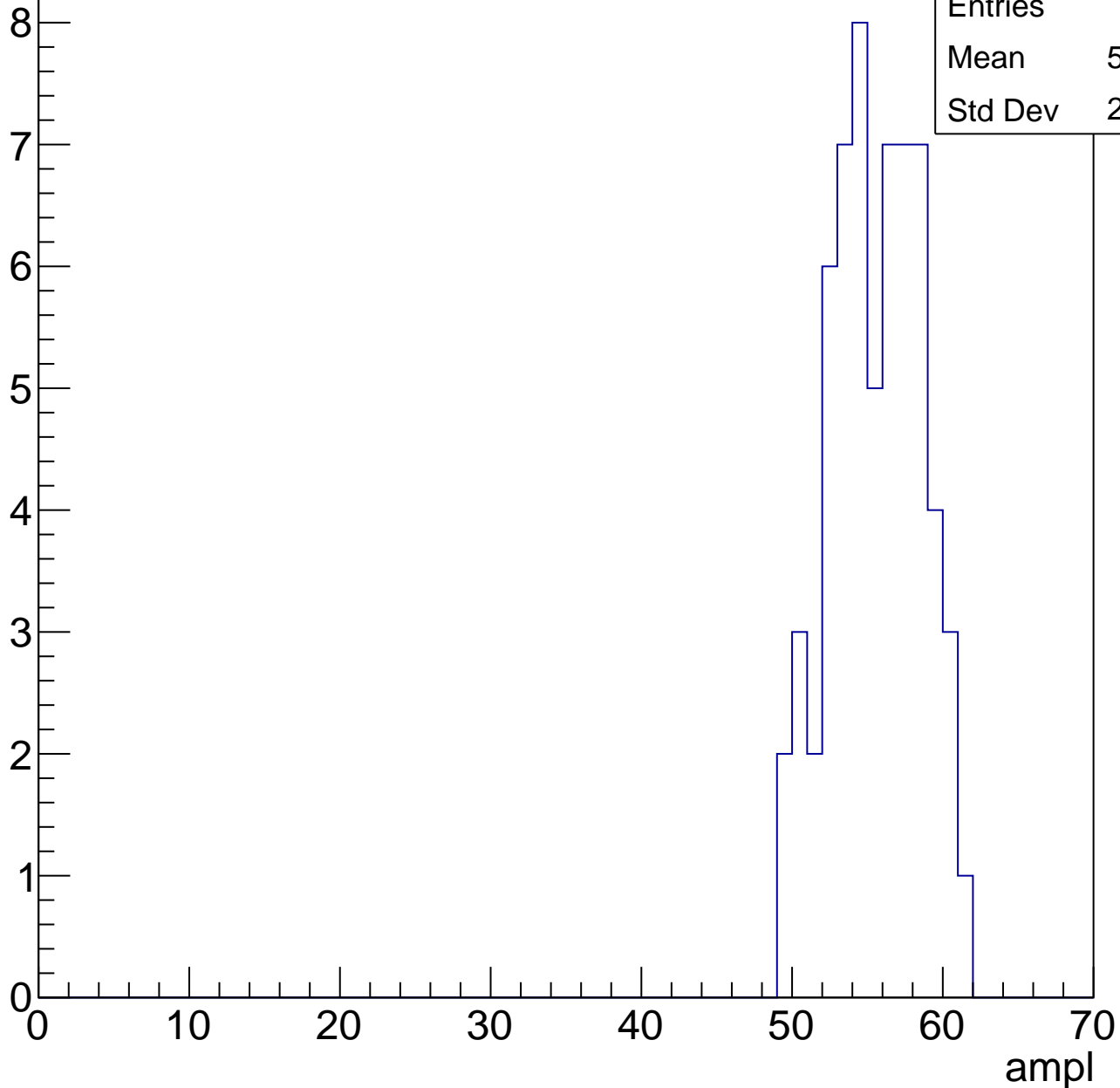
Entries	62
Mean	49
Std Dev	3.469



B1L102S, U22-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

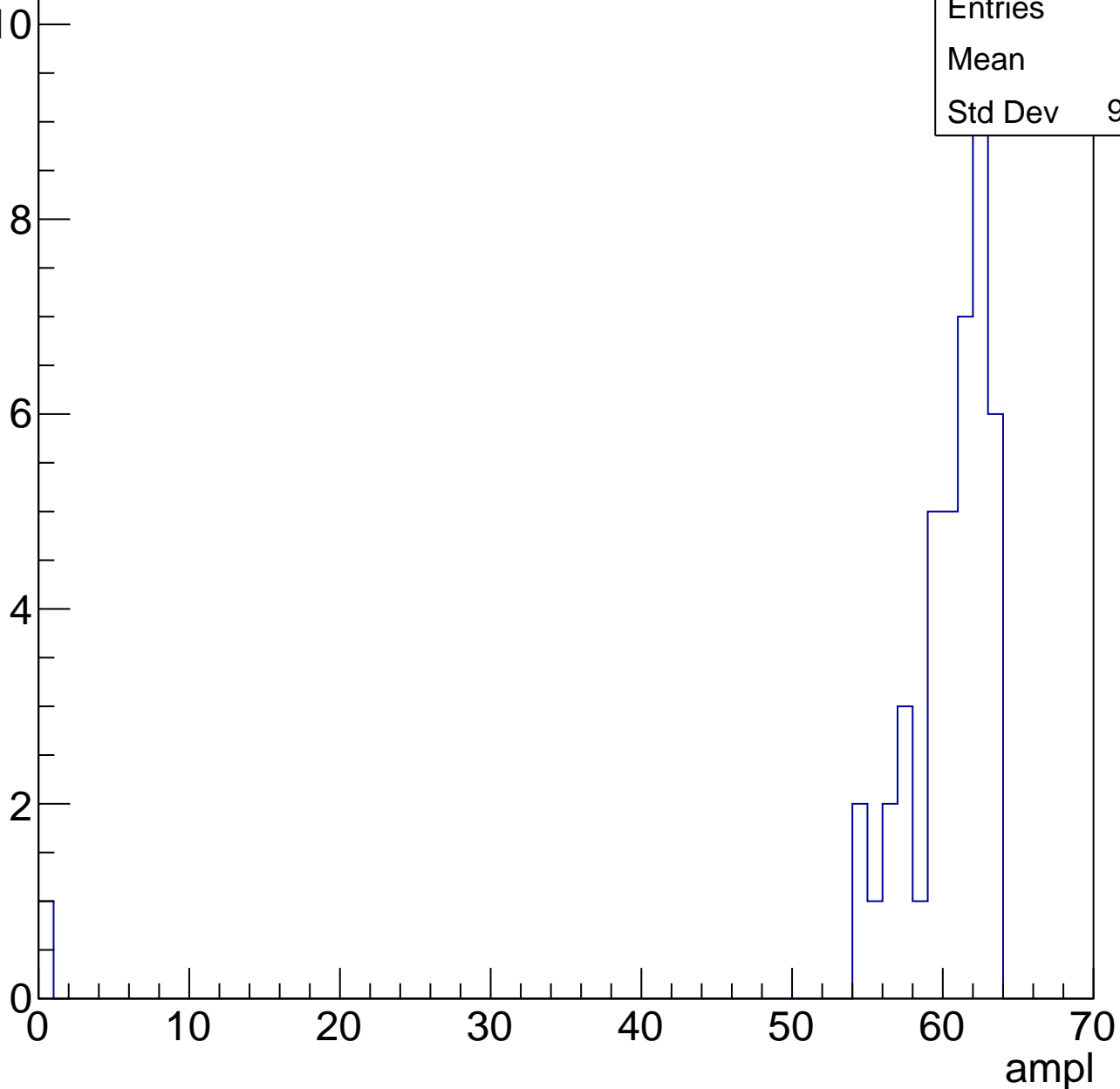


B1L102S, U22-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

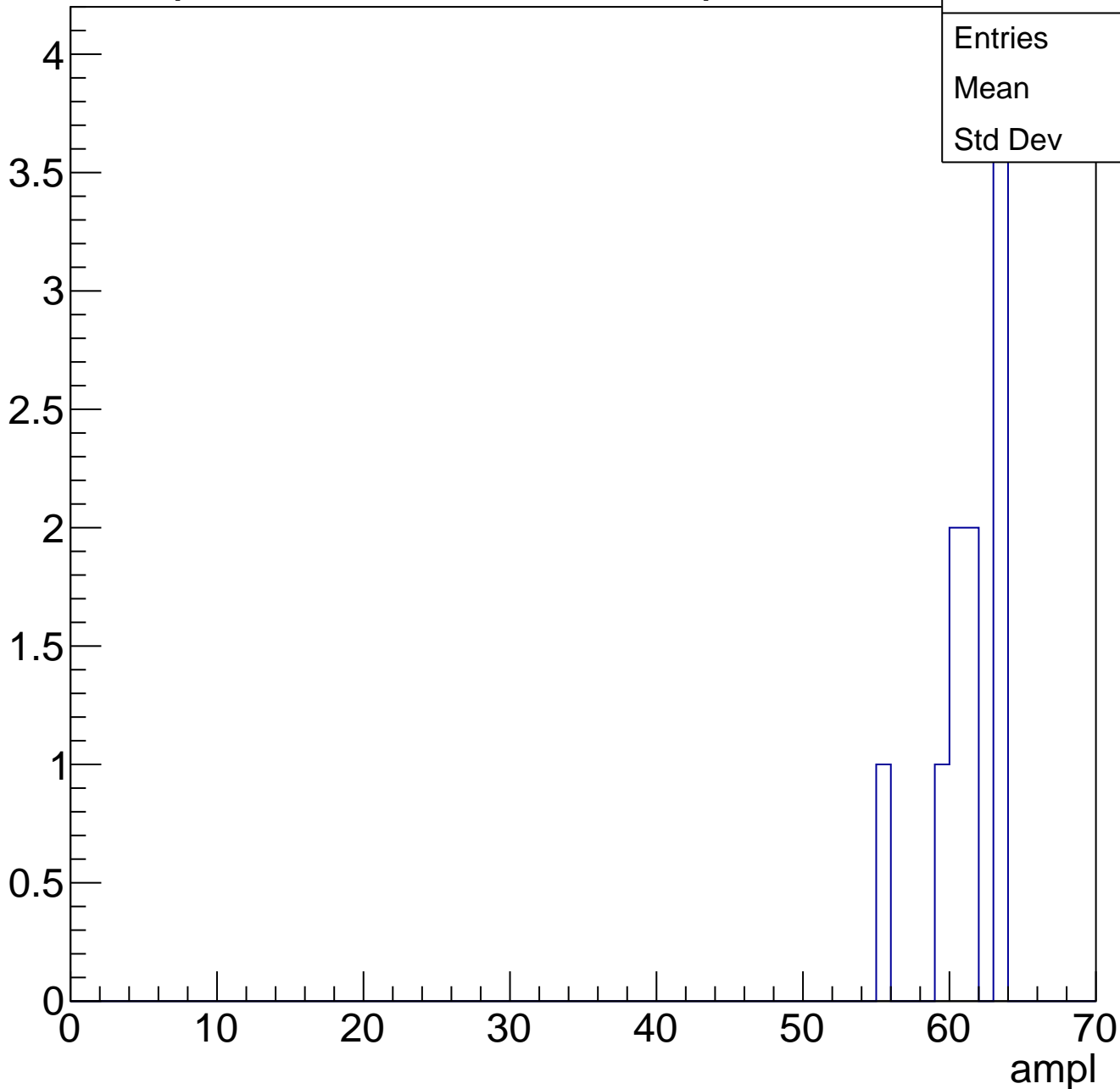
Entries	43
Mean	58.7
Std Dev	9.392



B1L102S, U22-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch101, adc0

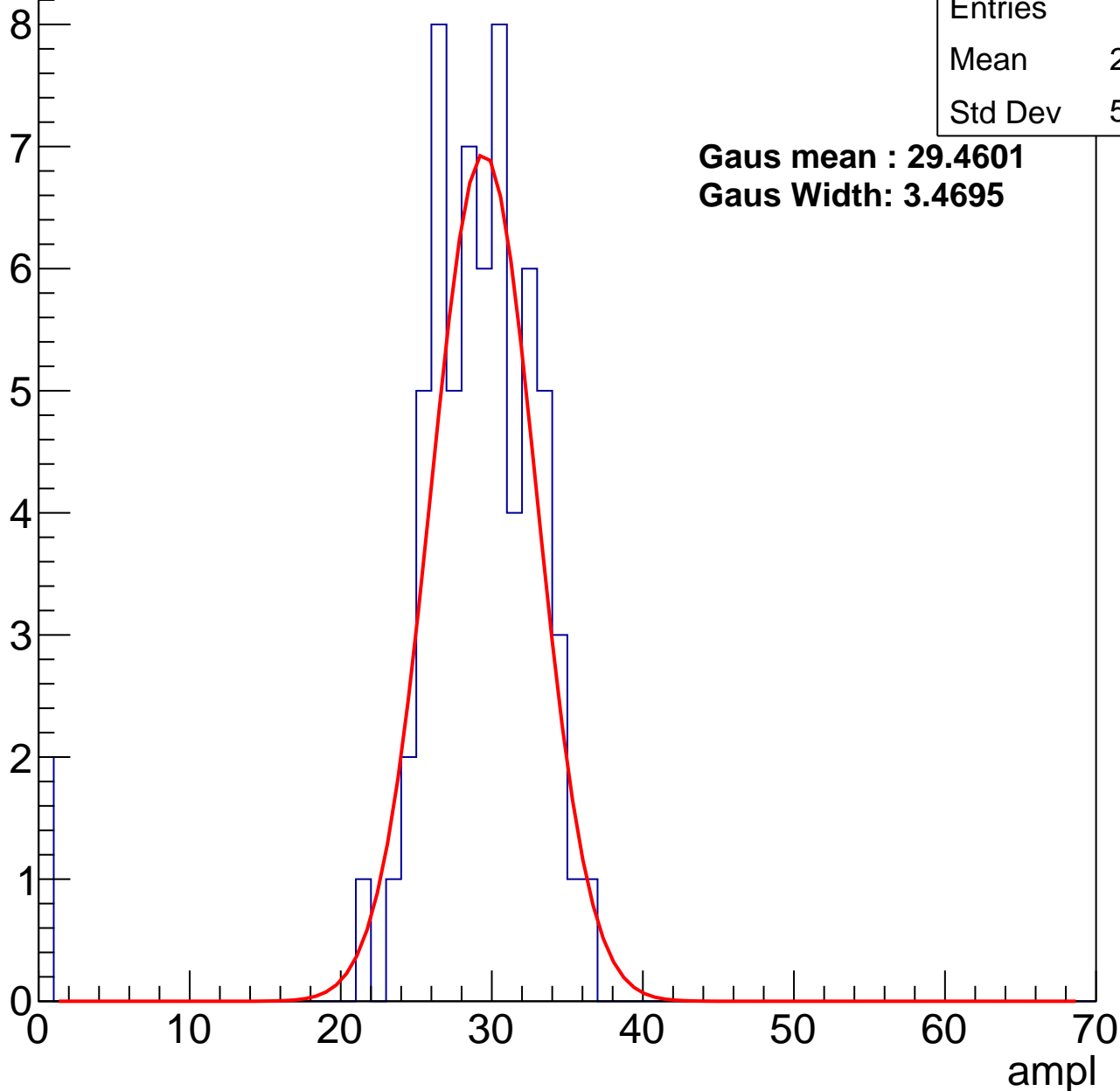
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.06
Std Dev	5.912

Gaus mean : 29.4601

Gaus Width: 3.4695



B1L102S, U22-ch101, adc1

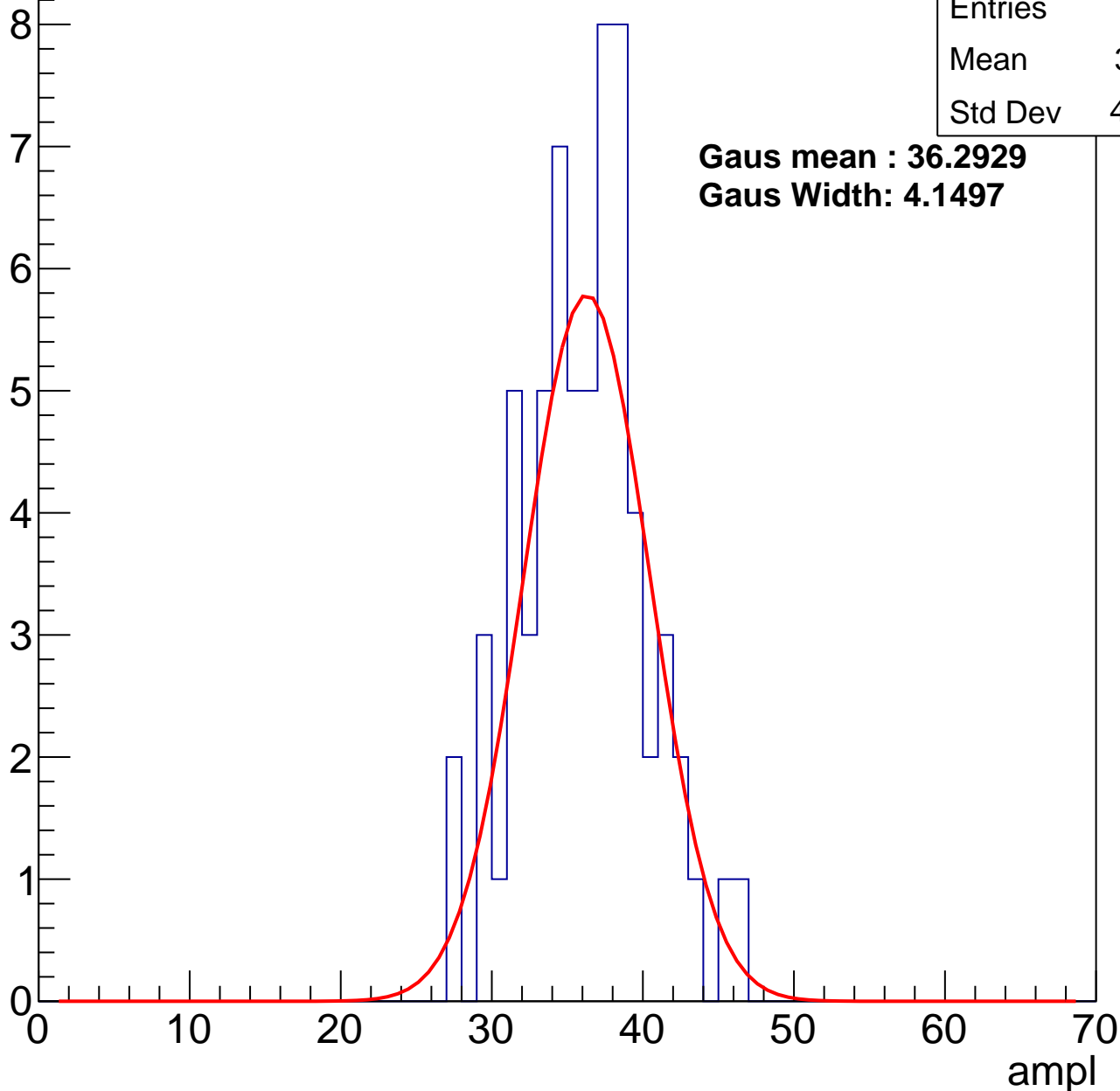
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.71
Std Dev	4.037

Gaus mean : 36.2929

Gaus Width: 4.1497



B1L102S, U22-ch101, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	42.09
Std Dev	3.907

Gaus mean : 43.0871

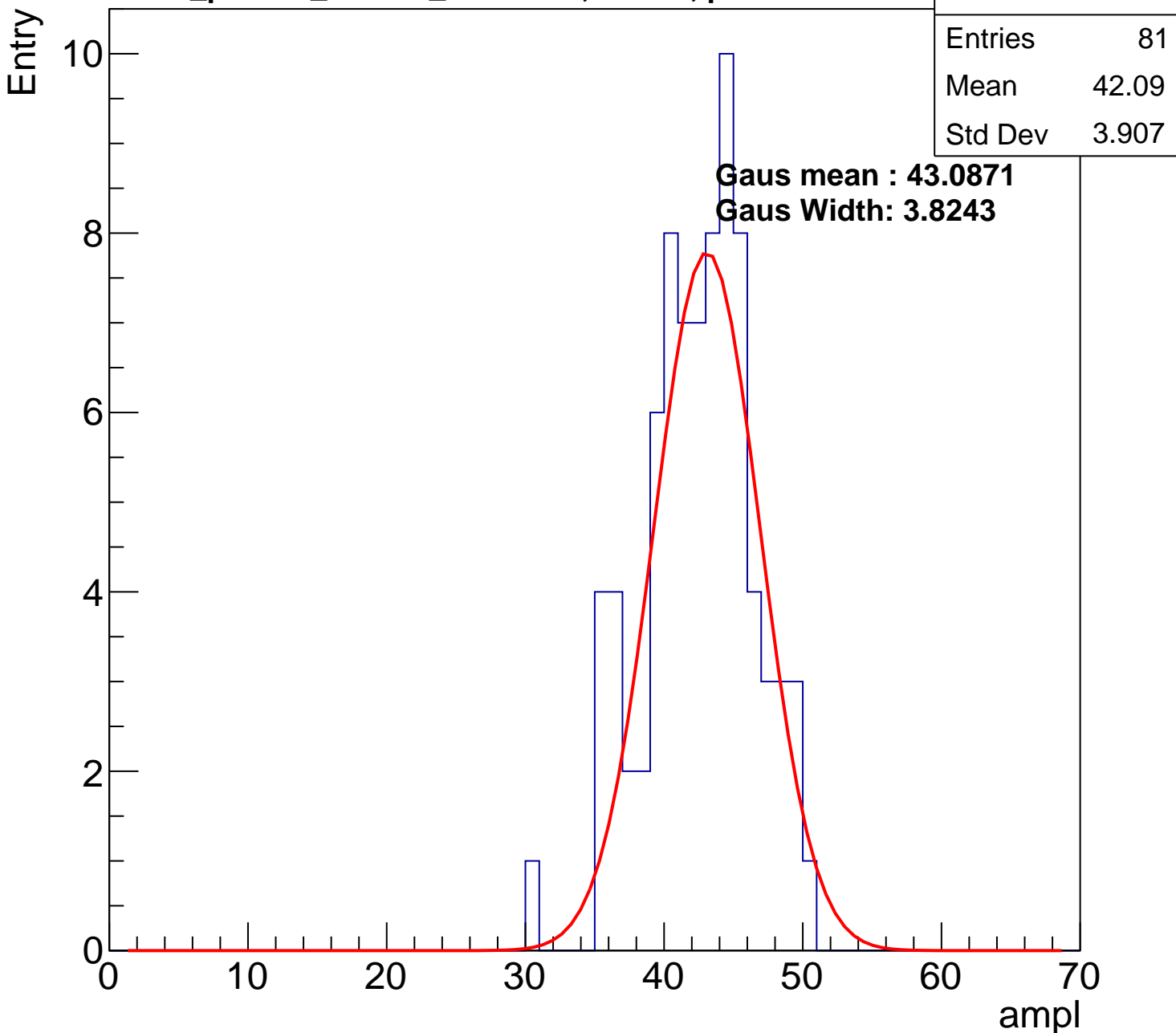
Gaus Width: 3.8243

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

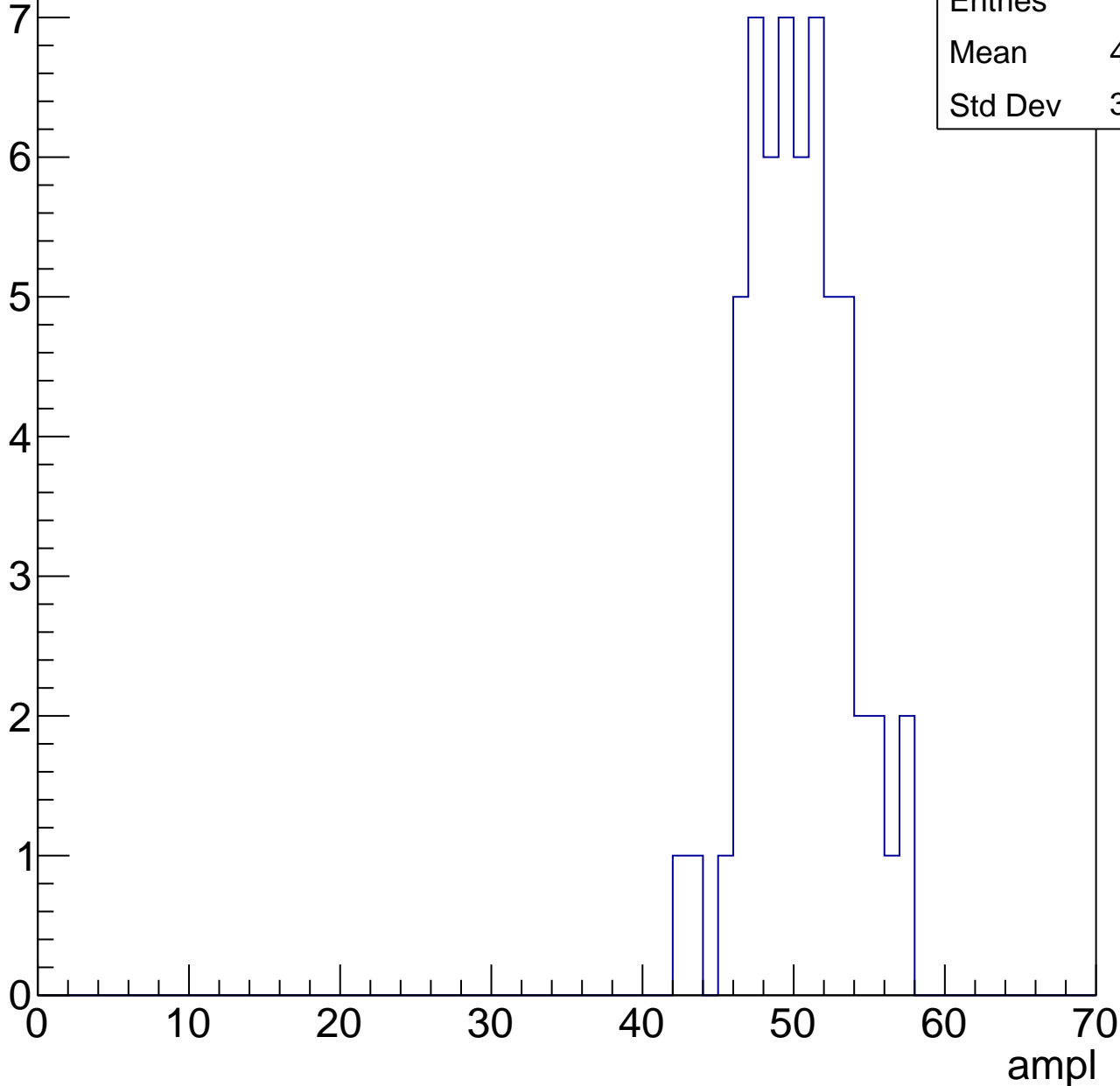


B1L102S, U22-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	49.83
Std Dev	3.212

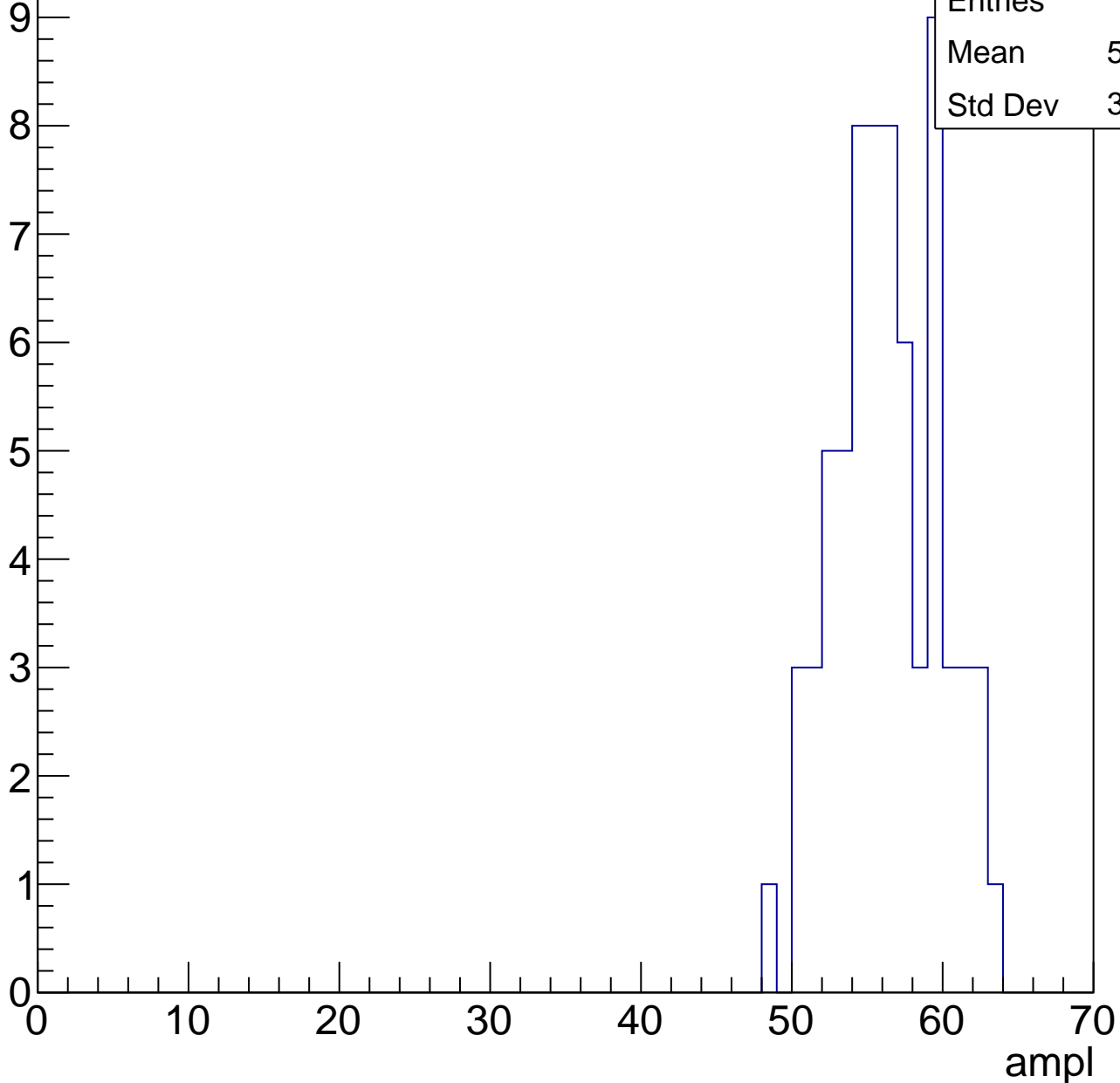


B1L102S, U22-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	55.87
Std Dev	3.383

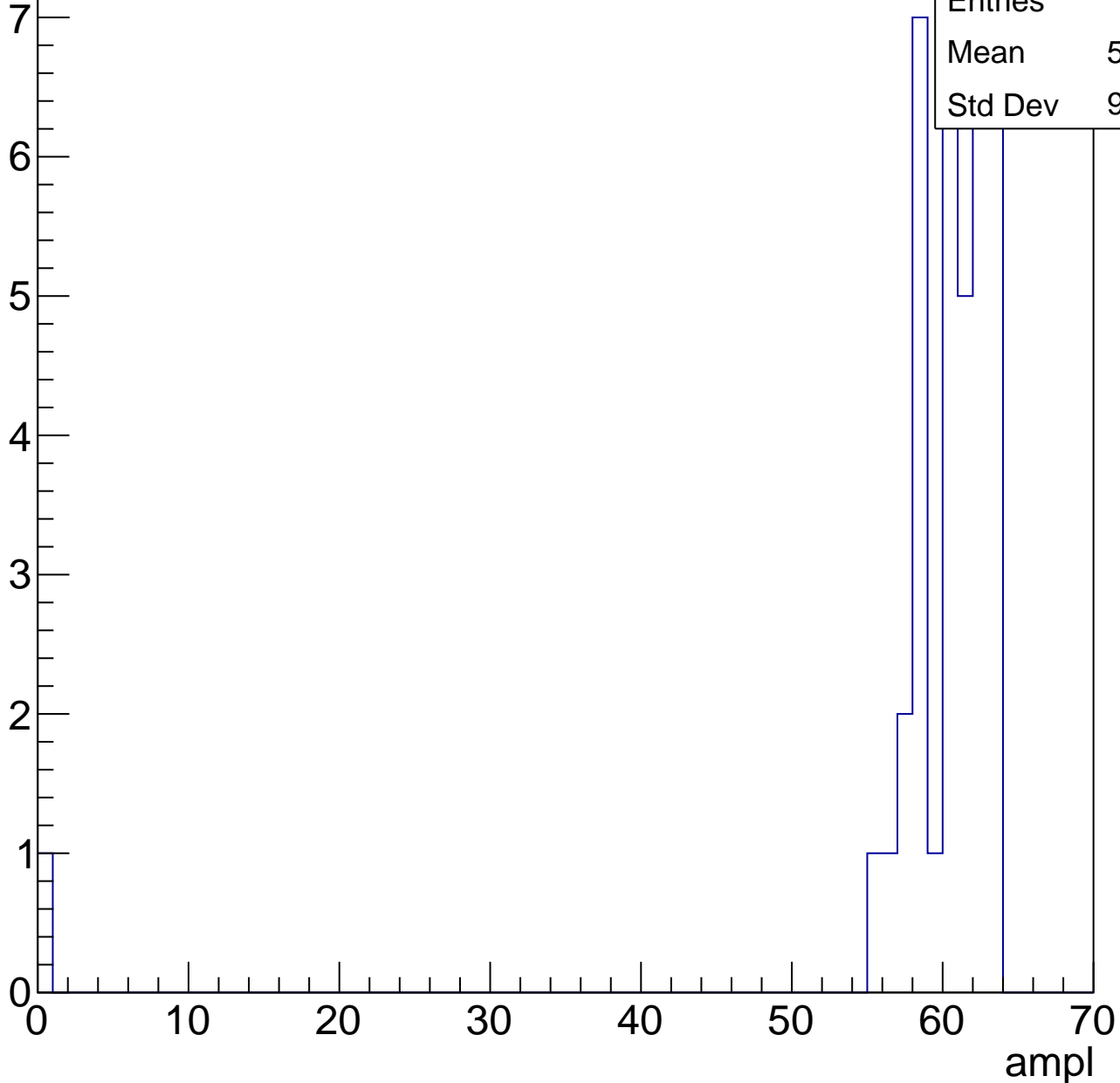


B1L102S, U22-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	58.72
Std Dev	9.766



B1L102S, U22-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

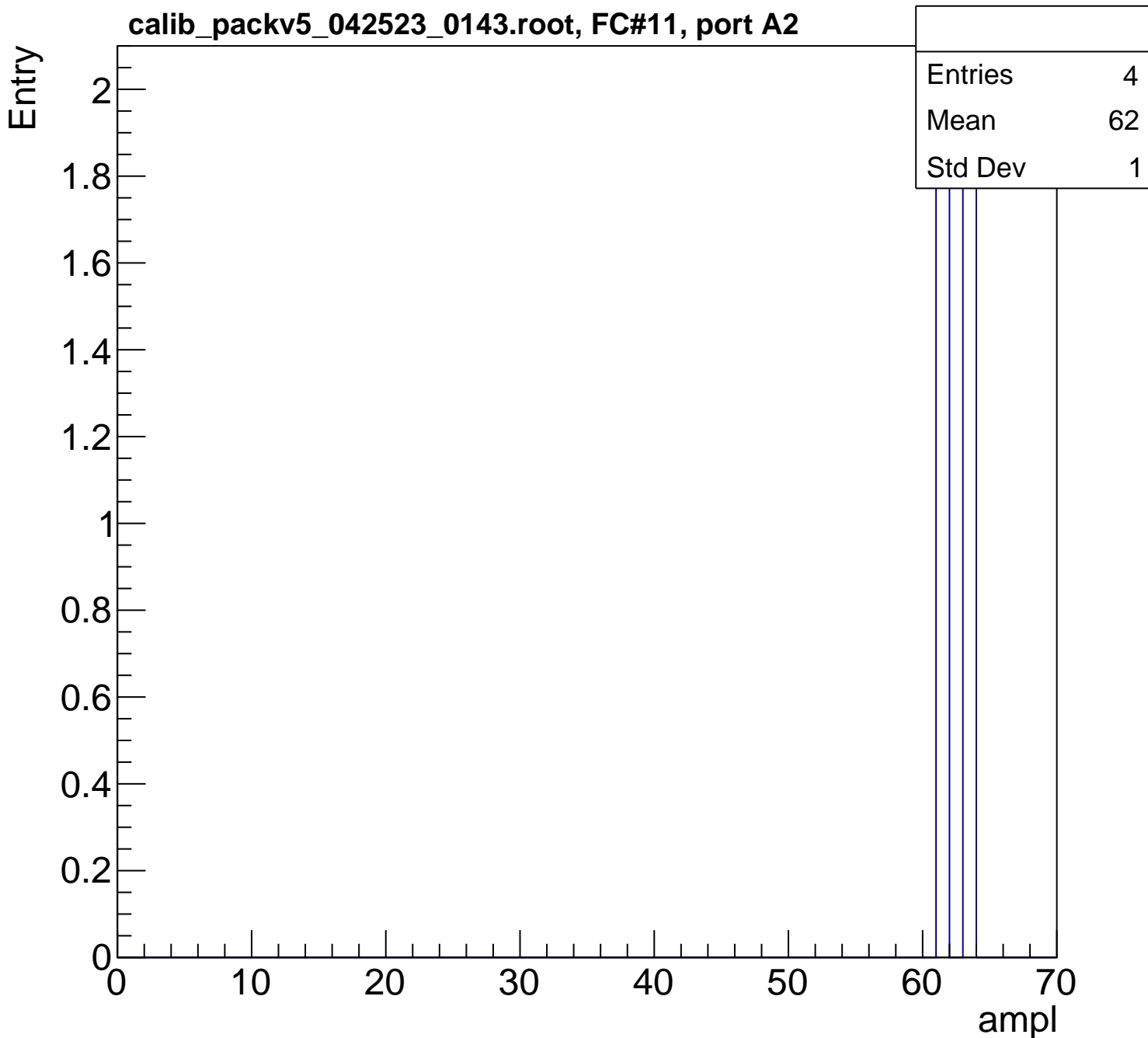
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1

0 10 20 30 40 50 60 70

ampl



B1L102S, U22-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch102, adc0

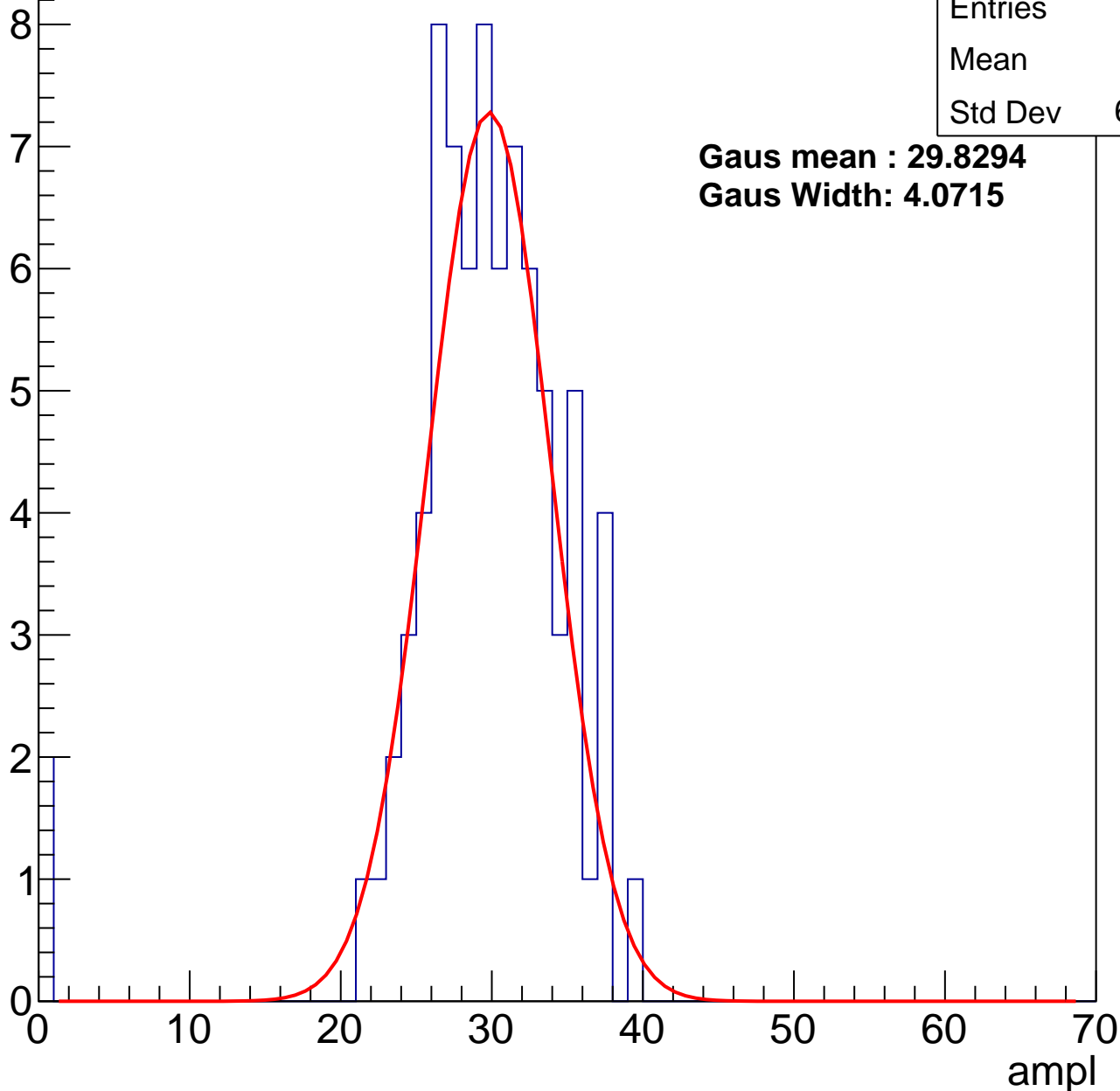
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.9
Std Dev	6.061

Gaus mean : 29.8294

Gaus Width: 4.0715



B1L102S, U22-ch102, adc1

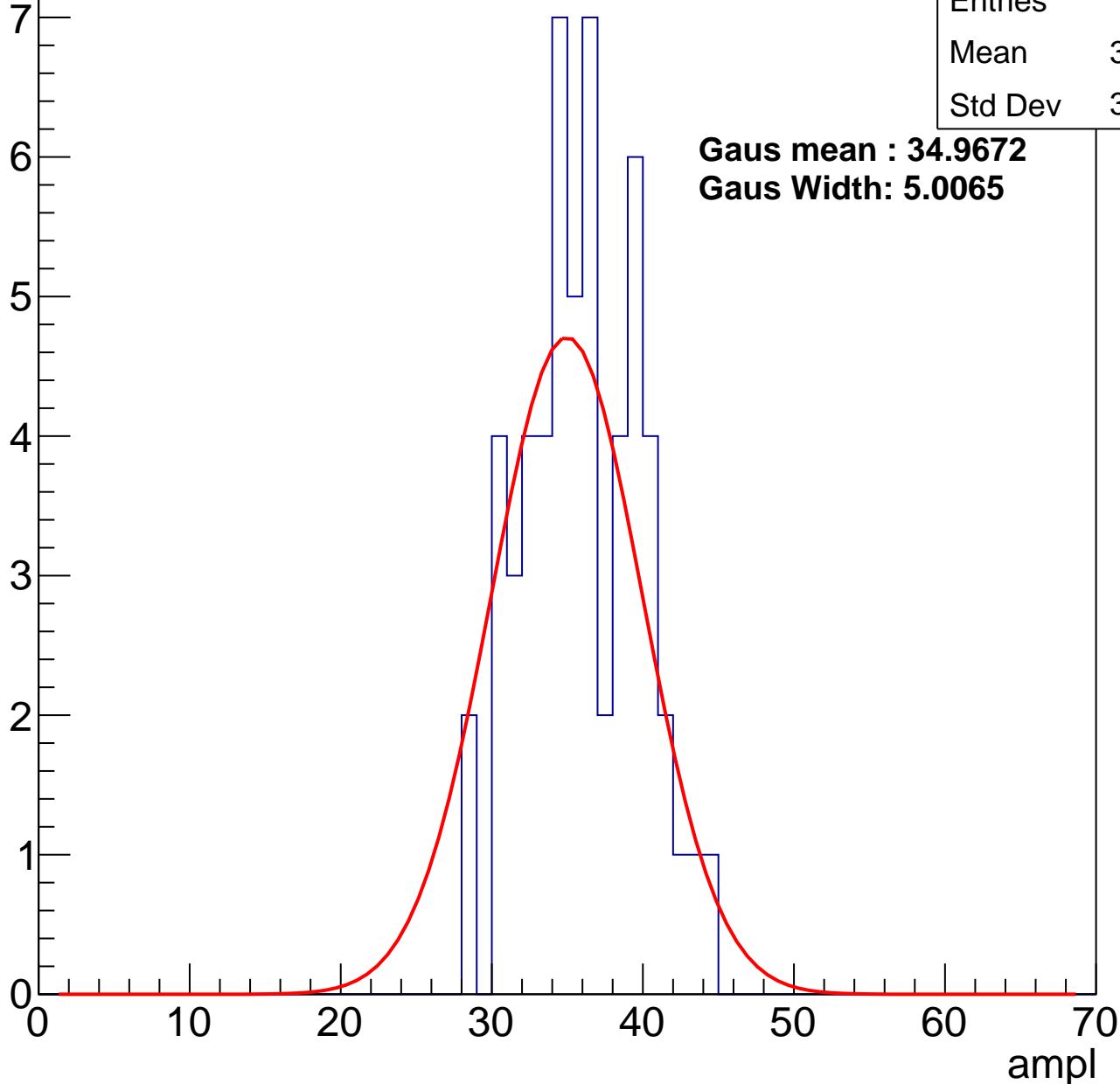
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.53
Std Dev	3.752

Gaus mean : 34.9672

Gaus Width: 5.0065



B1L102S, U22-ch102, adc2

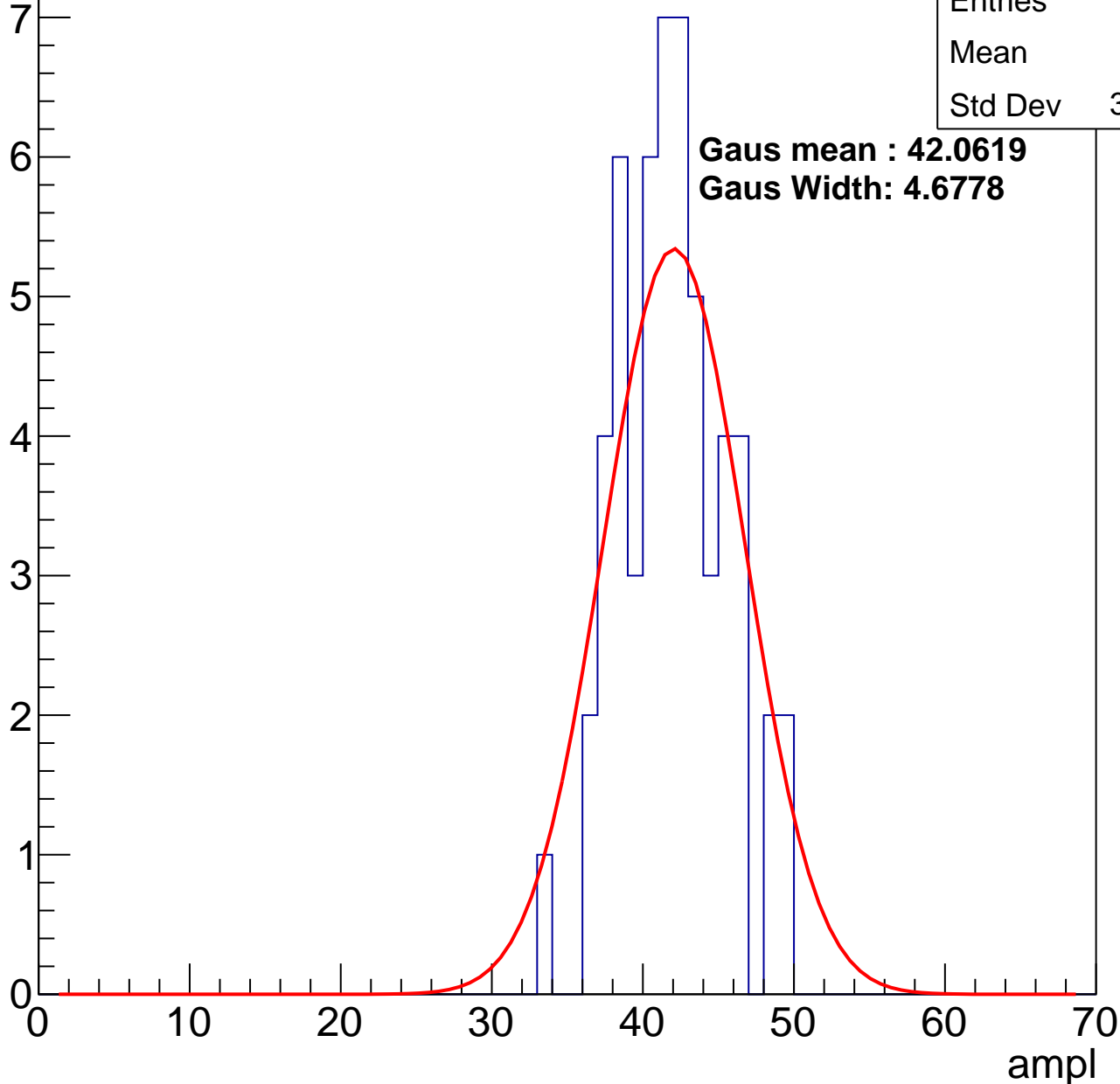
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.5
Std Dev	3.485

Gaus mean : 42.0619

Gaus Width: 4.6778

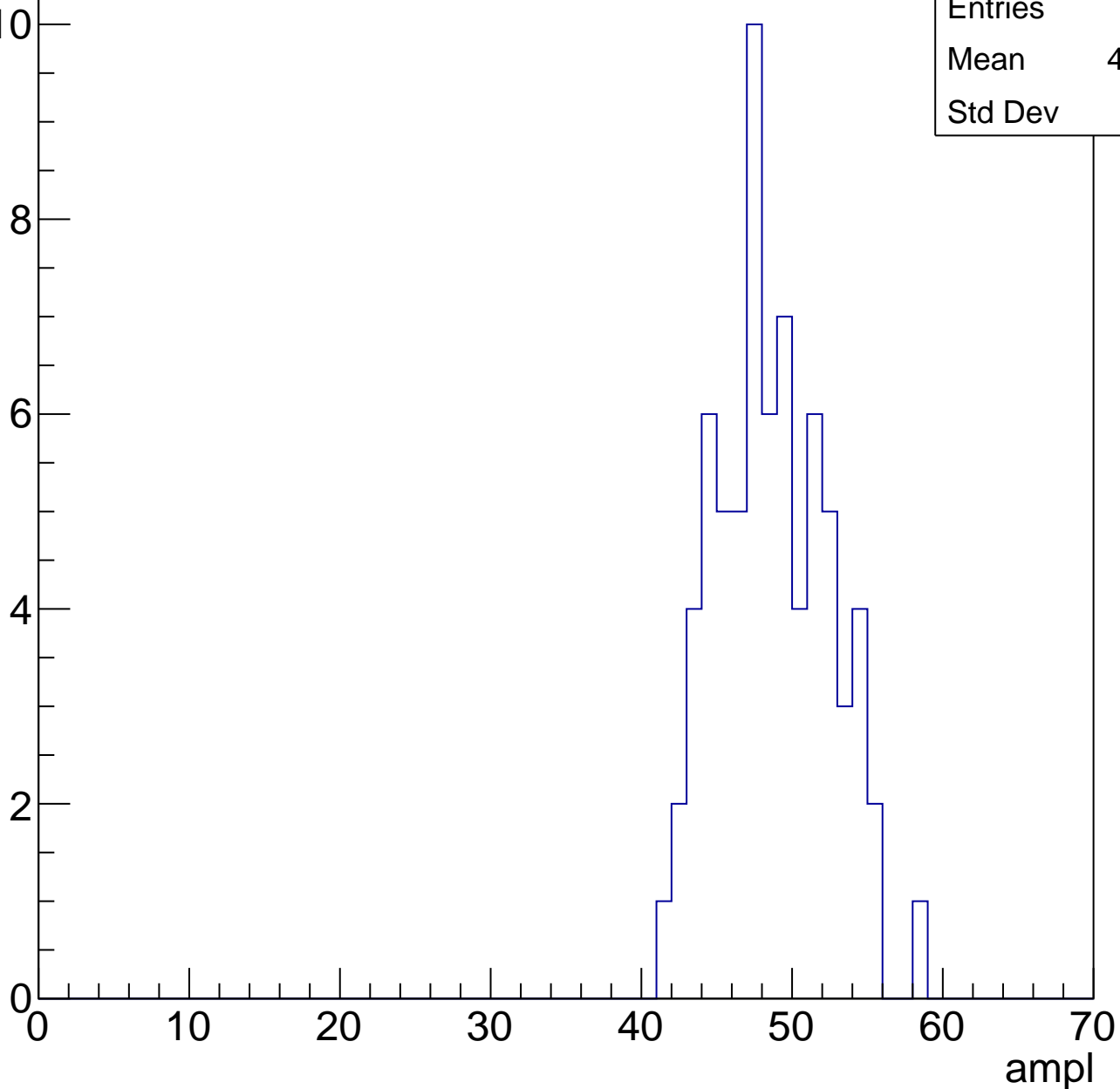


B1L102S, U22-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	48.25
Std Dev	3.68

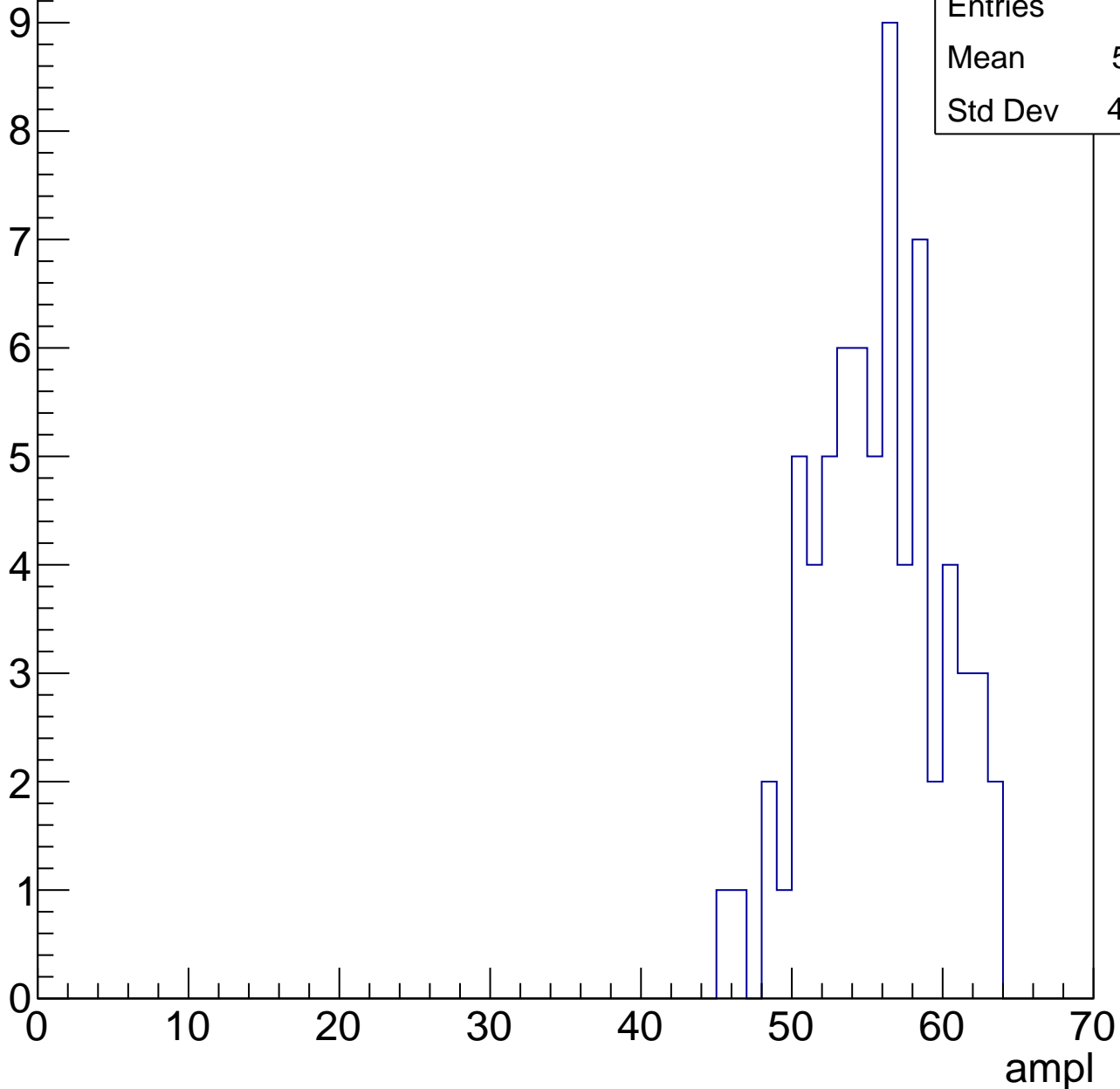


B1L102S, U22-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	55.11
Std Dev	4.108

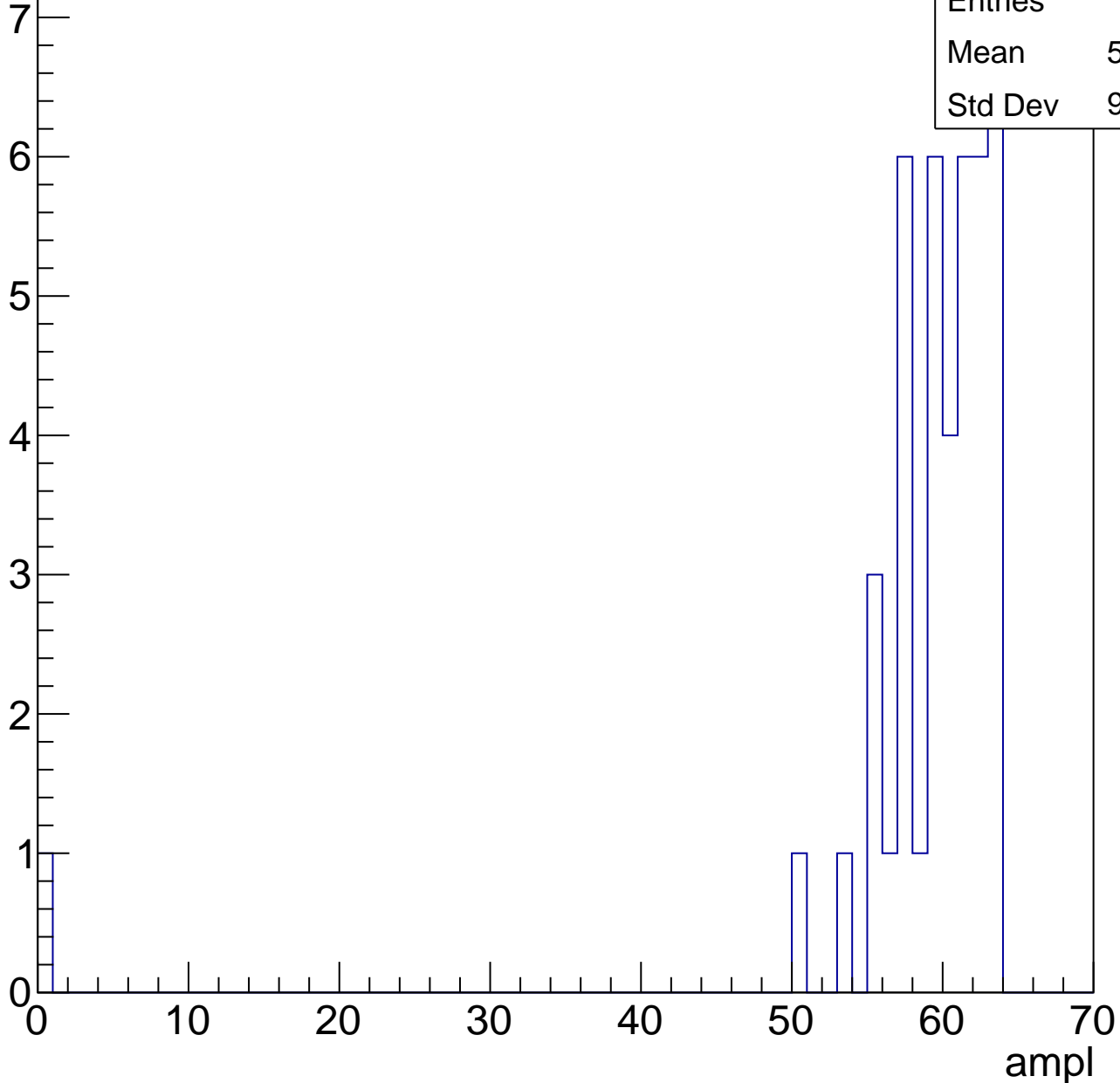


B1L102S, U22-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.07
Std Dev	9.446



B1L102S, U22-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

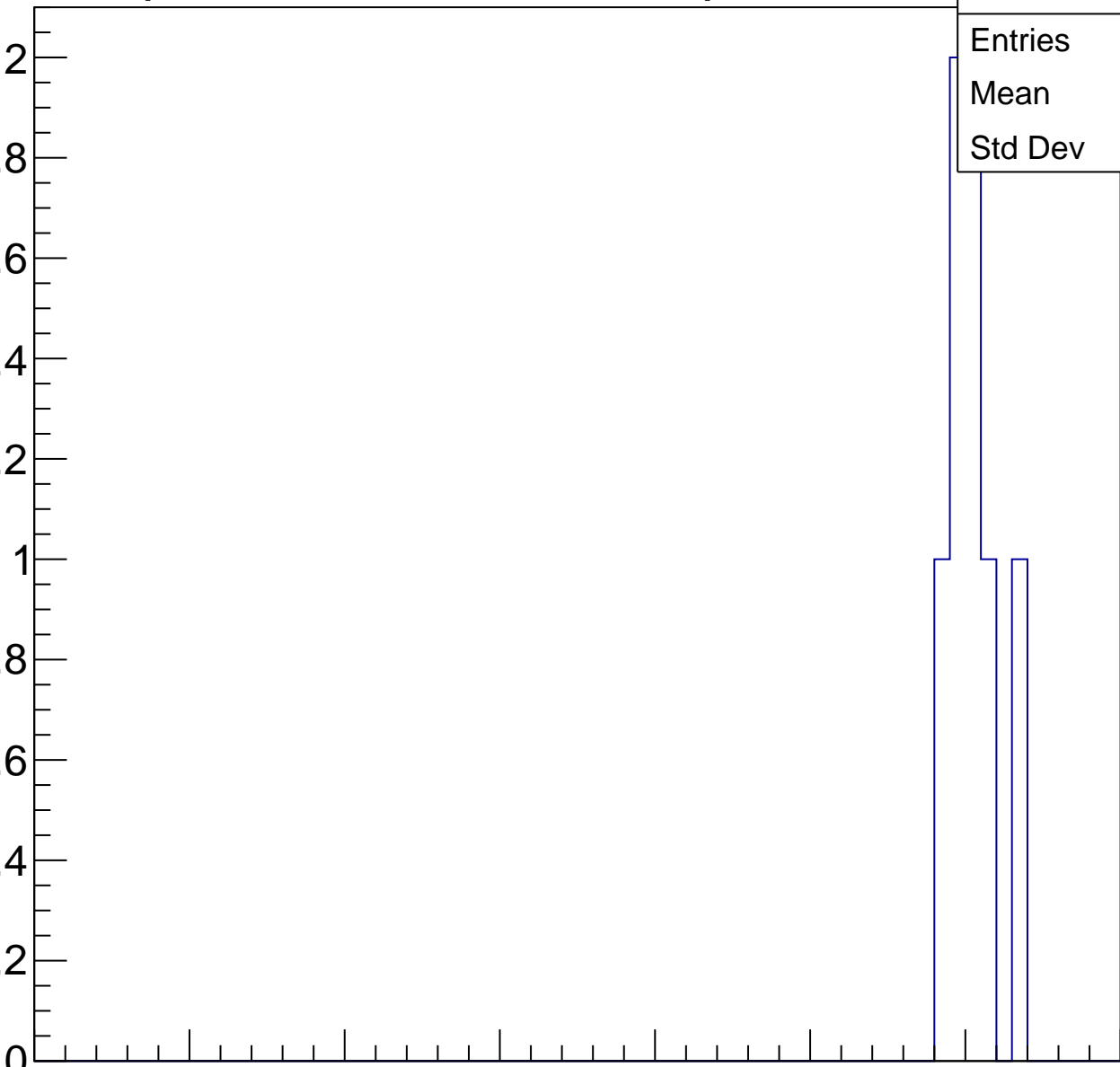
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60
Std Dev	1.512

0 10 20 30 40 50 60 70

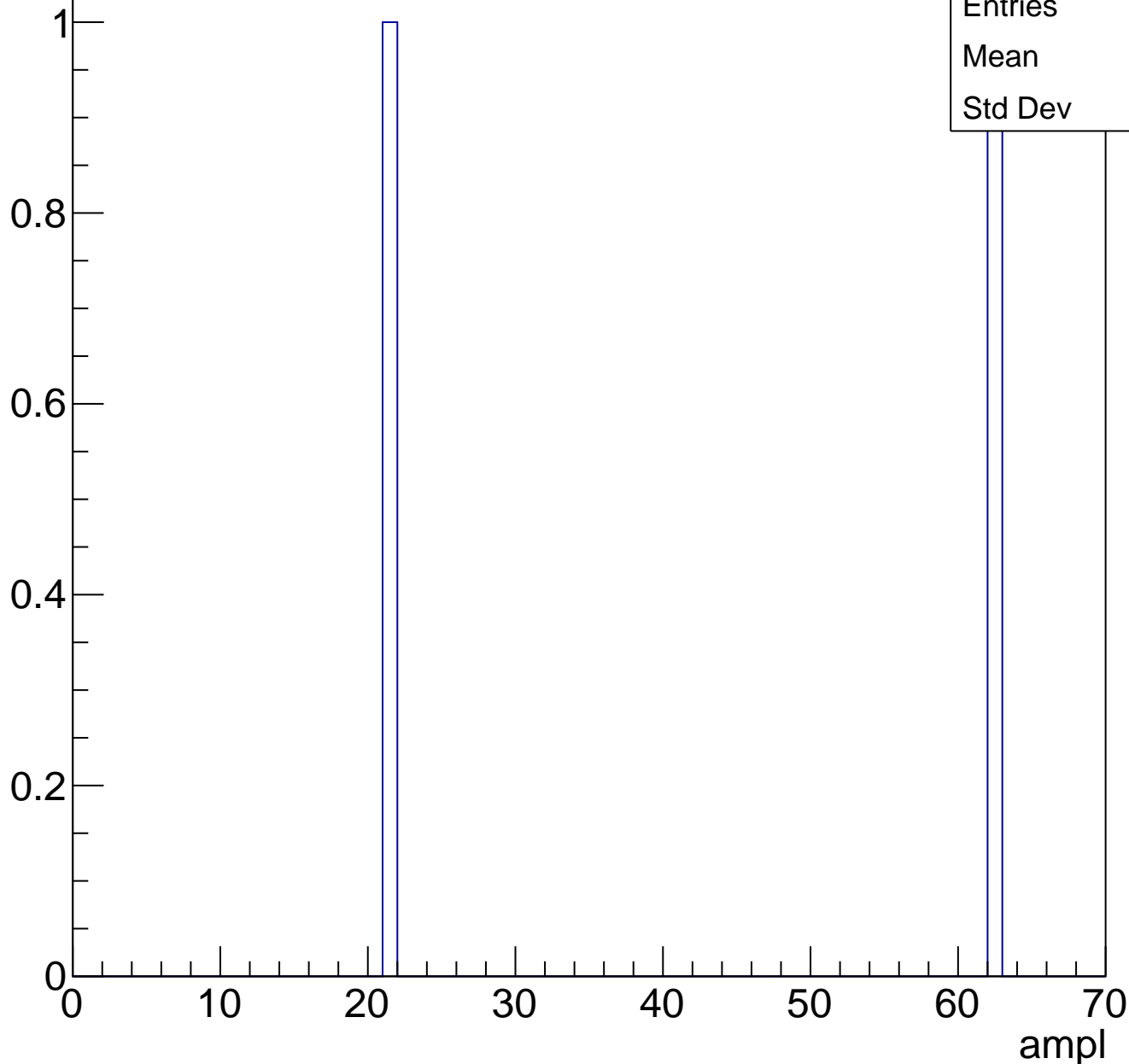
ampl



B1L102S, U22-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch103, adc0

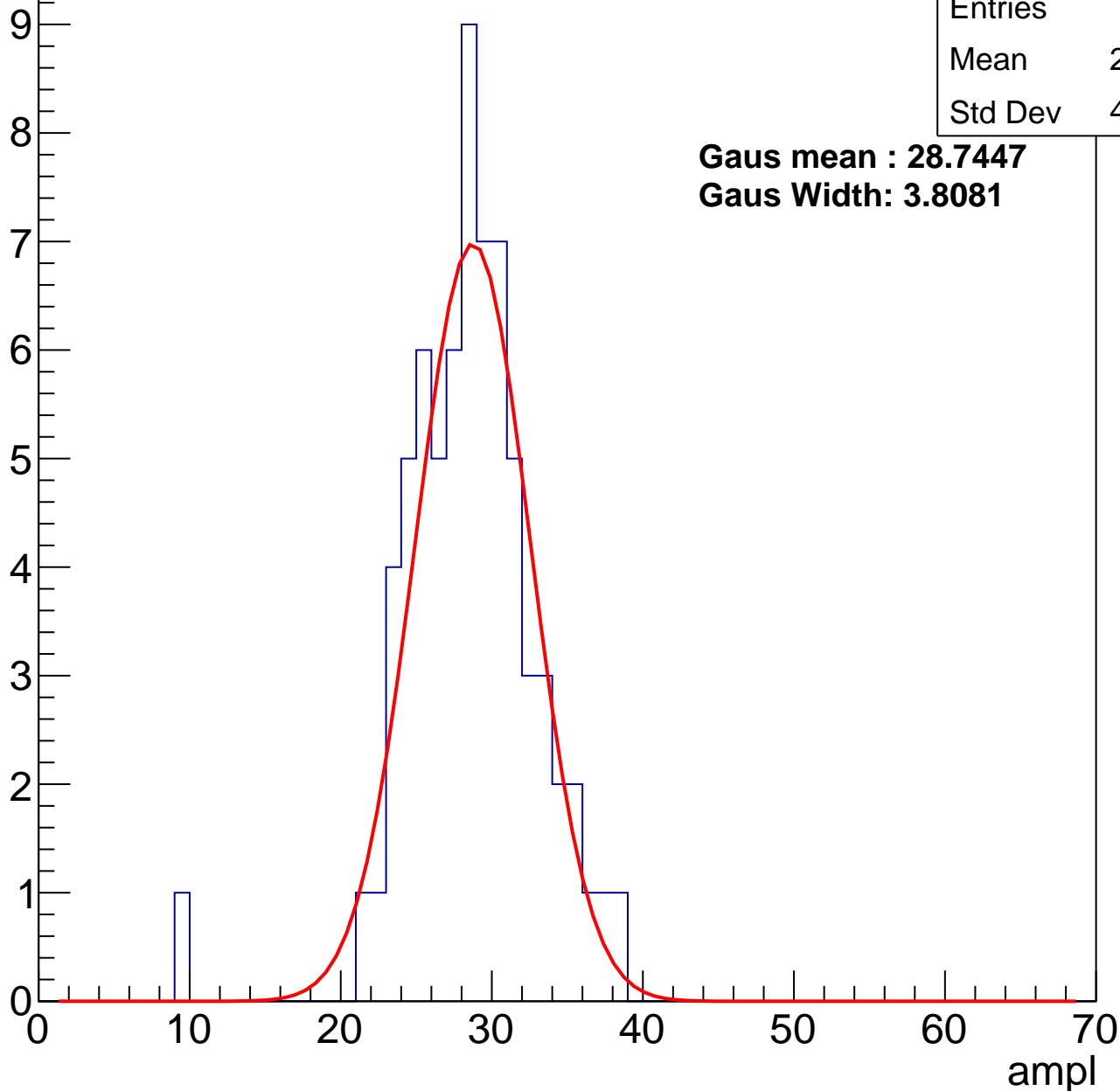
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.14
Std Dev	4.347

Gaus mean : 28.7447

Gaus Width: 3.8081



B1L102S, U22-ch103, adc1

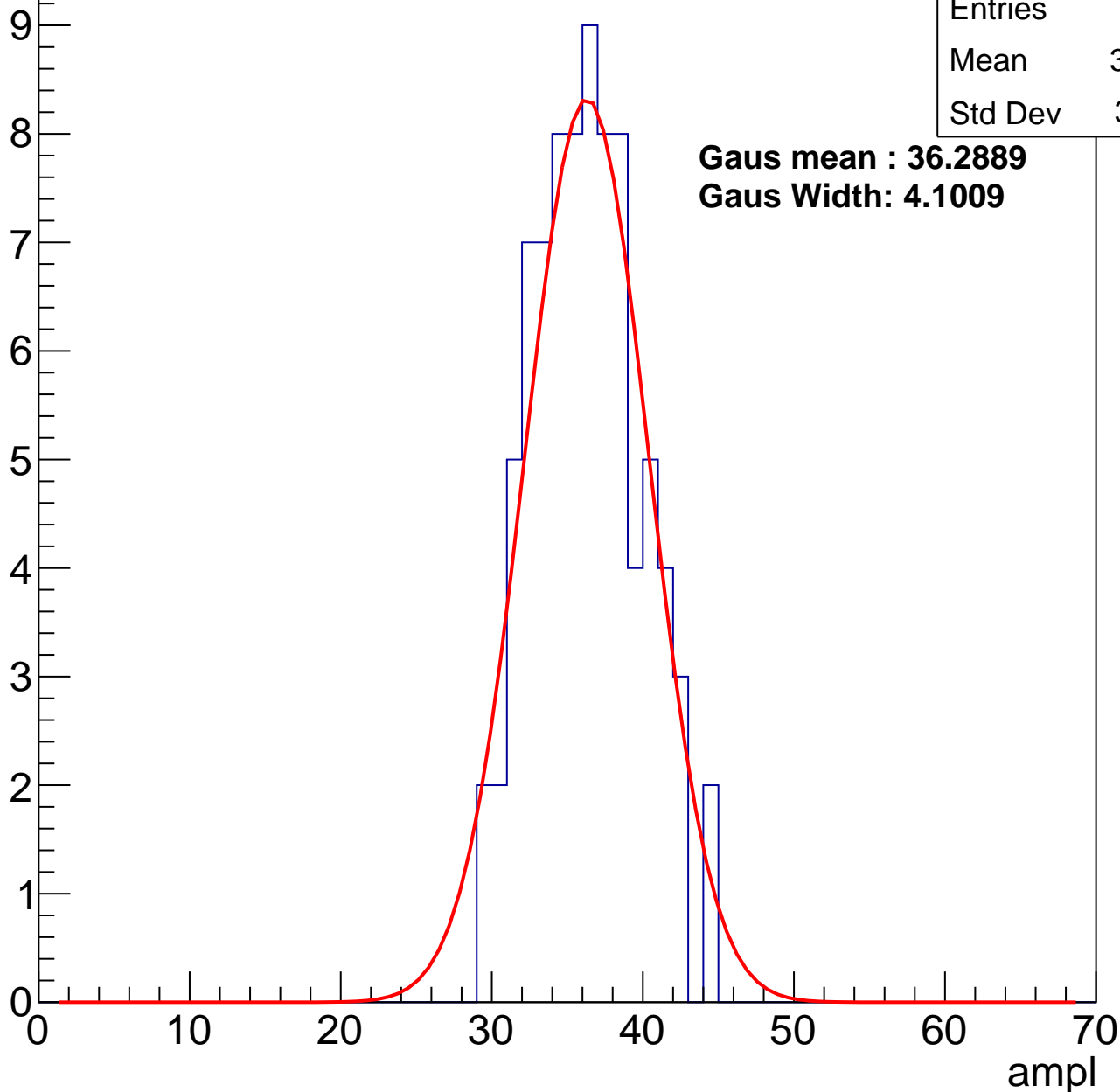
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	35.83
Std Dev	3.491

Gaus mean : 36.2889

Gaus Width: 4.1009



B1L102S, U22-ch103, adc2

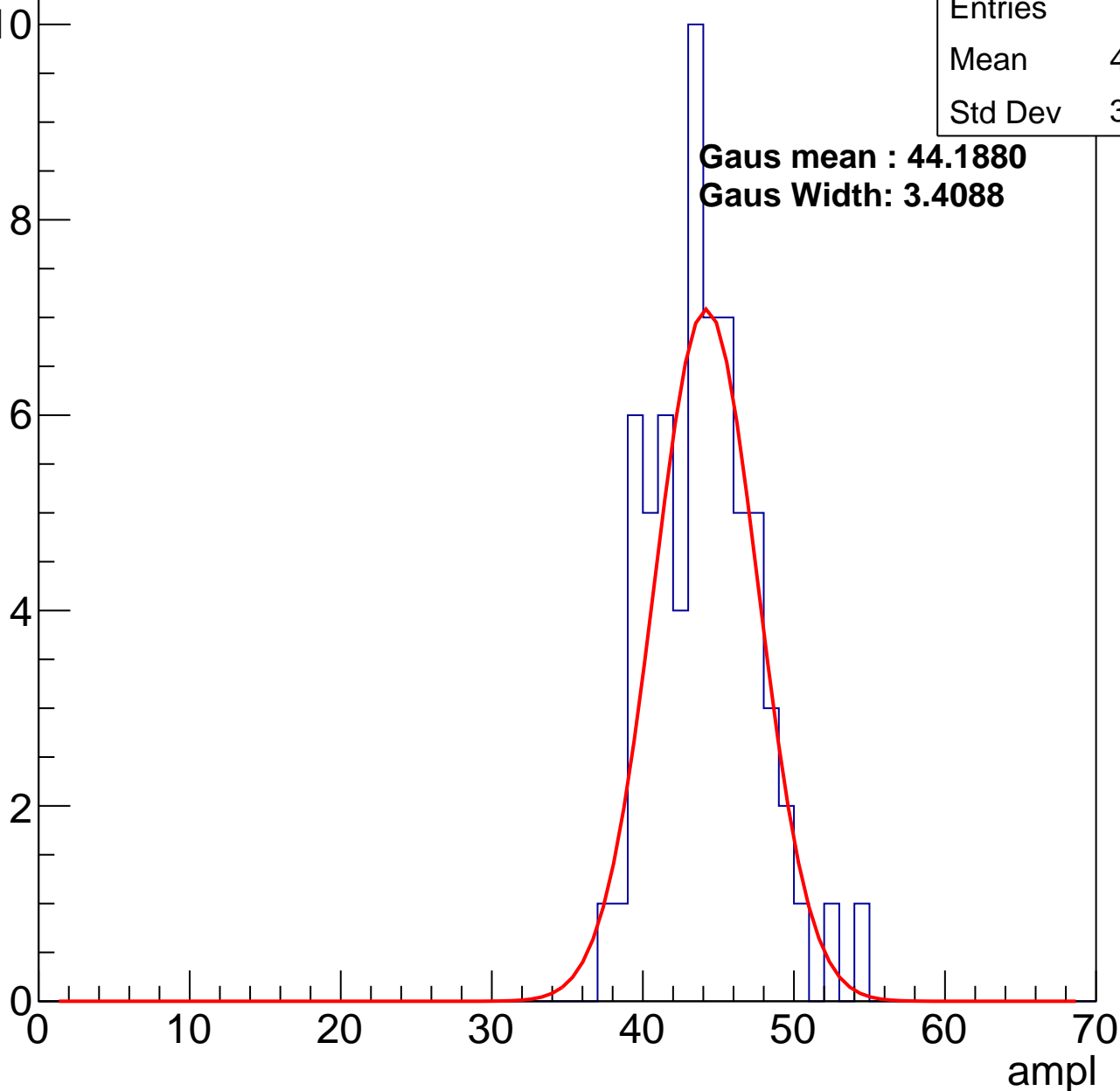
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.68
Std Dev	3.415

Gaus mean : 44.1880

Gaus Width: 3.4088

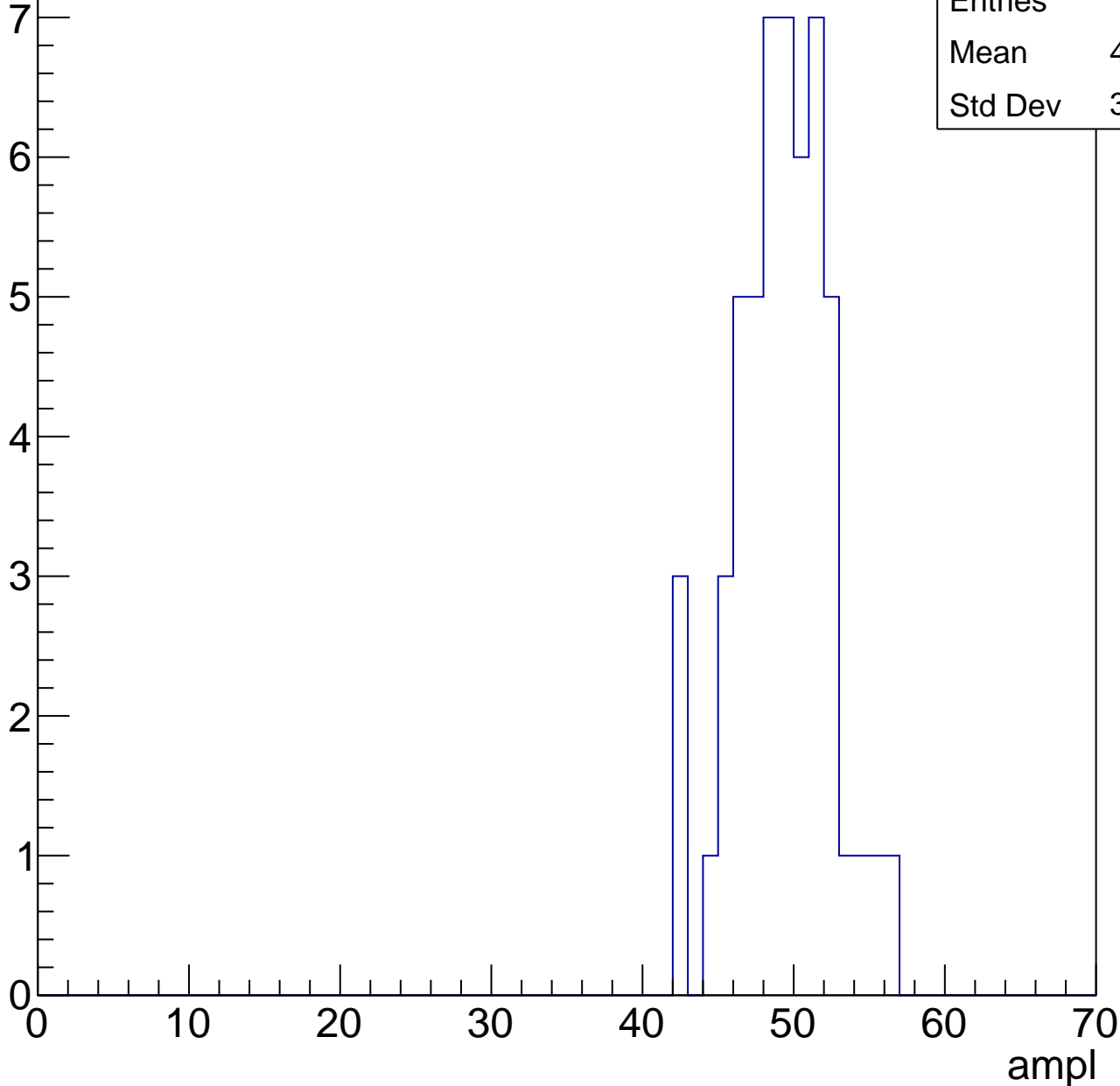


B1L102S, U22-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.75
Std Dev	3.052

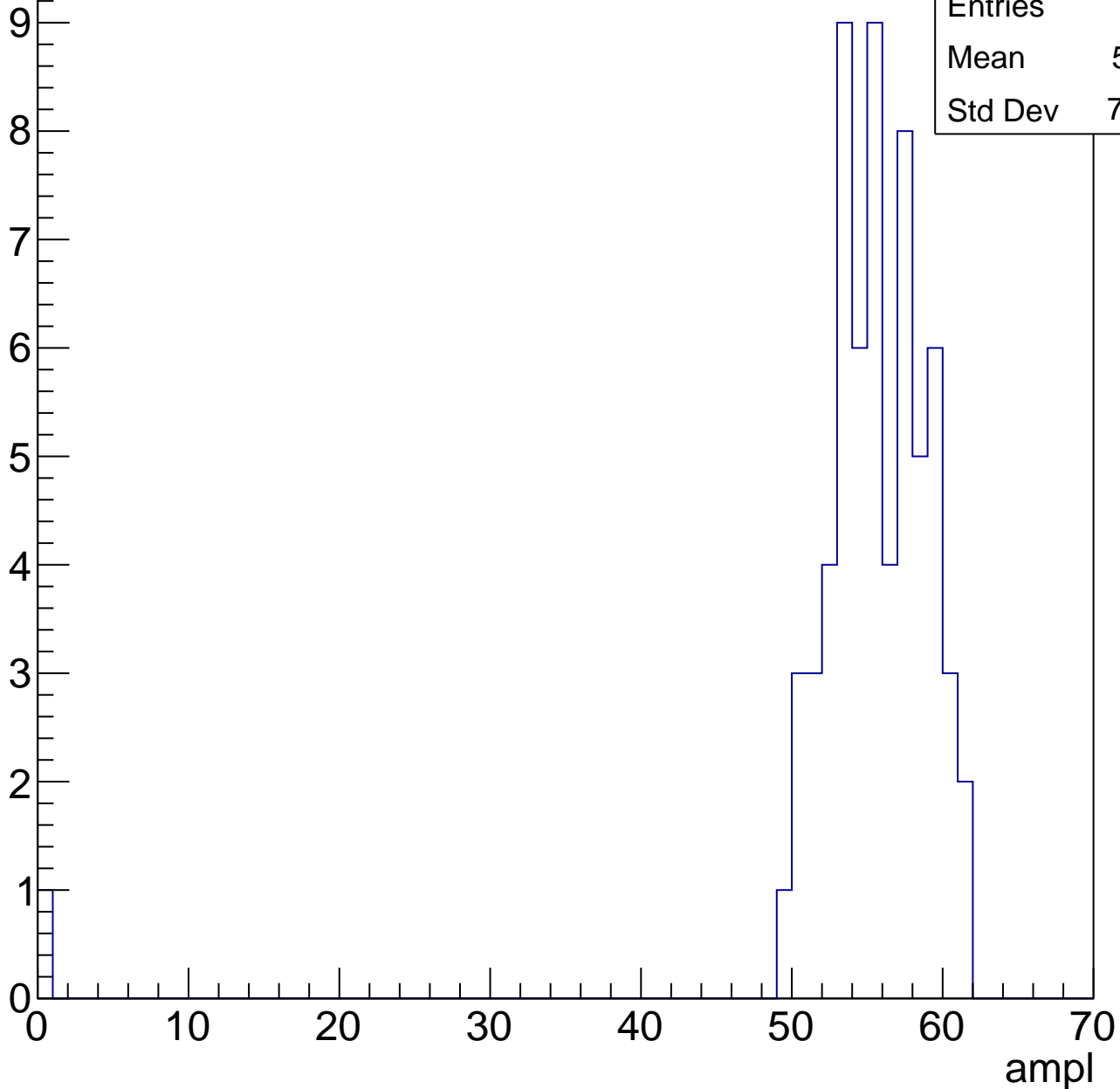


B1L102S, U22-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.41
Std Dev	7.462

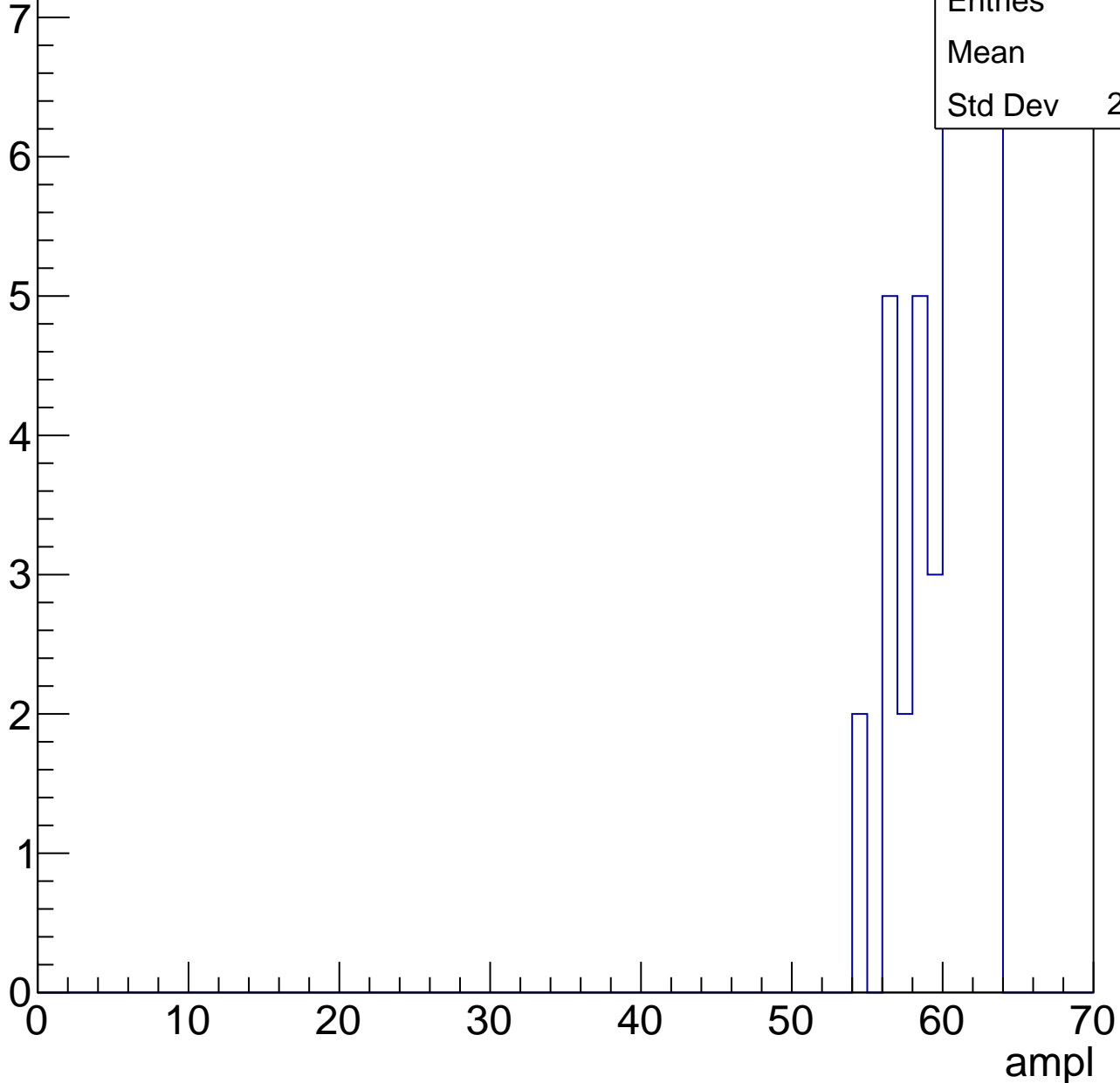


B1L102S, U22-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

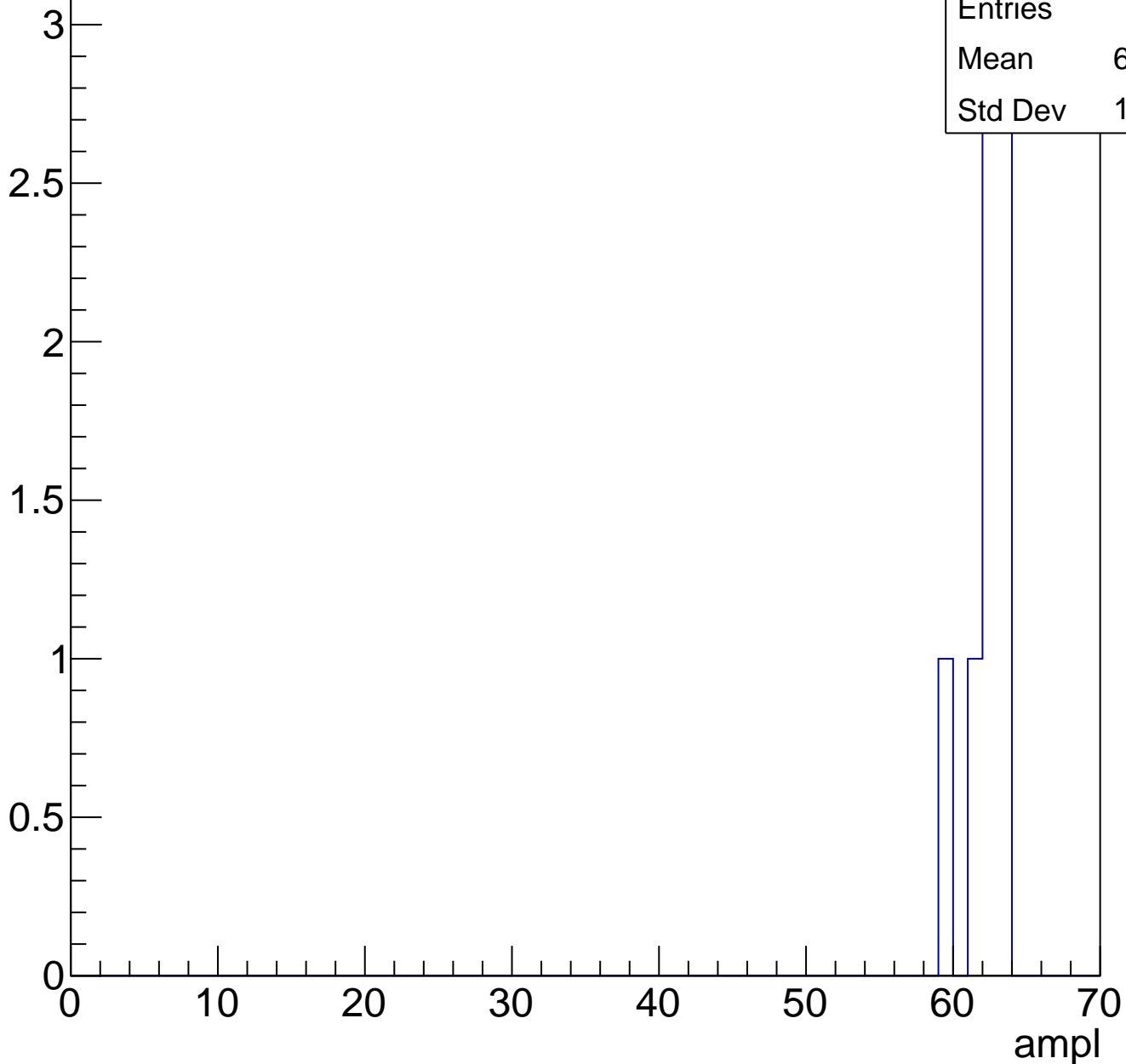
Entries	45
Mean	59.8
Std Dev	2.535



B1L102S, U22-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch104, adc0

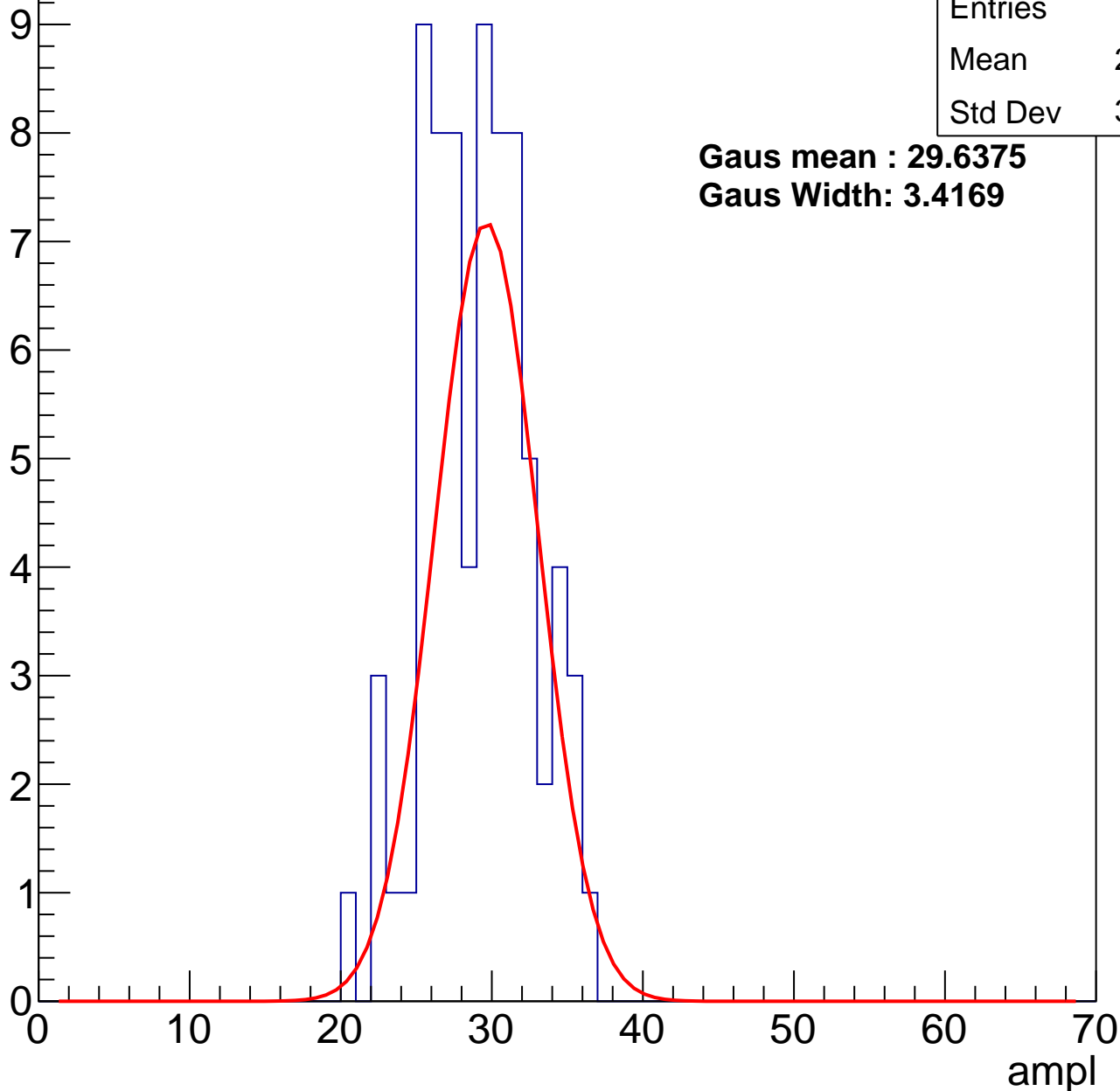
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.61
Std Dev	3.471

Gaus mean : 29.6375

Gaus Width: 3.4169



B1L102S, U22-ch104, adc1

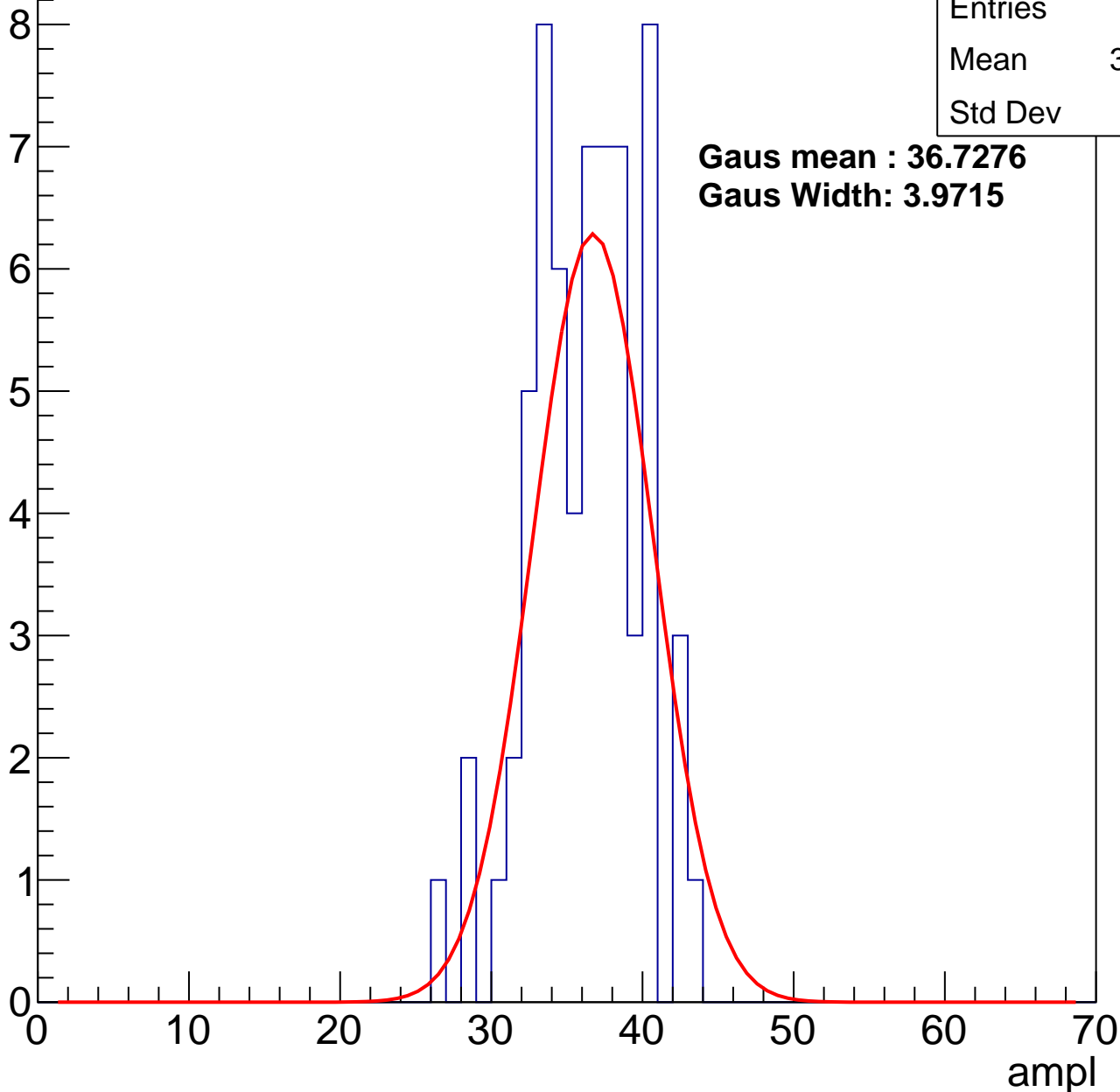
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.77
Std Dev	3.59

Gaus mean : 36.7276

Gaus Width: 3.9715



B1L102S, U22-ch104, adc2

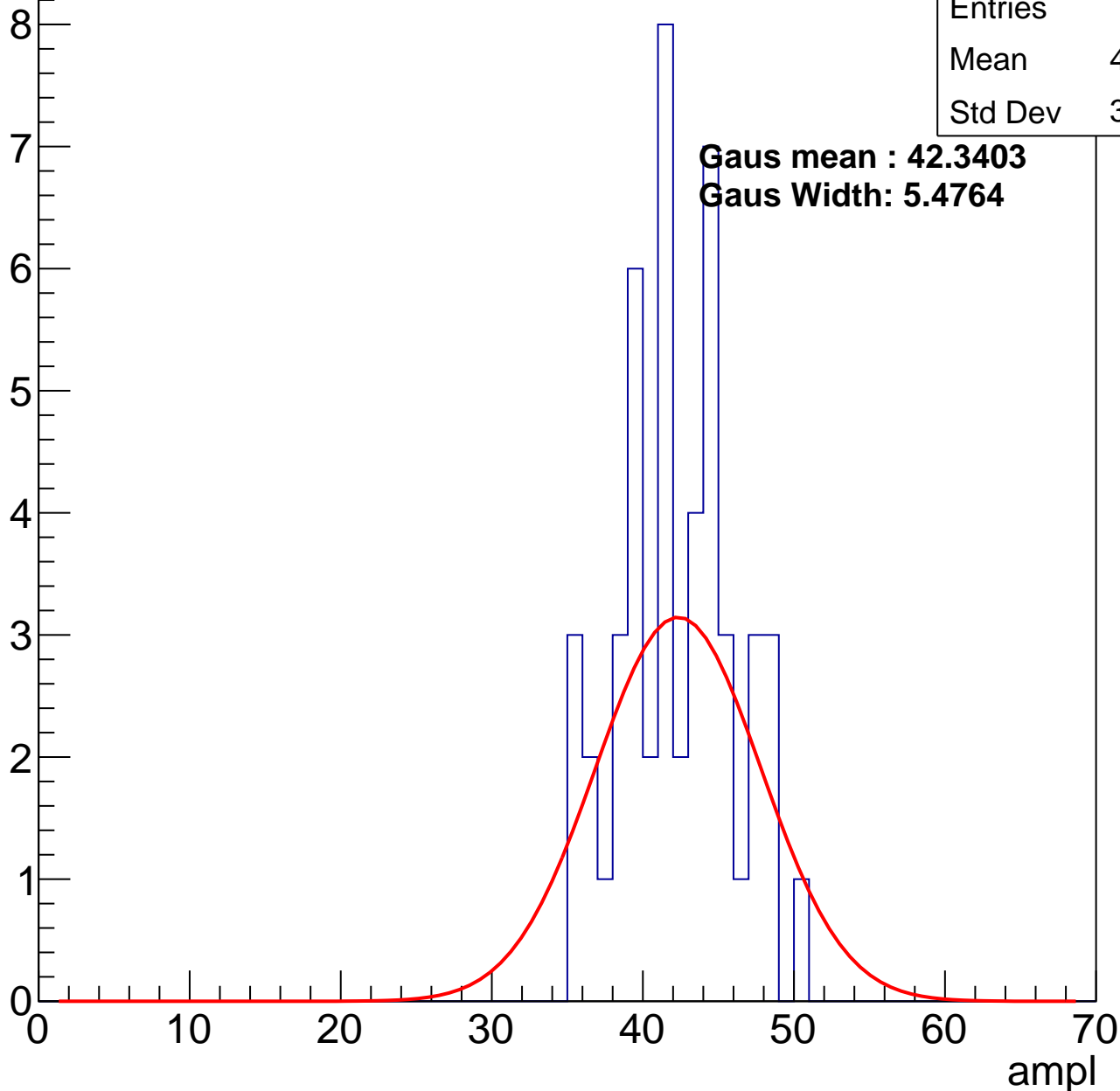
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	41.84
Std Dev	3.733

Gaus mean : 42.3403

Gaus Width: 5.4764

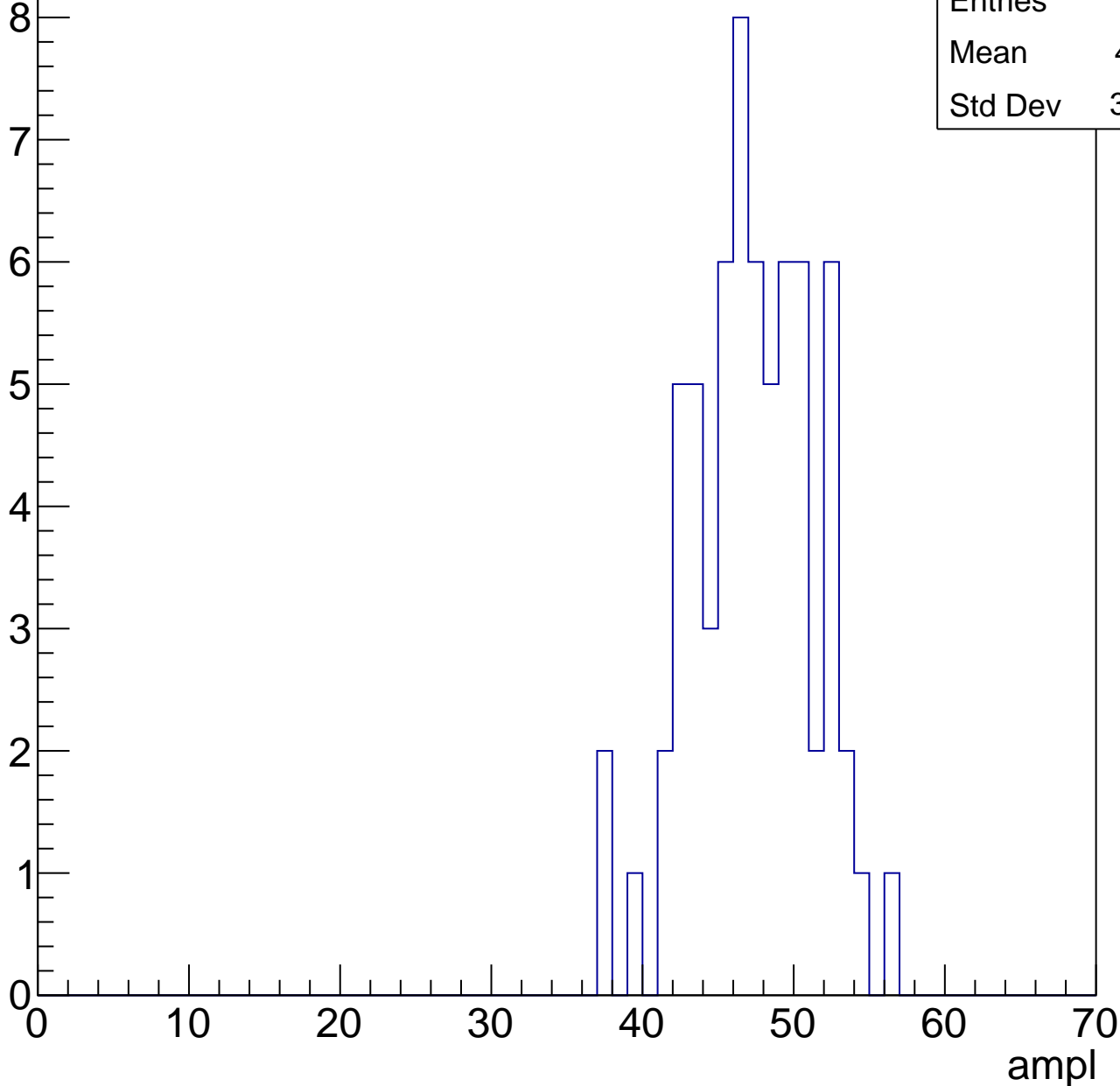


B1L102S, U22-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	46.81
Std Dev	3.986

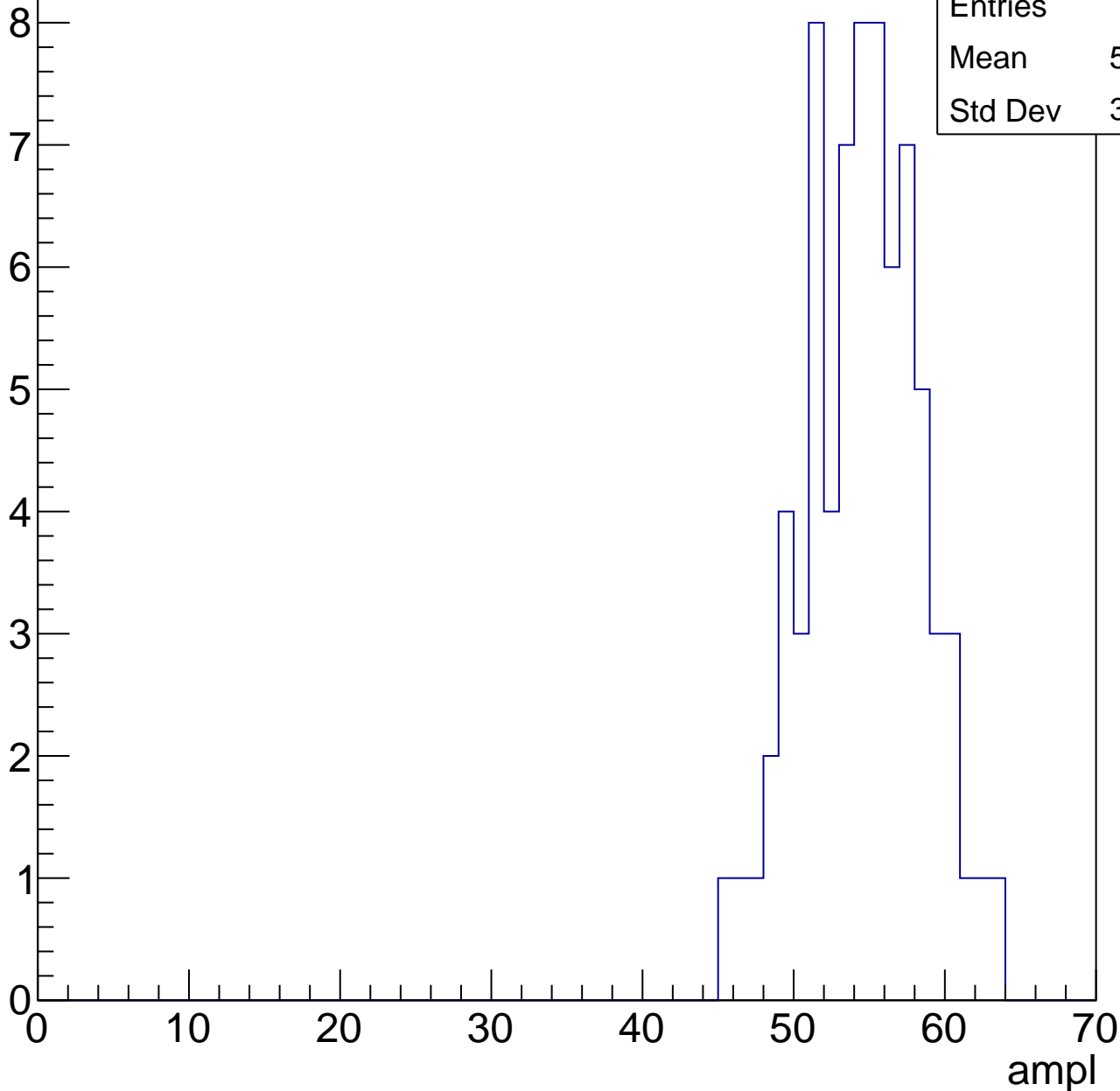


B1L102S, U22-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	54.15
Std Dev	3.784

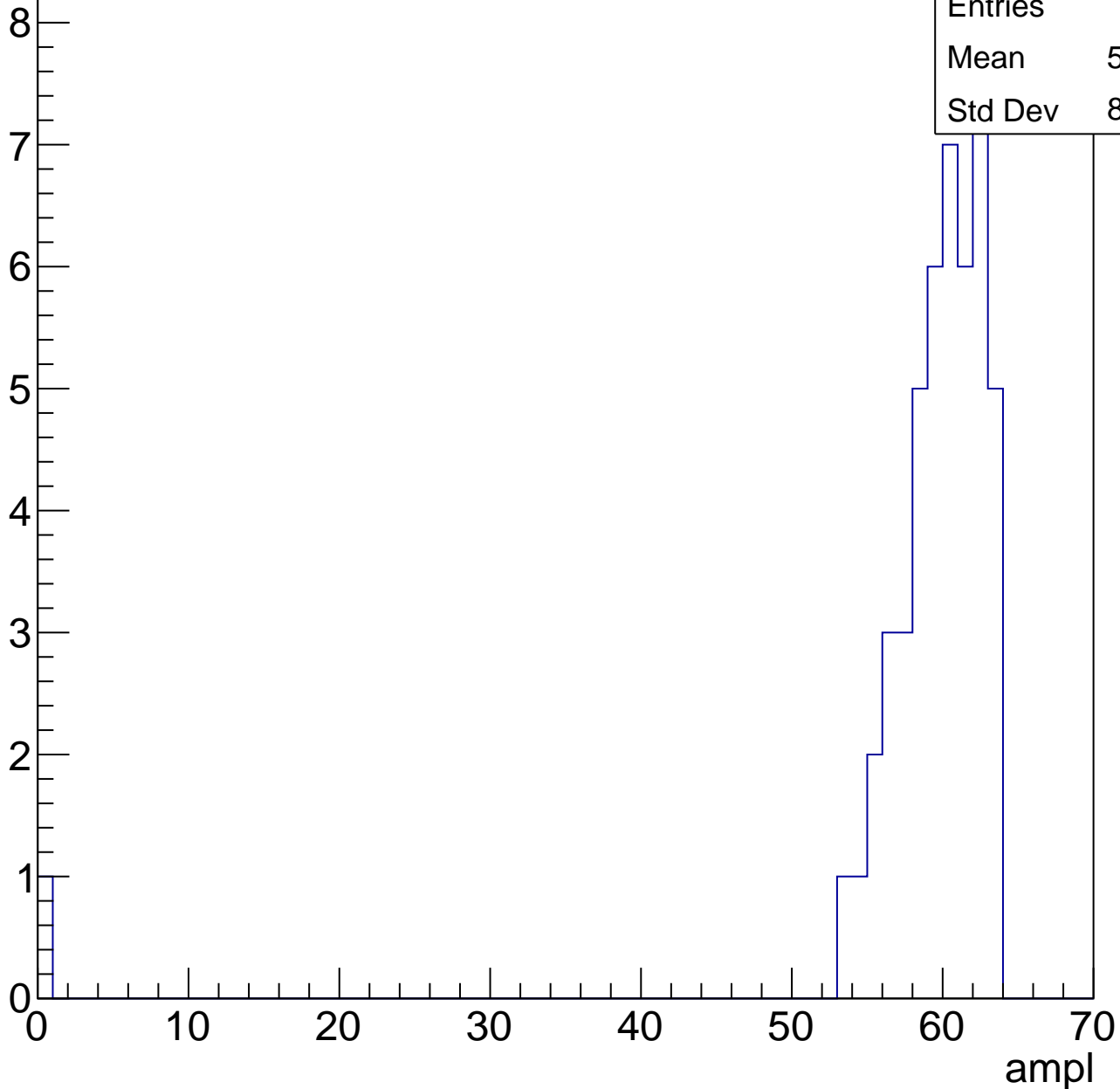


B1L102S, U22-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.27
Std Dev	8.869



B1L102S, U22-ch104, adc6

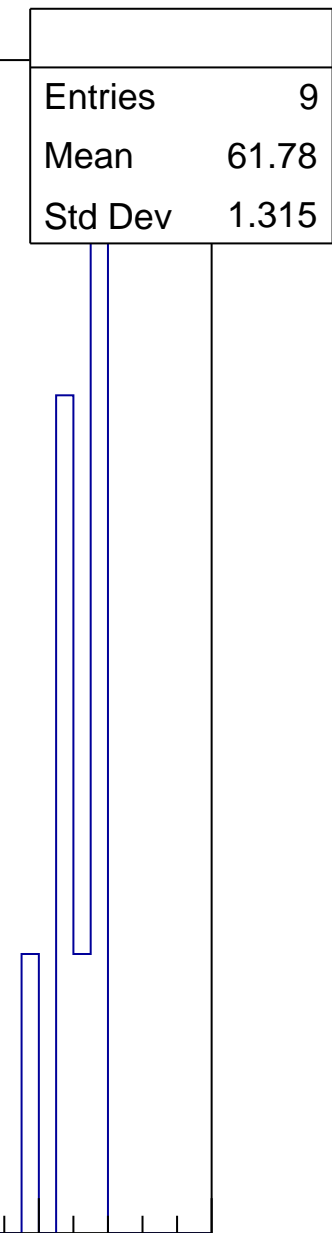
calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.78
Std Dev	1.315

ampl



B1L102S, U22-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L102S, U22-ch105, adc0

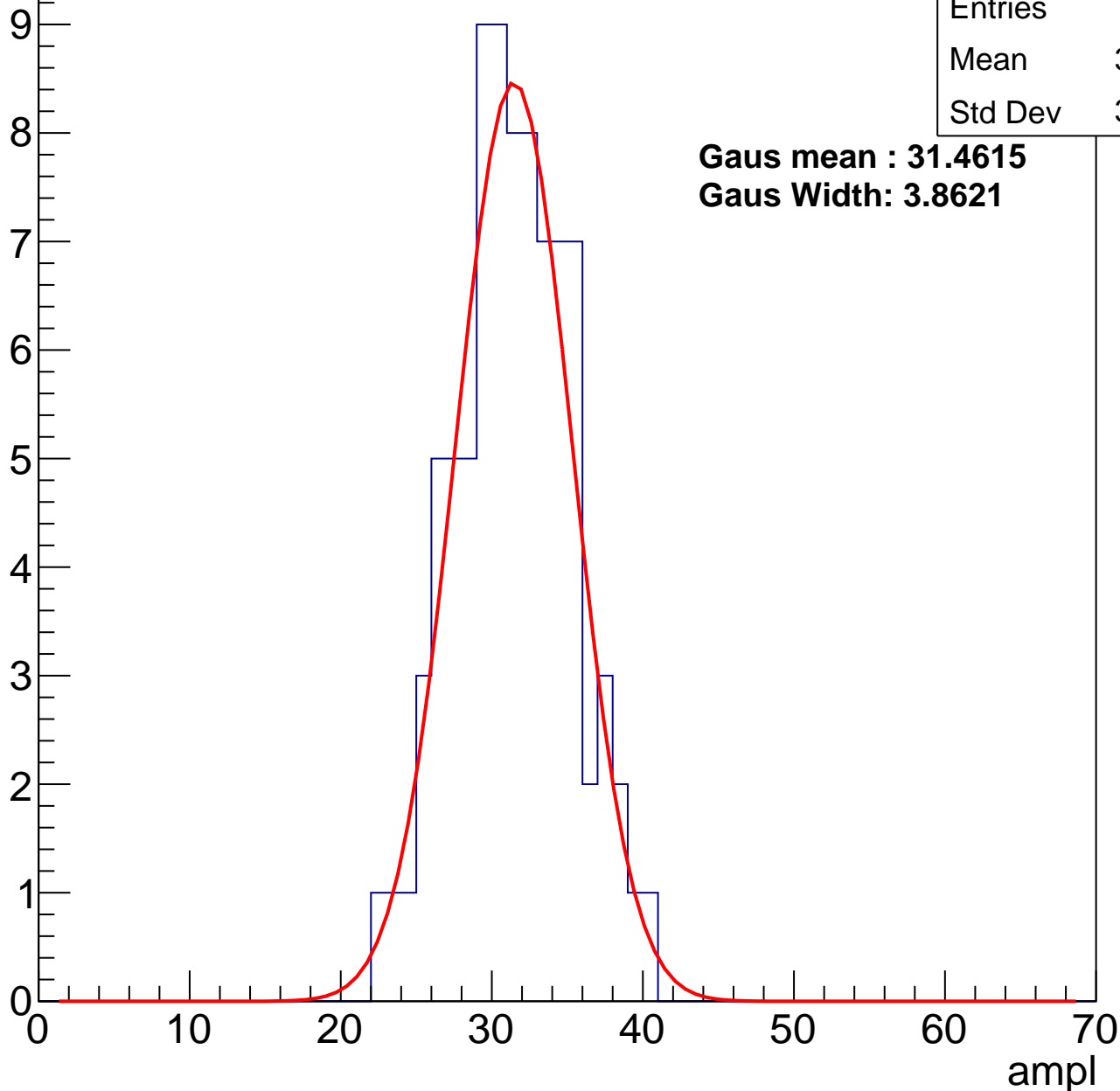
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	31.01
Std Dev	3.771

Gaus mean : 31.4615

Gaus Width: 3.8621

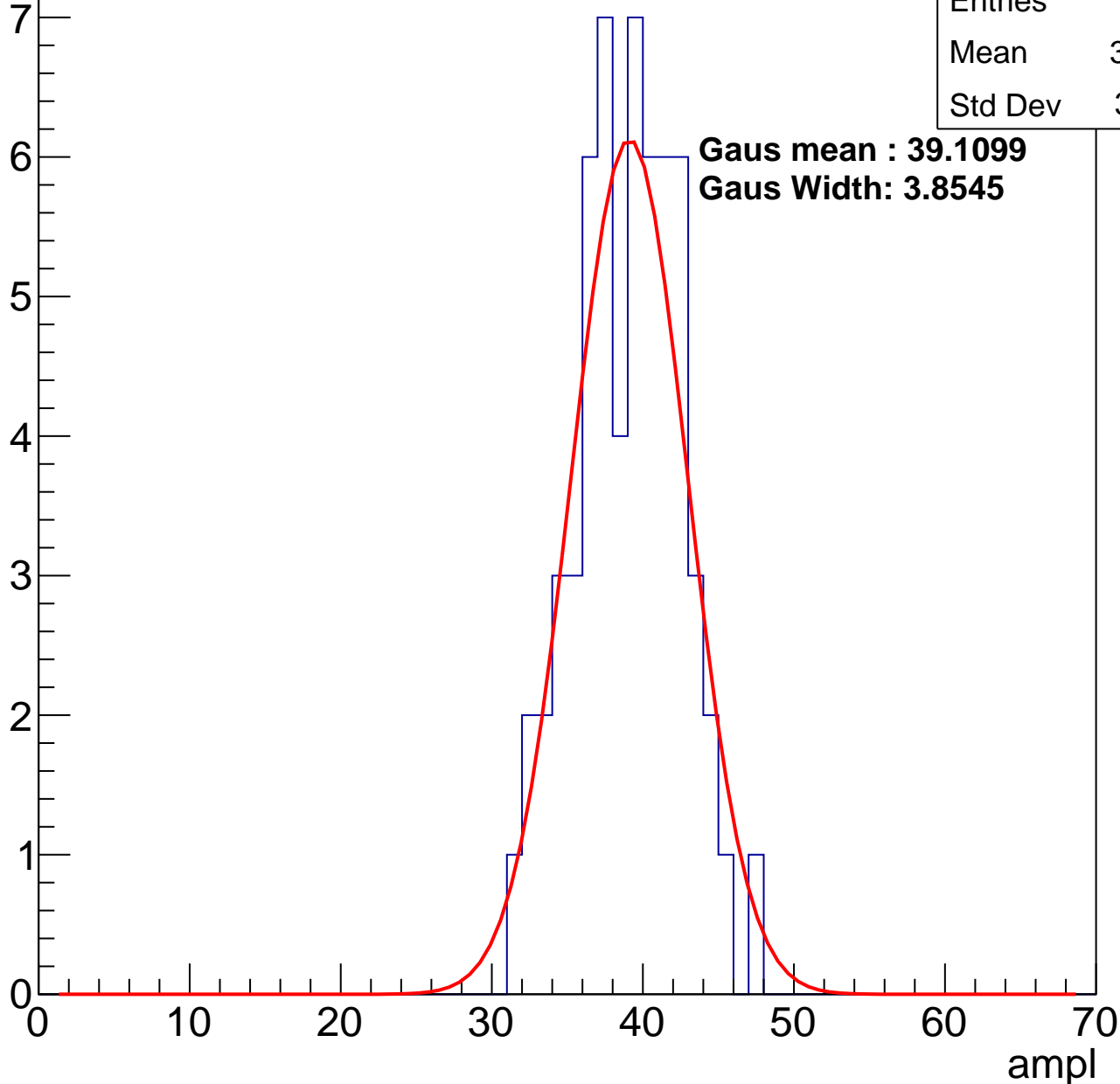


B1L102S, U22-ch105, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	38.58
Std Dev	3.451



B1L102S, U22-ch105, adc2

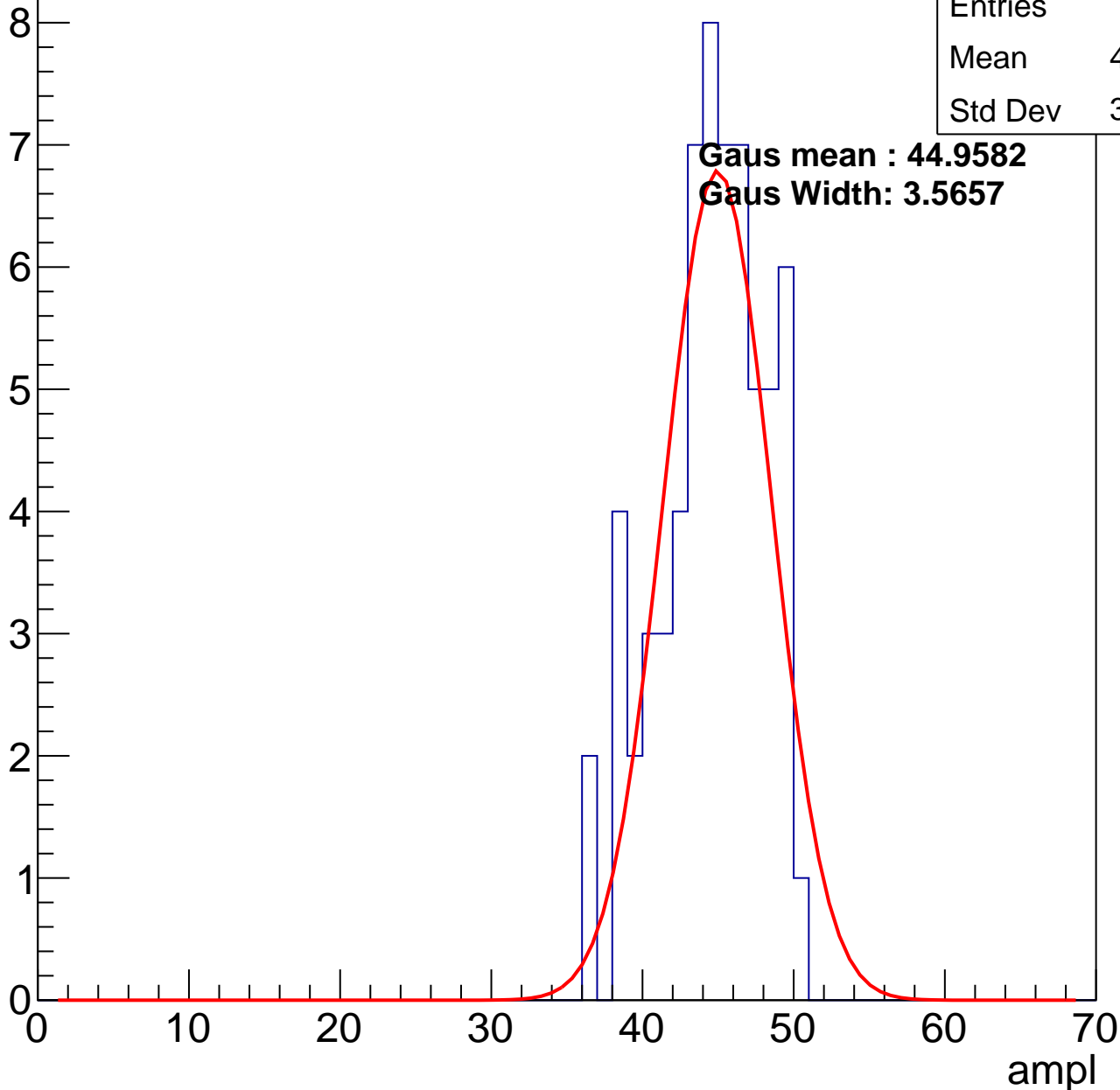
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	44.09
Std Dev	3.458

Gaus mean : 44.9582

Gaus Width: 3.5657

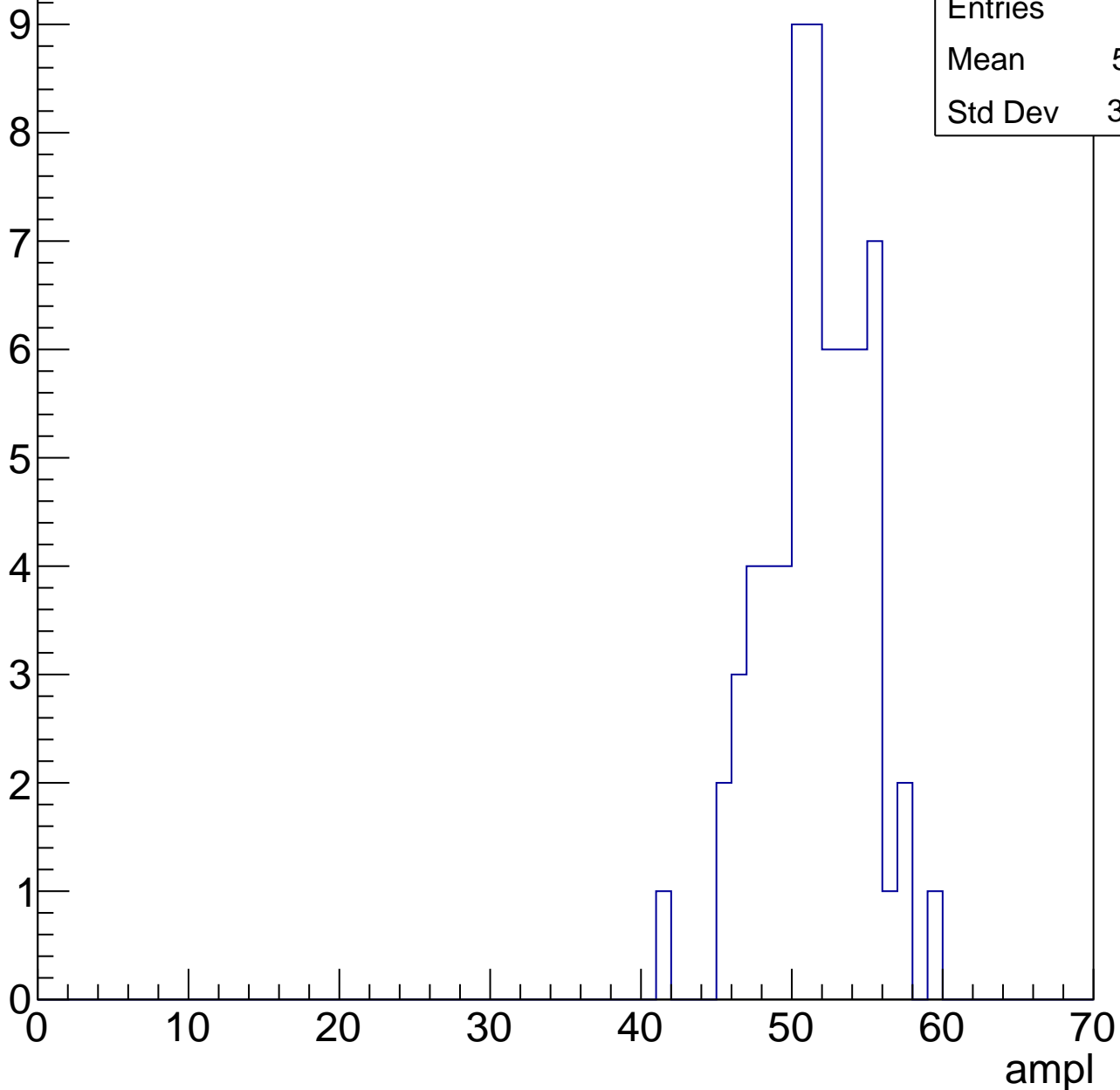


B1L102S, U22-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	51.11
Std Dev	3.347

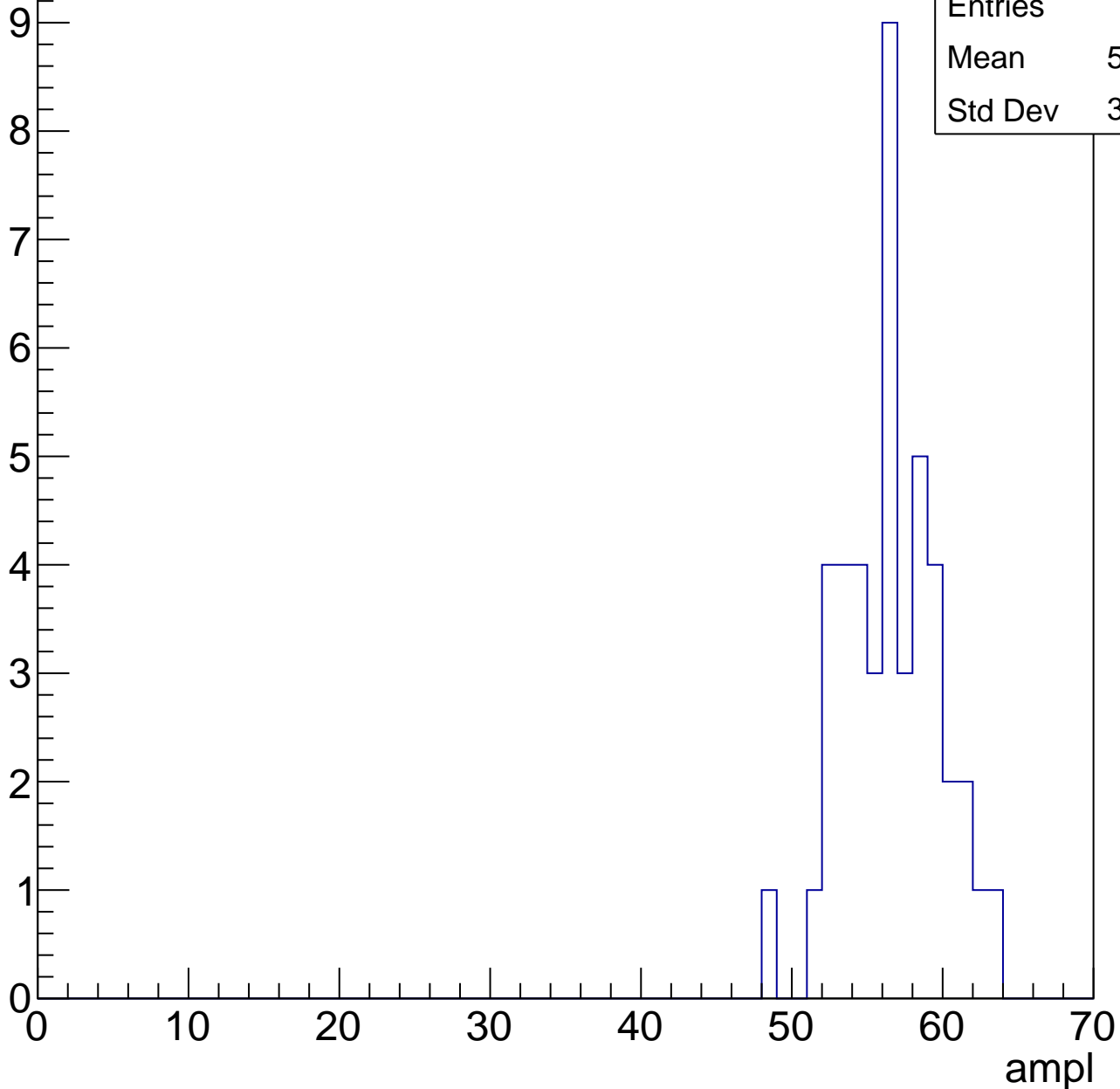


B1L102S, U22-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	56.09
Std Dev	3.139

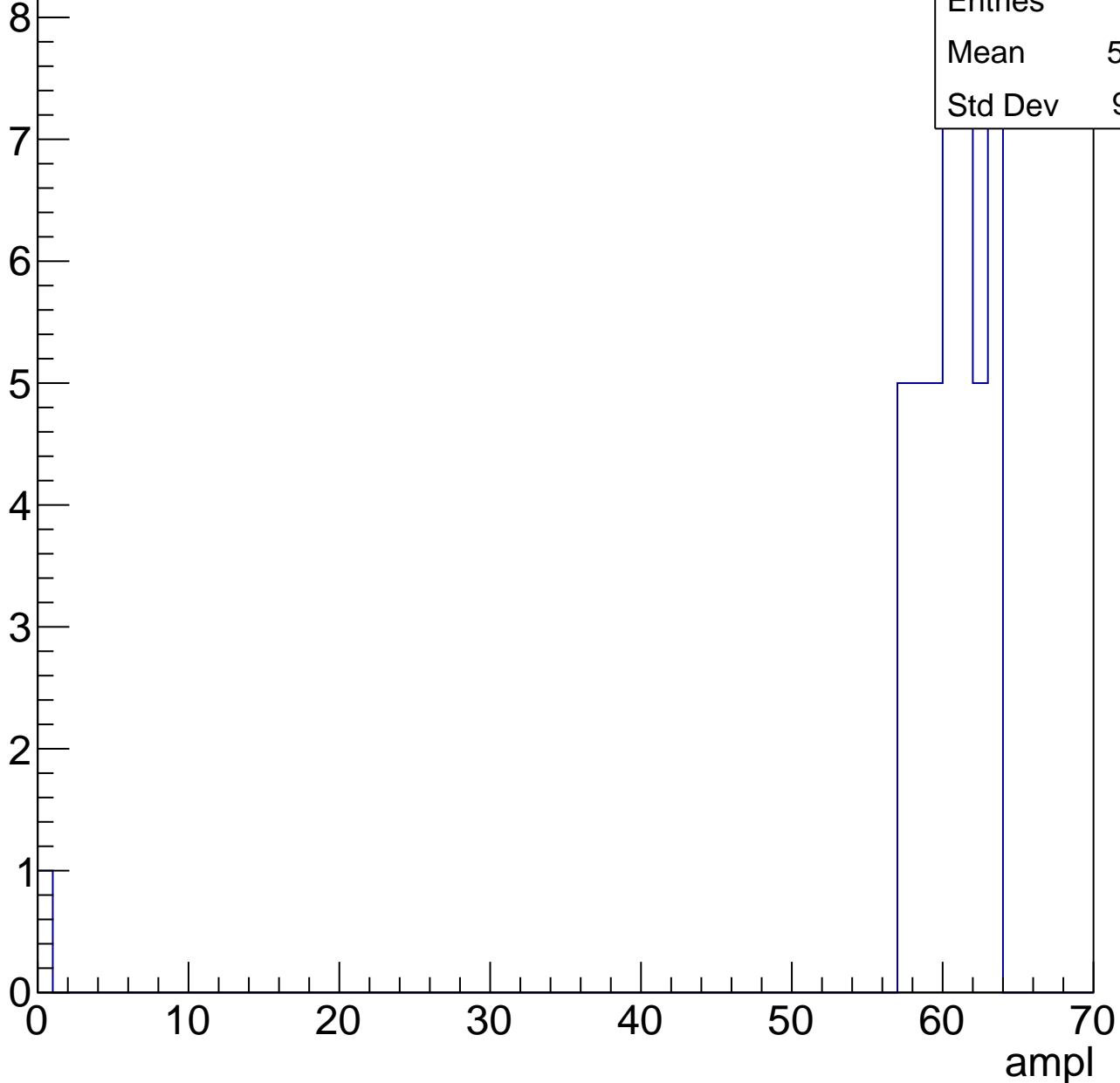


B1L102S, U22-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

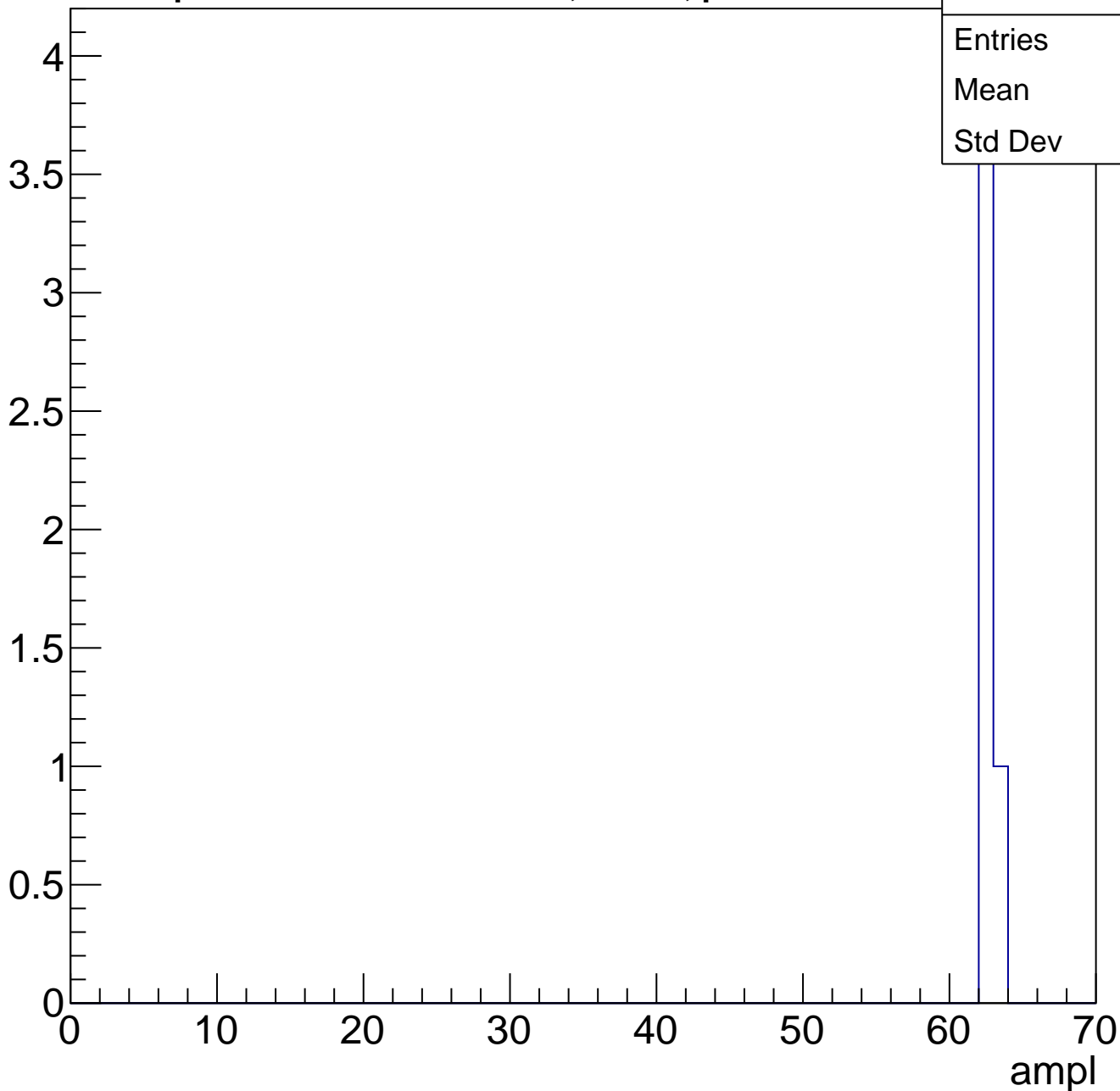
Entries	45
Mean	58.93
Std Dev	9.091



B1L102S, U22-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch106, adc0

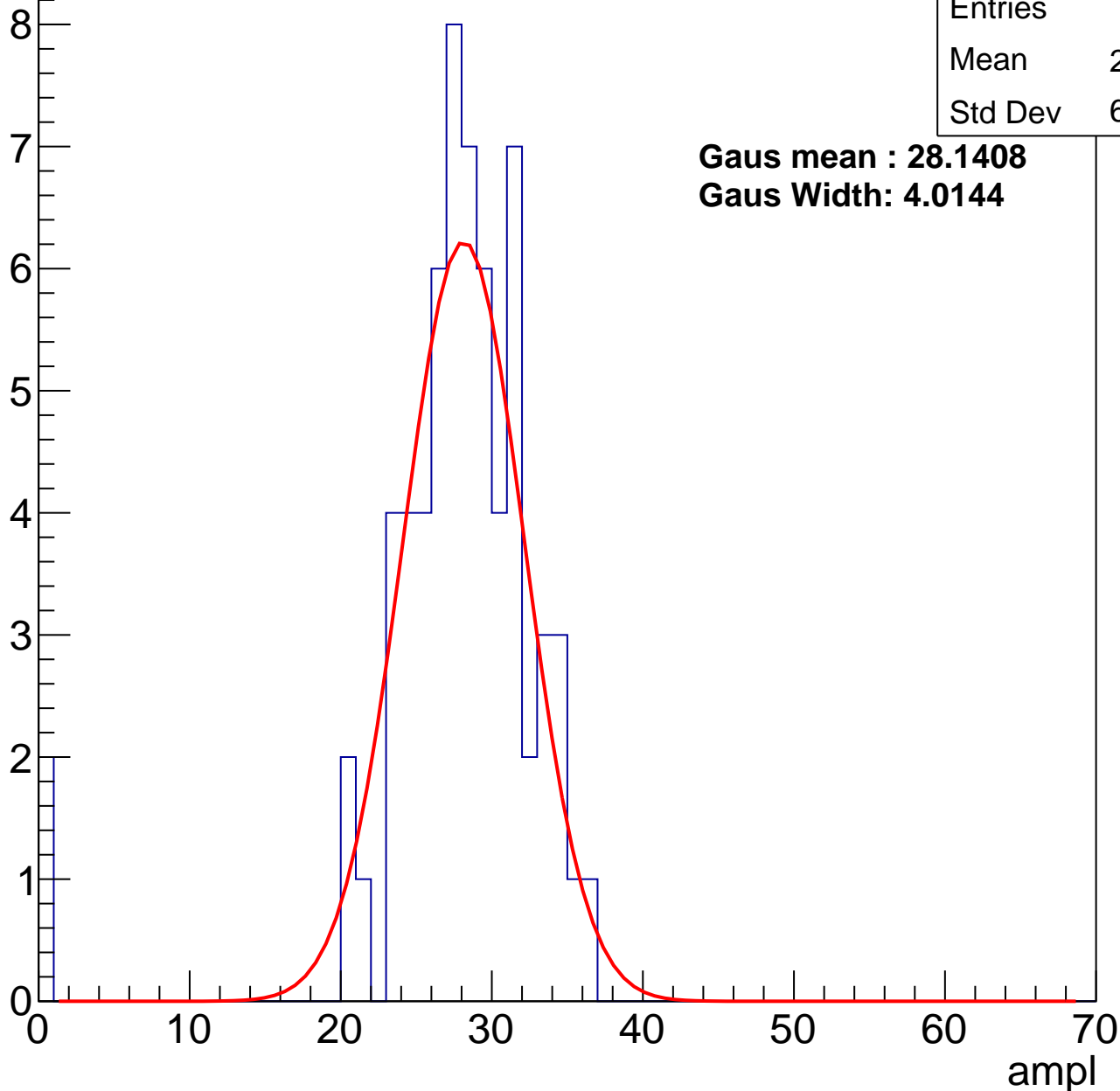
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.14
Std Dev	6.002

Gaus mean : 28.1408

Gaus Width: 4.0144



B1L102S, U22-ch106, adc1

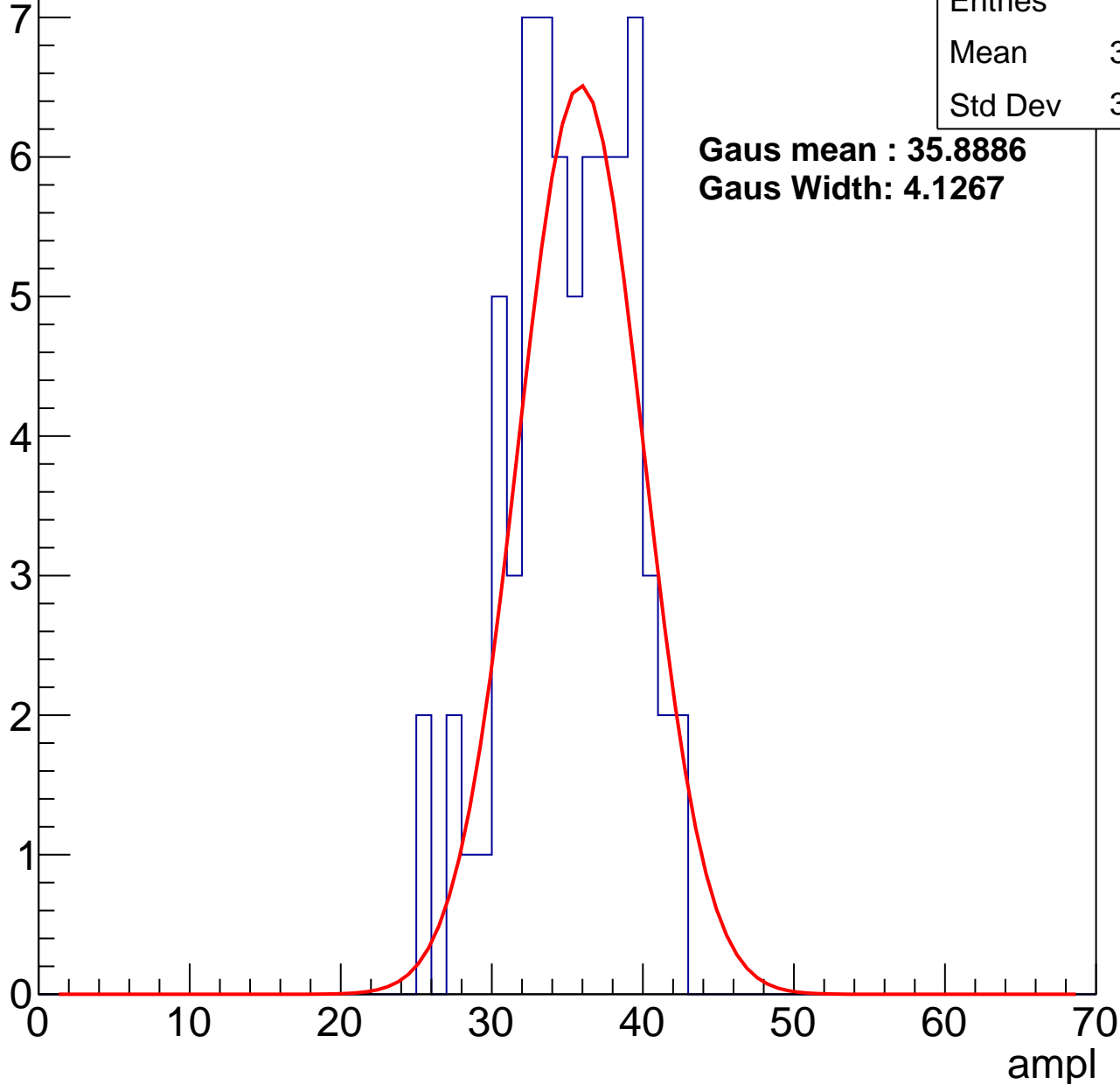
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.69
Std Dev	3.967

Gaus mean : 35.8886

Gaus Width: 4.1267



B1L102S, U22-ch106, adc2

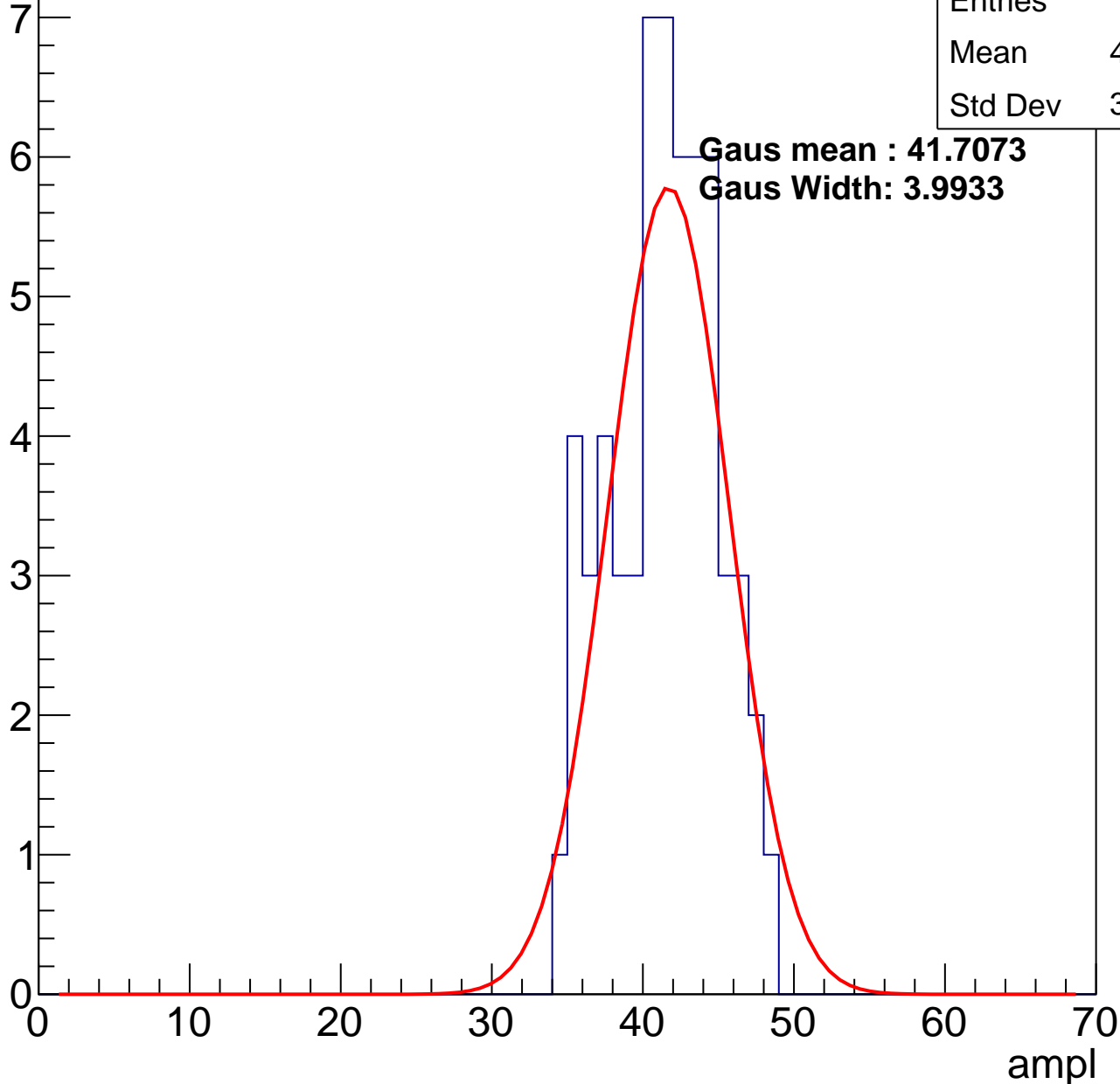
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	40.97
Std Dev	3.459

Gaus mean : 41.7073

Gaus Width: 3.9933

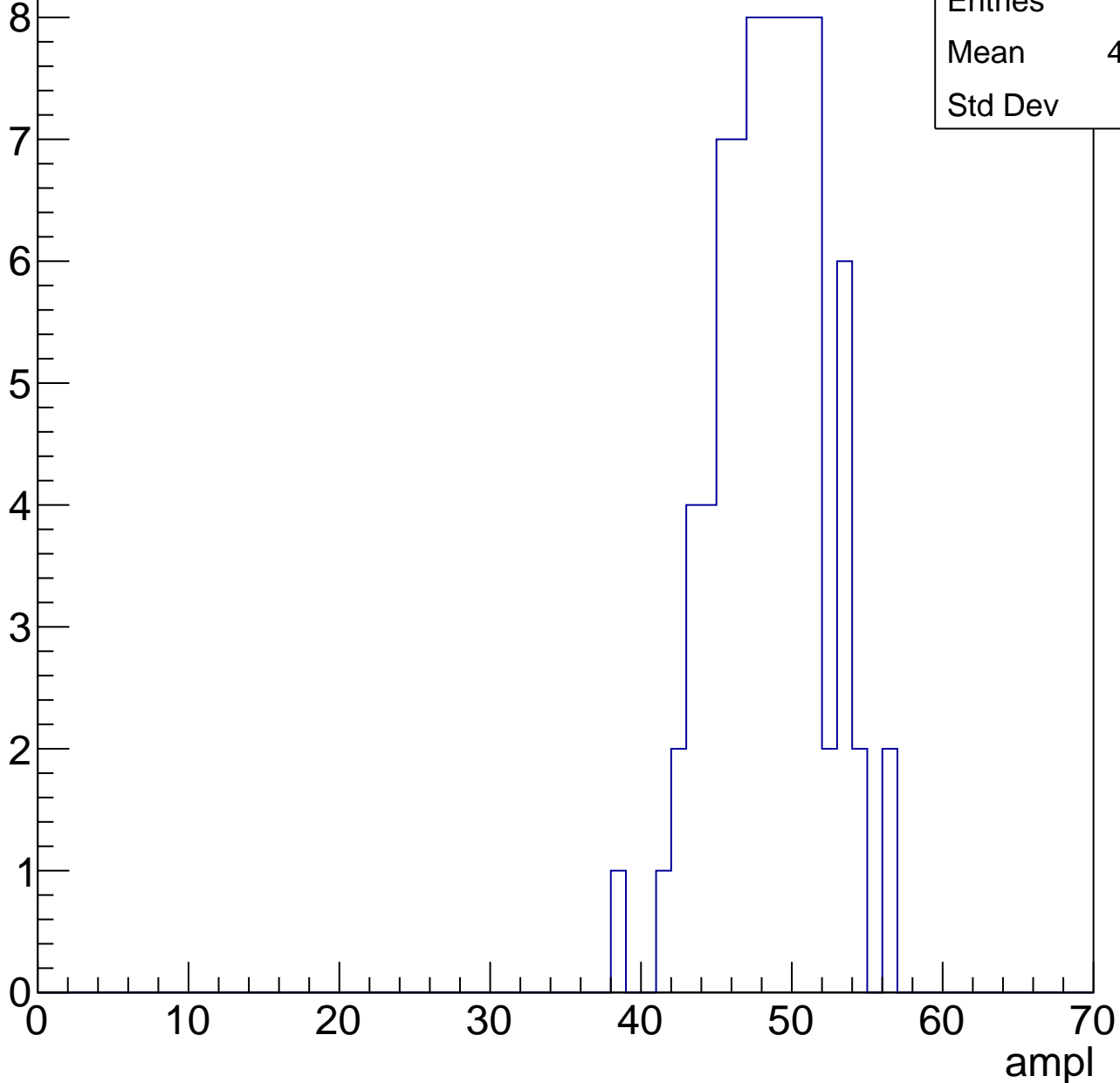


B1L102S, U22-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	48.08
Std Dev	3.54

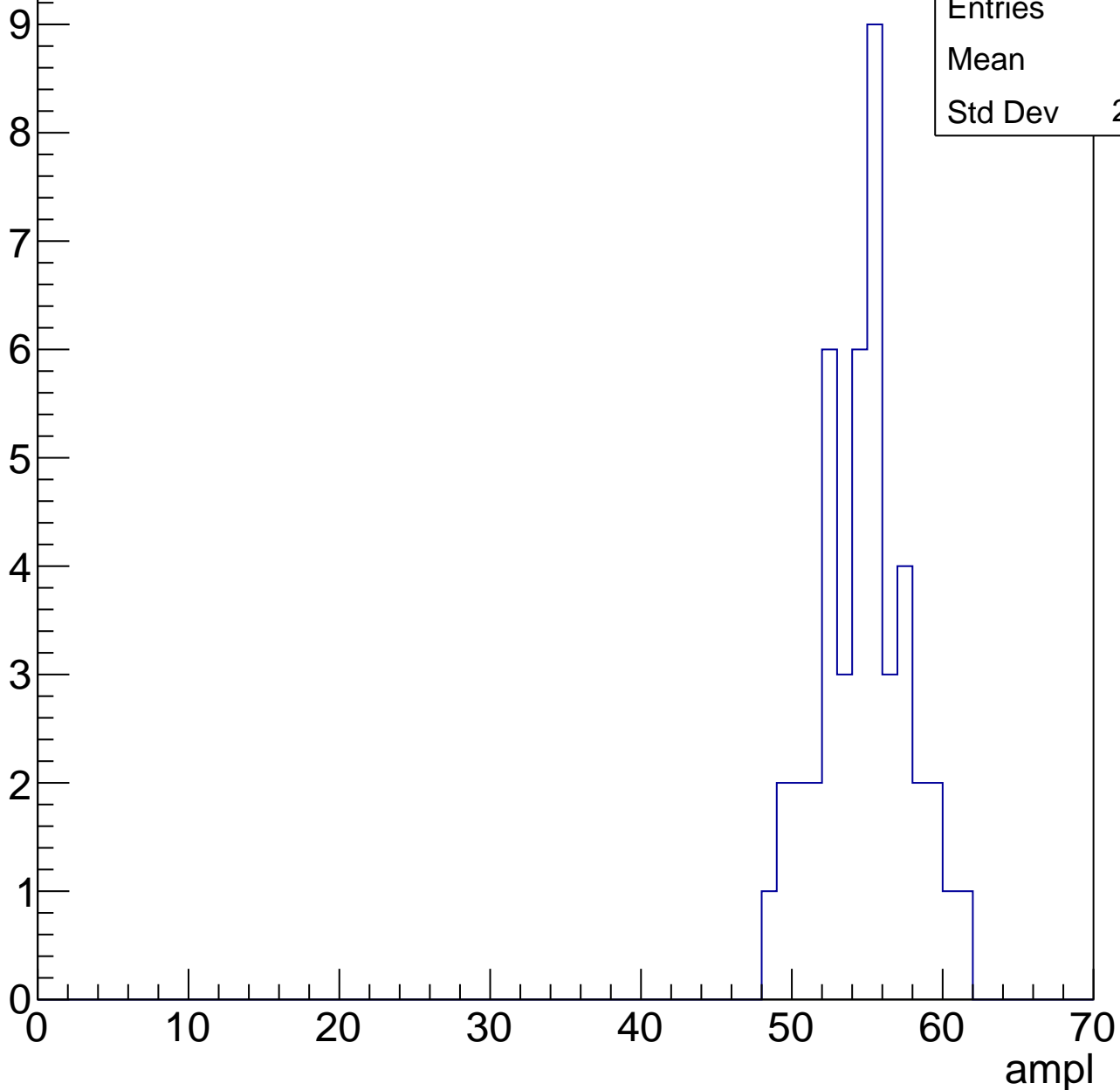


B1L102S, U22-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	54.3
Std Dev	2.951



B1L102S, U22-ch106, adc5

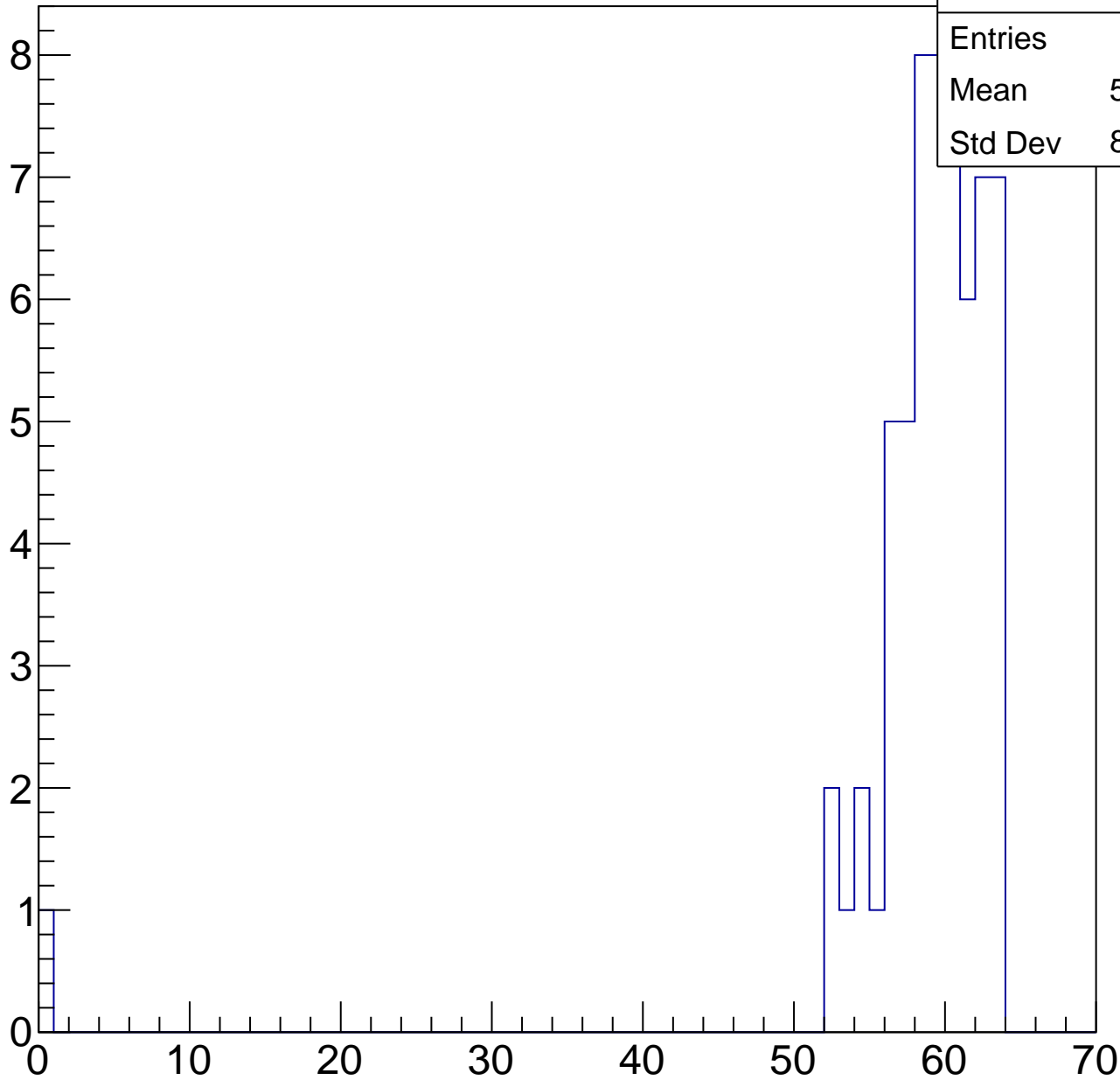
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	58.07
Std Dev	8.004

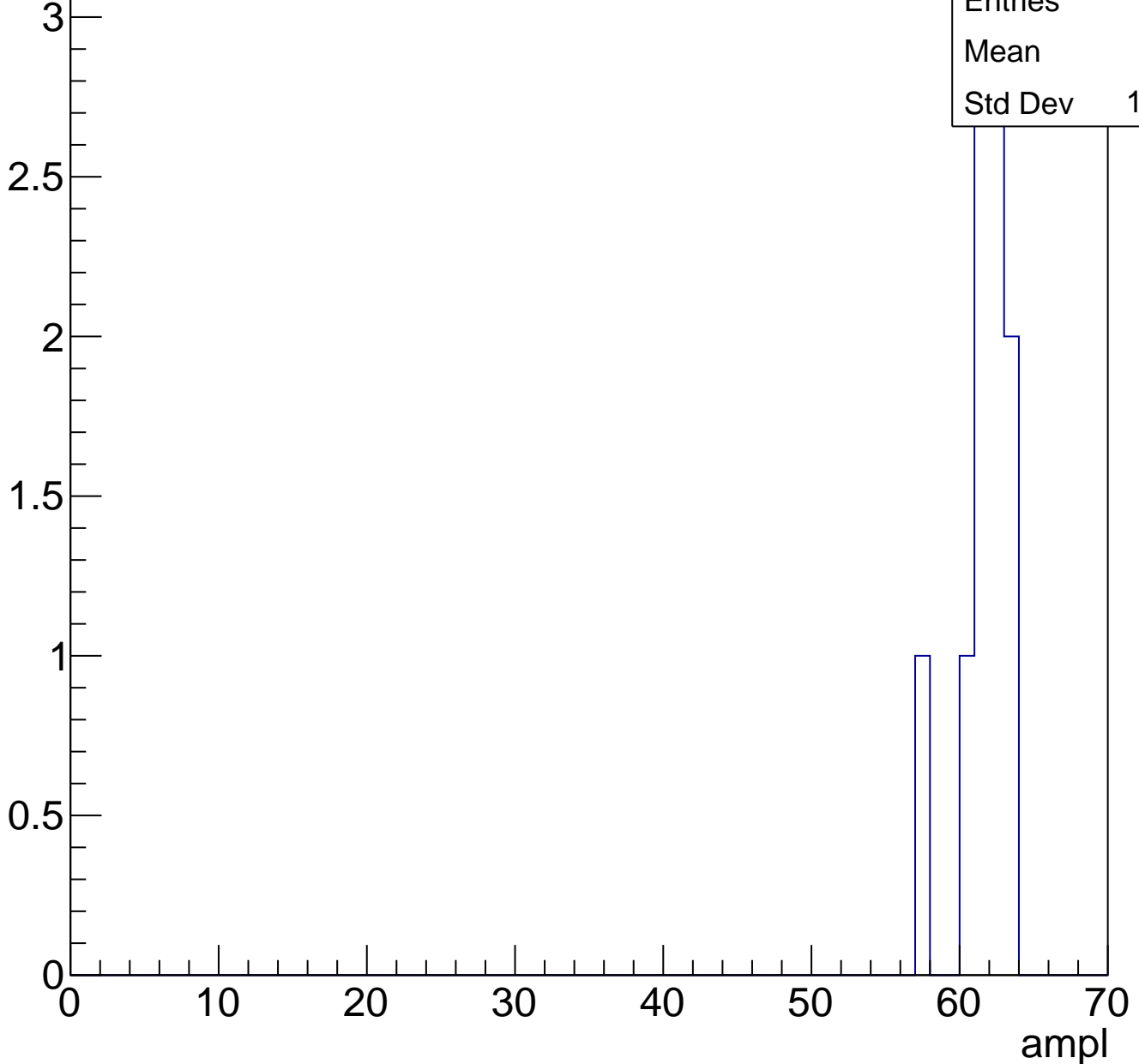
ampl



B1L102S, U22-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch107, adc0

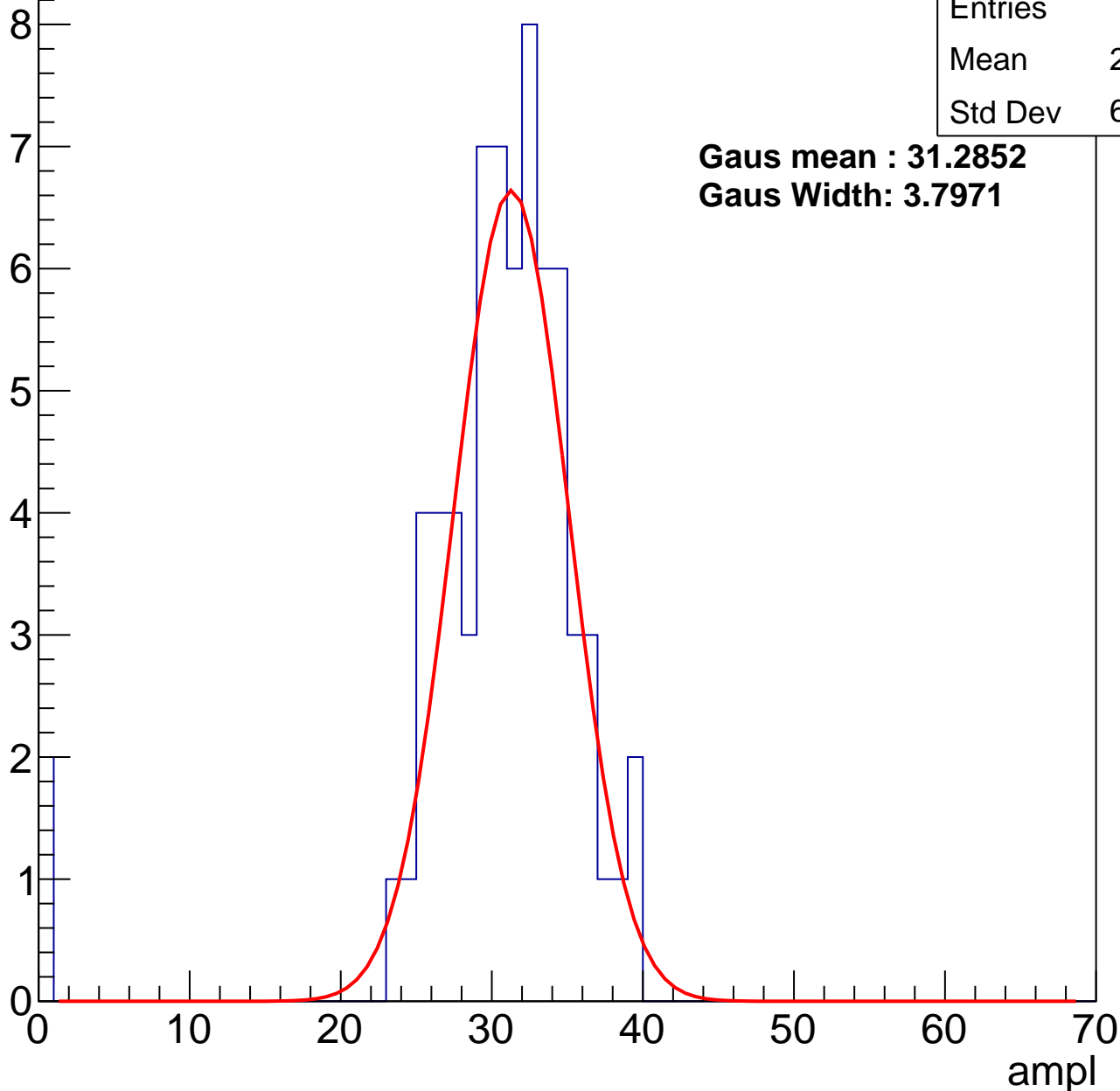
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.94
Std Dev	6.313

Gaus mean : 31.2852

Gaus Width: 3.7971



B1L102S, U22-ch107, adc1

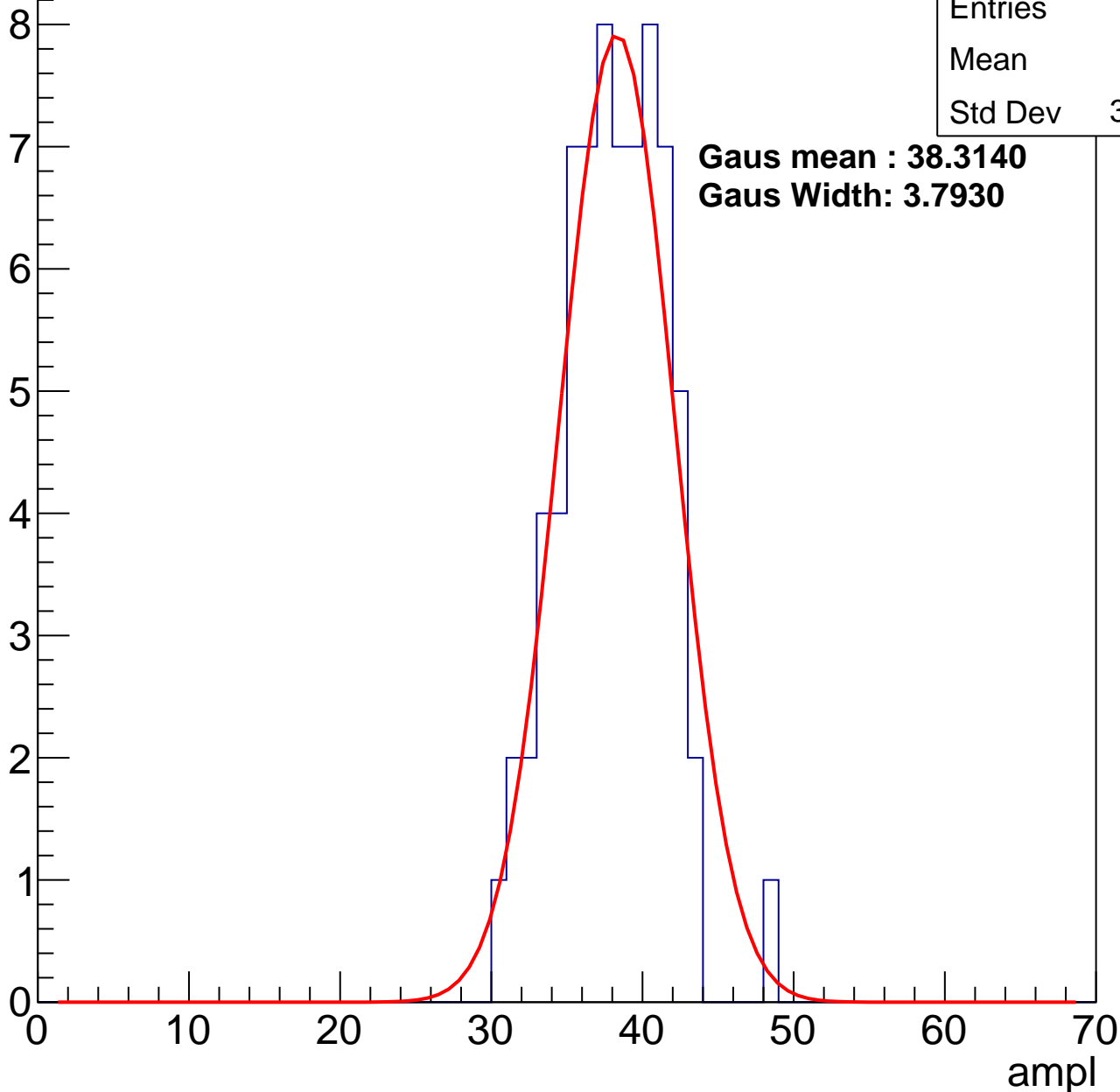
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	37.6
Std Dev	3.369

Gaus mean : 38.3140

Gaus Width: 3.7930



B1L102S, U22-ch107, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.73
Std Dev	3.17

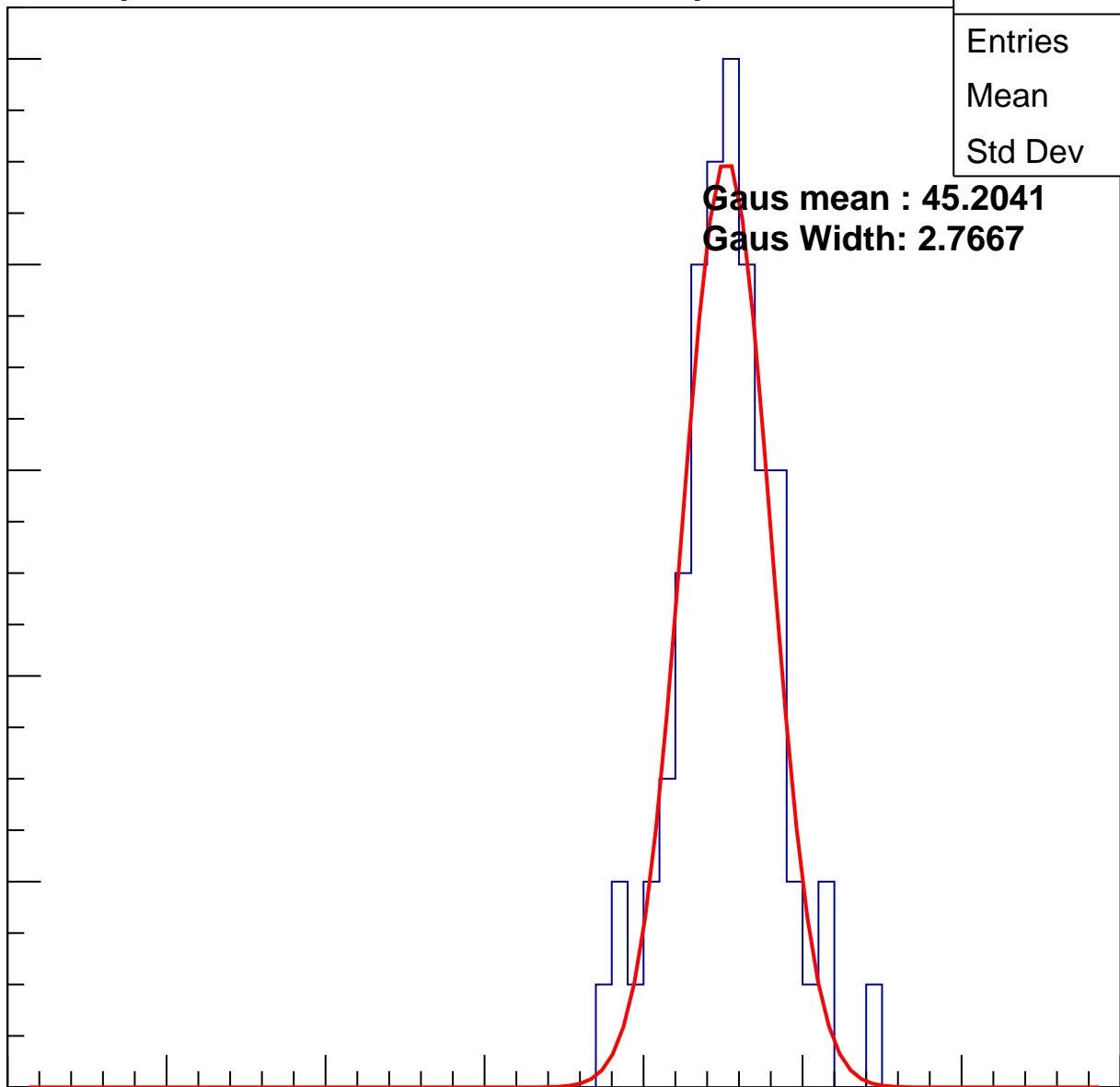
Gaus mean : 45.2041

Gaus Width: 2.7667

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

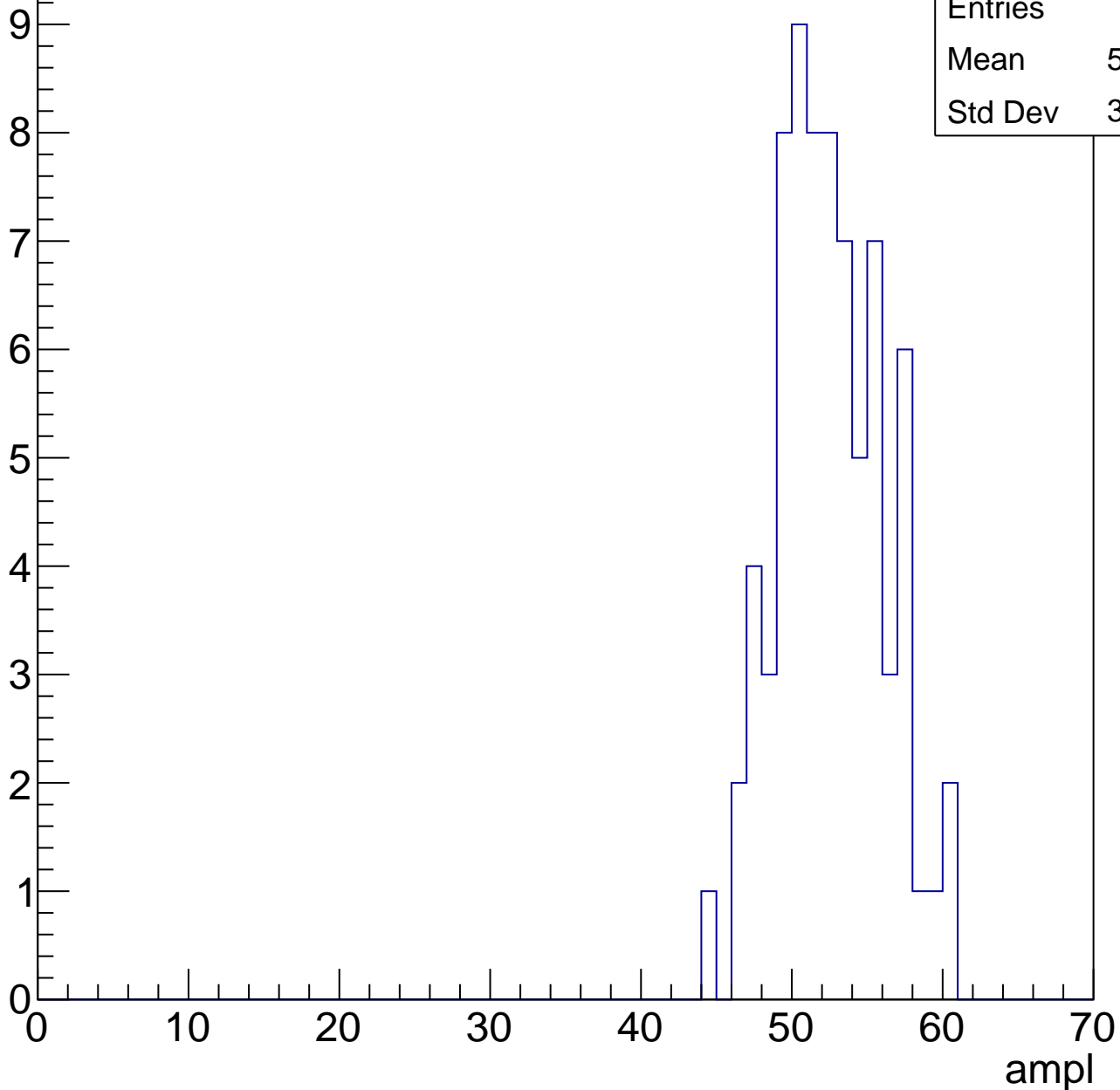


B1L102S, U22-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	52.09
Std Dev	3.465

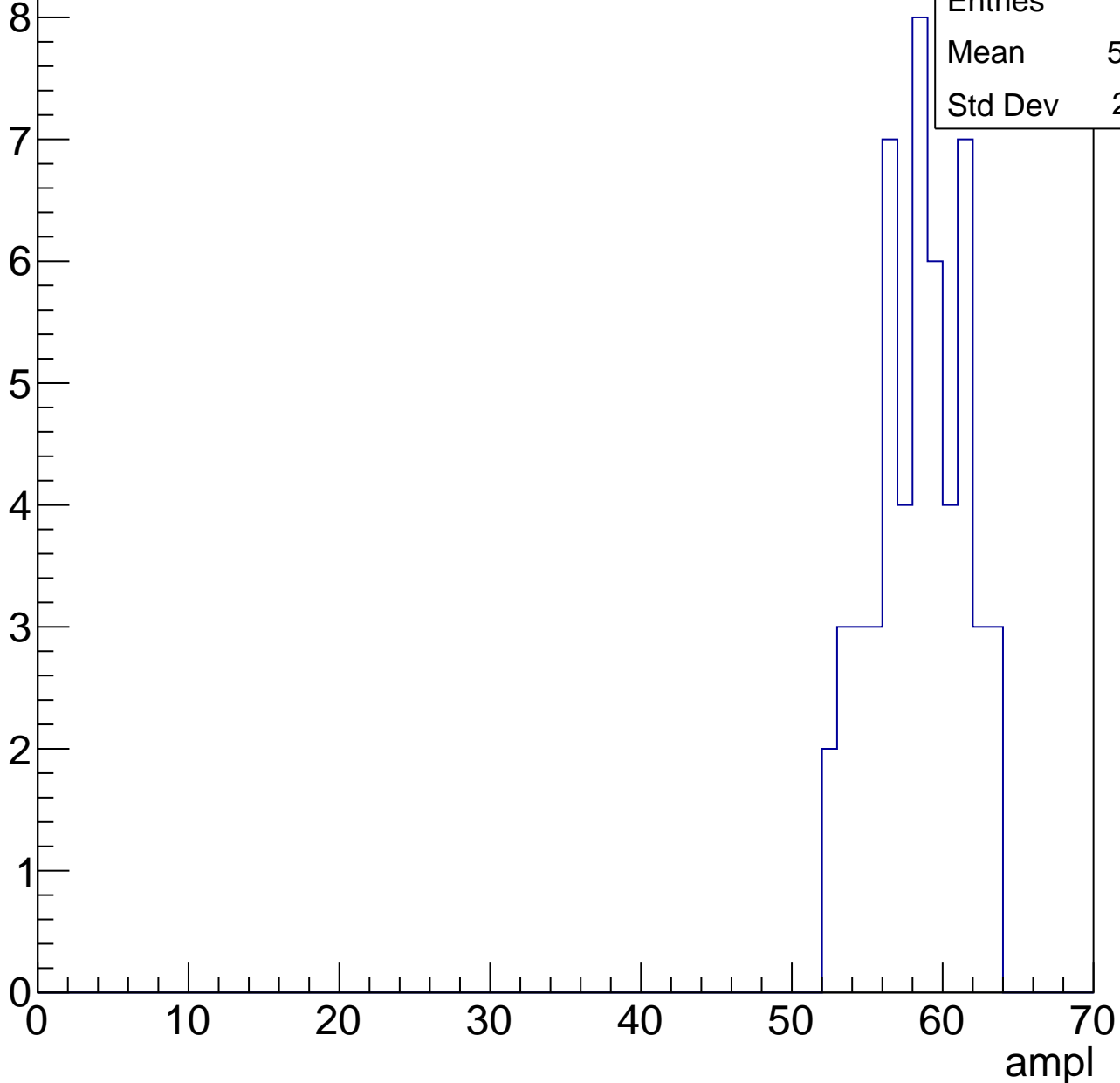


B1L102S, U22-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.92
Std Dev	2.951

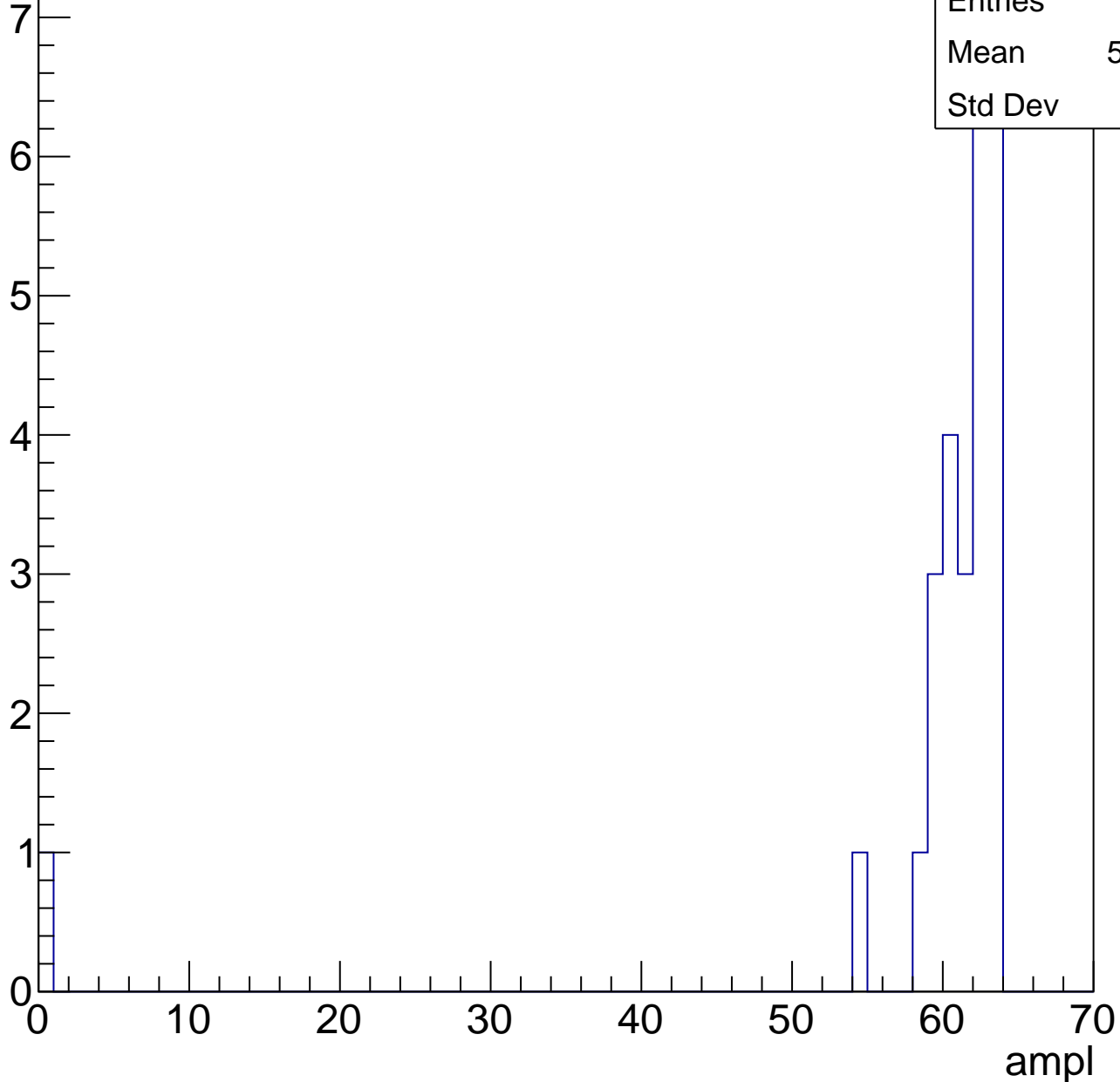


B1L102S, U22-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	27
Mean	58.78
Std Dev	11.7



B1L102S, U22-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch108, adc0

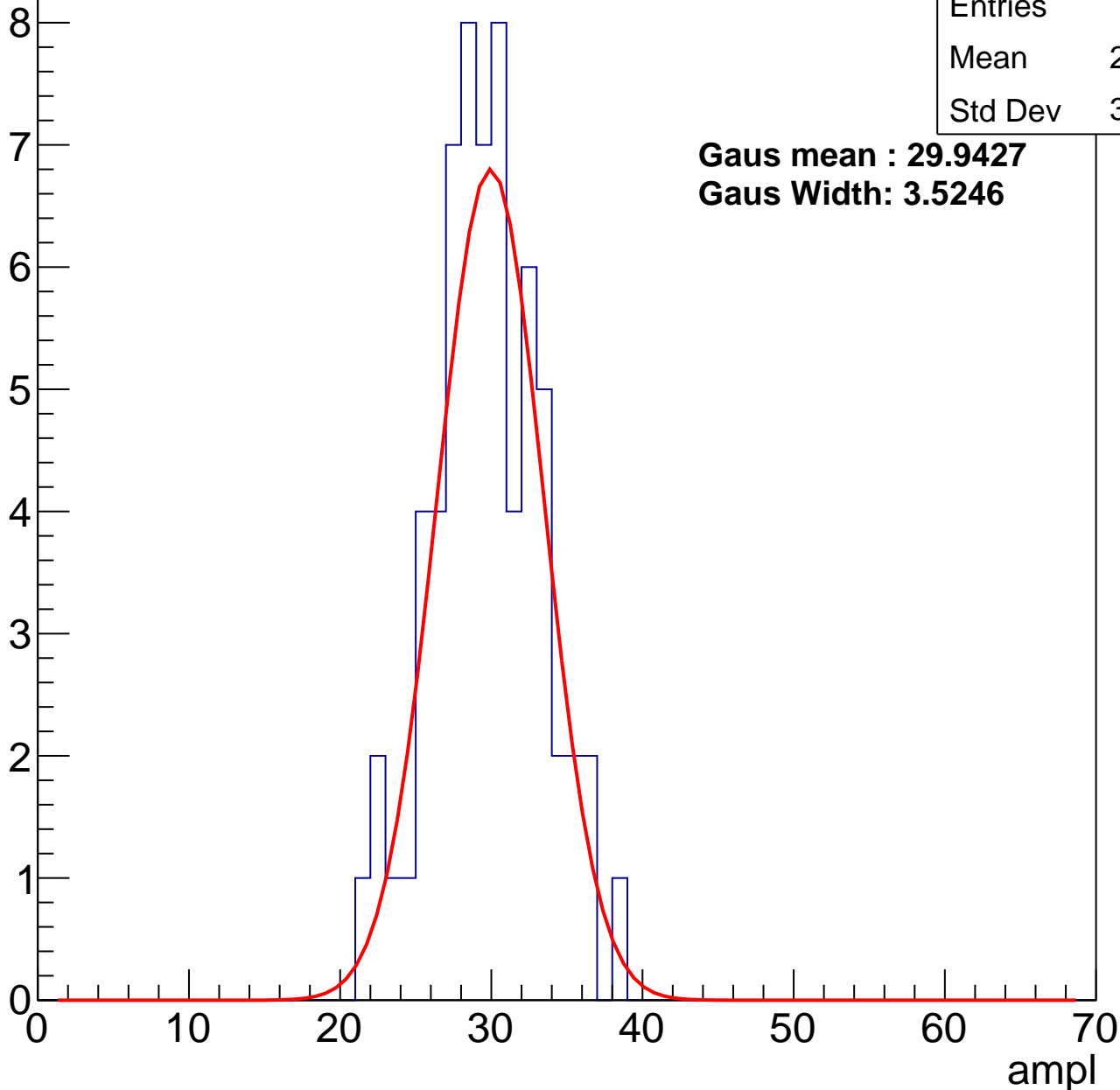
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.25
Std Dev	3.539

Gaus mean : 29.9427

Gaus Width: 3.5246



B1L102S, U22-ch108, adc1

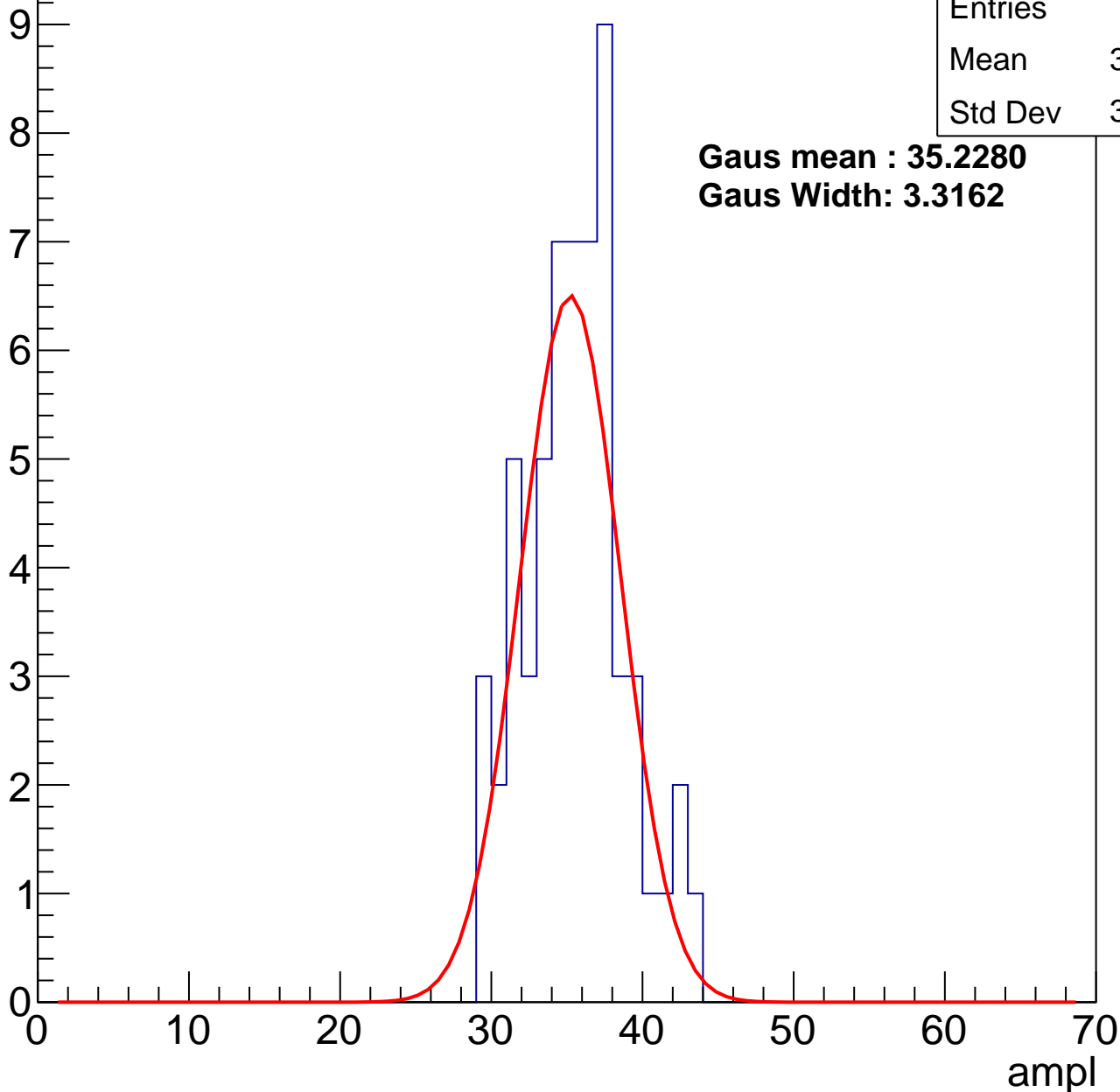
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.08
Std Dev	3.274

Gaus mean : 35.2280

Gaus Width: 3.3162



B1L102S, U22-ch108, adc2

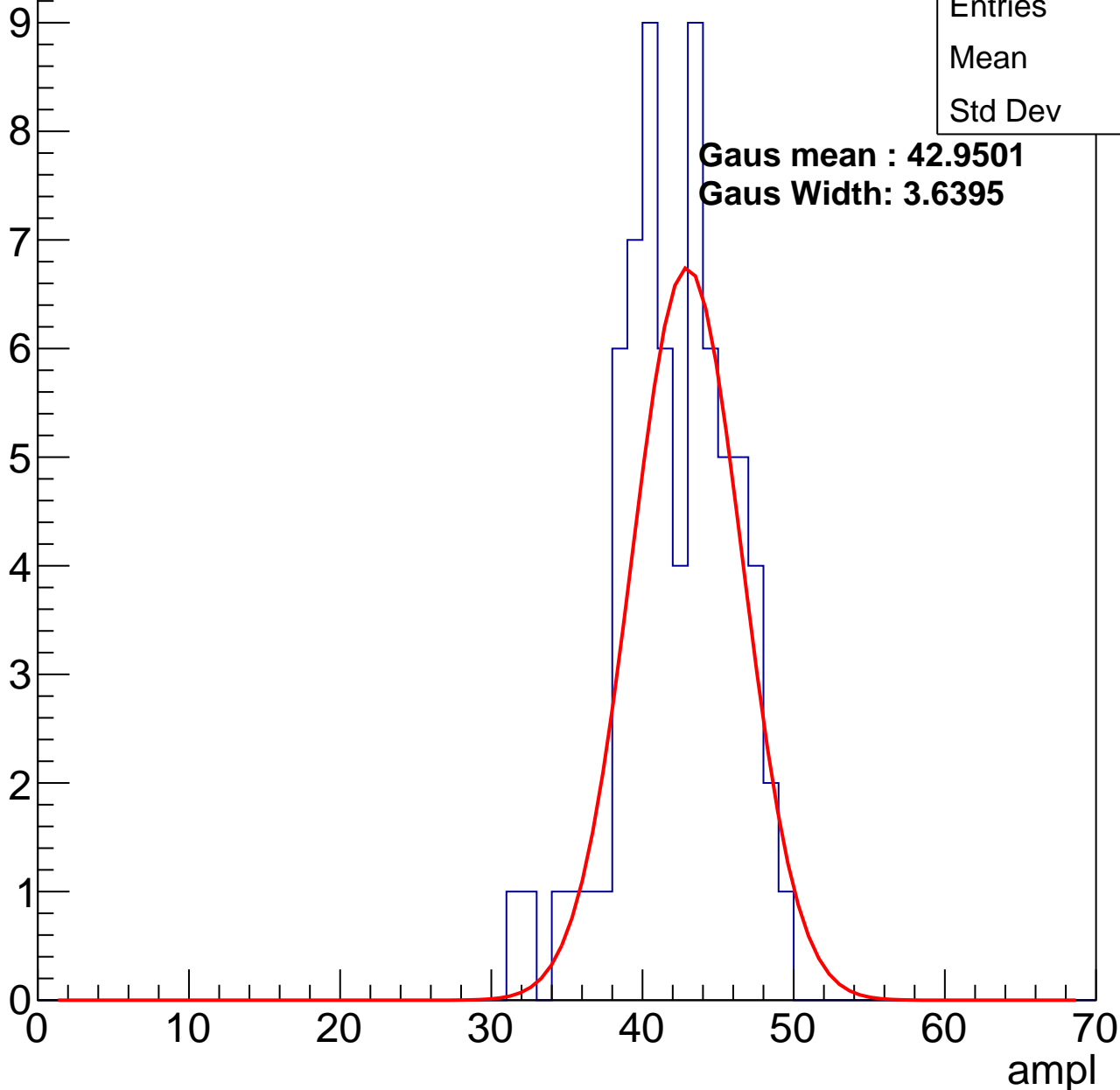
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.7
Std Dev	3.72

Gaus mean : 42.9501

Gaus Width: 3.6395

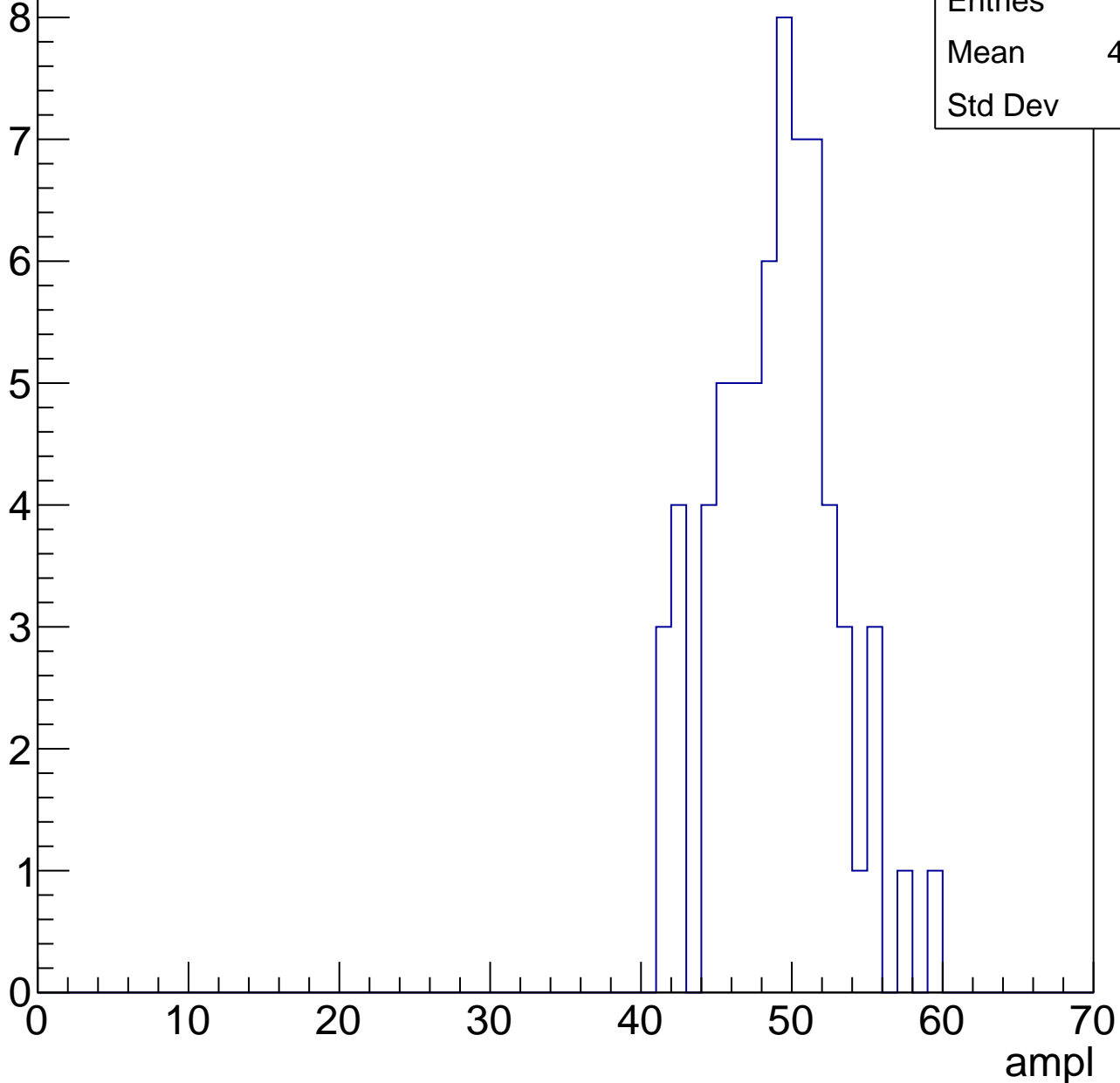


B1L102S, U22-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.45
Std Dev	3.93

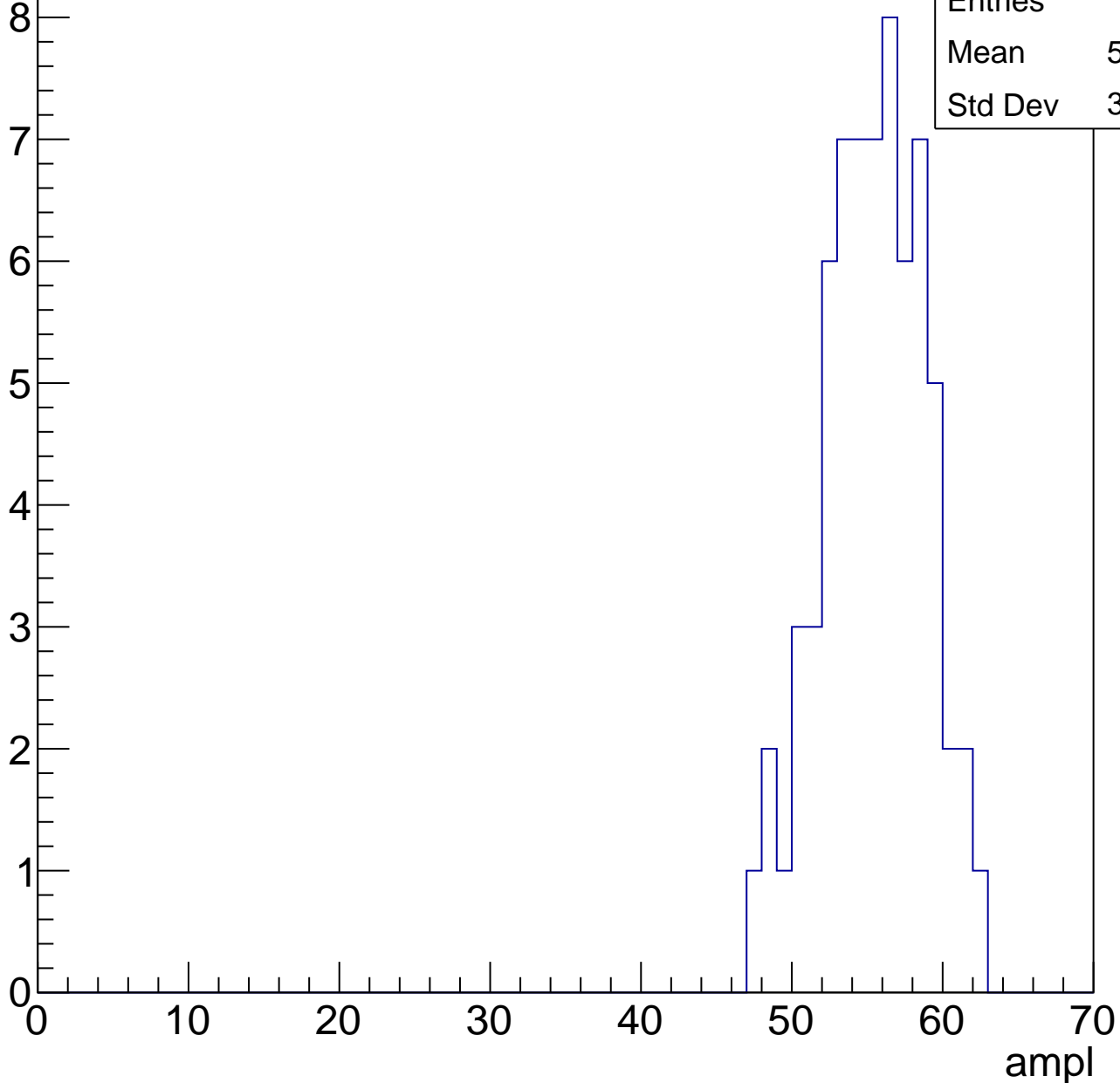


B1L102S, U22-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.94
Std Dev	3.334

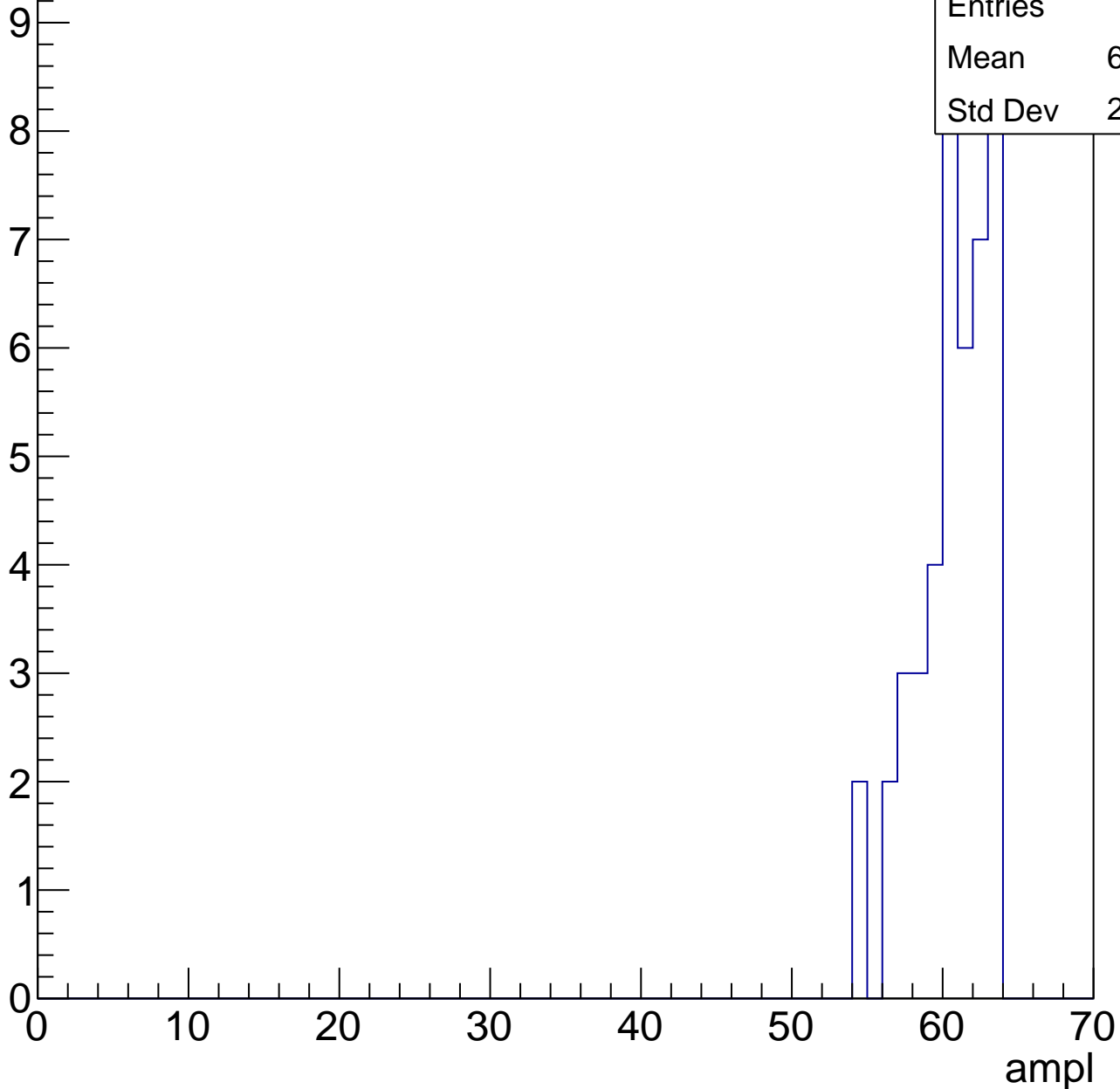


B1L102S, U22-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	60.18
Std Dev	2.433



B1L102S, U22-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.5
Std Dev	23.04

ampl

0 10 20 30 40 50 60 70

B1L102S, U22-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch109, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	27.11
Std Dev	5.761

Gaus mean : 28.5836

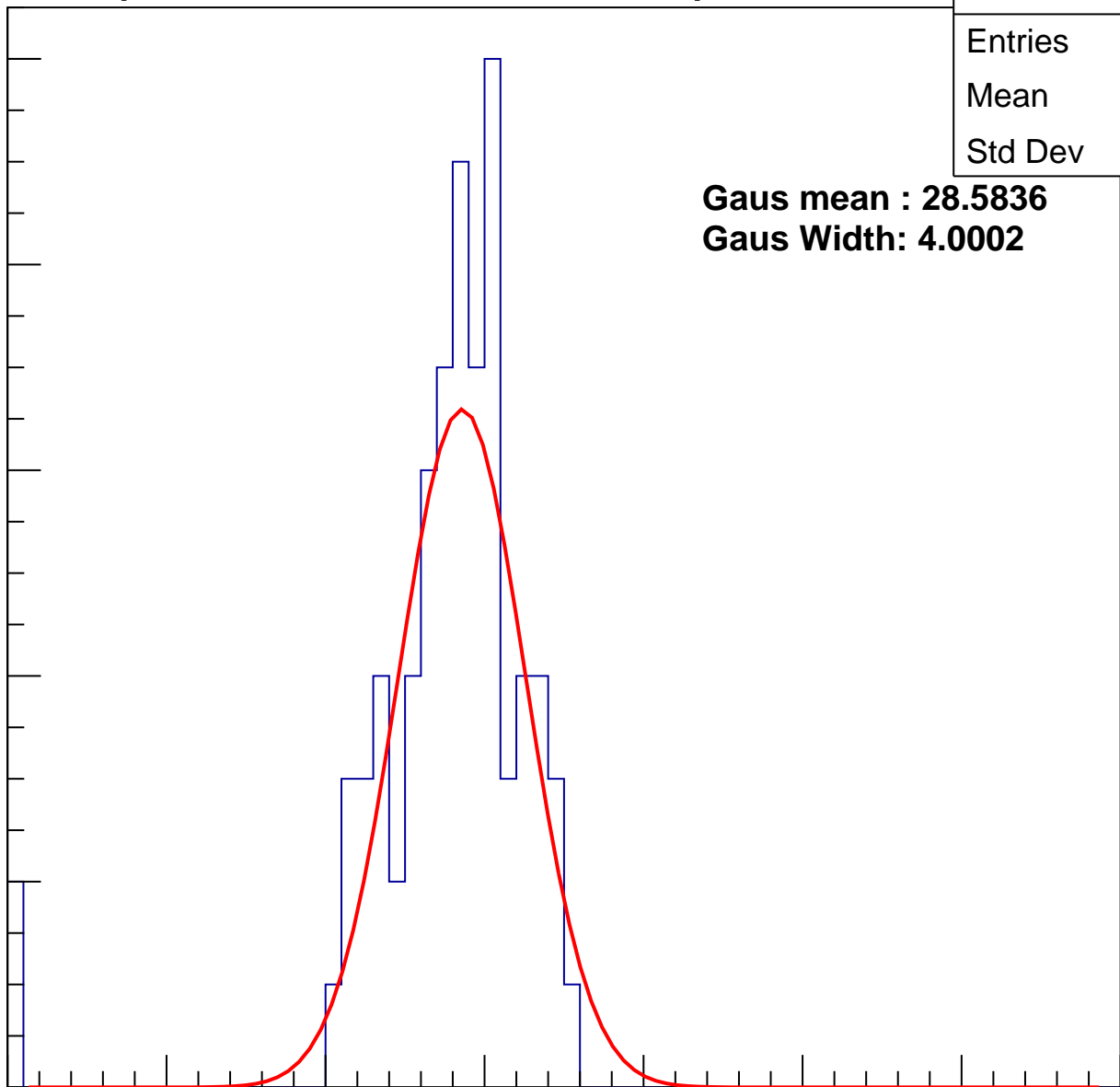
Gaus Width: 4.0002

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch109, adc1

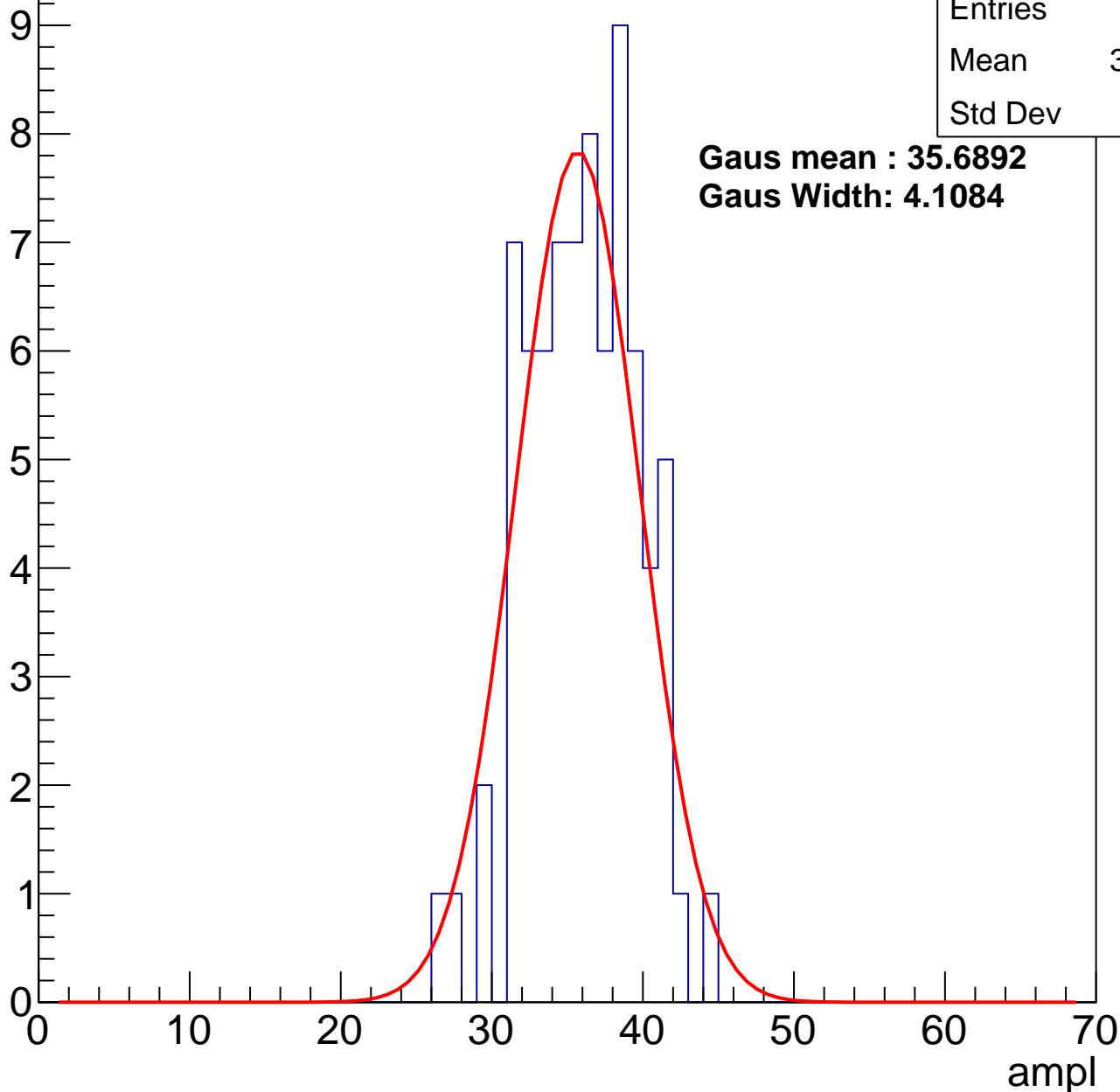
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.56
Std Dev	3.62

Gaus mean : 35.6892

Gaus Width: 4.1084



B1L102S, U22-ch109, adc2

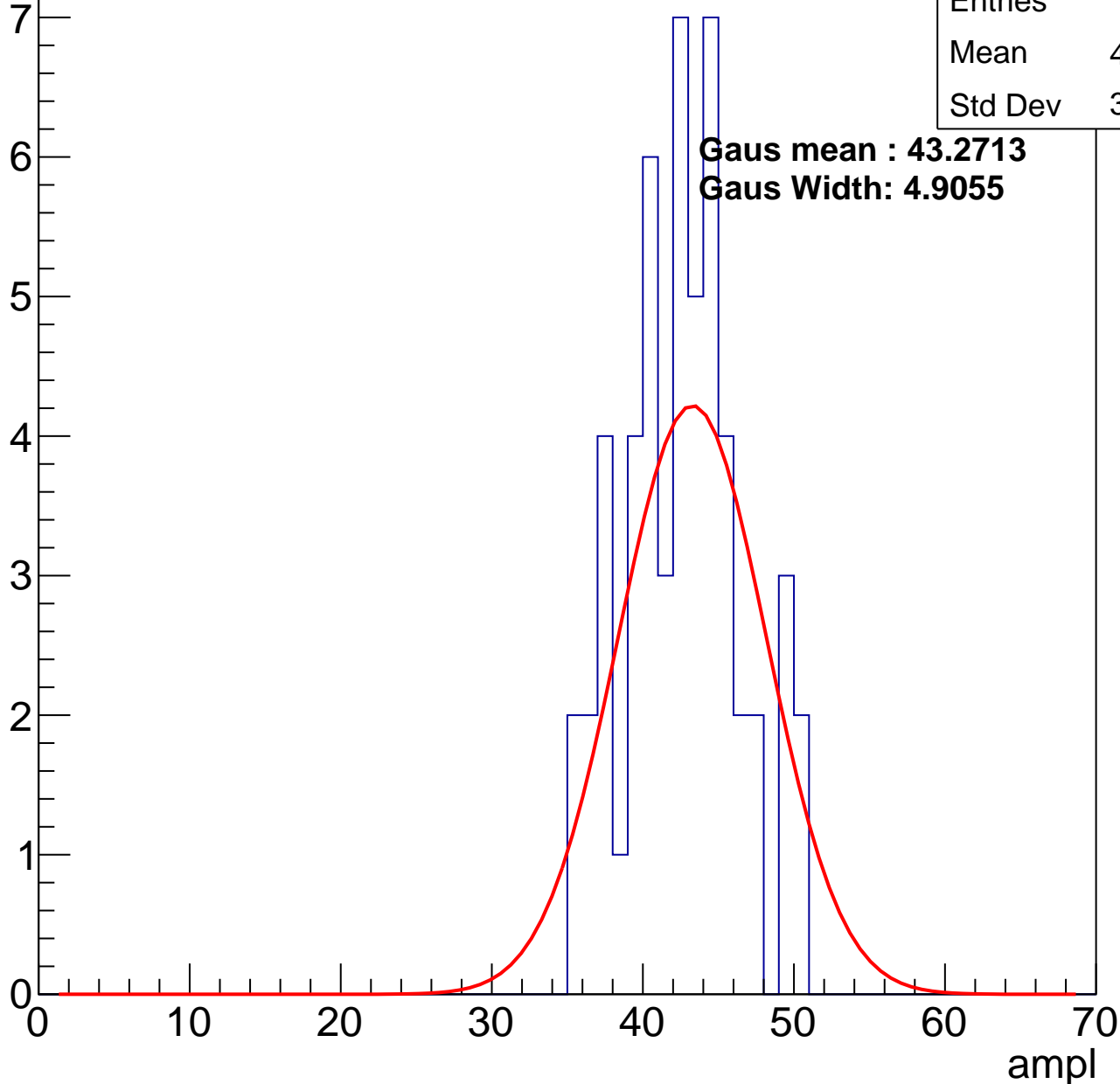
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	42.17
Std Dev	3.785

Gaus mean : 43.2713

Gaus Width: 4.9055



B1L102S, U22-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	48.89
Std Dev	4.135

Entry

10

8

6

4

2

0

0

10

20

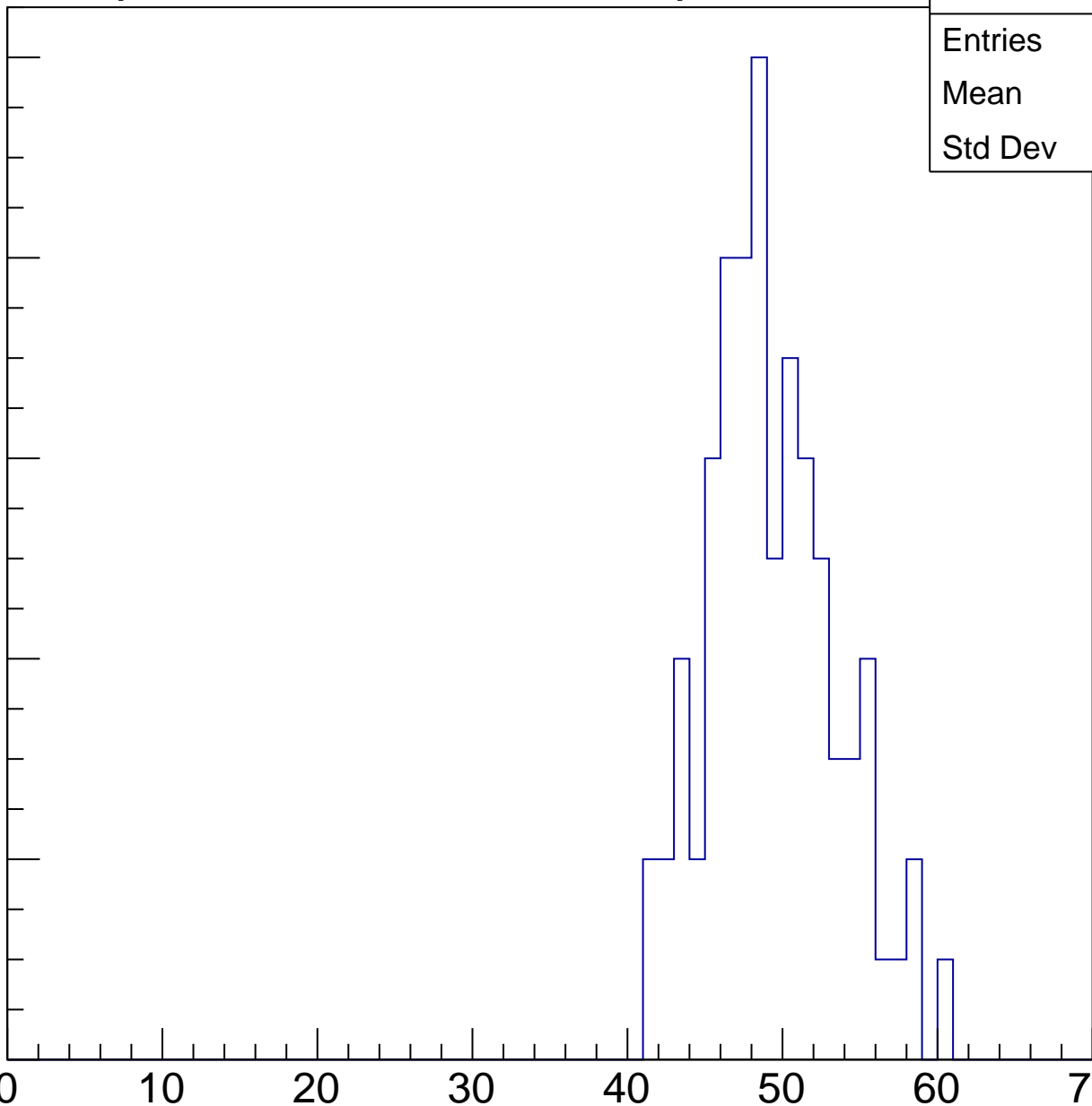
30

40

50

60

ampl

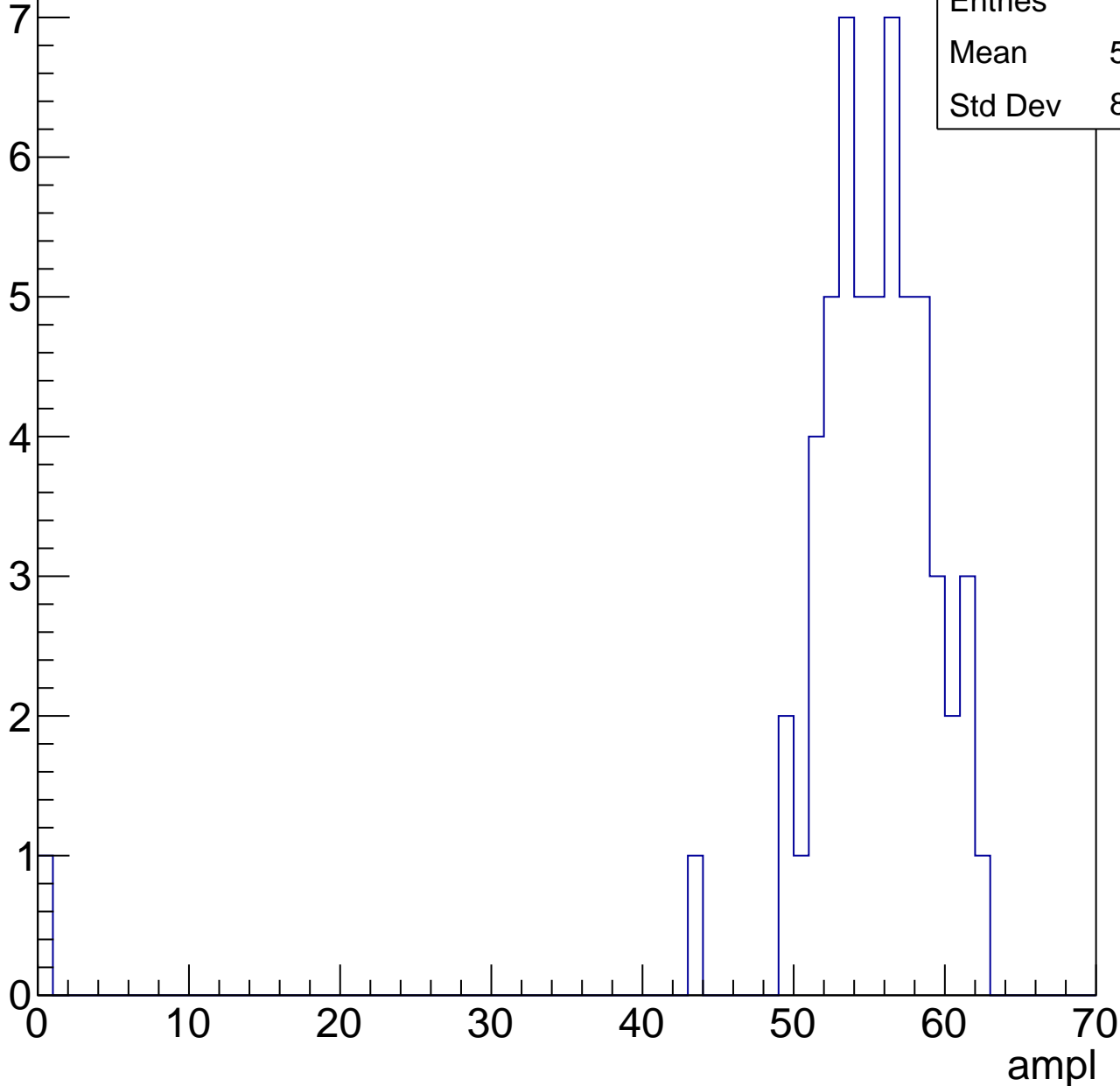


B1L102S, U22-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.04
Std Dev	8.037

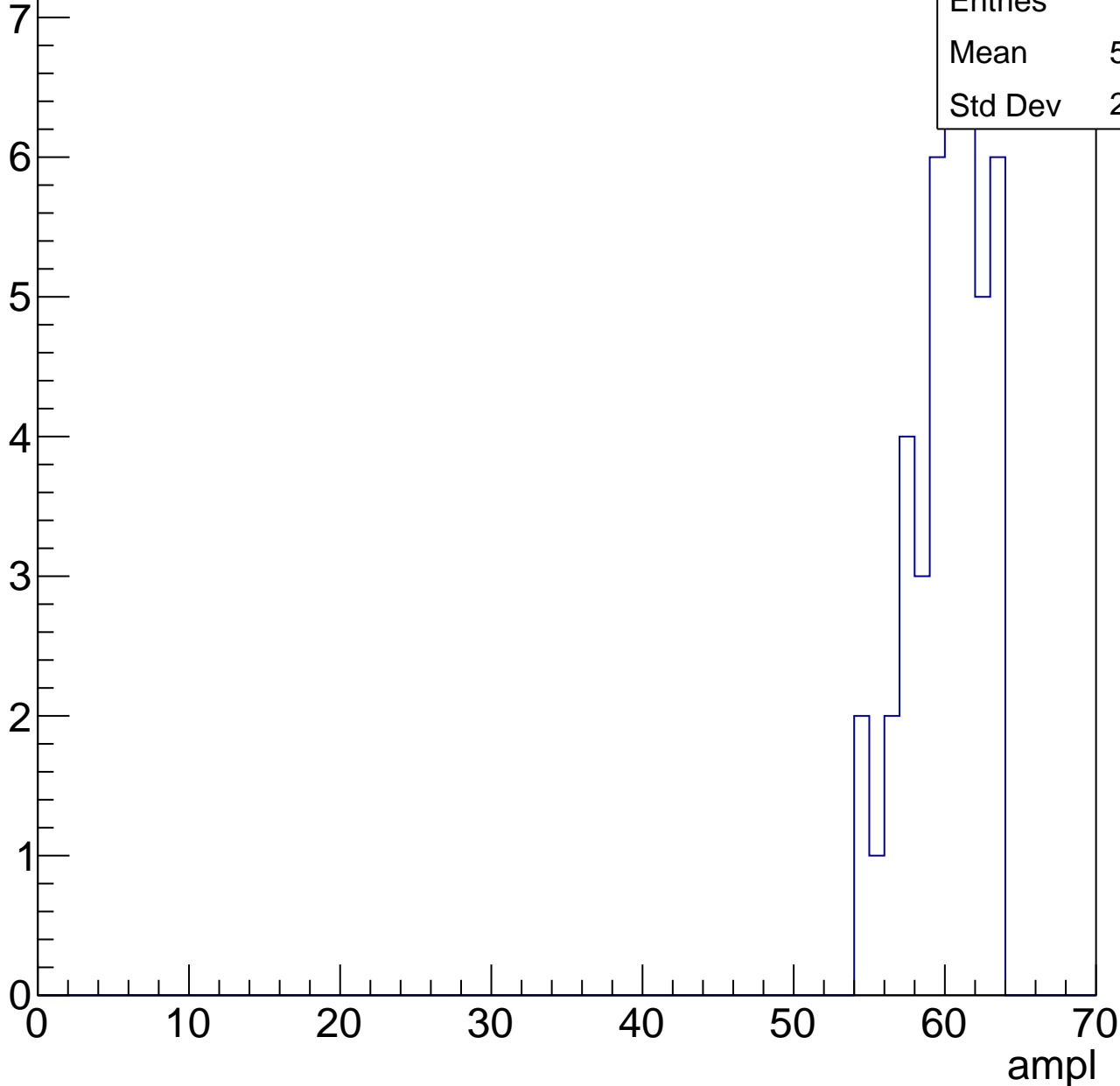


B1L102S, U22-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

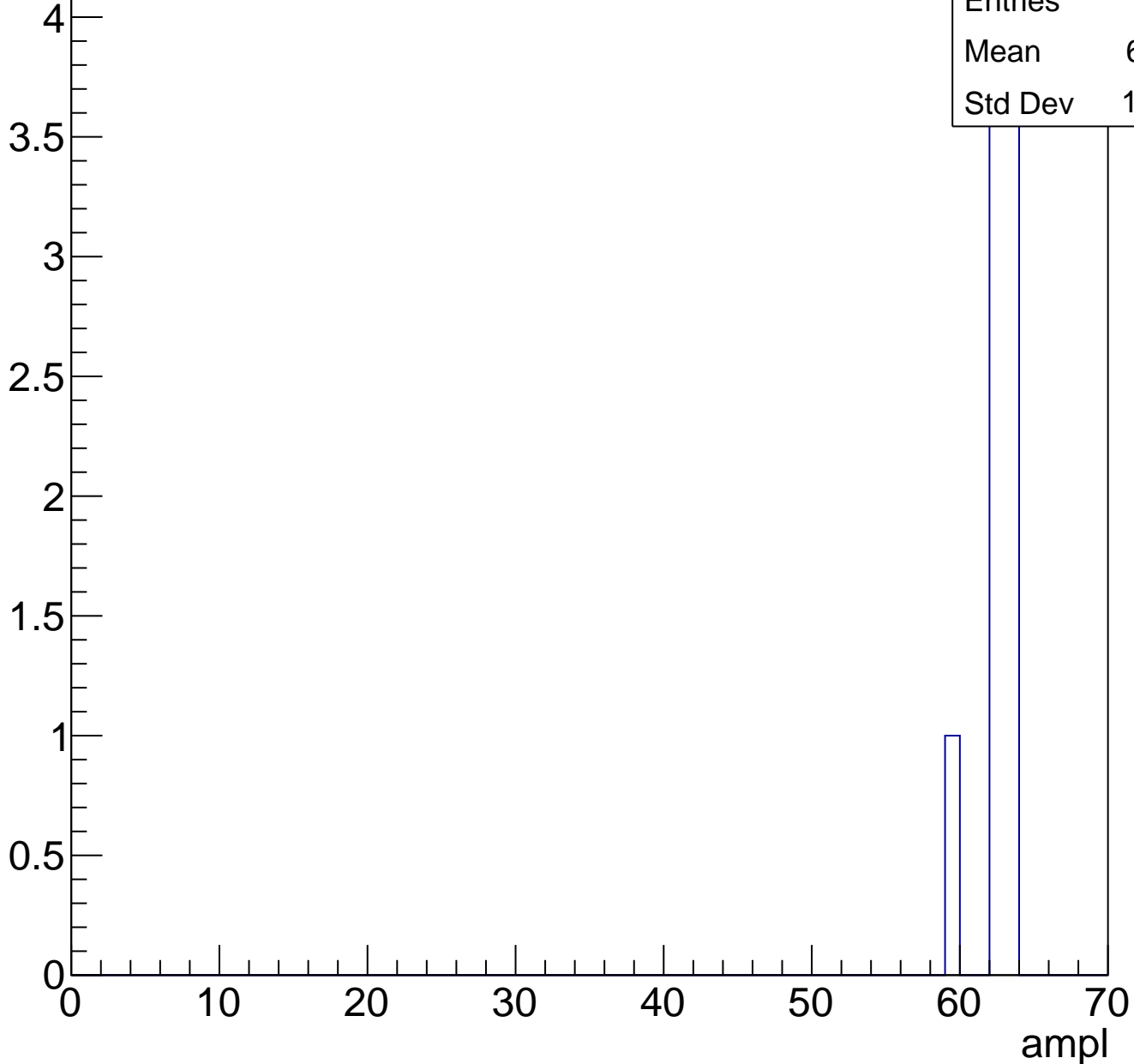
Entries	43
Mean	59.67
Std Dev	2.456



B1L102S, U22-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch110, adc0

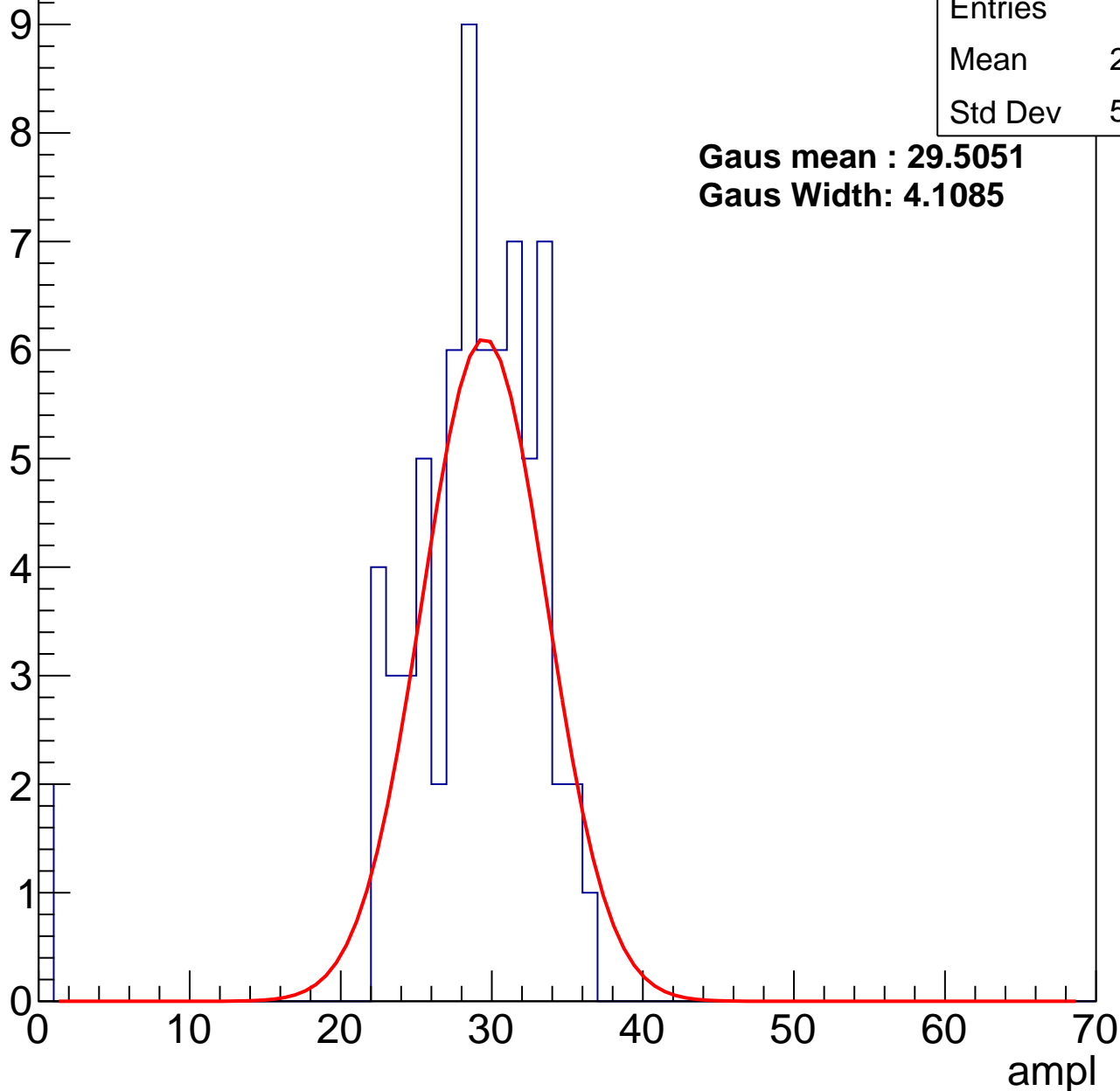
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.94
Std Dev	5.947

Gaus mean : 29.5051

Gaus Width: 4.1085



B1L102S, U22-ch110, adc1

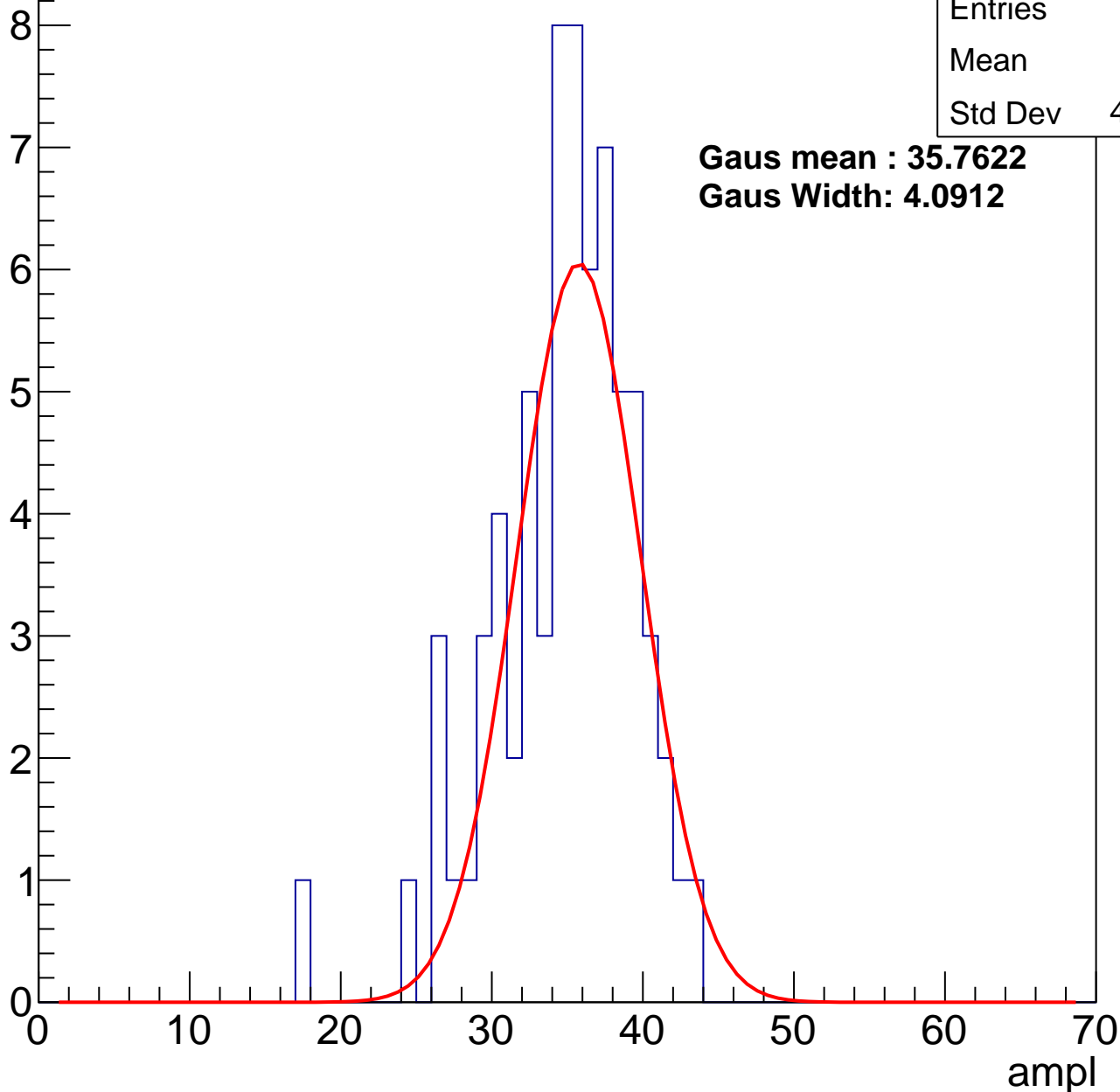
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.3
Std Dev	4.615

Gaus mean : 35.7622

Gaus Width: 4.0912



B1L102S, U22-ch110, adc2

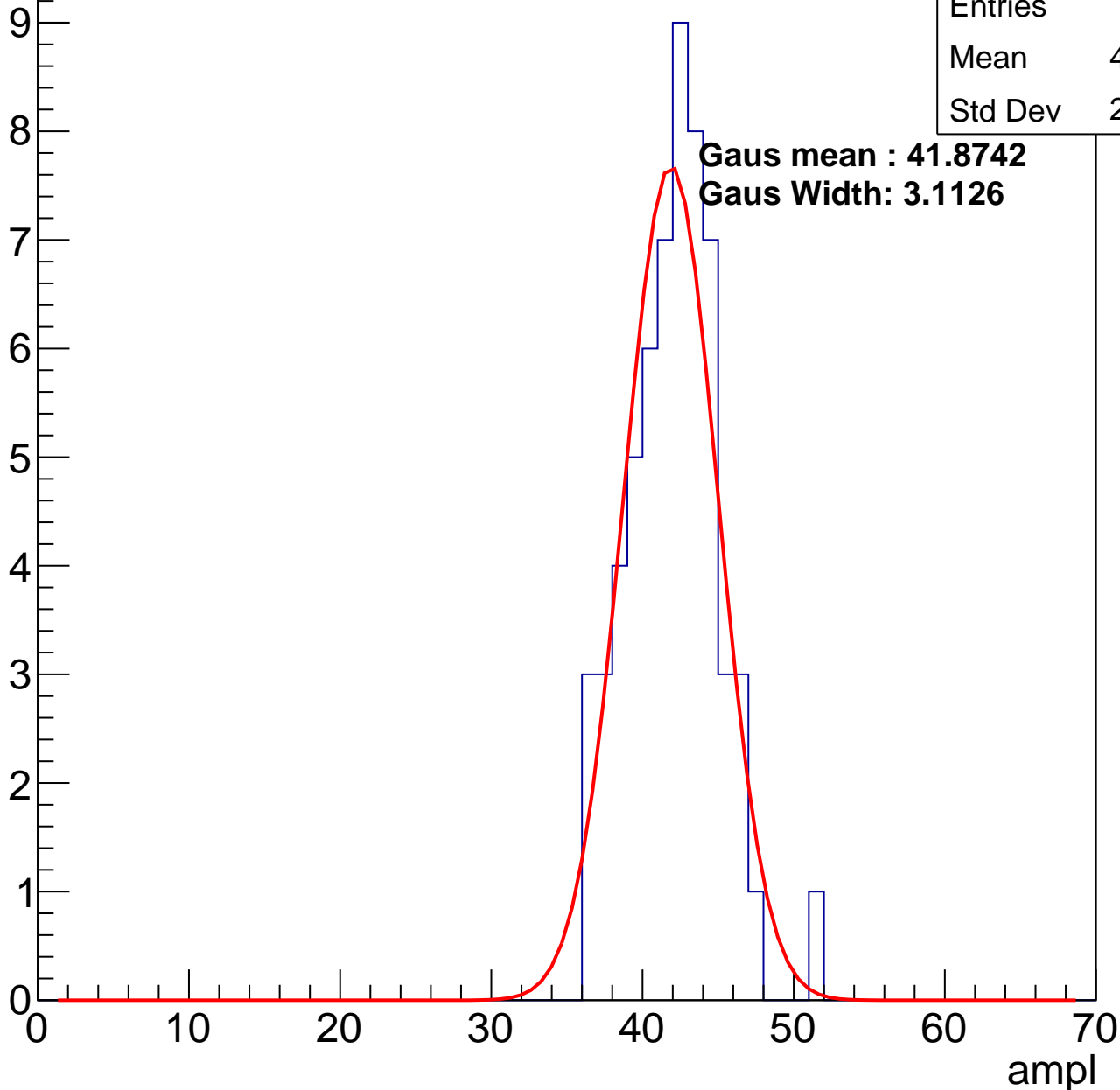
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.57
Std Dev	2.969

Gaus mean : 41.8742

Gaus Width: 3.1126

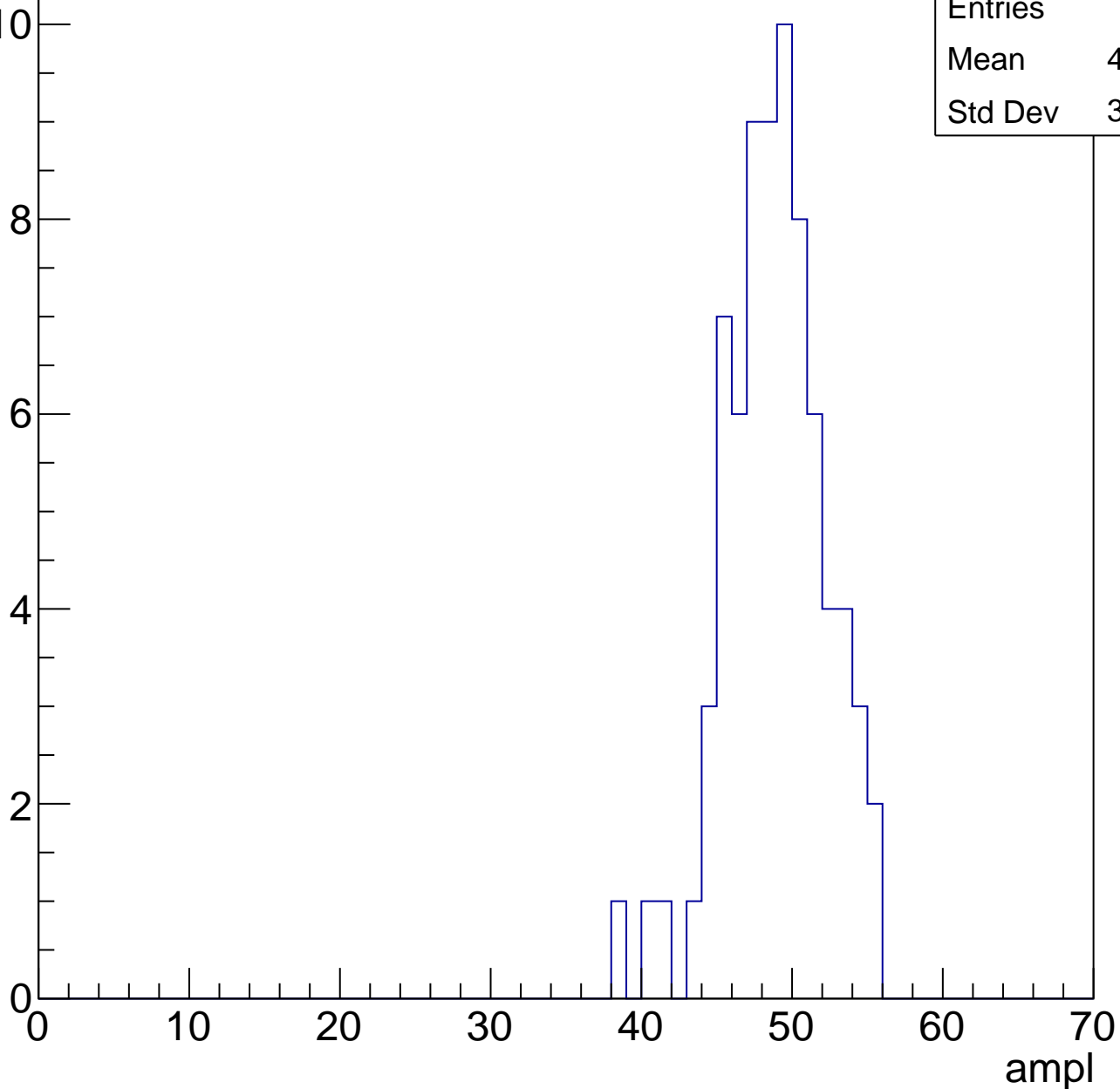


B1L102S, U22-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	48.37
Std Dev	3.342

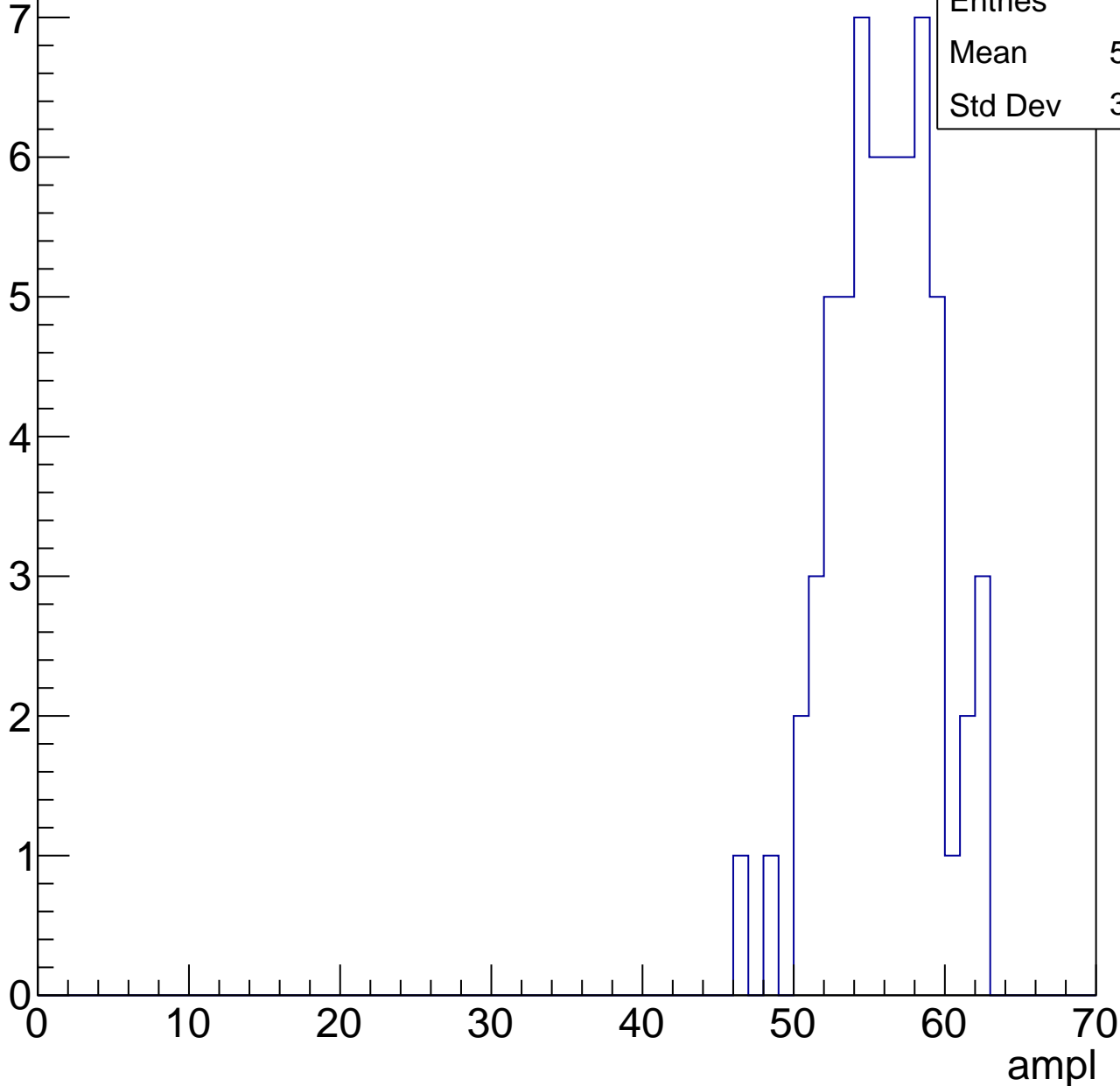


B1L102S, U22-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.45
Std Dev	3.427

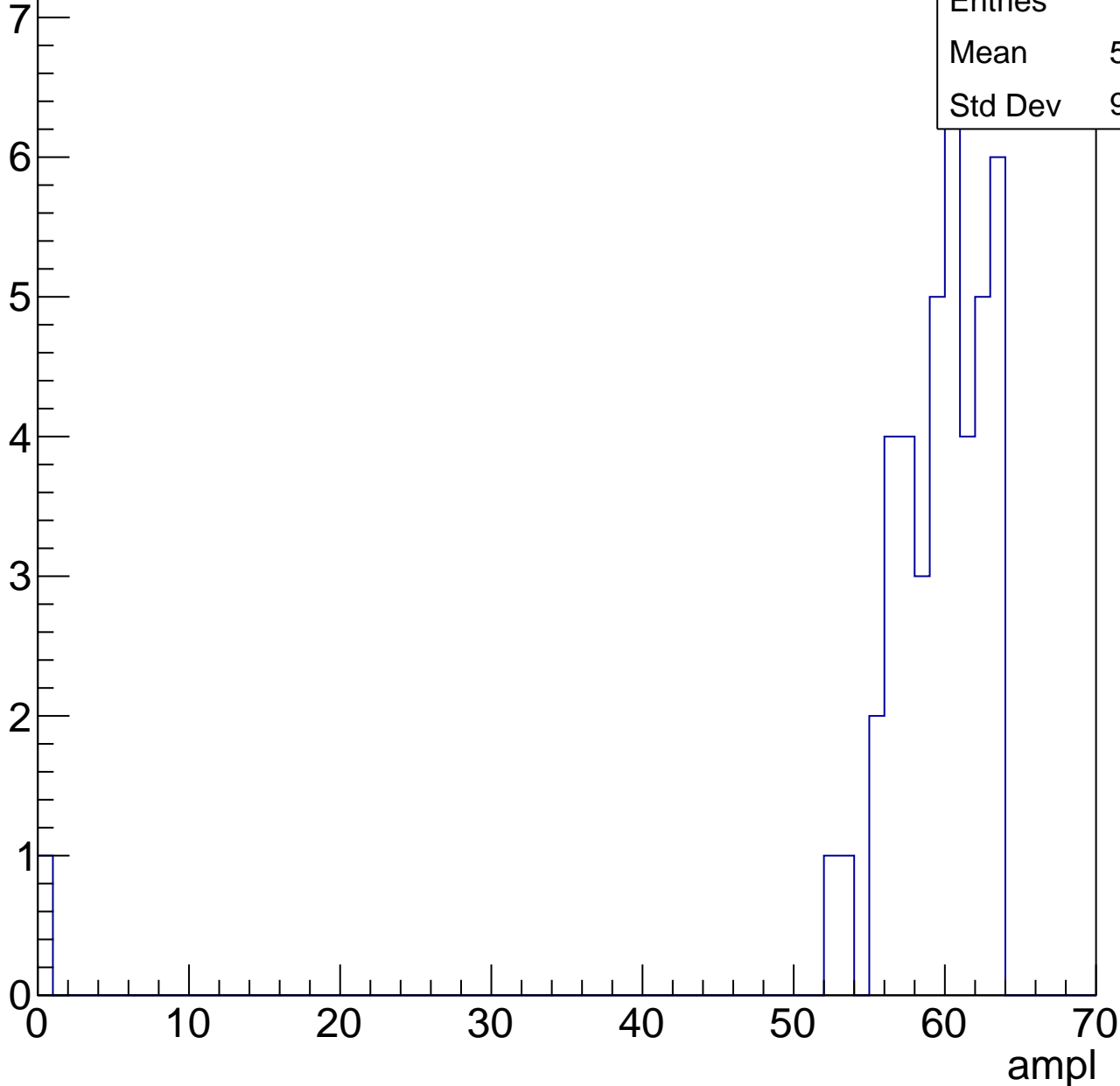


B1L102S, U22-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	57.86
Std Dev	9.352



B1L102S, U22-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	61.55
Std Dev	1.076

ampl

0 10 20 30 40 50 60 70

B1L102S, U22-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch111, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	28.9
Std Dev	5.725

Gaus mean : 30.0169

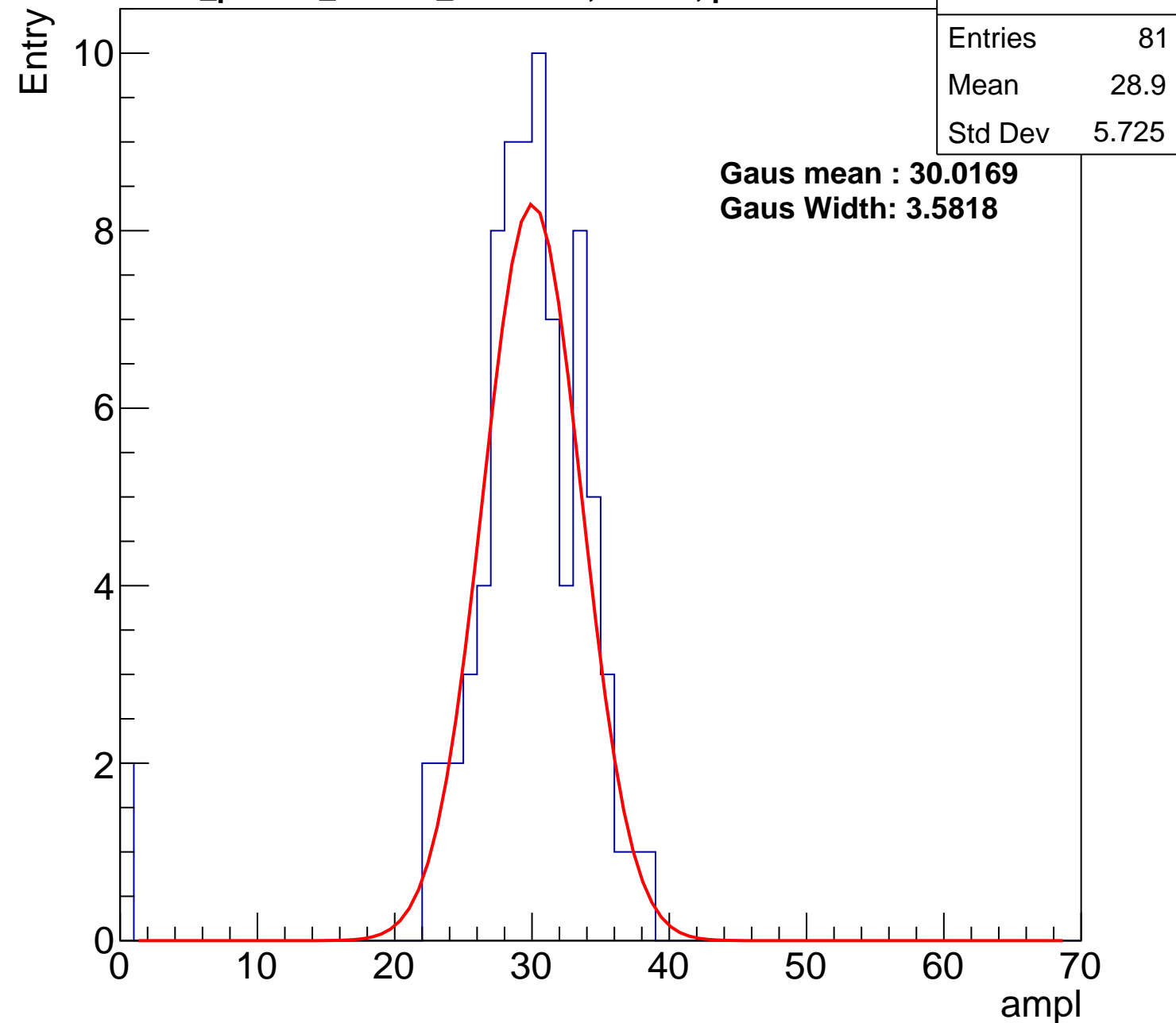
Gaus Width: 3.5818

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch111, adc1

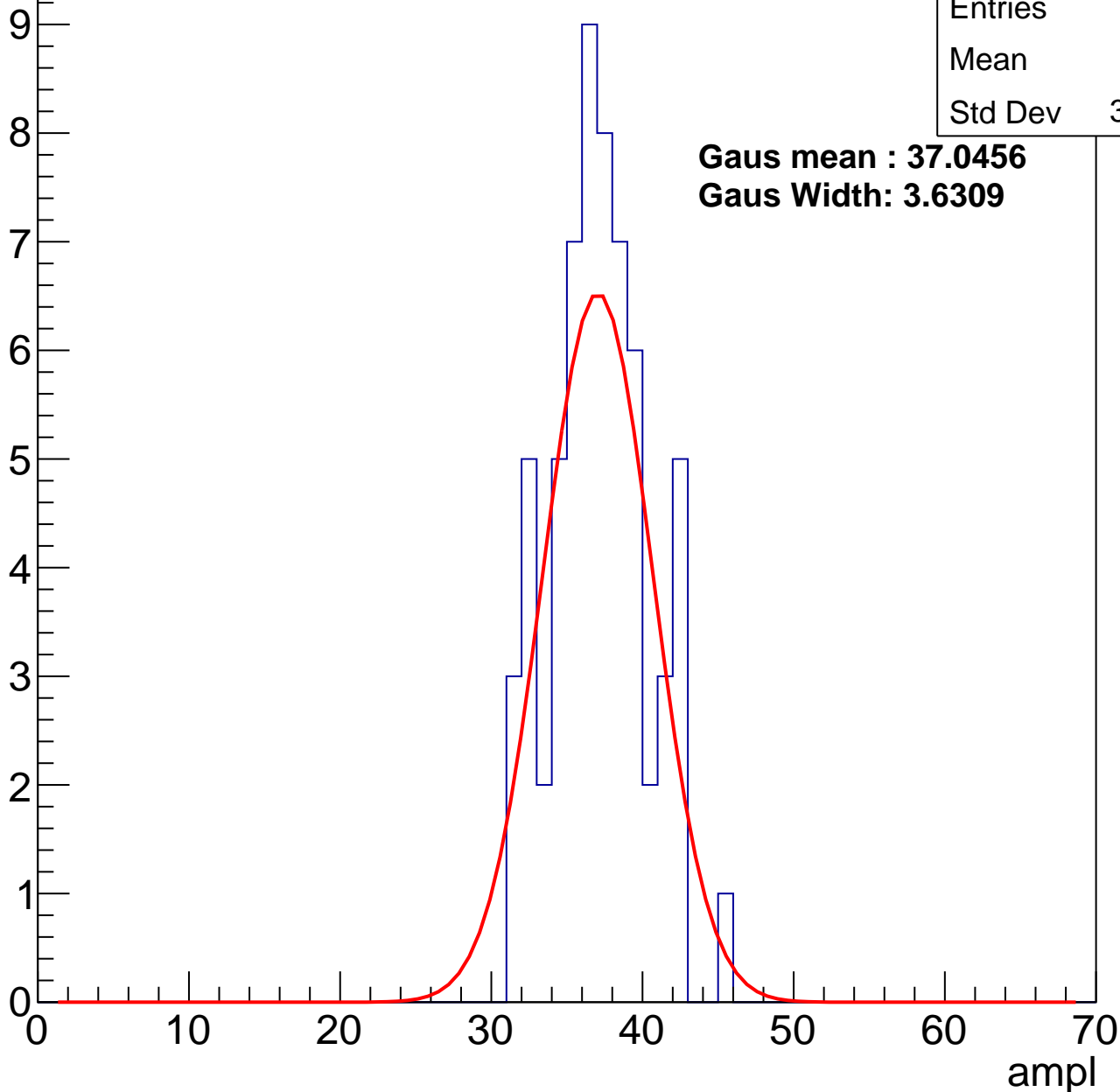
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.7
Std Dev	3.155

Gaus mean : 37.0456

Gaus Width: 3.6309



B1L102S, U22-ch111, adc2

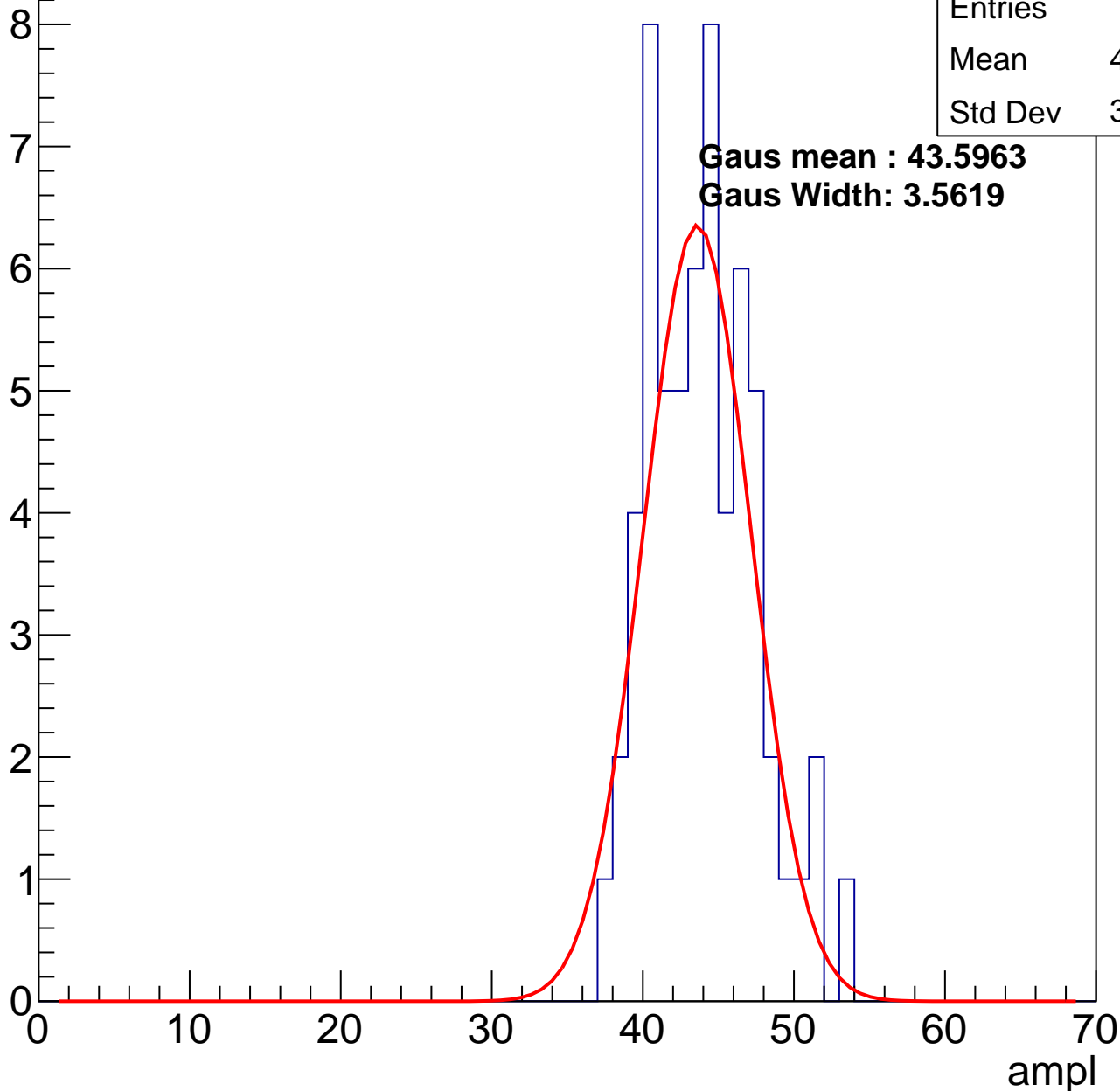
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.52
Std Dev	3.509

Gaus mean : 43.5963

Gaus Width: 3.5619

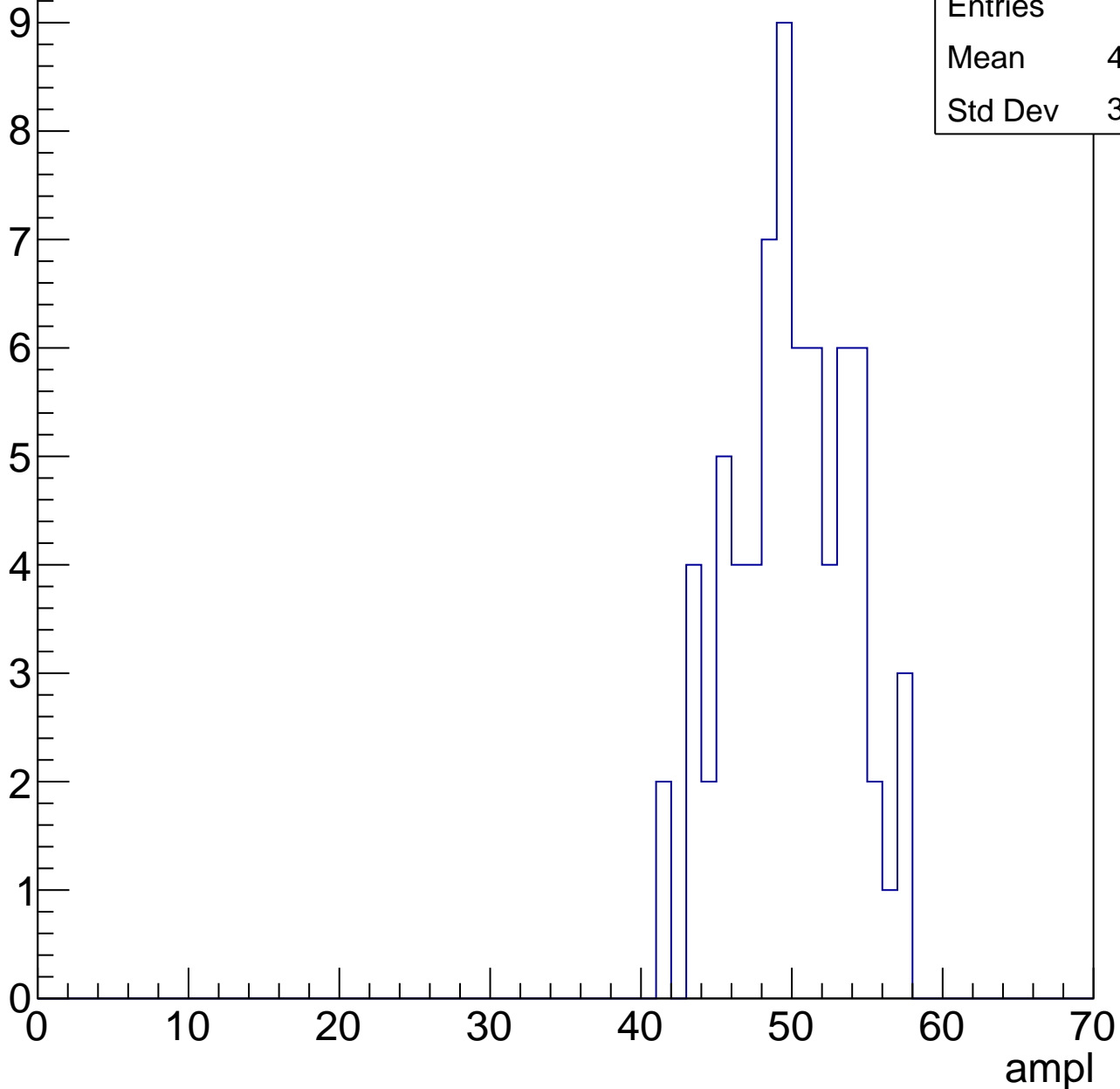


B1L102S, U22-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	49.42
Std Dev	3.888

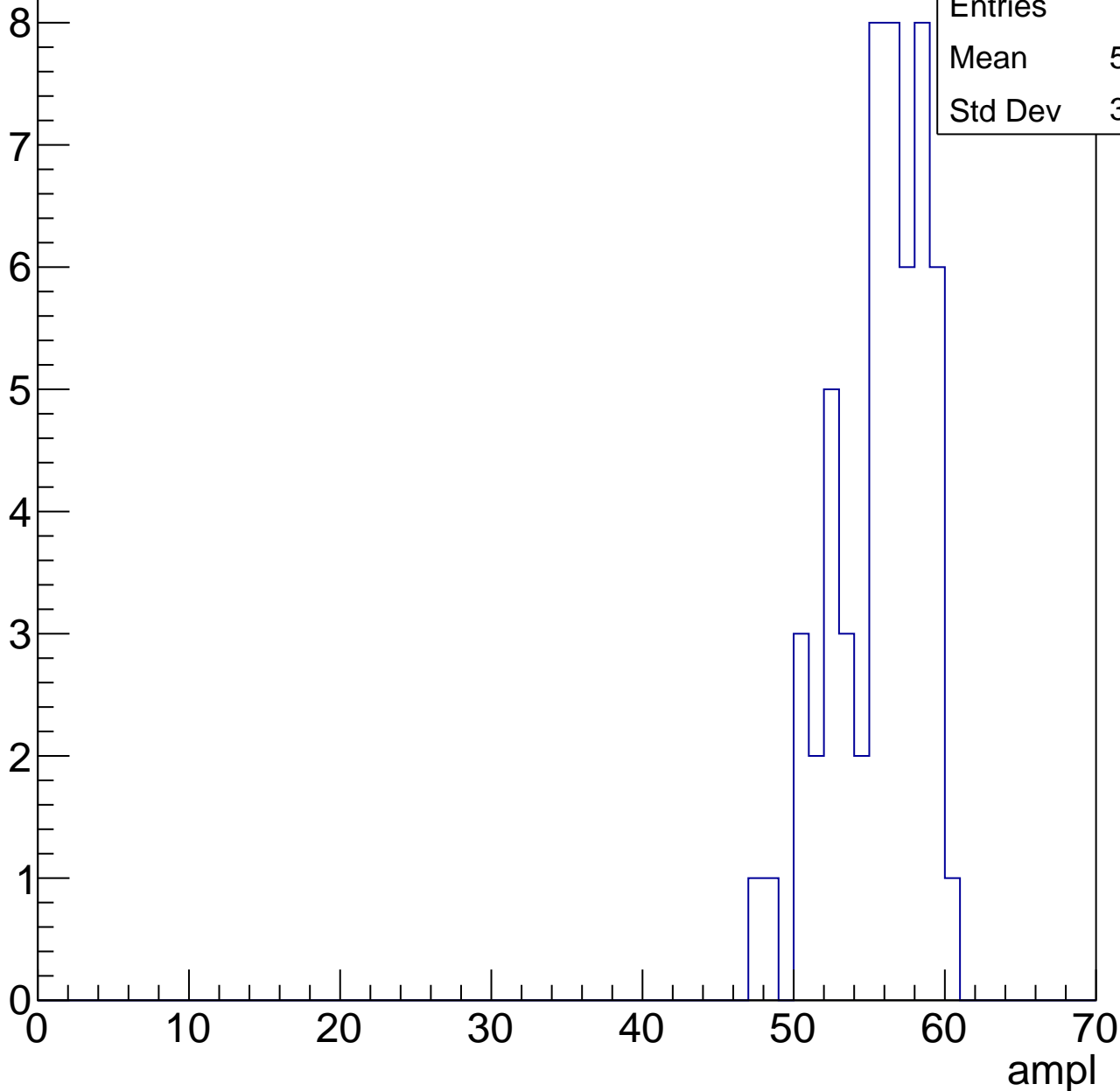


B1L102S, U22-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.22
Std Dev	3.047

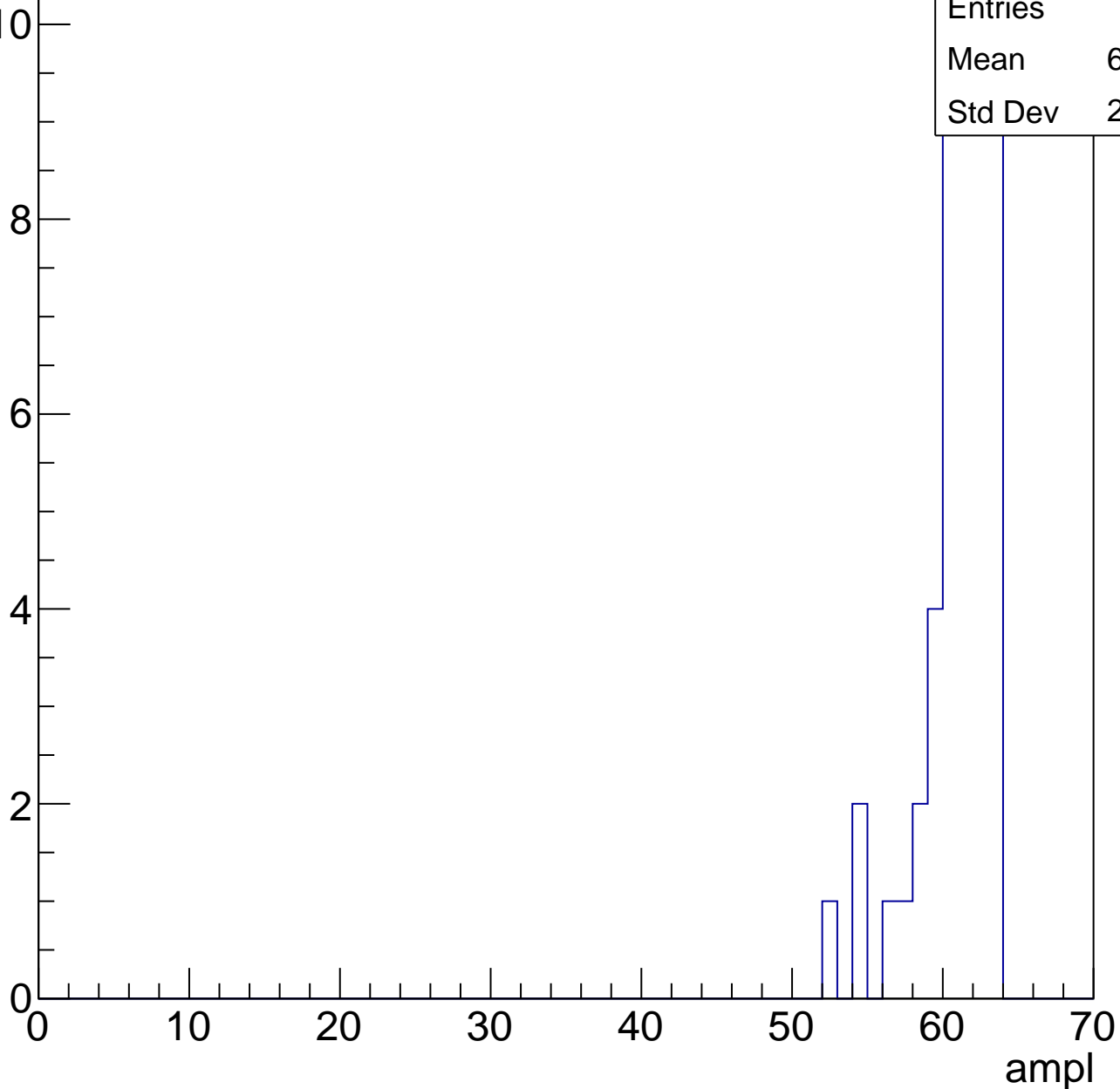


B1L102S, U22-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

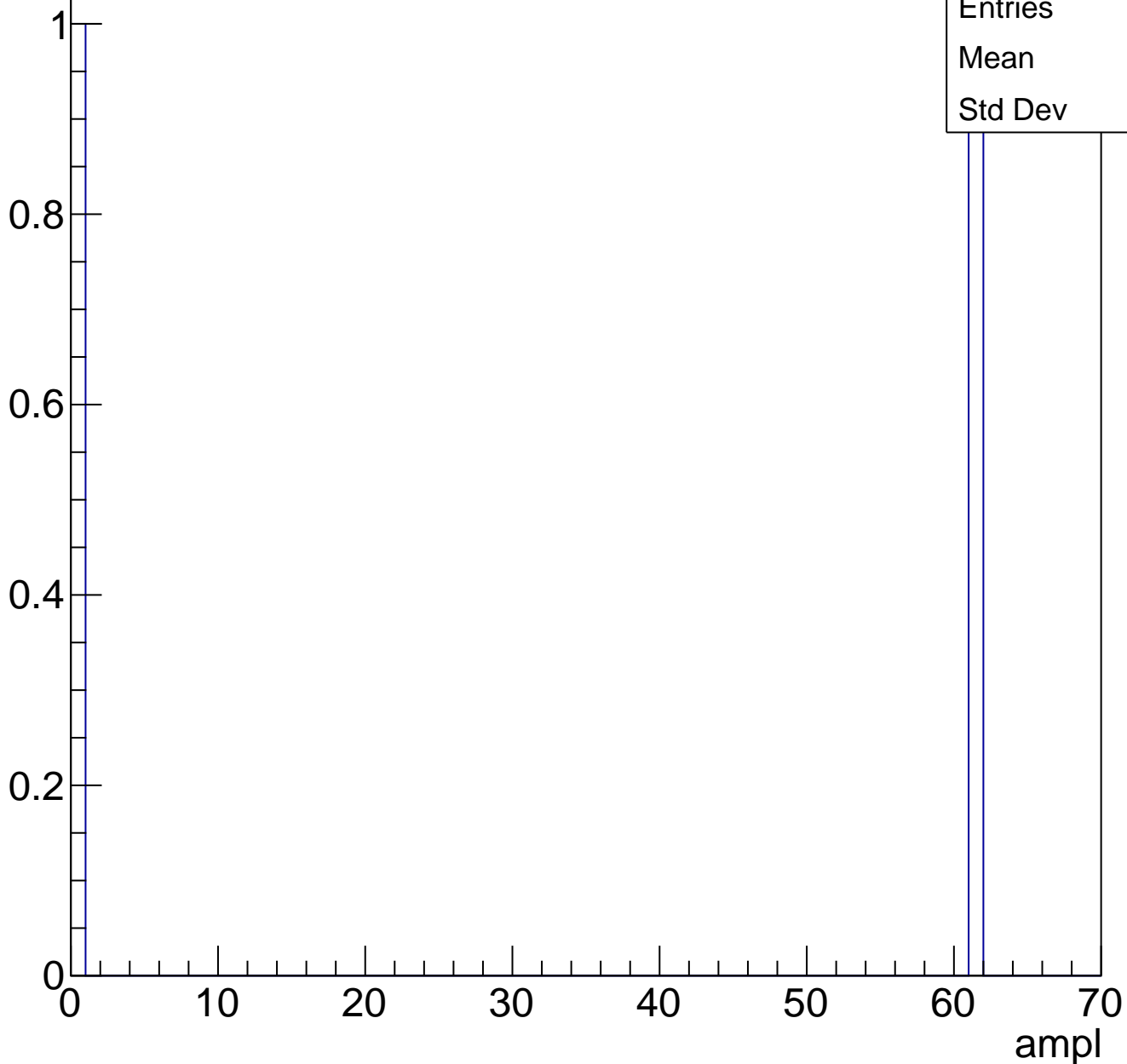
Entries	49
Mean	60.49
Std Dev	2.475



B1L102S, U22-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U22-ch112, adc0

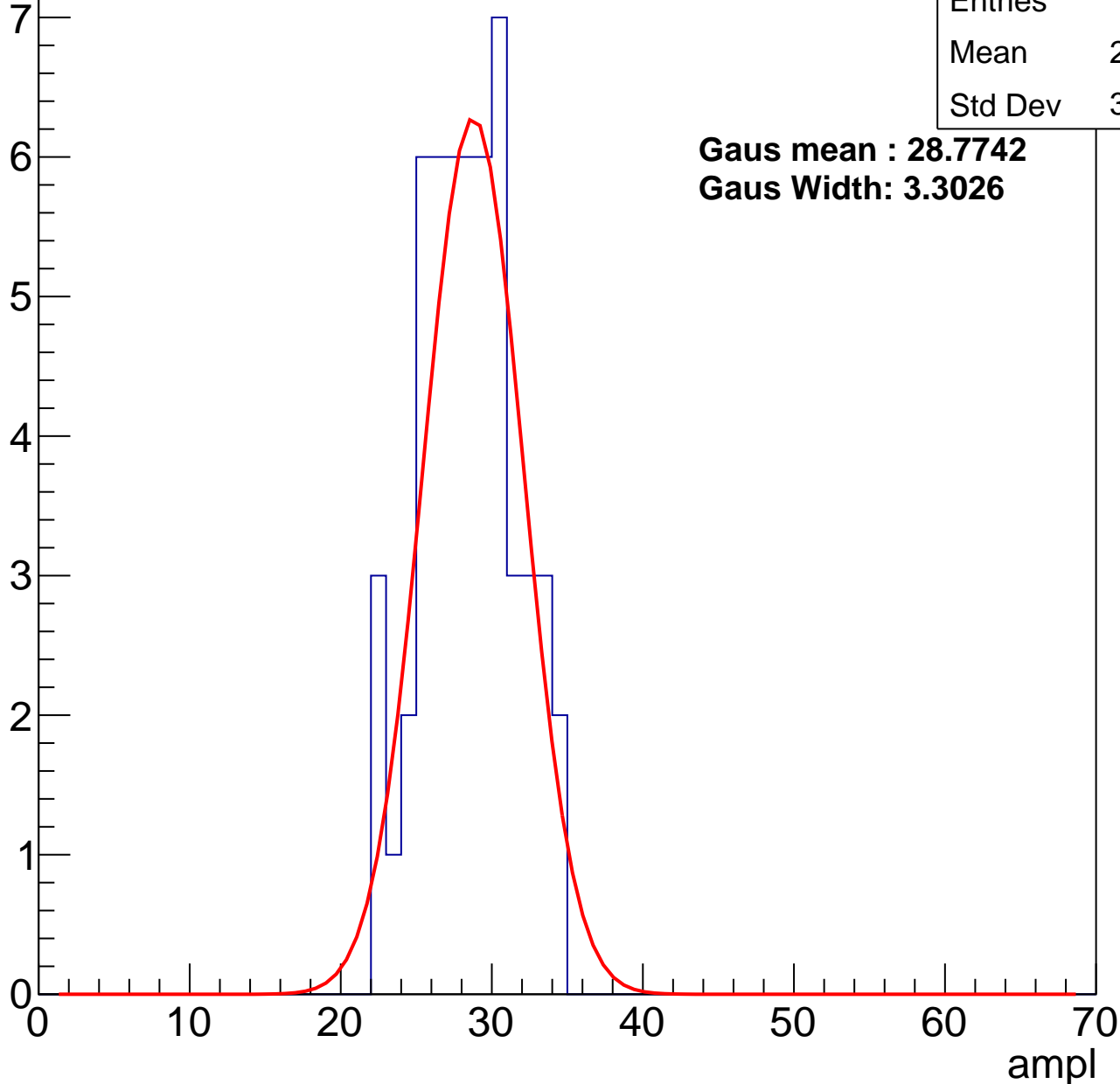
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	28.02
Std Dev	3.058

Gaus mean : 28.7742

Gaus Width: 3.3026



B1L102S, U22-ch112, adc1

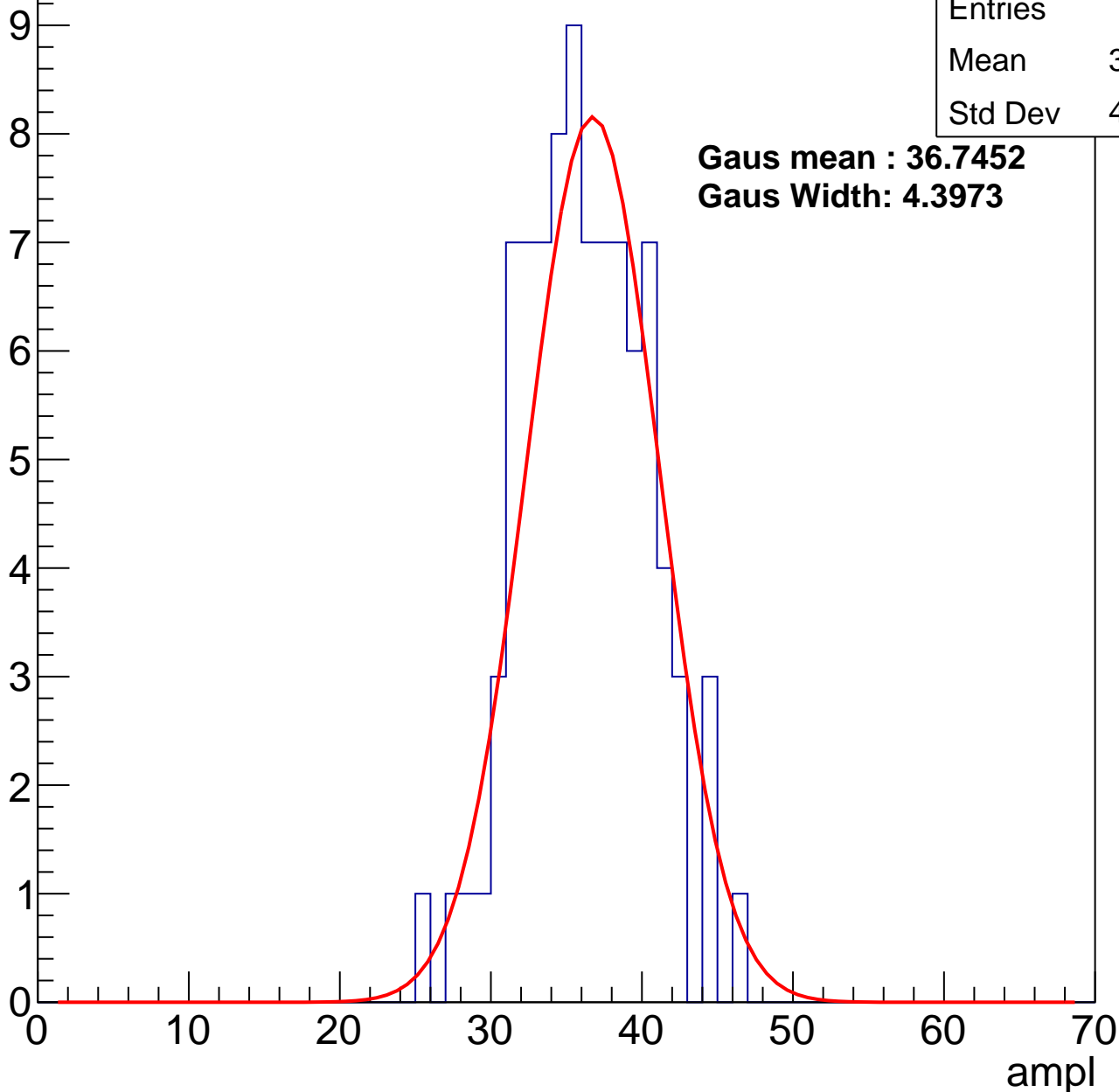
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	90
Mean	35.74
Std Dev	4.095

Gaus mean : 36.7452

Gaus Width: 4.3973



B1L102S, U22-ch112, adc2

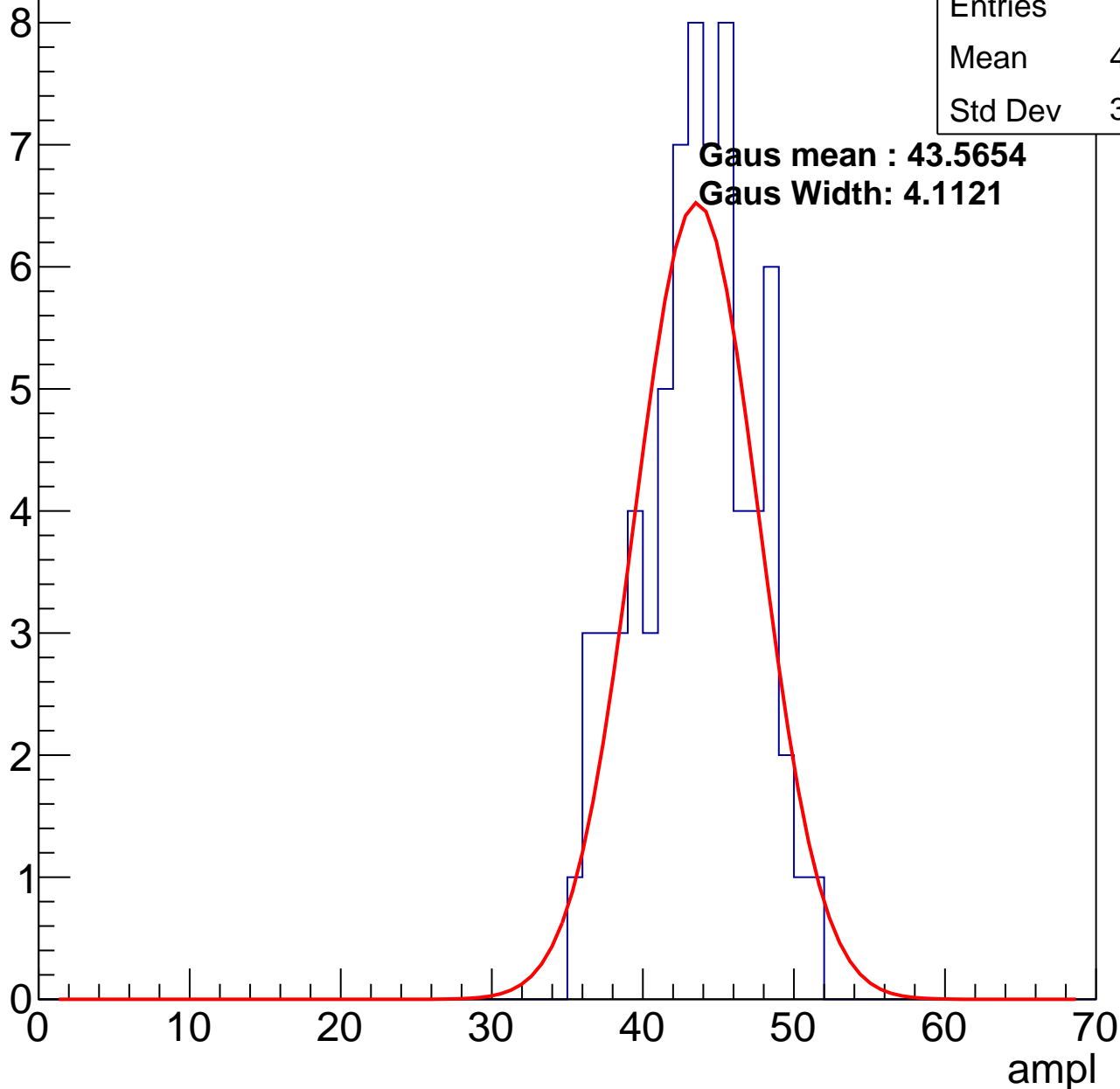
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.06
Std Dev	3.753

Gaus mean : 43.5654

Gaus Width: 4.1121

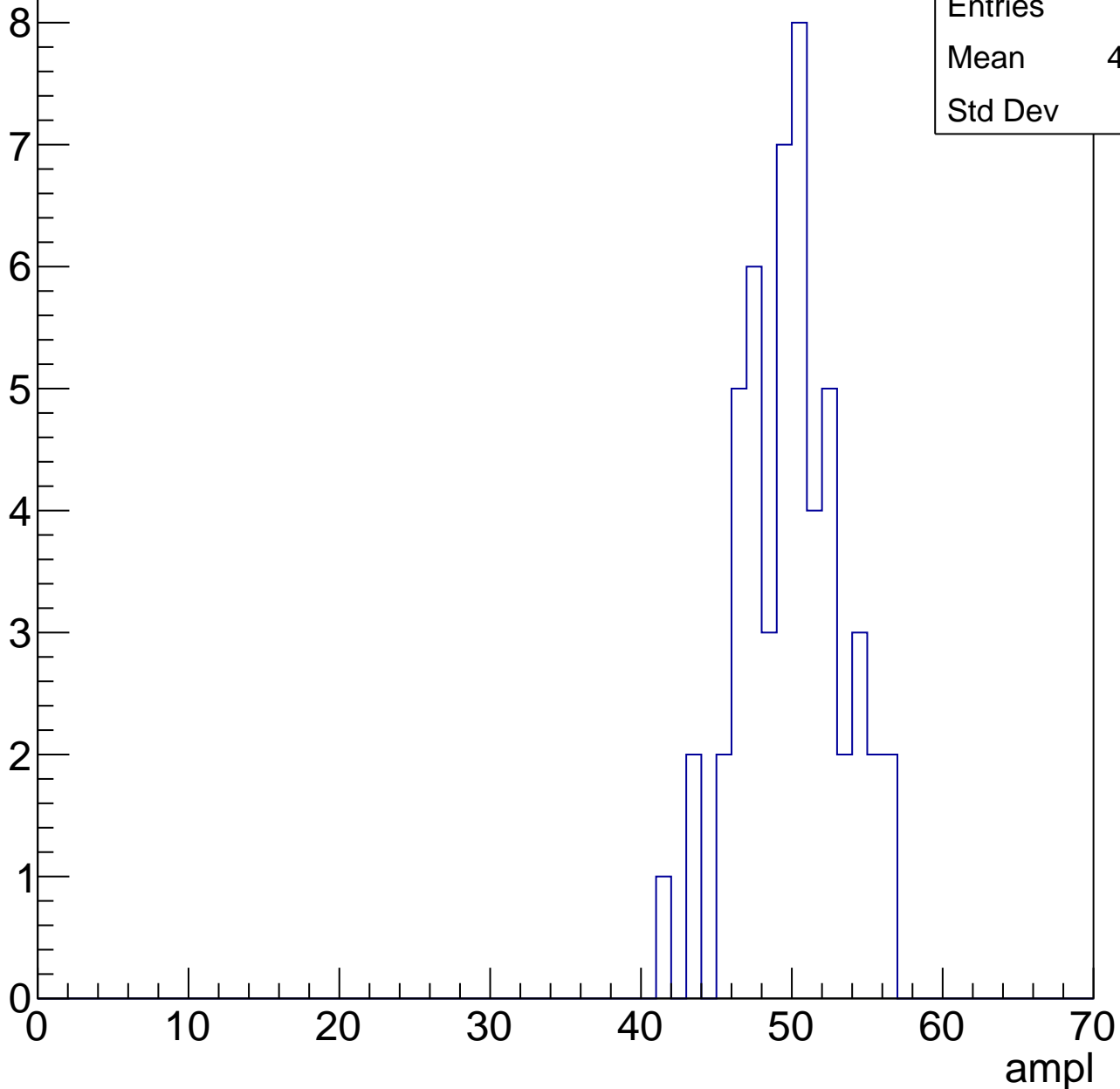


B1L102S, U22-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	49.42
Std Dev	3.33

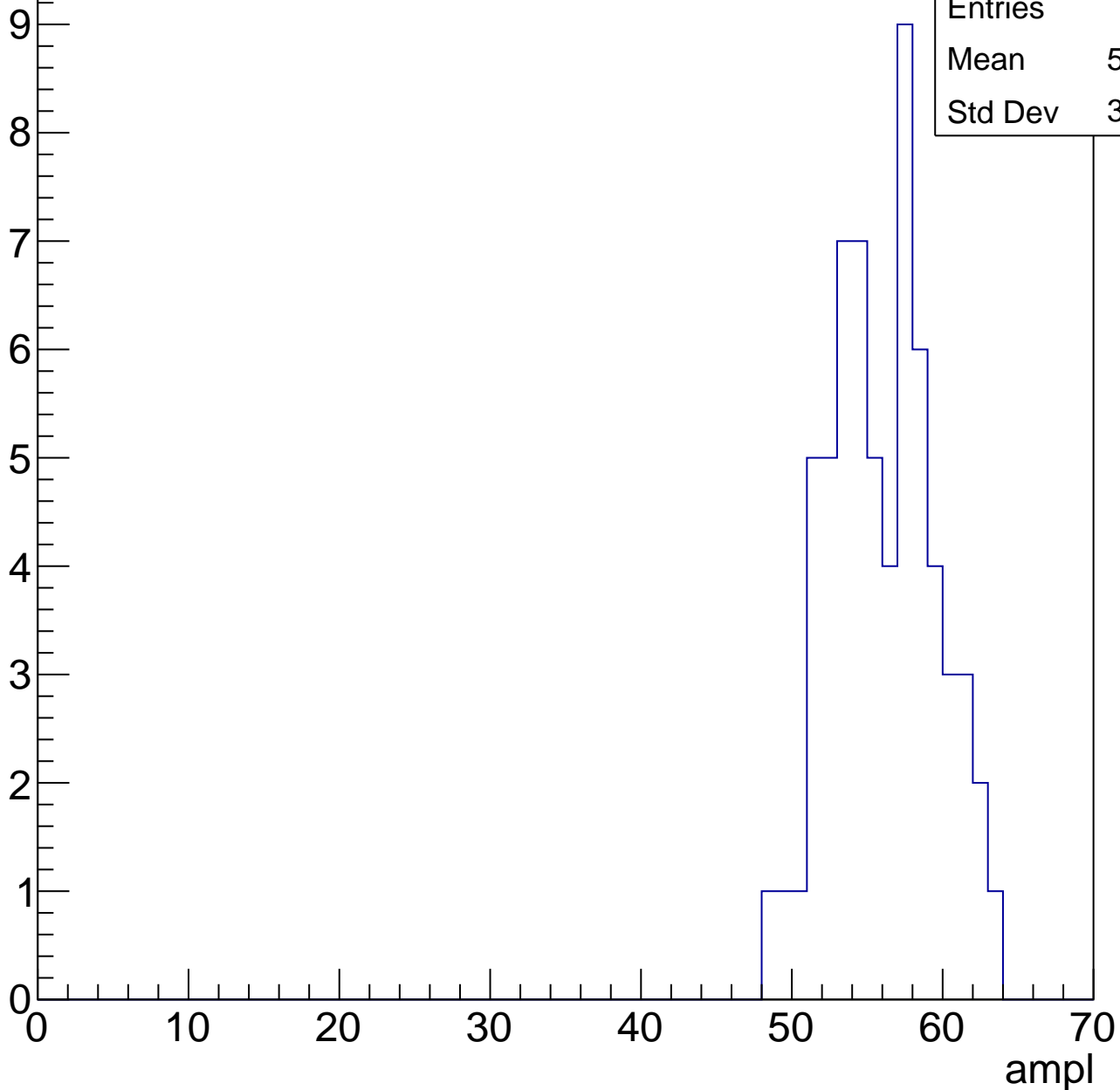


B1L102S, U22-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55.58
Std Dev	3.427

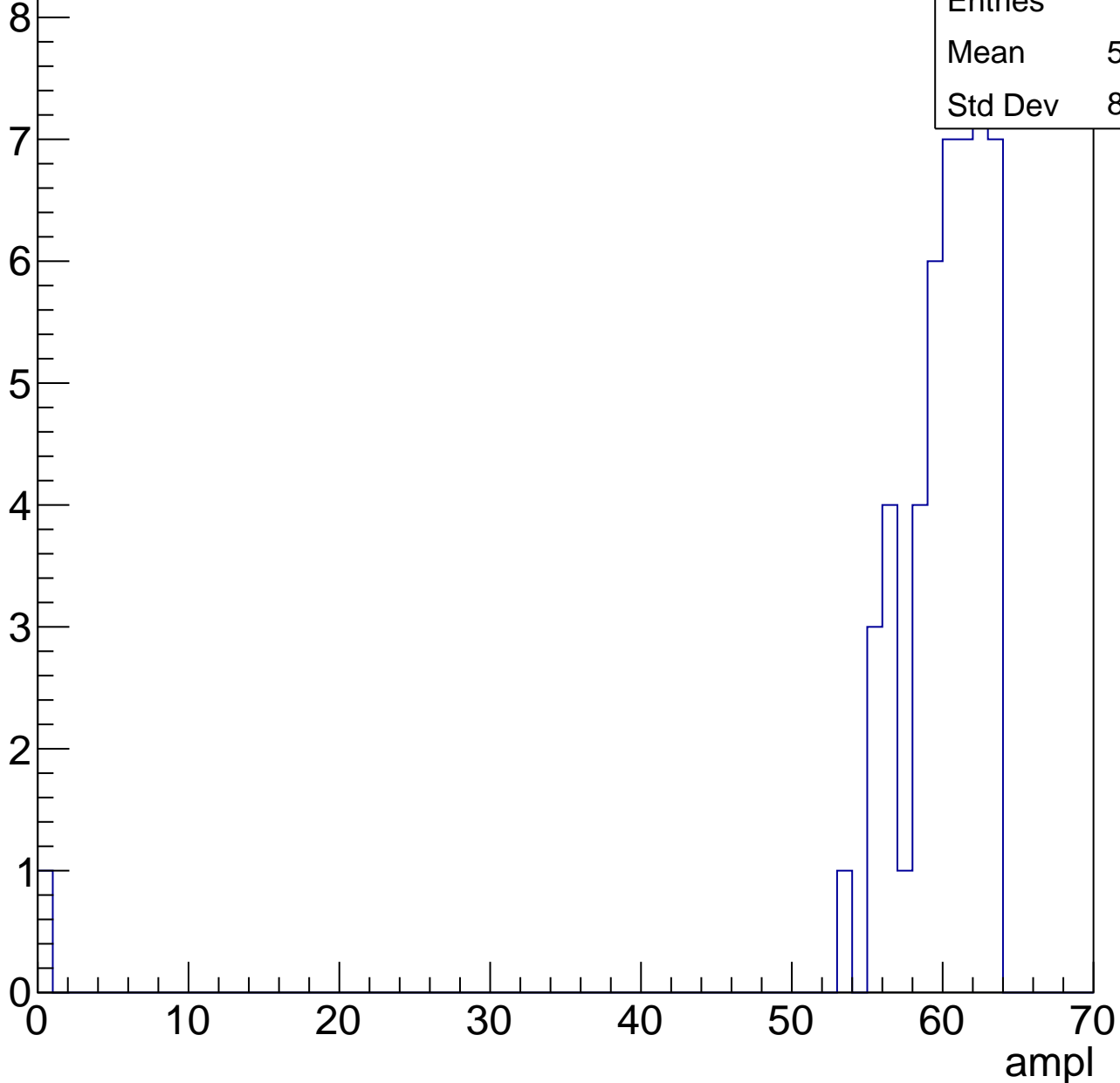


B1L102S, U22-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.55
Std Dev	8.827



B1L102S, U22-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch113, adc0

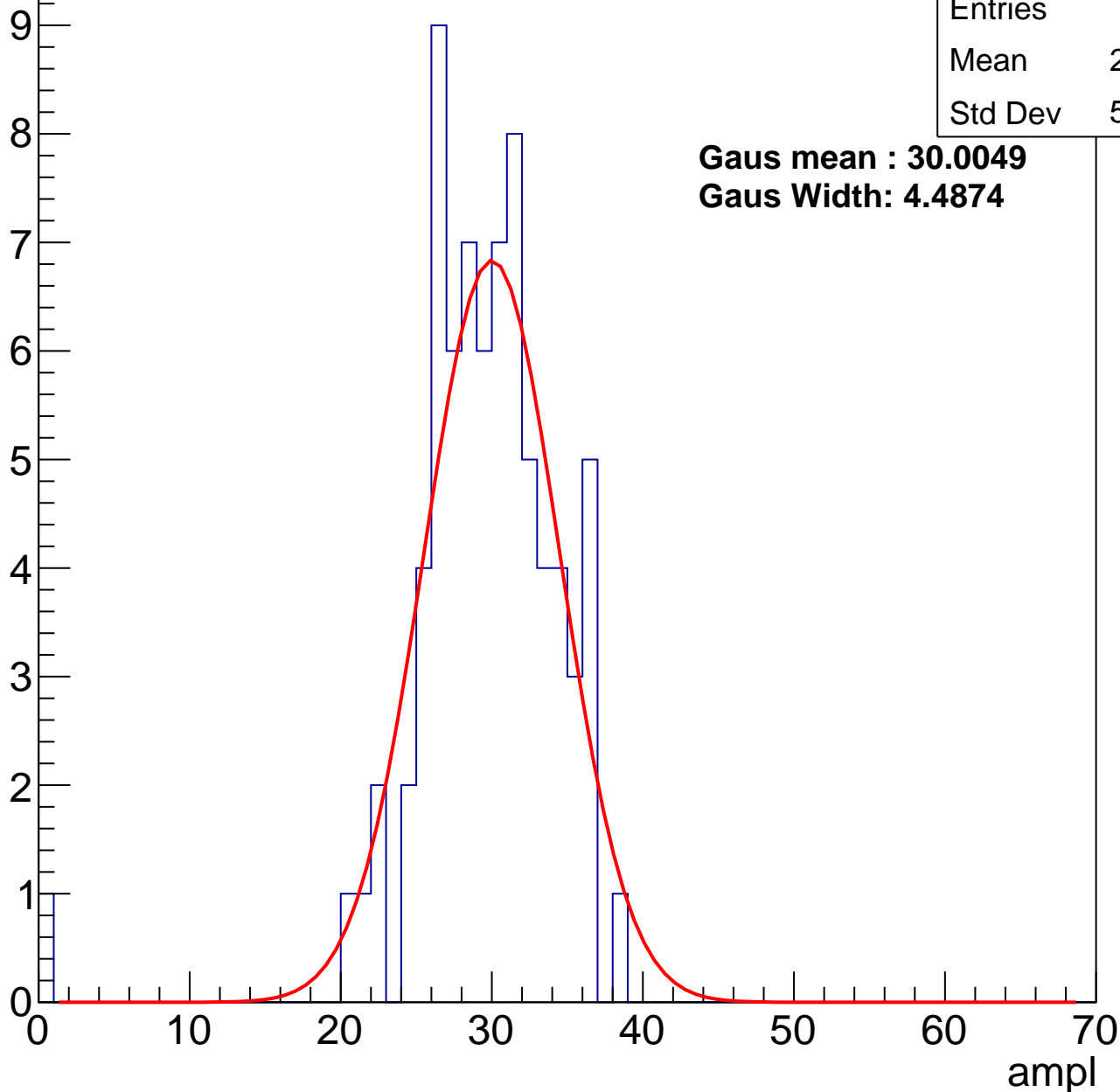
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.05
Std Dev	5.119

Gaus mean : 30.0049

Gaus Width: 4.4874



B1L102S, U22-ch113, adc1

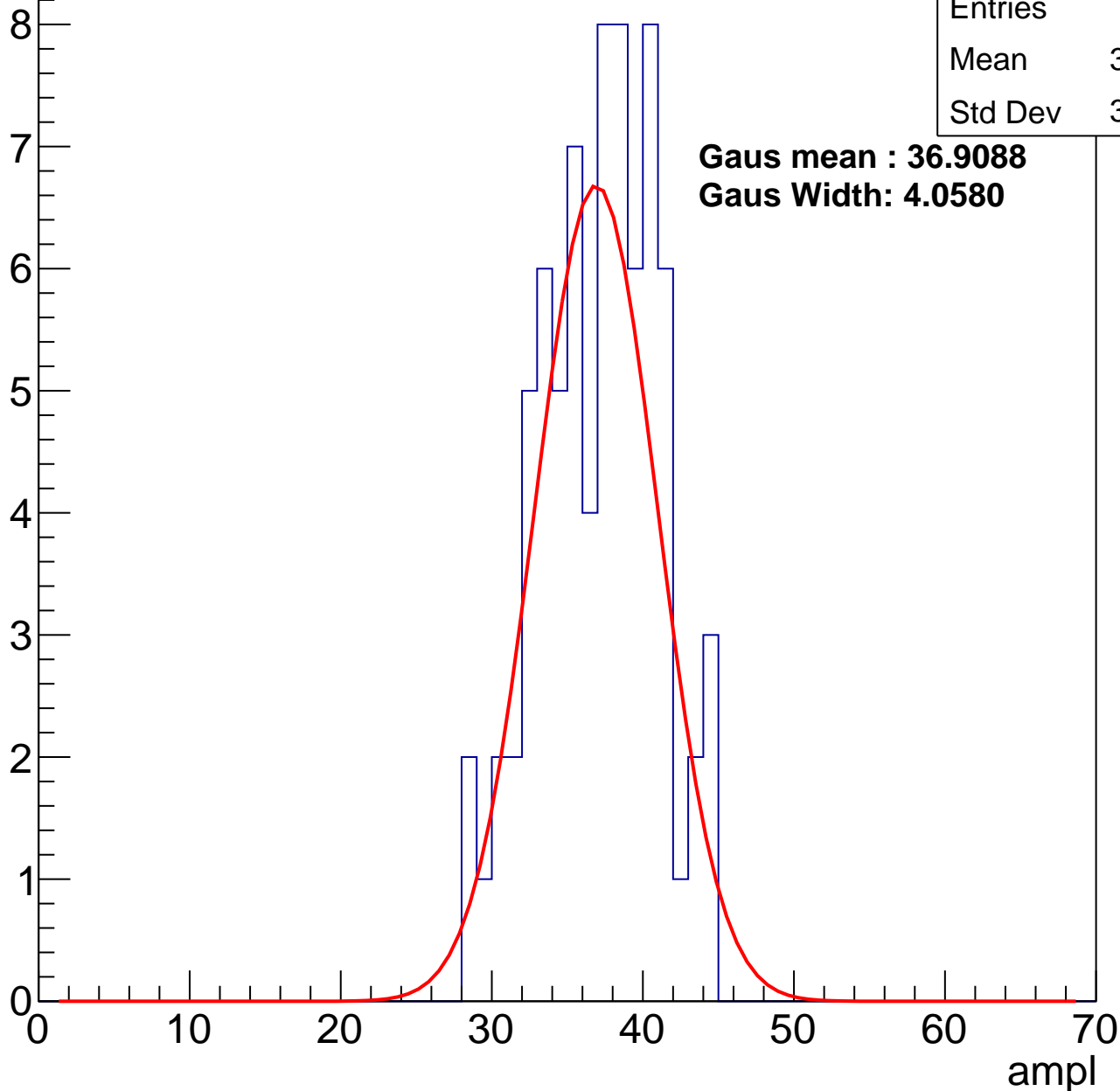
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.63
Std Dev	3.852

Gaus mean : 36.9088

Gaus Width: 4.0580



B1L102S, U22-ch113, adc2

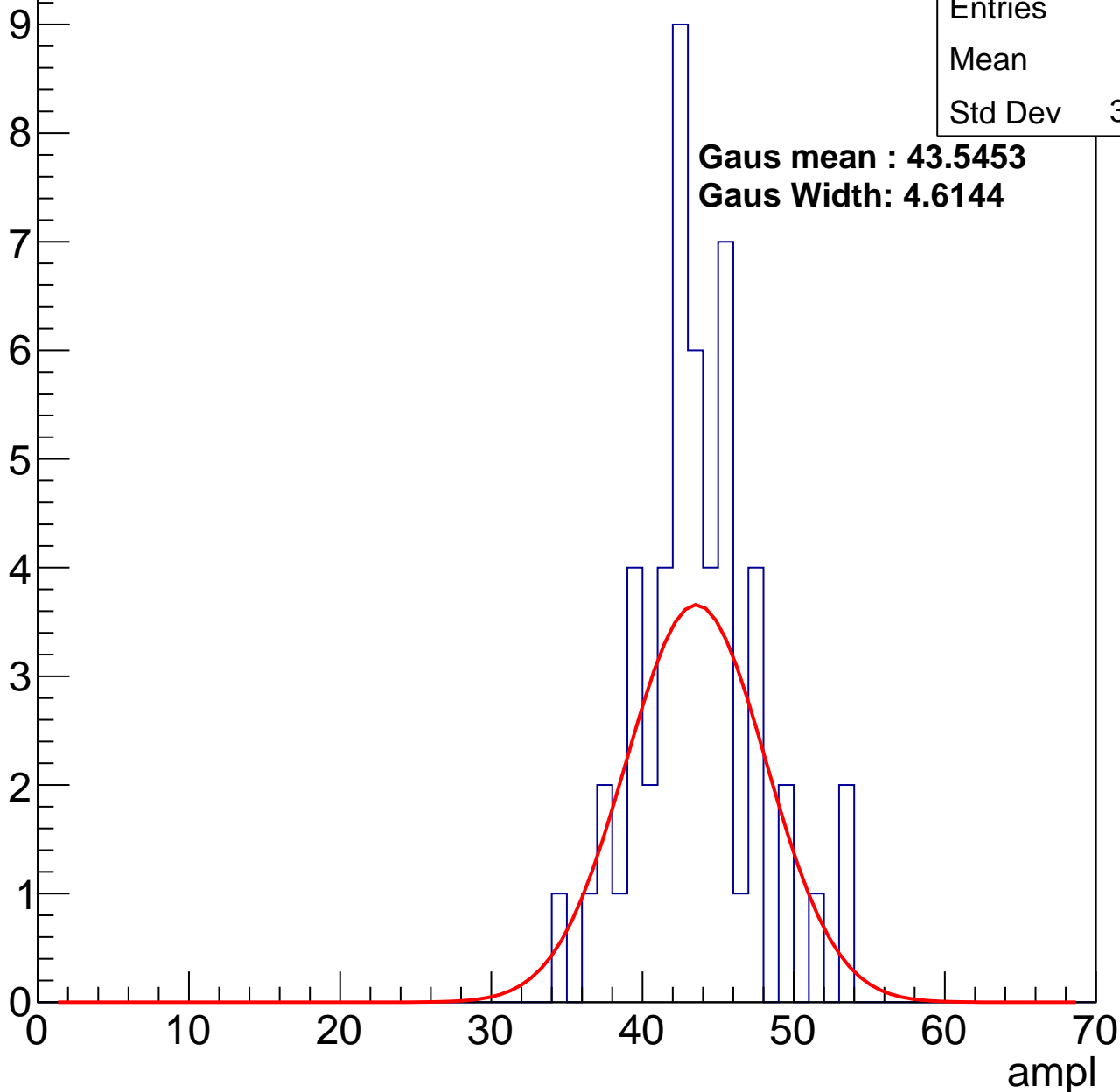
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	43.1
Std Dev	3.897

Gaus mean : 43.5453

Gaus Width: 4.6144

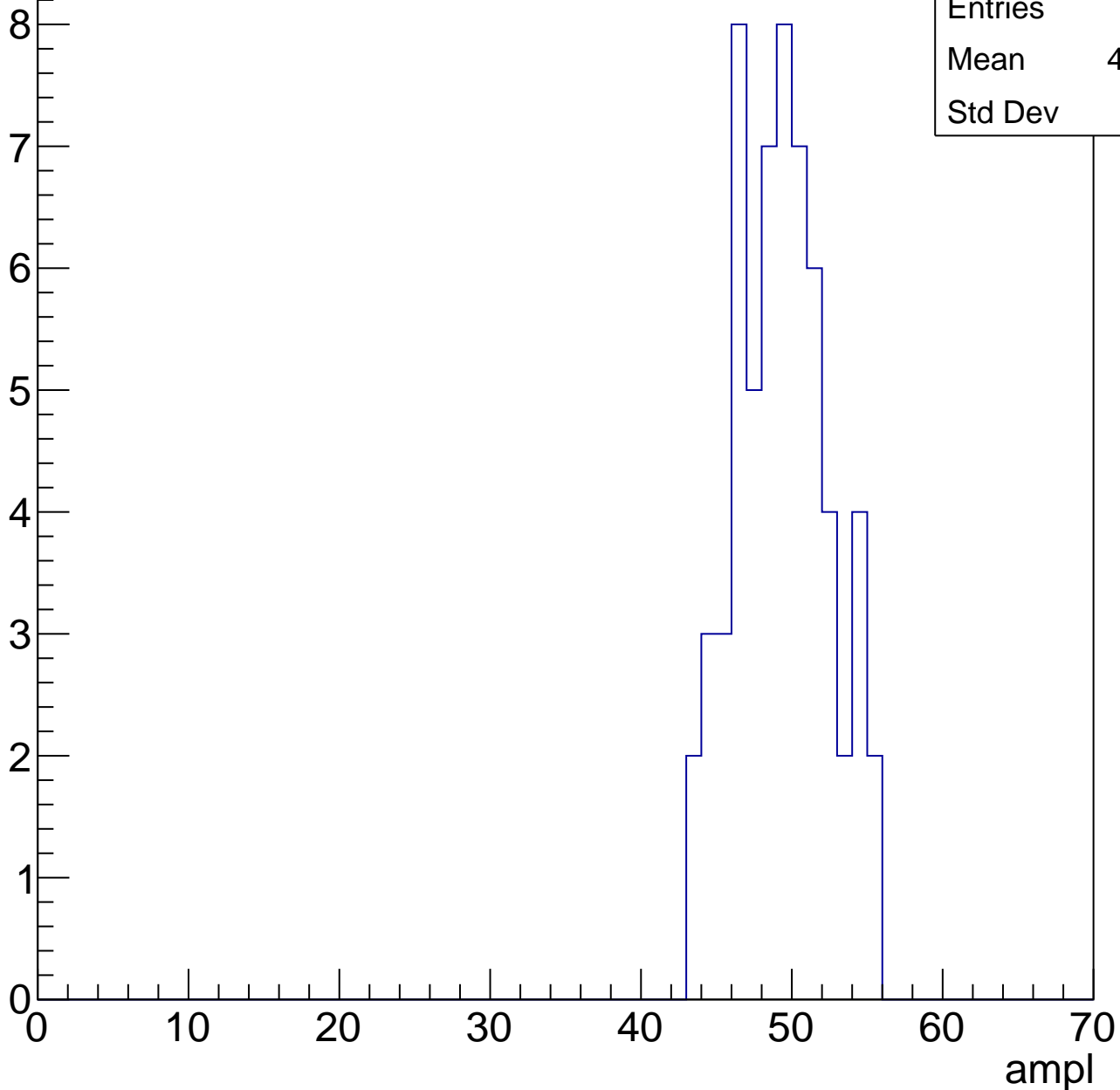


B1L102S, U22-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.85
Std Dev	3.04

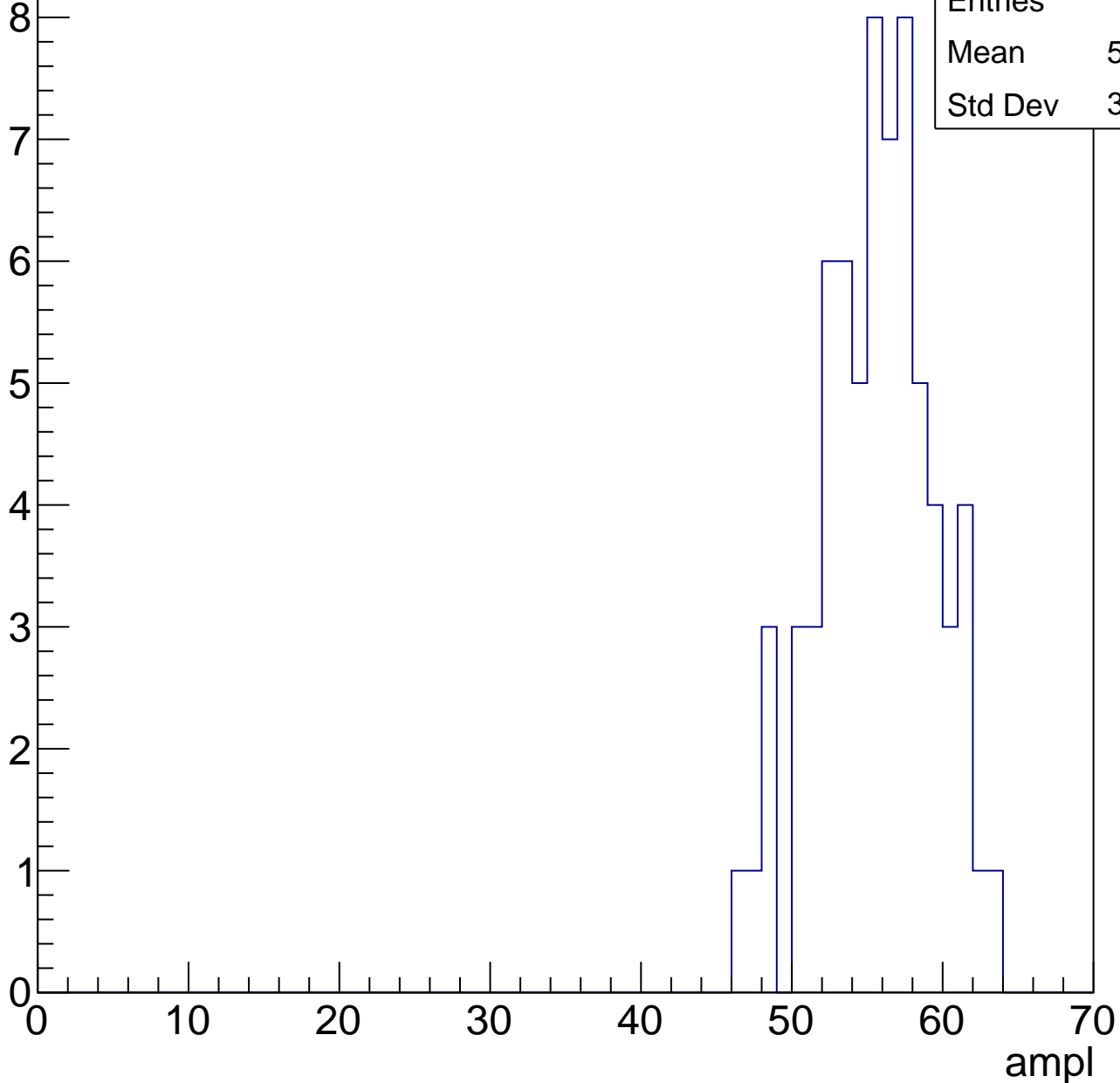


B1L102S, U22-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	55.12
Std Dev	3.763

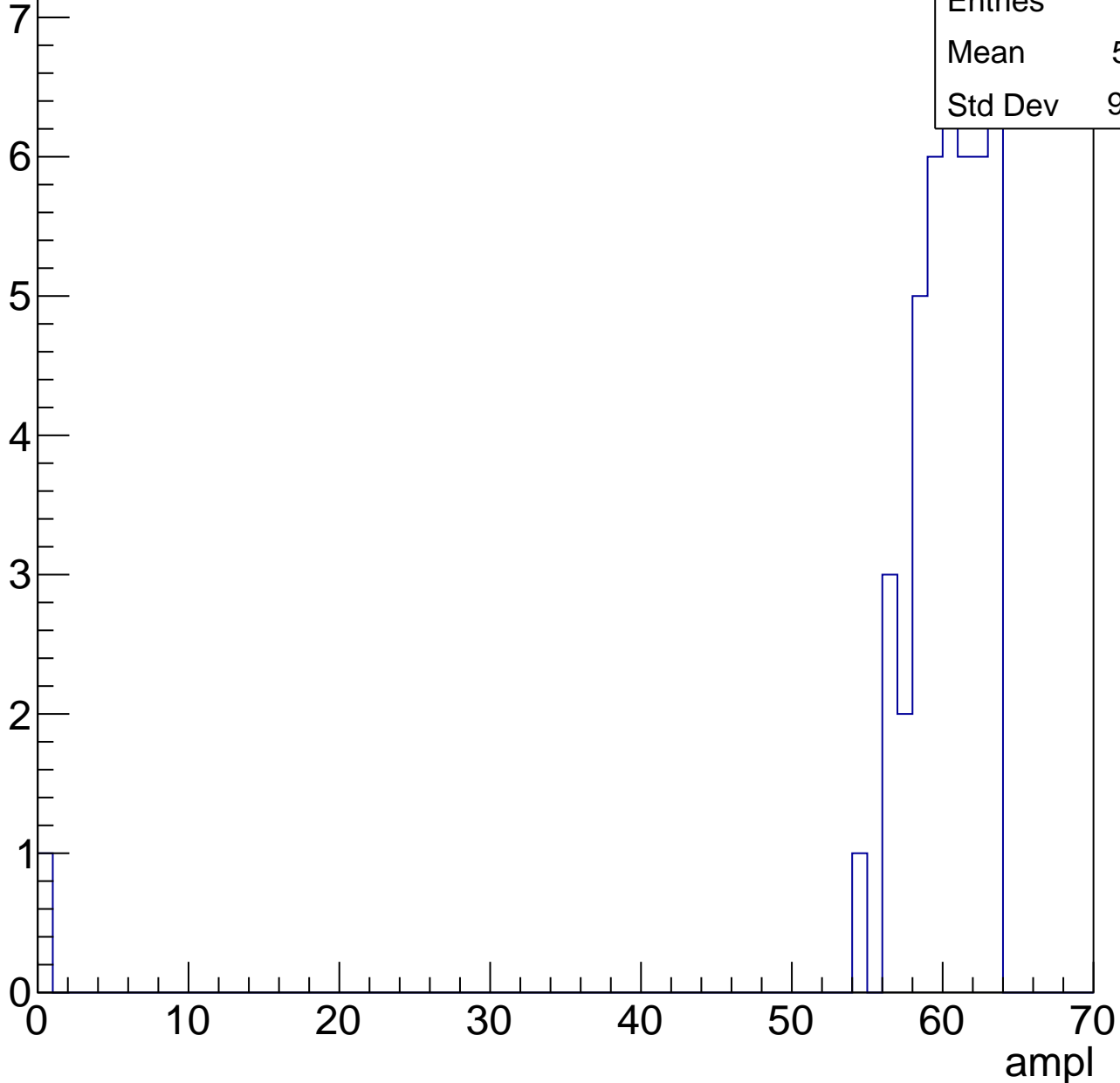


B1L102S, U22-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

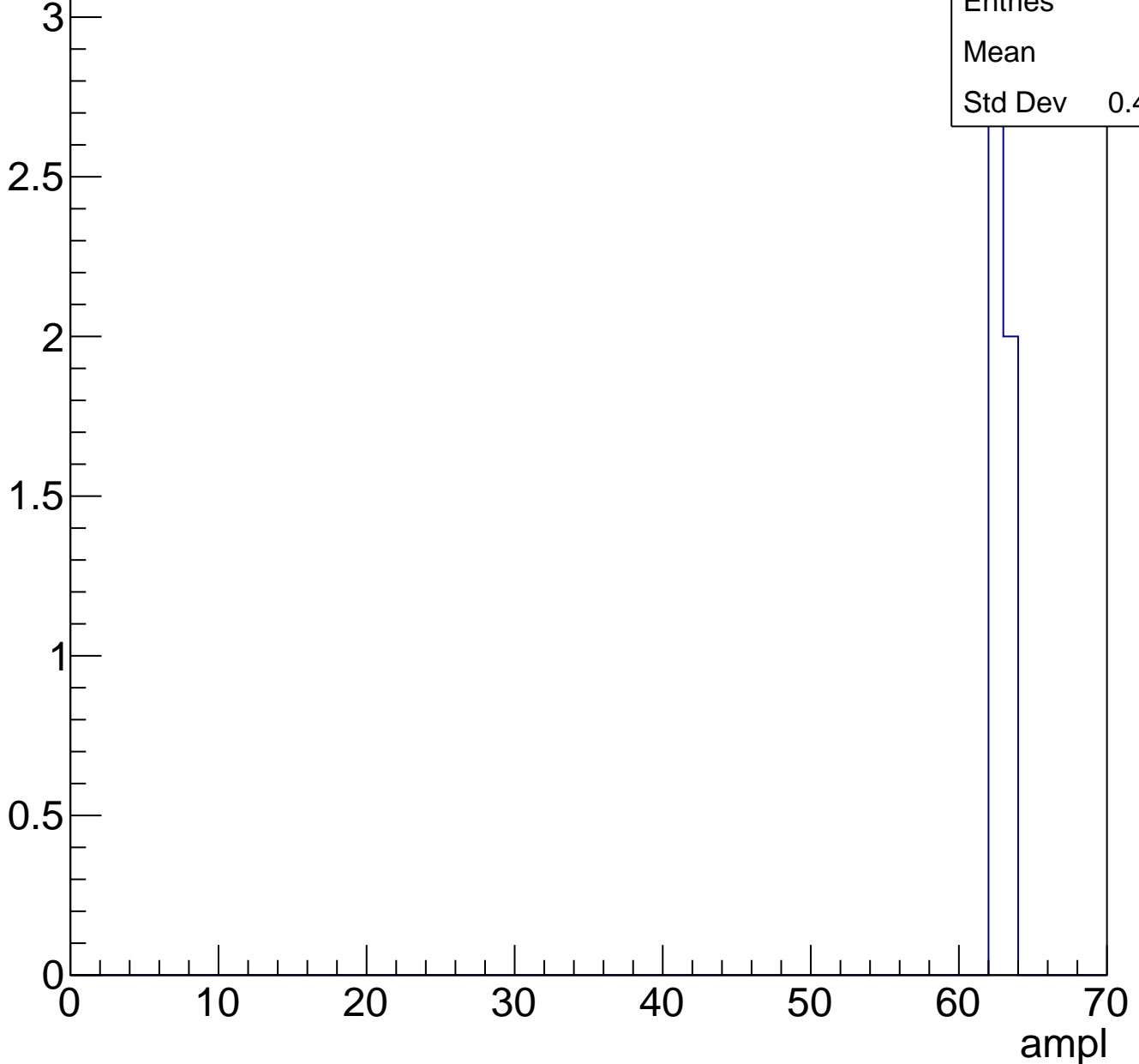
Entries	44
Mean	58.61
Std Dev	9.215



B1L102S, U22-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch114, adc0

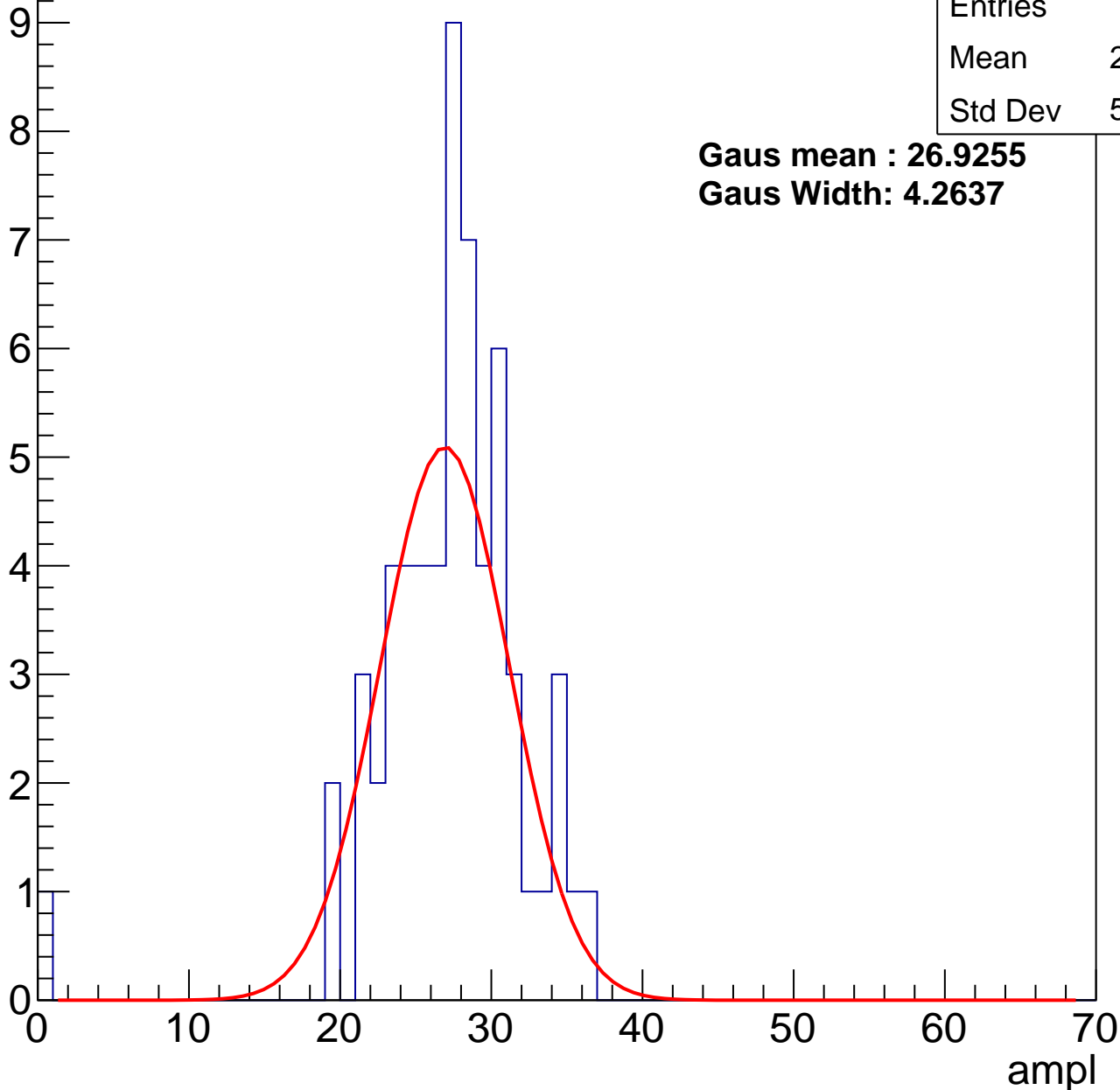
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	26.72
Std Dev	5.177

Gaus mean : 26.9255

Gaus Width: 4.2637



B1L102S, U22-ch114, adc1

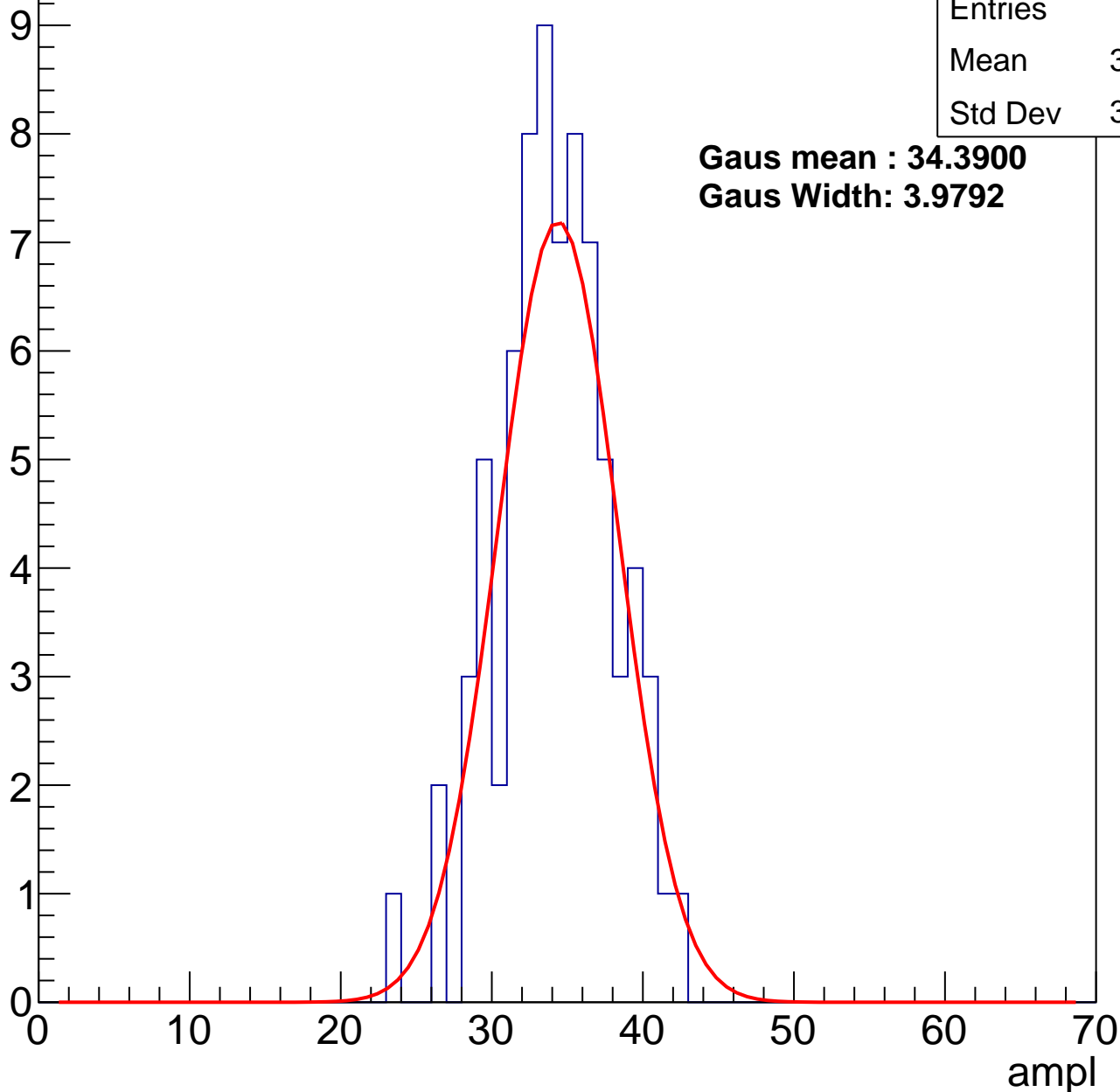
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	33.75
Std Dev	3.749

Gaus mean : 34.3900

Gaus Width: 3.9792



B1L102S, U22-ch114, adc2

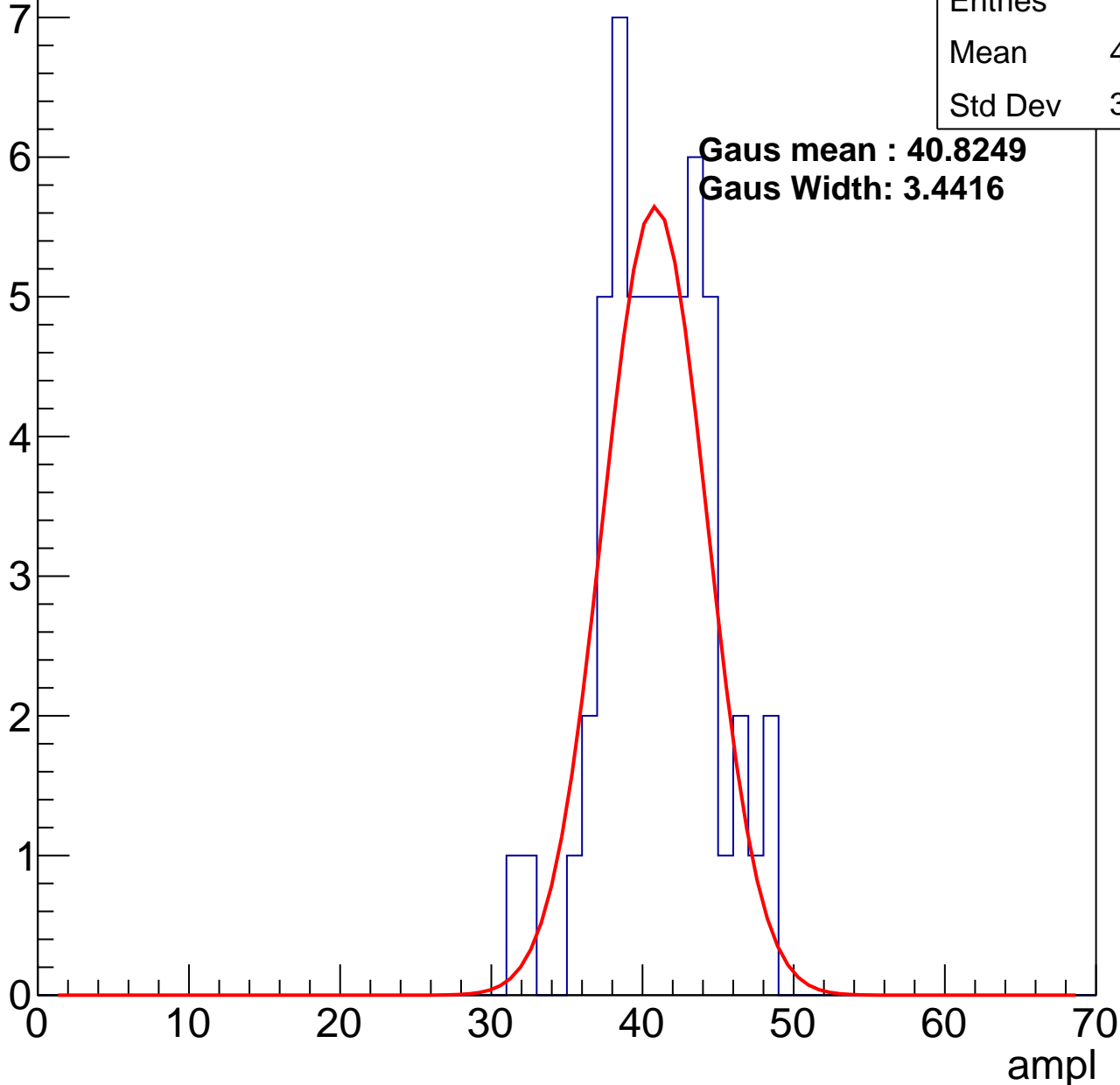
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	40.54
Std Dev	3.594

Gaus mean : 40.8249

Gaus Width: 3.4416

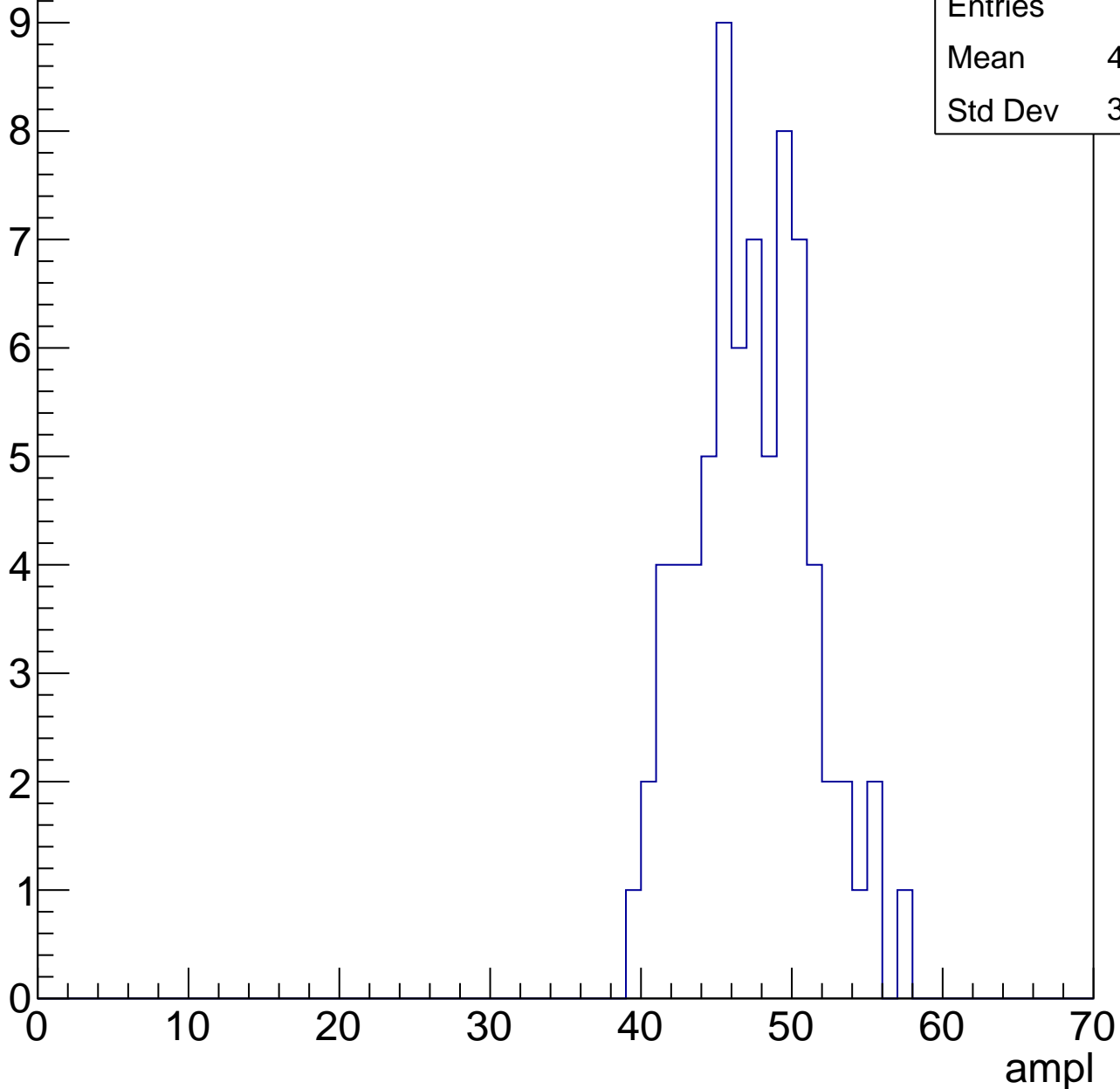


B1L102S, U22-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	46.89
Std Dev	3.889

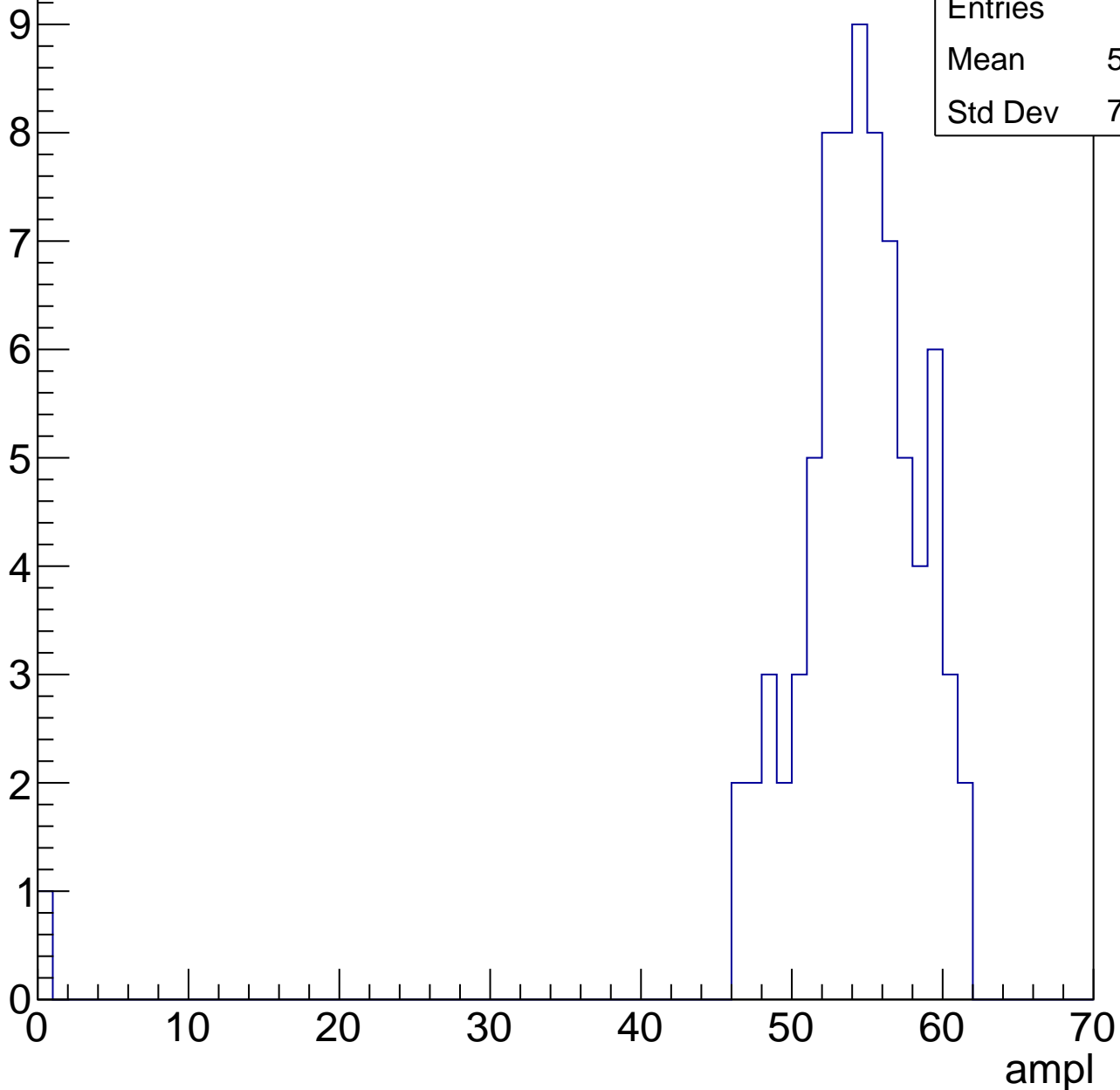


B1L102S, U22-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	53.38
Std Dev	7.075

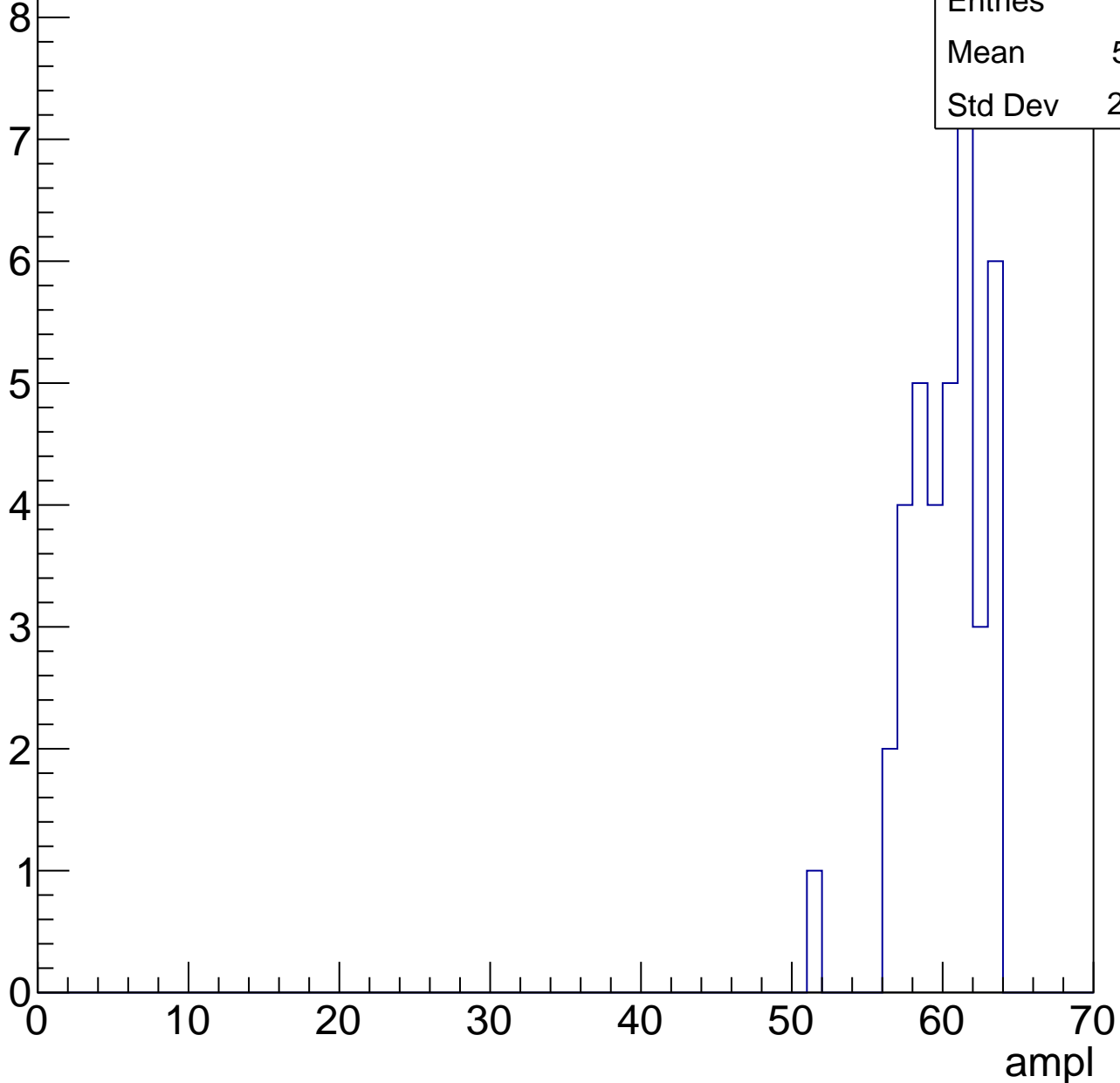


B1L102S, U22-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	59.71
Std Dev	2.533

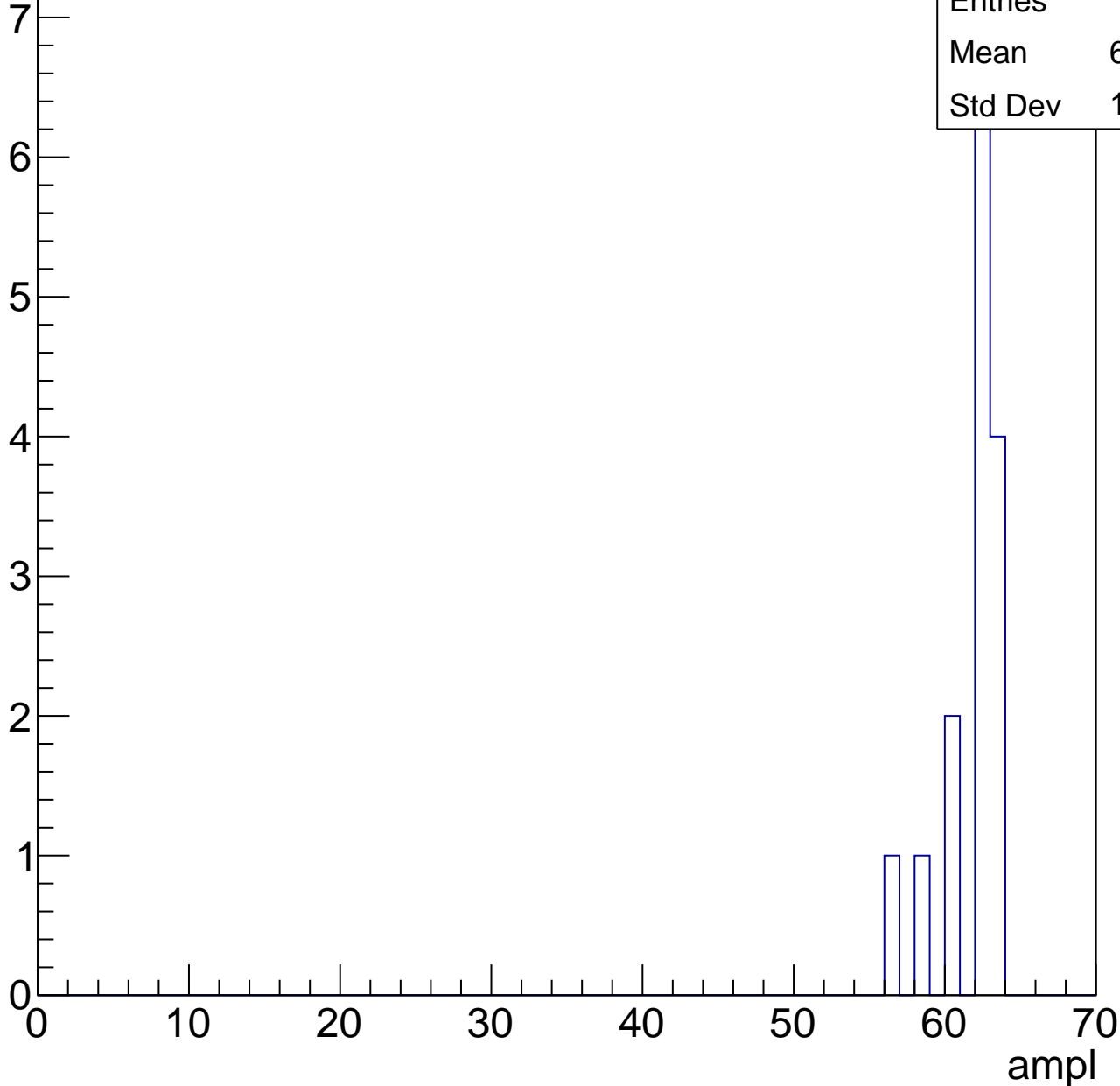


B1L102S, U22-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.33
Std Dev	1.955



B1L102S, U22-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch115, adc0

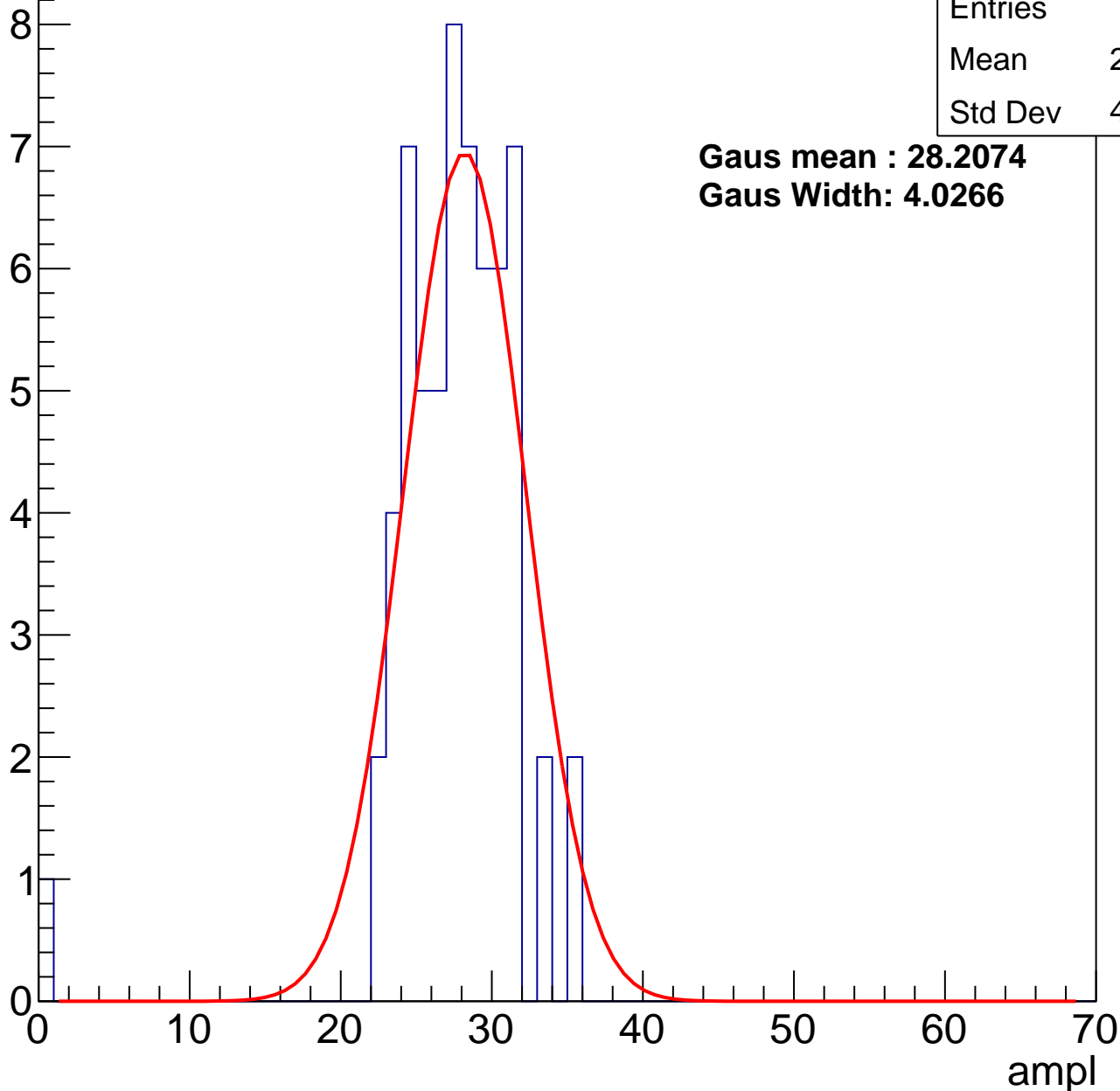
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.06
Std Dev	4.628

Gaus mean : 28.2074

Gaus Width: 4.0266



B1L102S, U22-ch115, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	95
Mean	35.19
Std Dev	5.116

Gaus mean : 36.0503

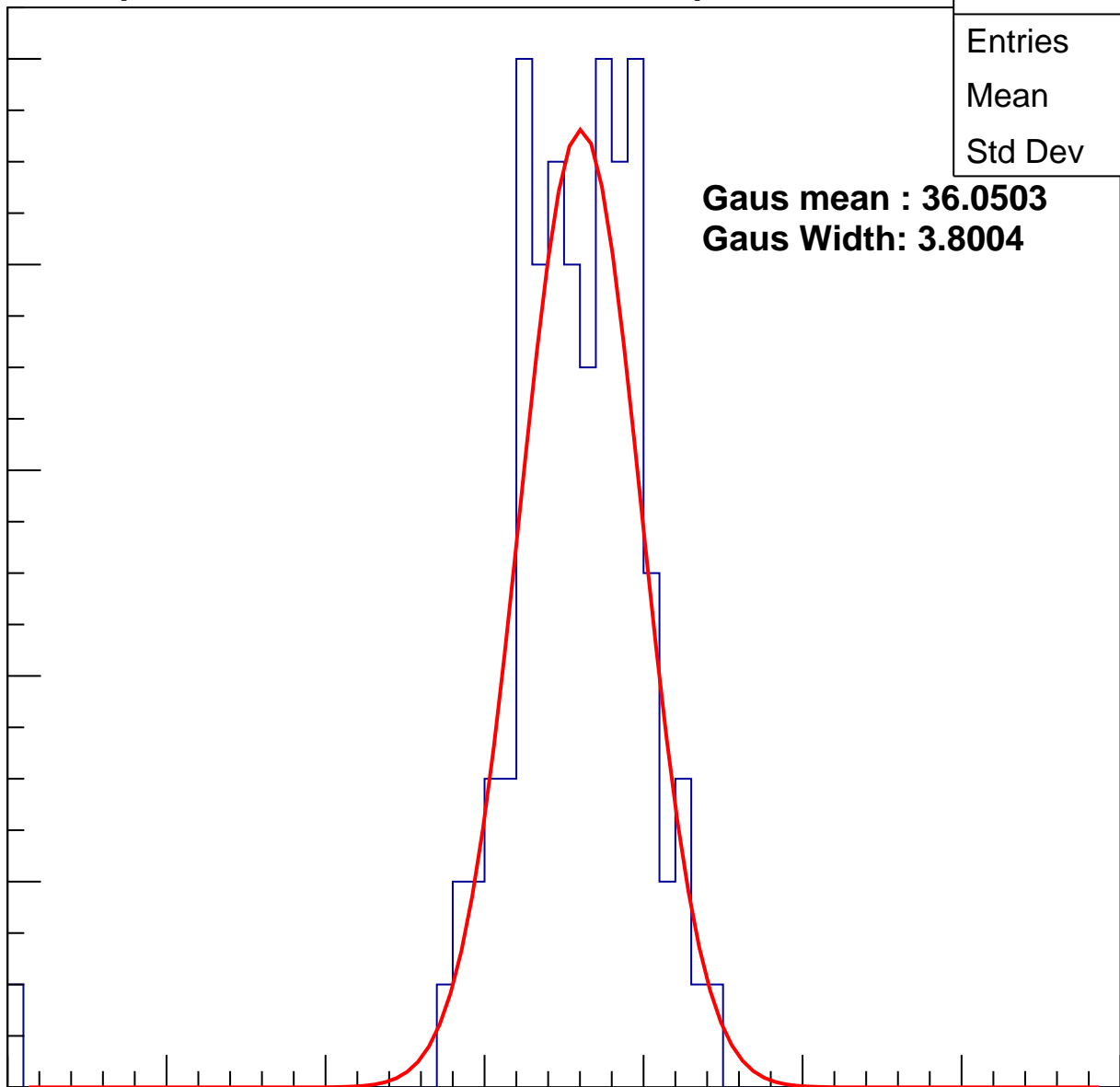
Gaus Width: 3.8004

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch115, adc2

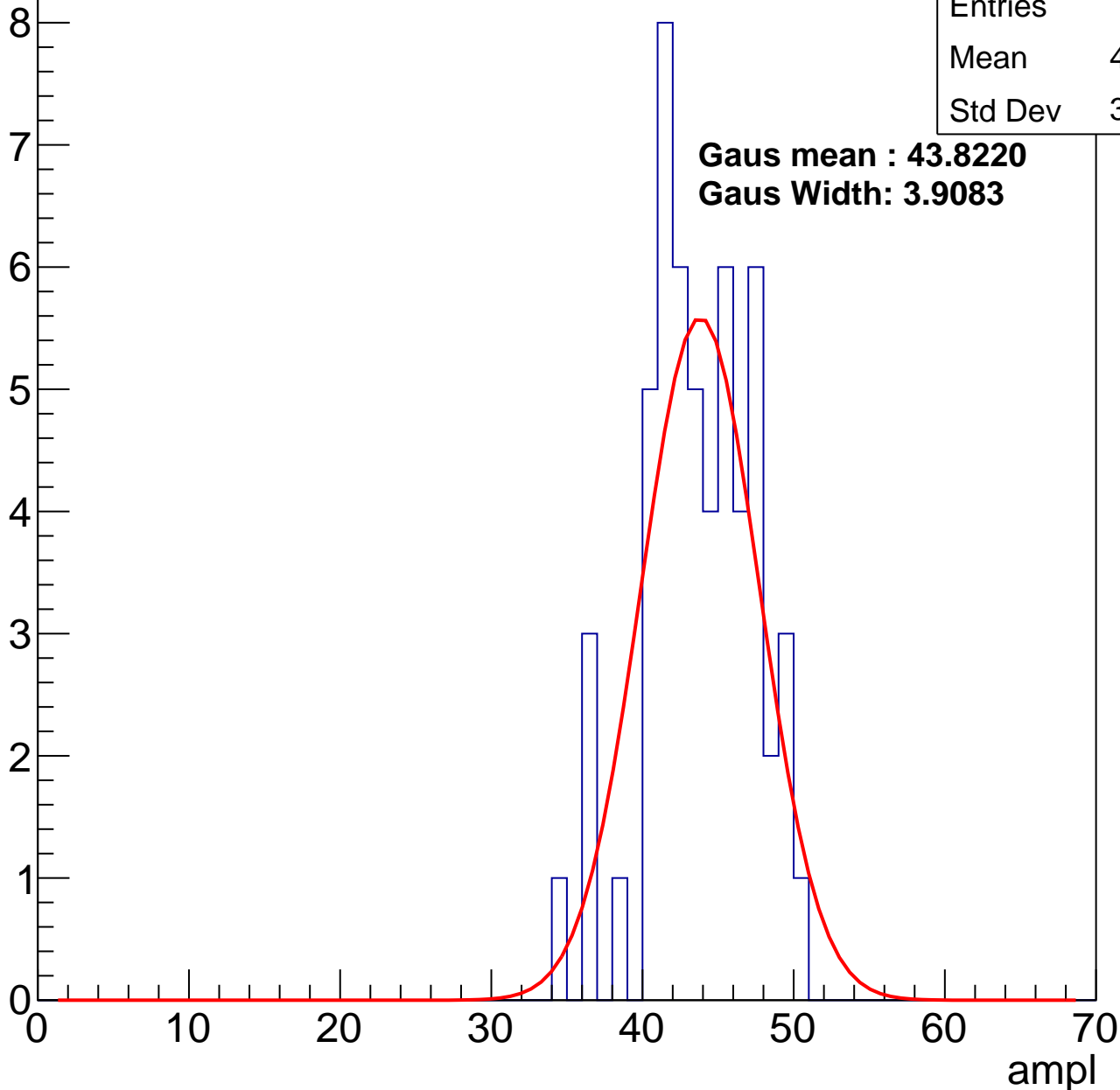
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	43.27
Std Dev	3.565

Gaus mean : 43.8220

Gaus Width: 3.9083

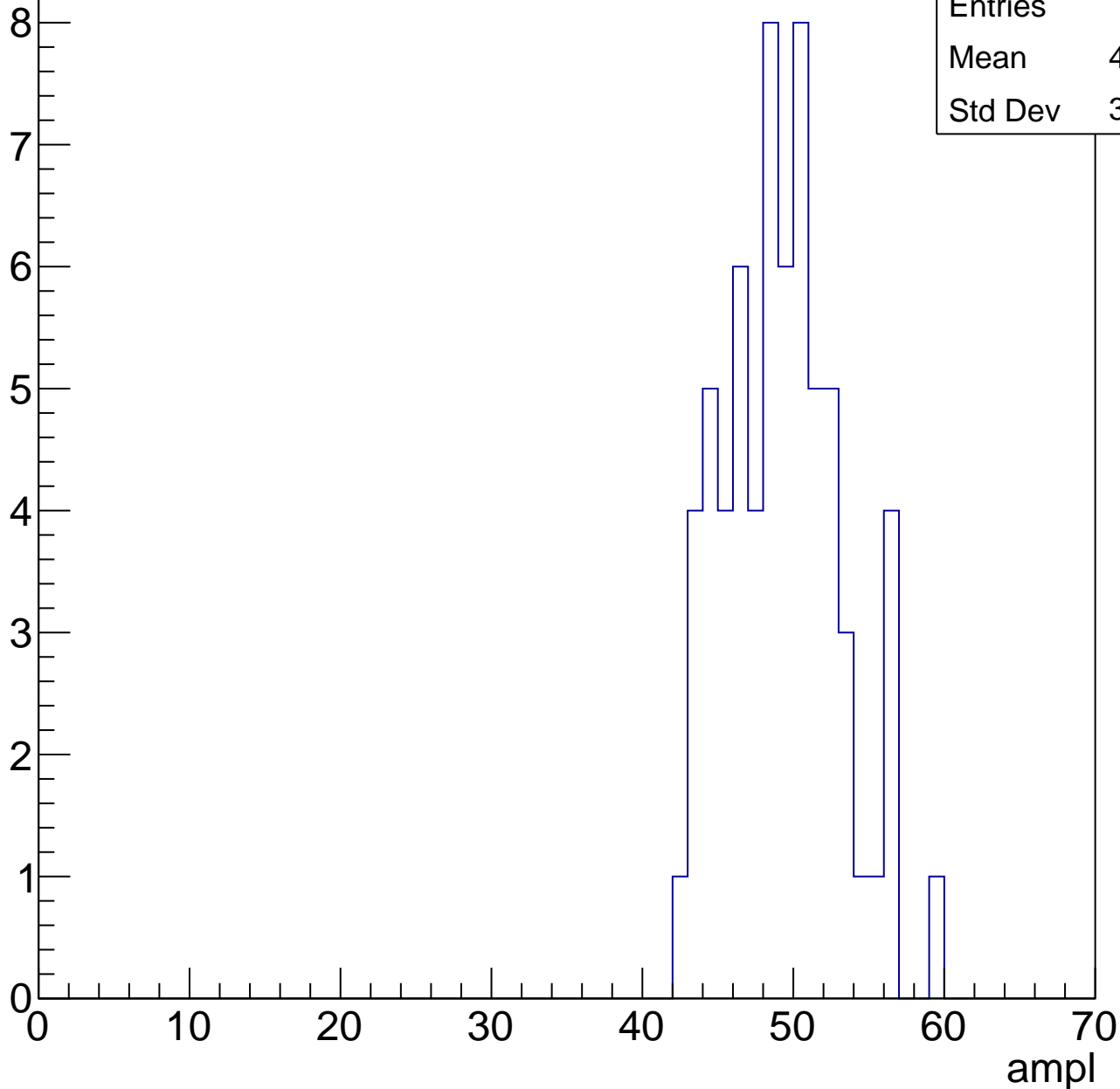


B1L102S, U22-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.82
Std Dev	3.766

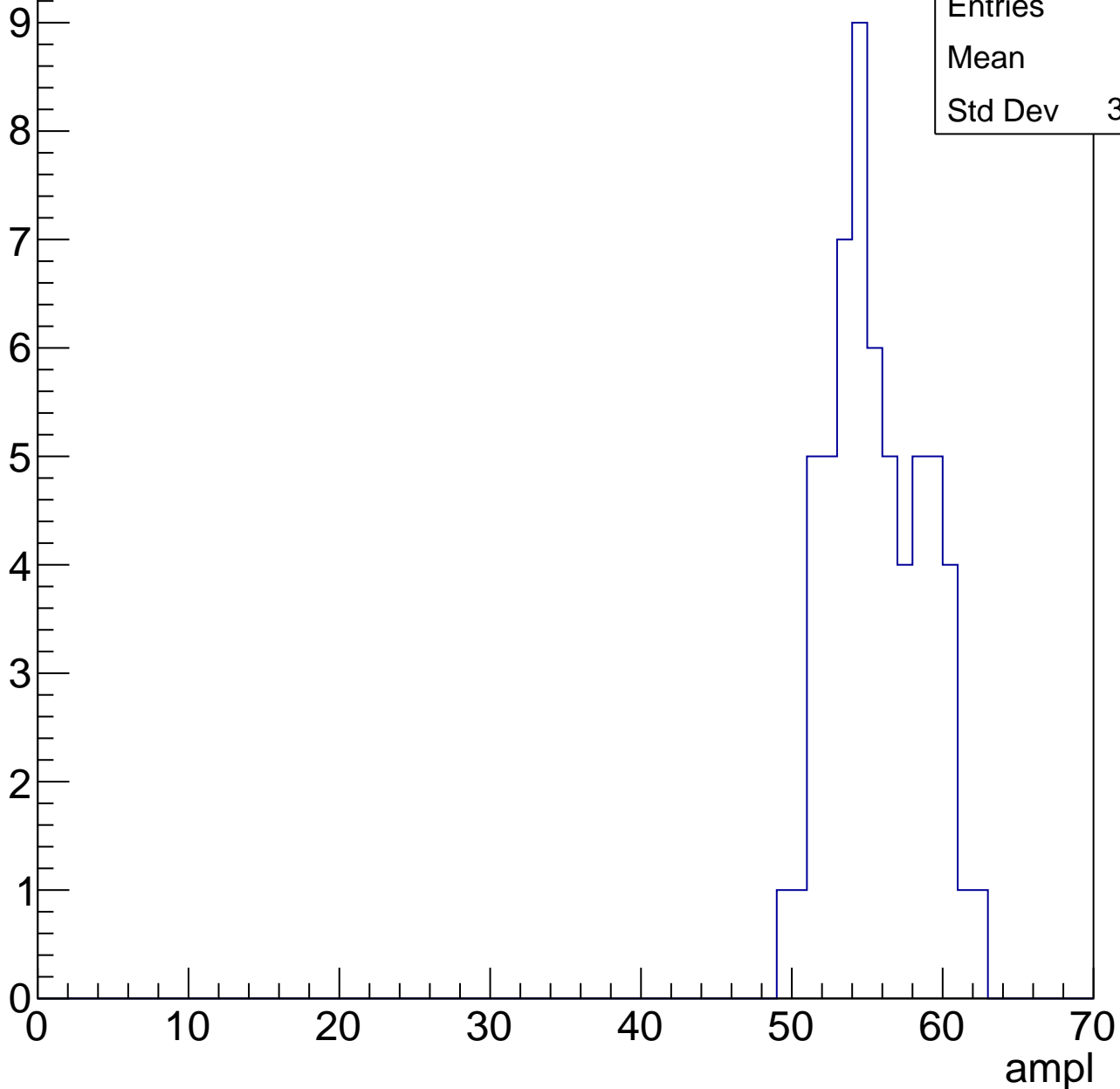


B1L102S, U22-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.2
Std Dev	3.058

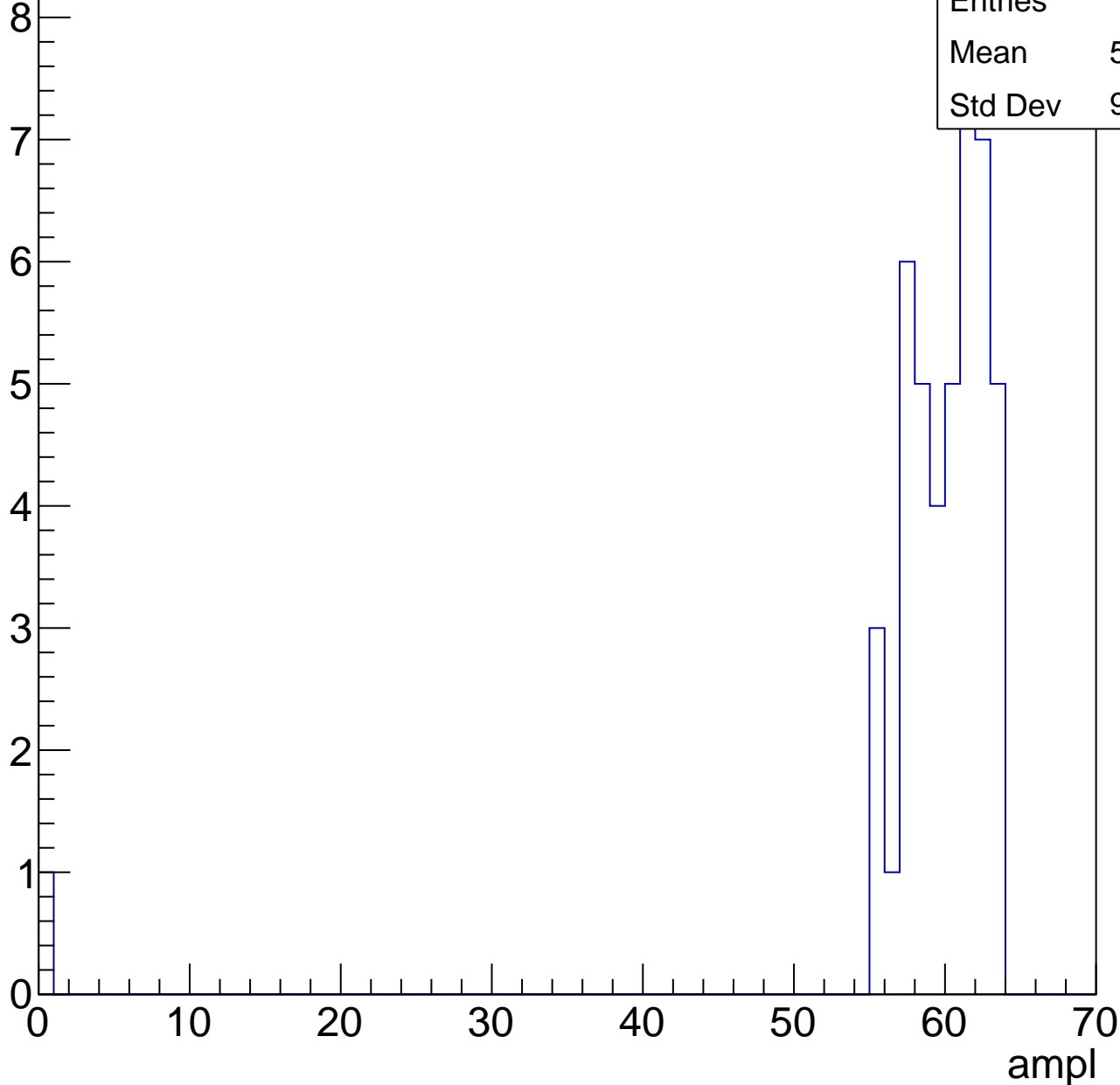


B1L102S, U22-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.36
Std Dev	9.102



B1L102S, U22-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch116, adc0

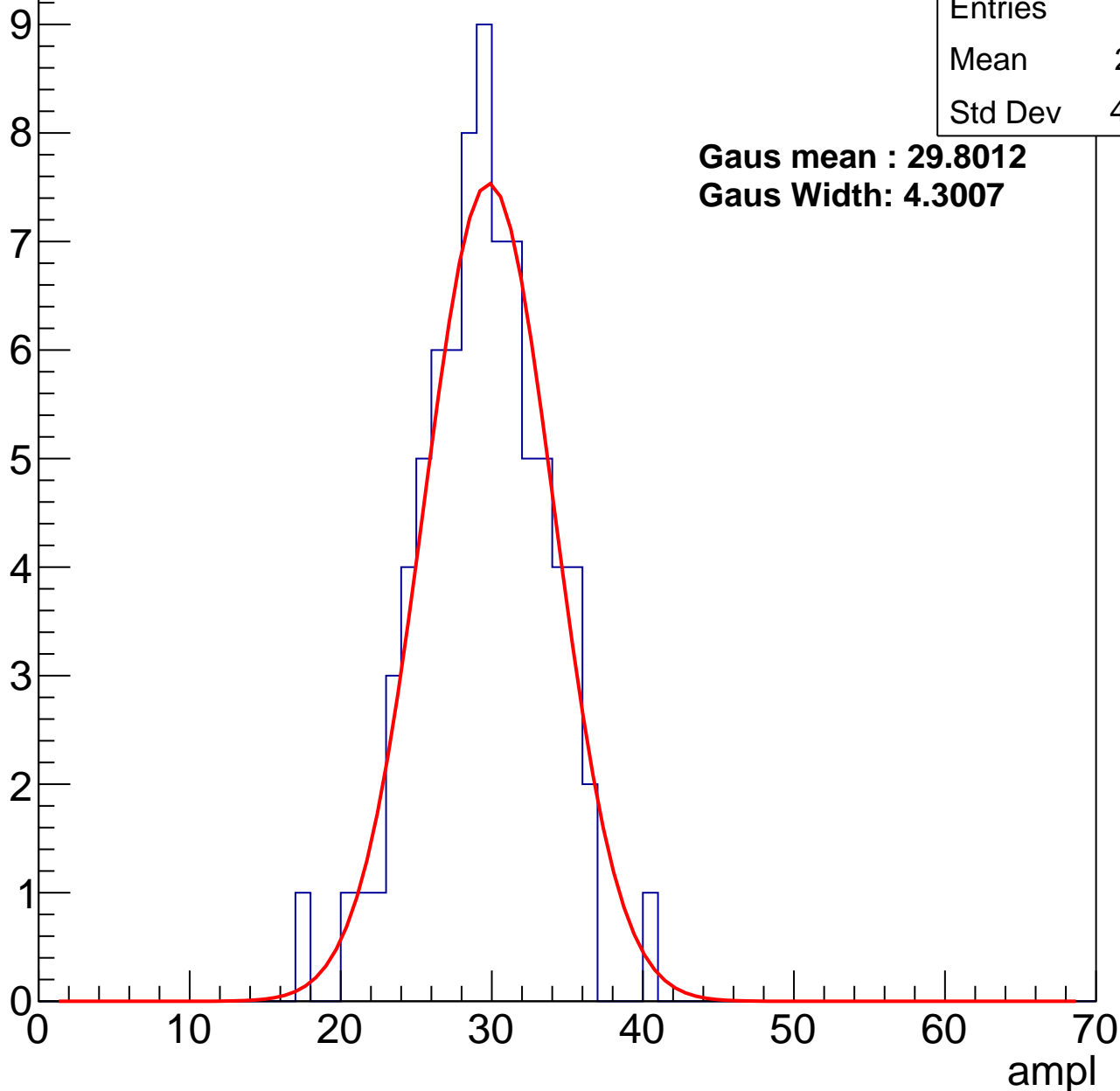
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.91
Std Dev	4.084

Gaus mean : 29.8012

Gaus Width: 4.3007



B1L102S, U22-ch116, adc1

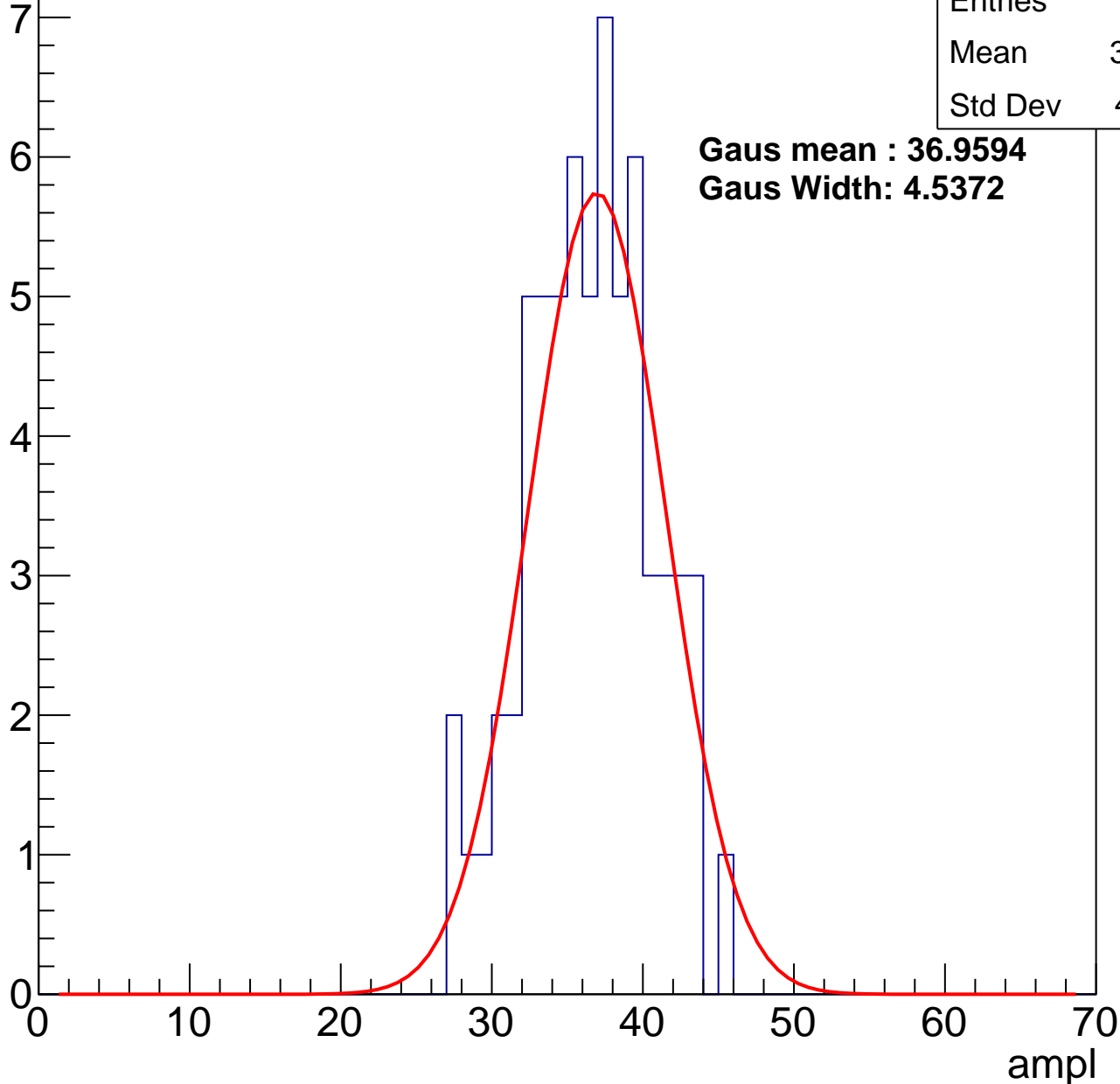
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.06
Std Dev	4.091

Gaus mean : 36.9594

Gaus Width: 4.5372



B1L102S, U22-ch116, adc2

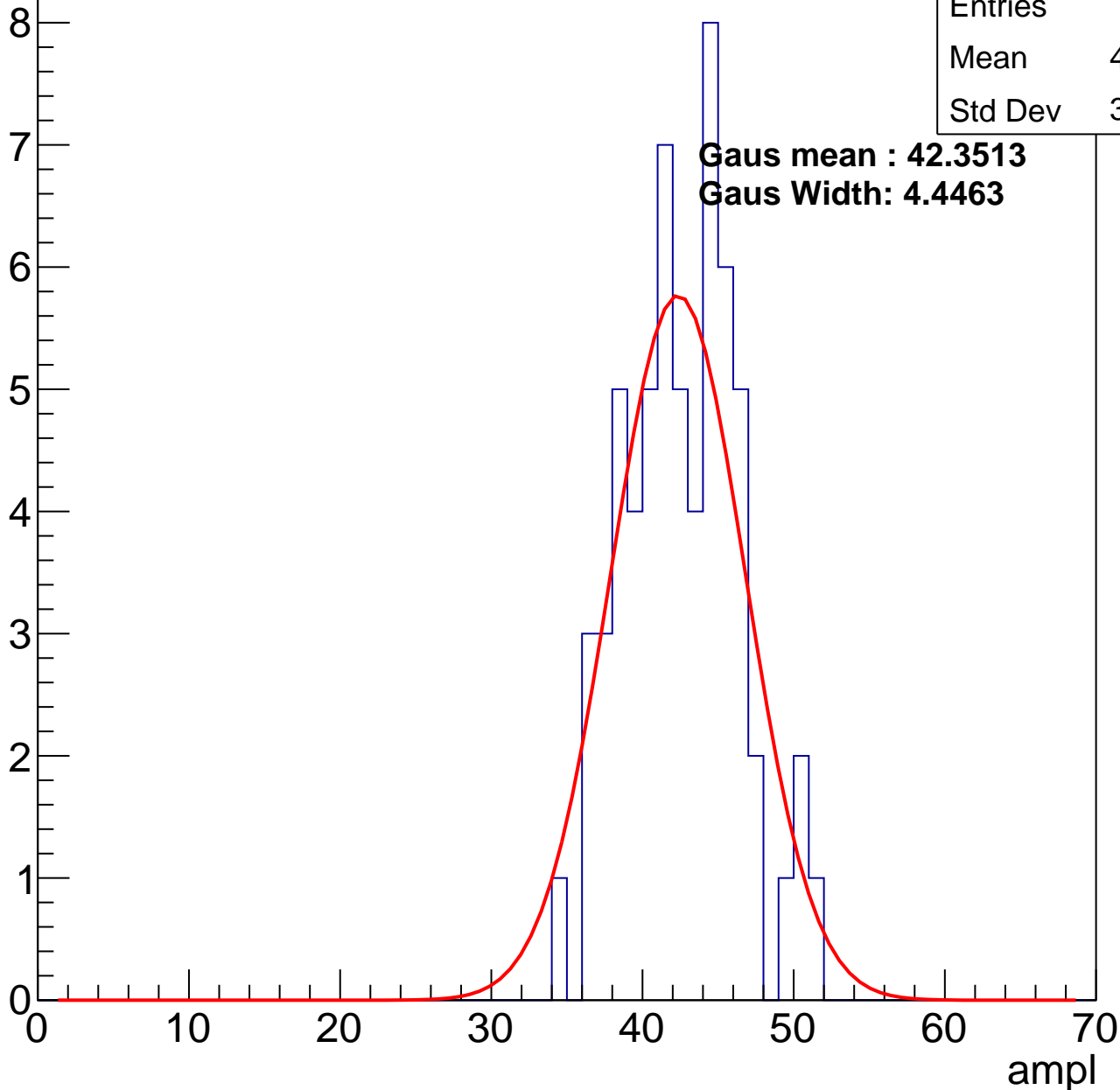
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.16
Std Dev	3.738

Gaus mean : 42.3513

Gaus Width: 4.4463

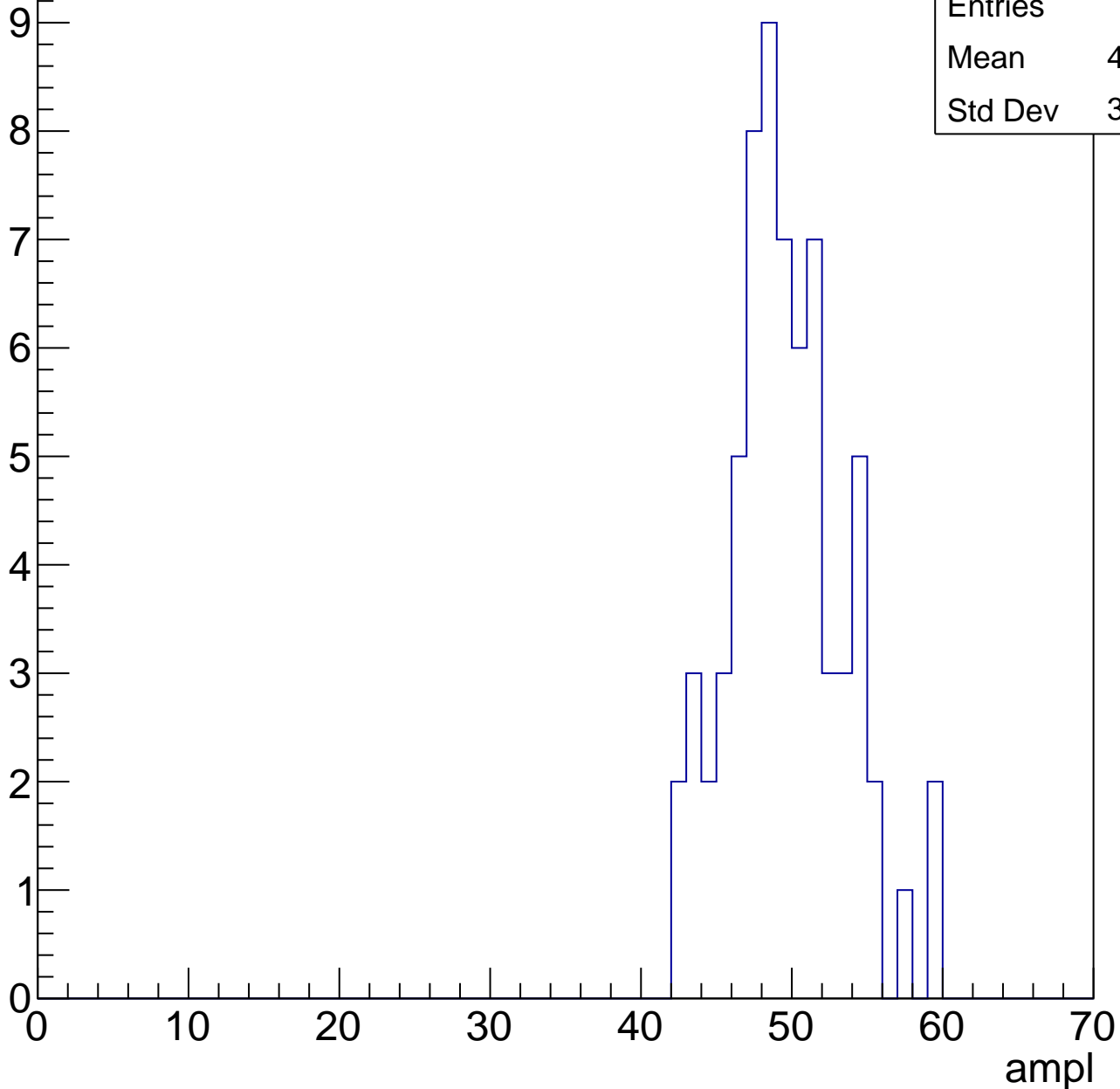


B1L102S, U22-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.18
Std Dev	3.753

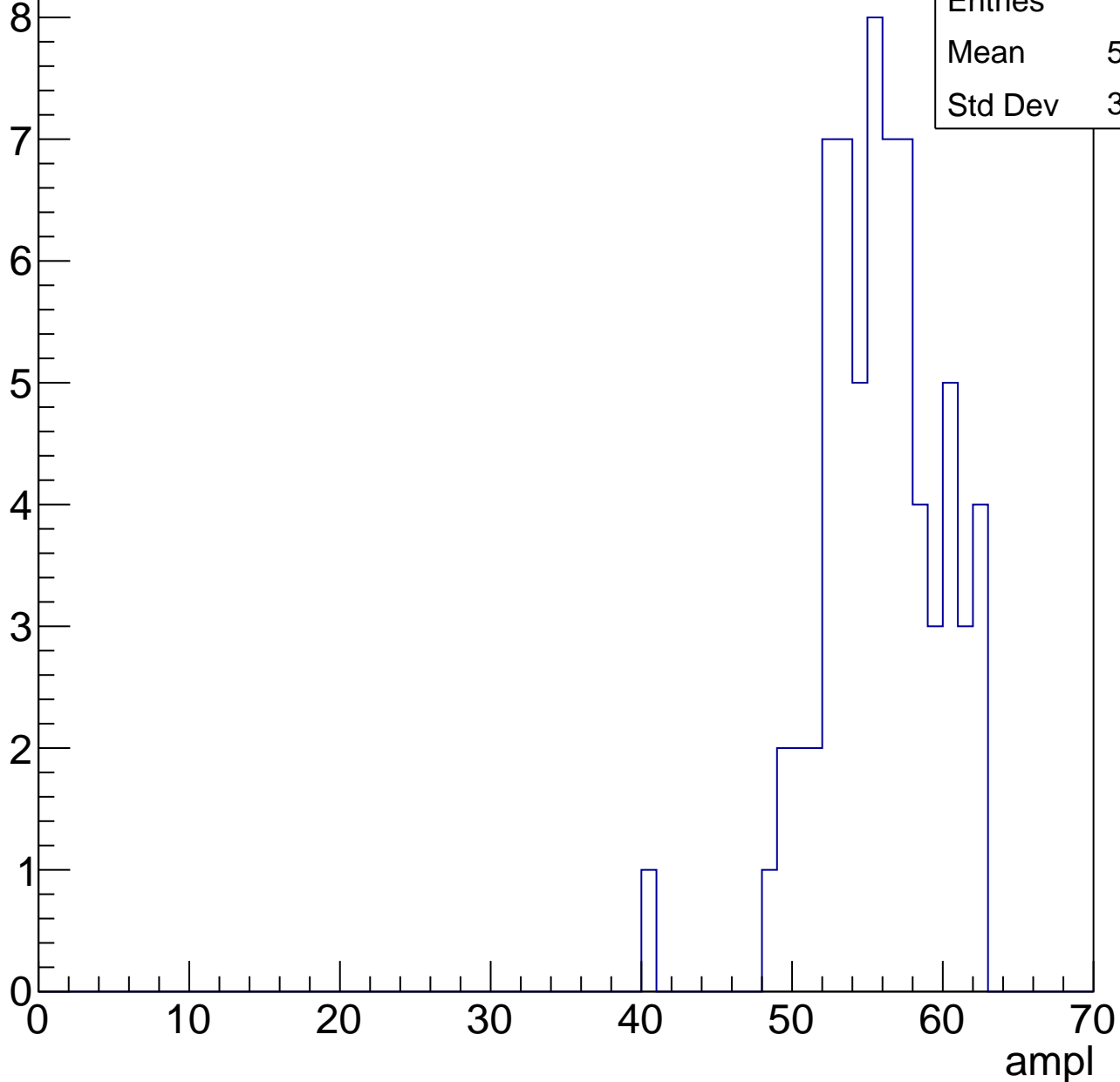


B1L102S, U22-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	55.35
Std Dev	3.955

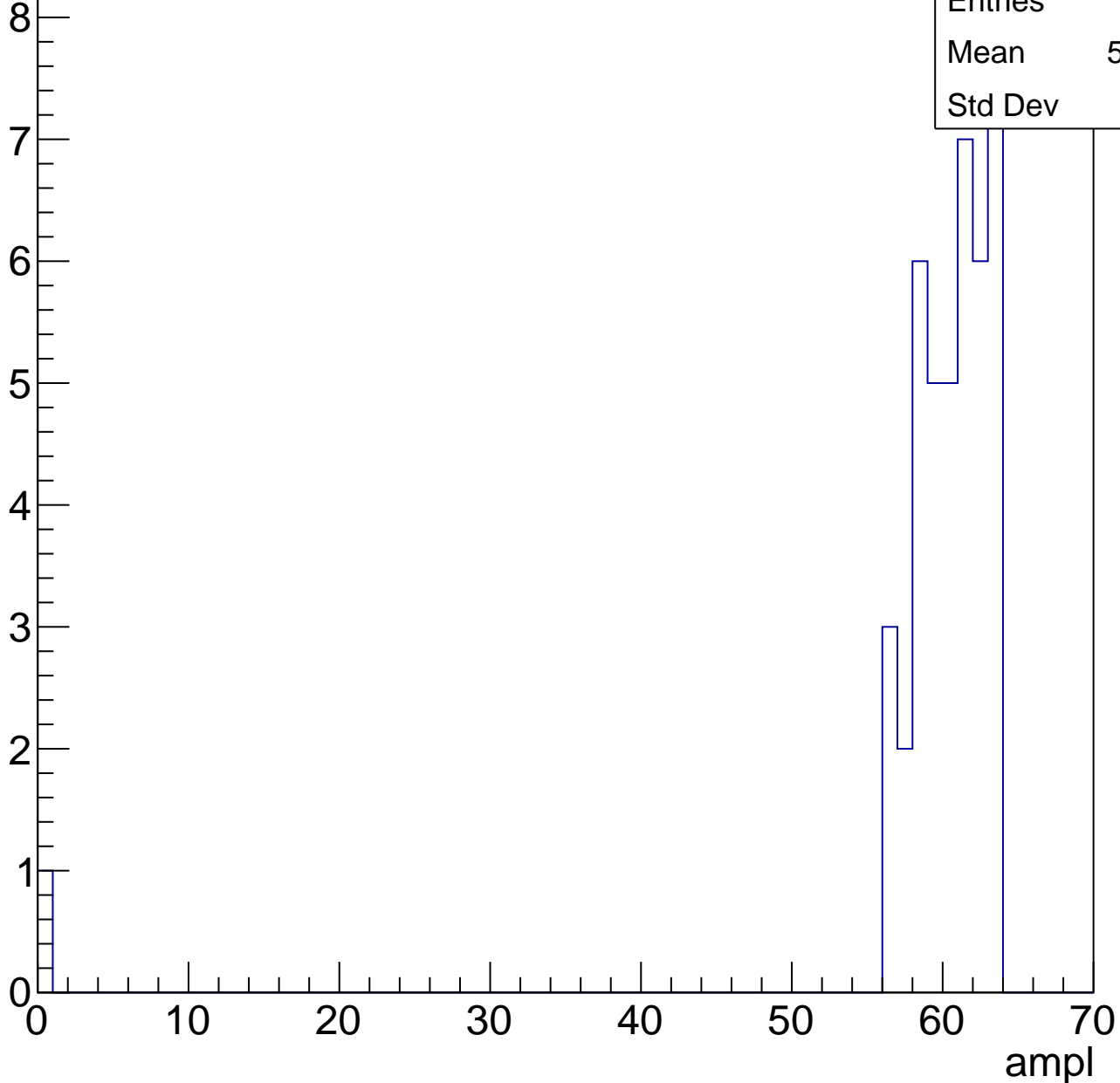


B1L102S, U22-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.79
Std Dev	9.32



B1L102S, U22-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch117, adc0

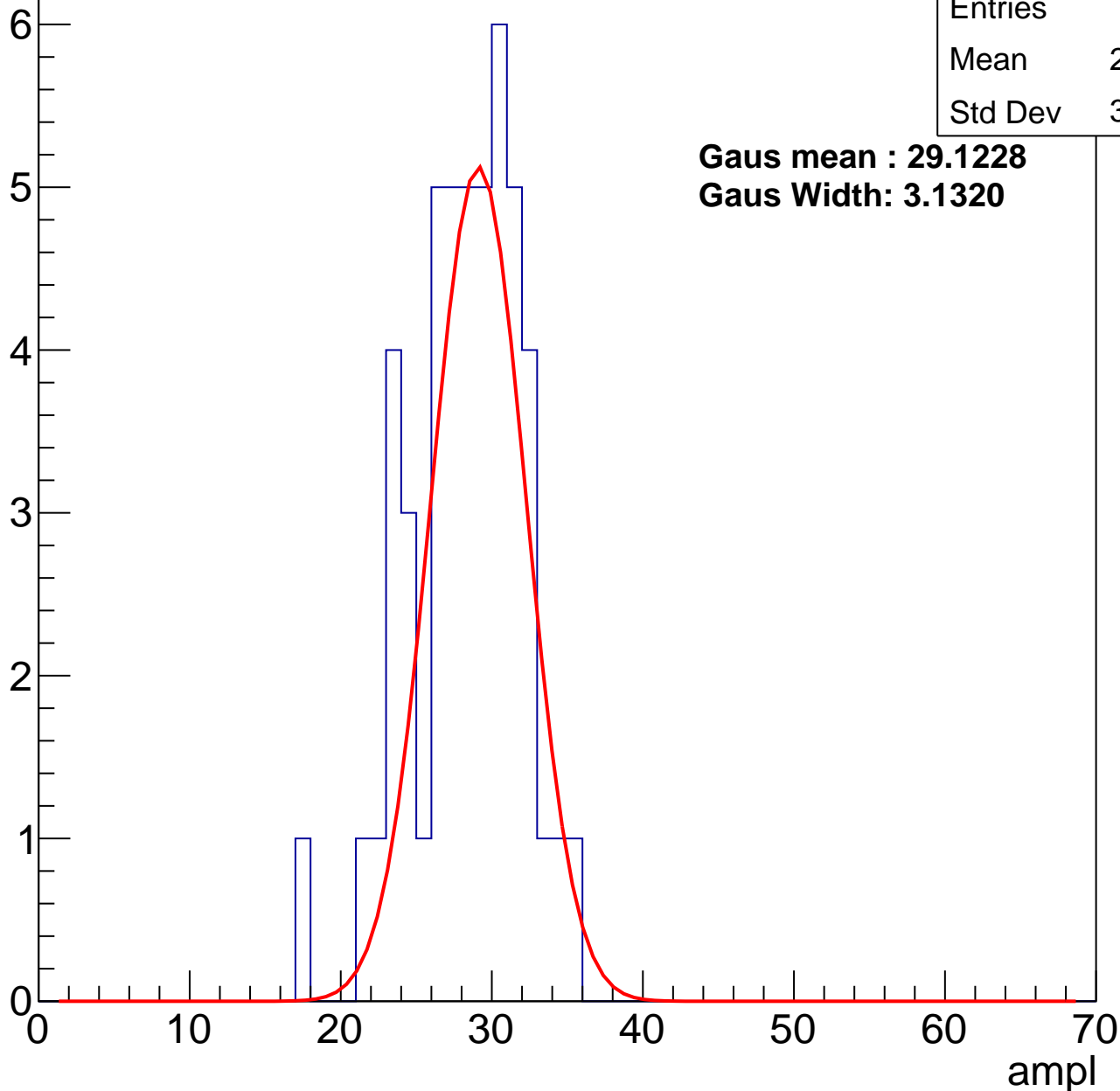
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	27.84
Std Dev	3.599

Gaus mean : 29.1228

Gaus Width: 3.1320



B1L102S, U22-ch117, adc1

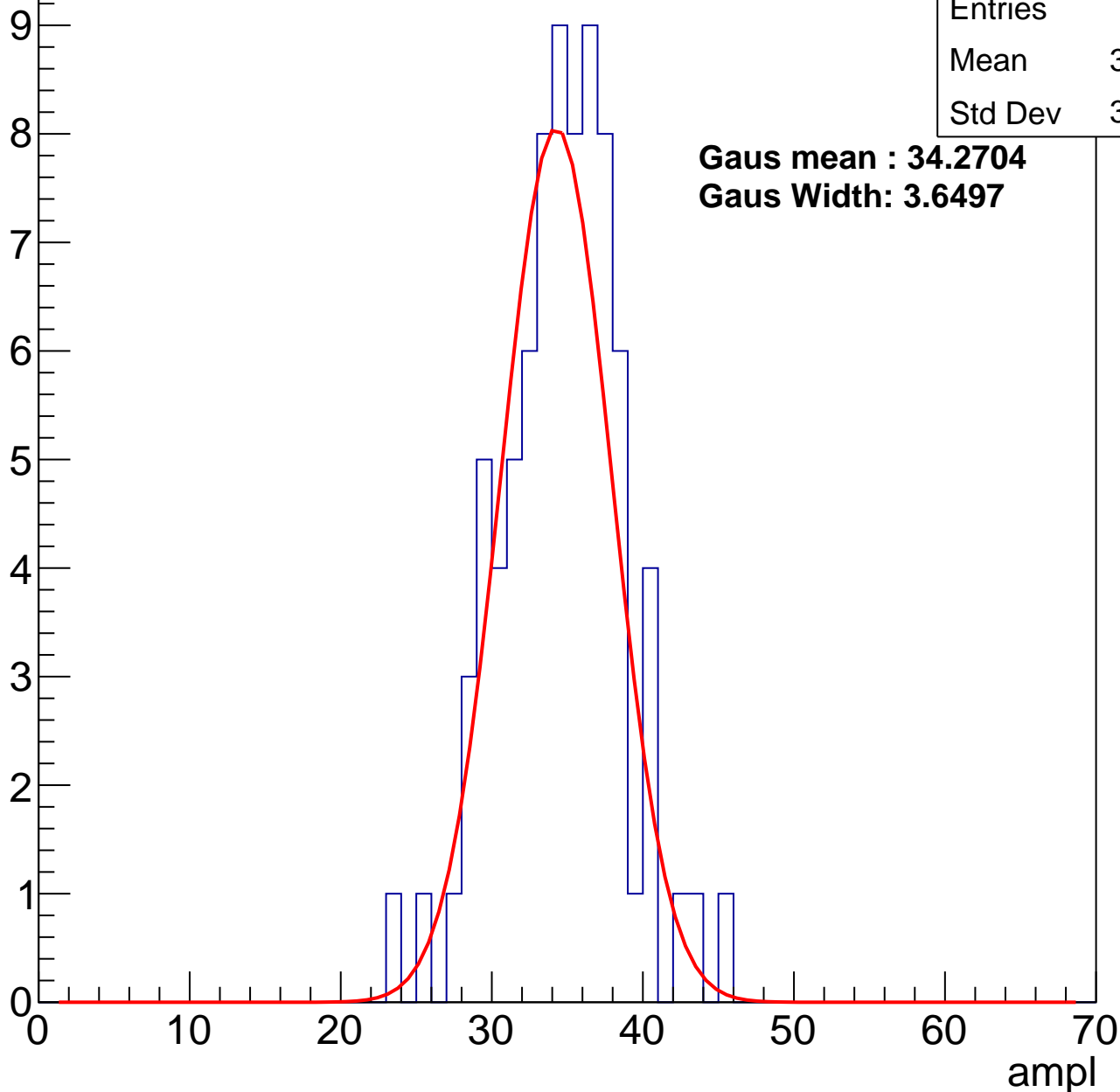
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	34.12
Std Dev	3.937

Gaus mean : 34.2704

Gaus Width: 3.6497



B1L102S, U22-ch117, adc2

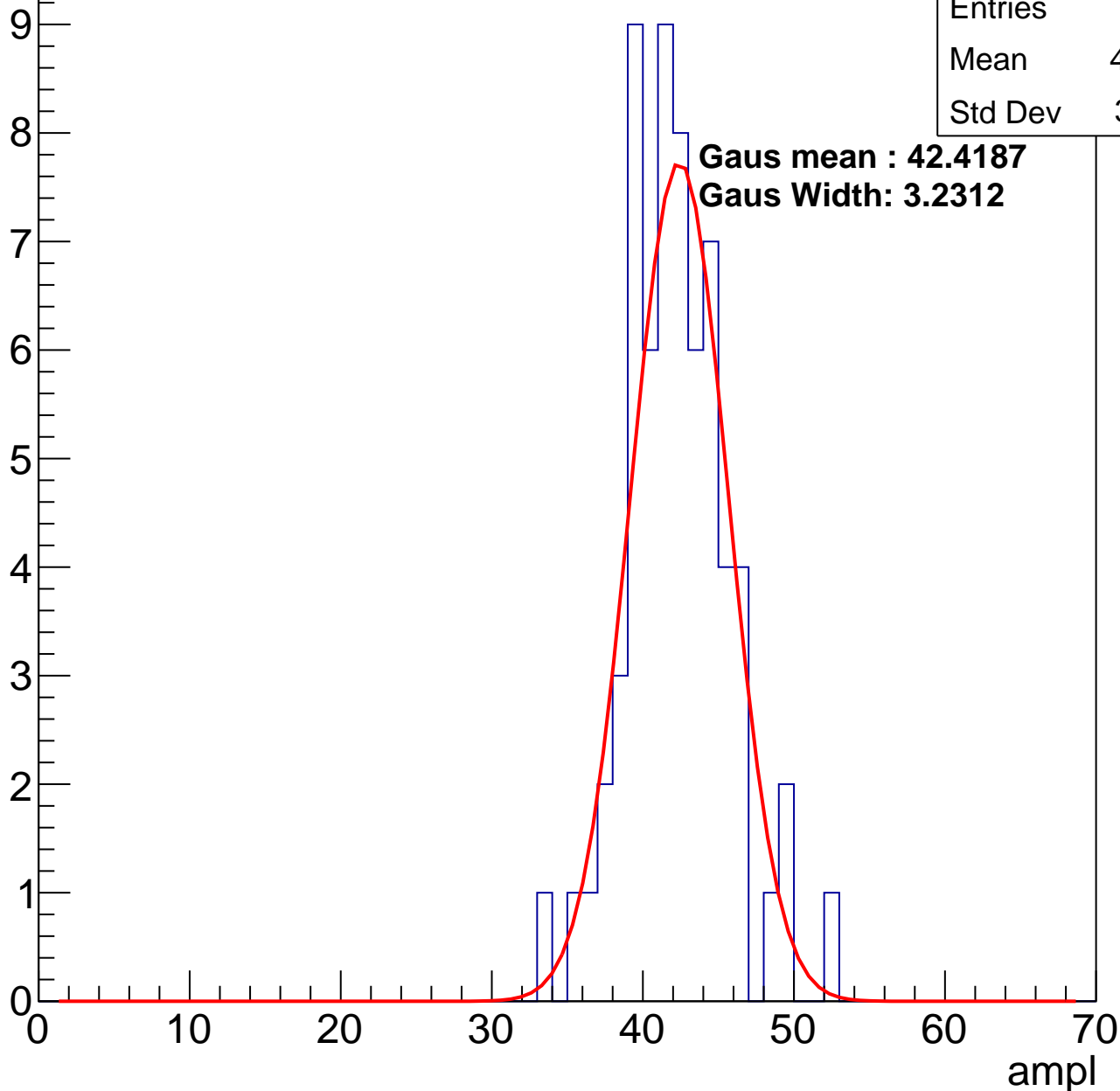
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.78
Std Dev	3.381

Gaus mean : 42.4187

Gaus Width: 3.2312

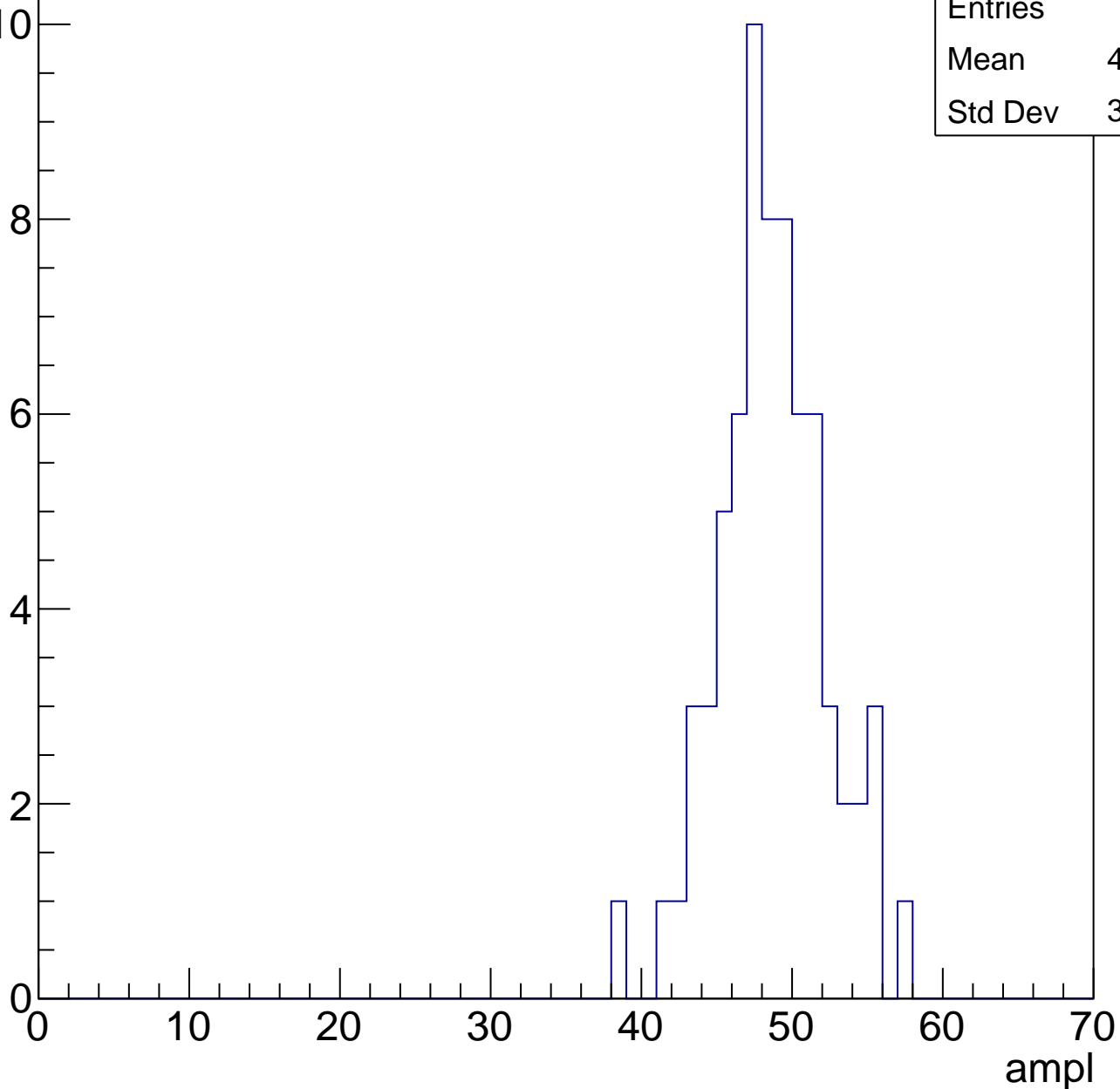


B1L102S, U22-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.22
Std Dev	3.546

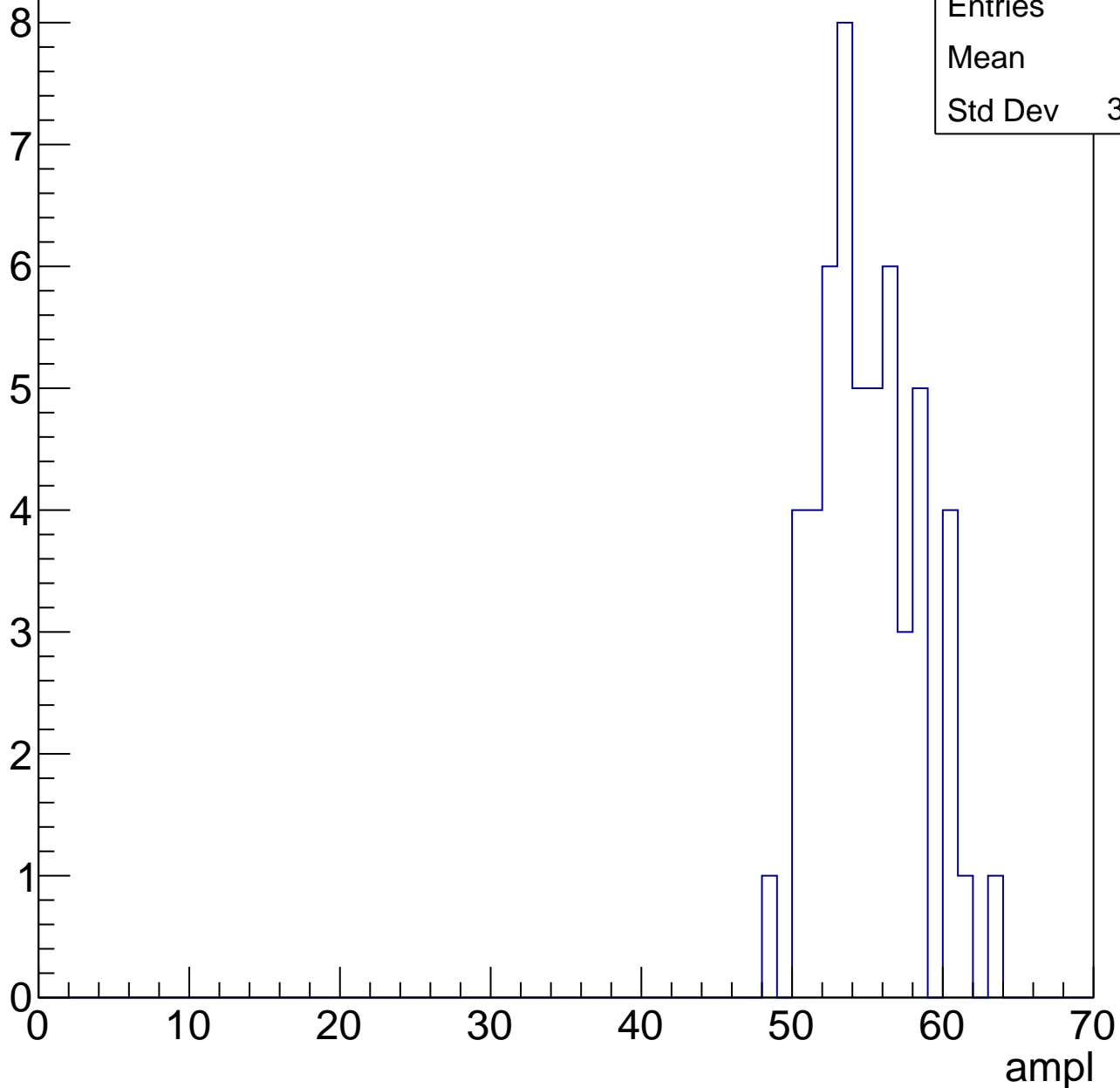


B1L102S, U22-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.6
Std Dev	3.247

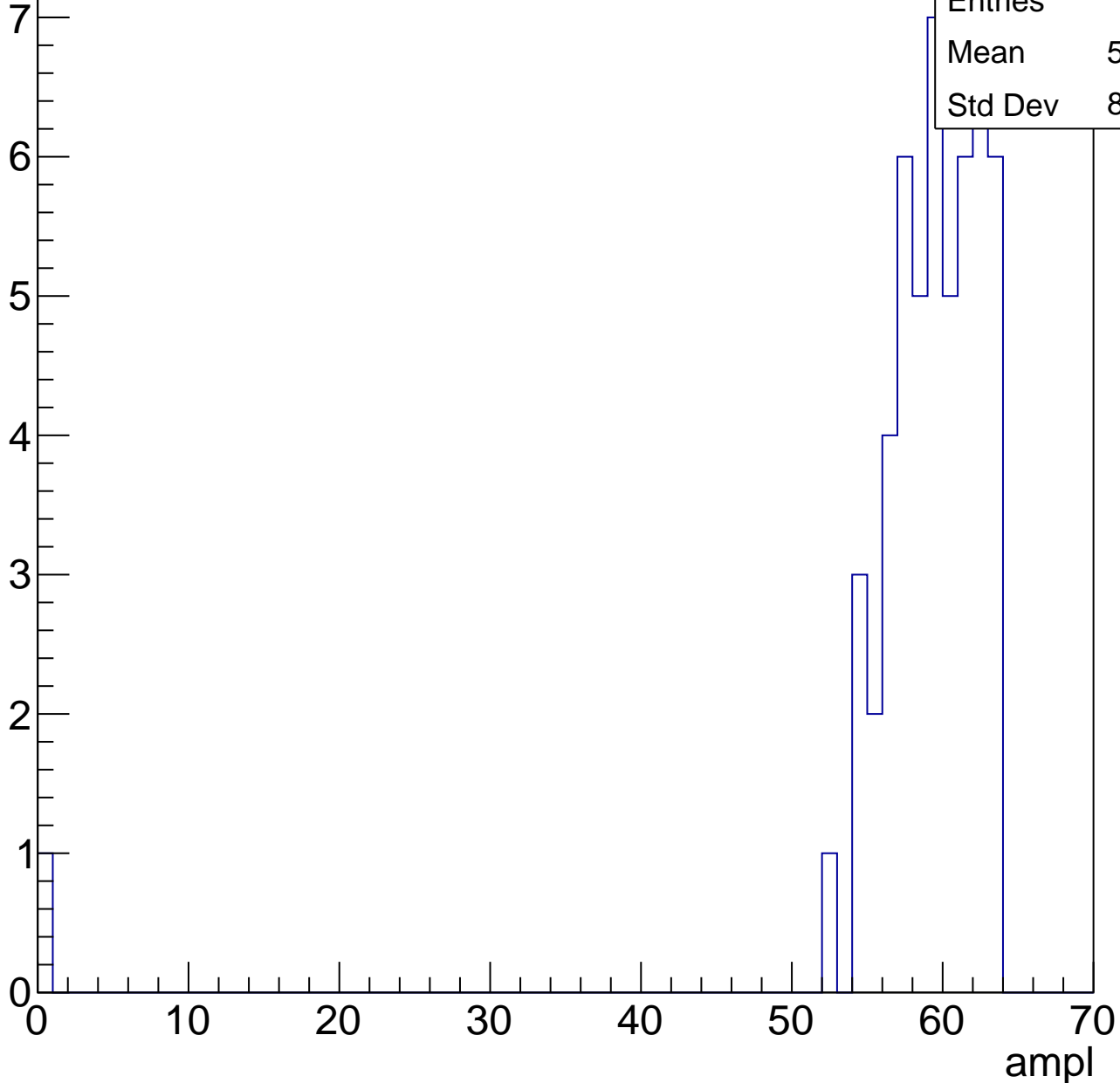


B1L102S, U22-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.94
Std Dev	8.502



B1L102S, U22-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

0

10

20

30

40

50

60

70

ampl

Entries	13
Mean	61.15
Std Dev	1.46

B1L102S, U22-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch118, adc0

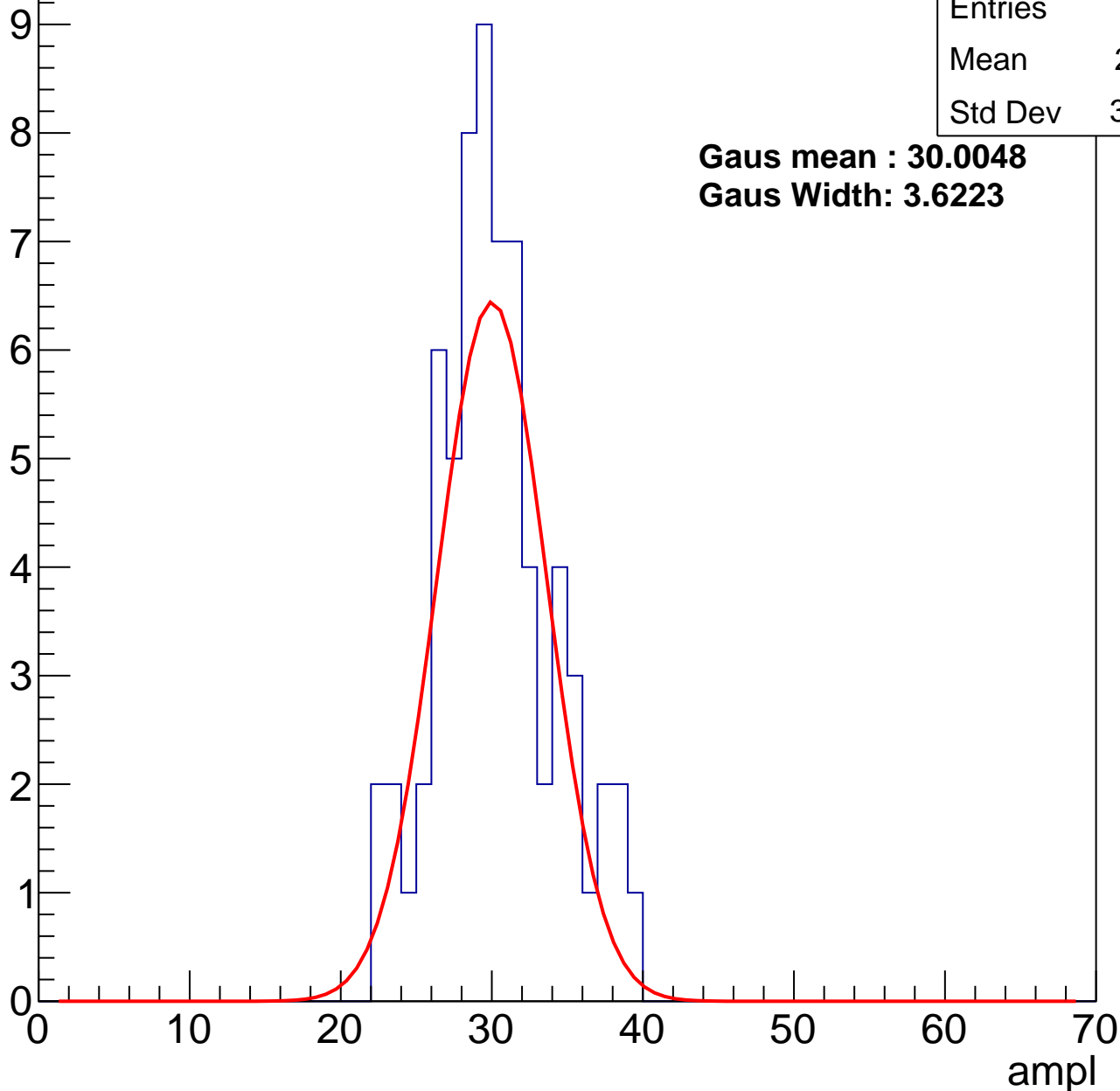
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.81
Std Dev	3.863

Gaus mean : 30.0048

Gaus Width: 3.6223



B1L102S, U22-ch118, adc1

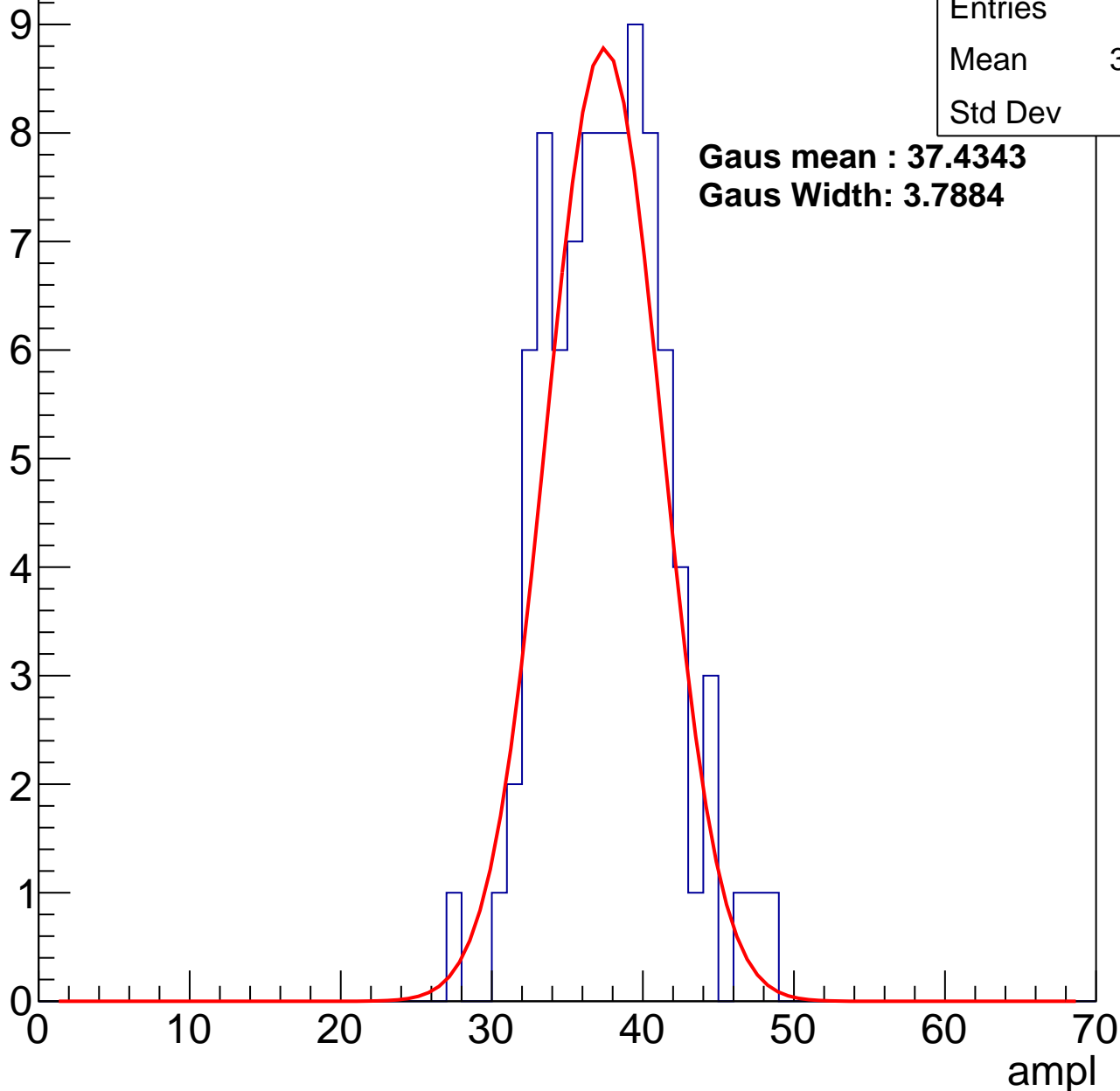
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	37.22
Std Dev	3.94

Gaus mean : 37.4343

Gaus Width: 3.7884



B1L102S, U22-ch118, adc2

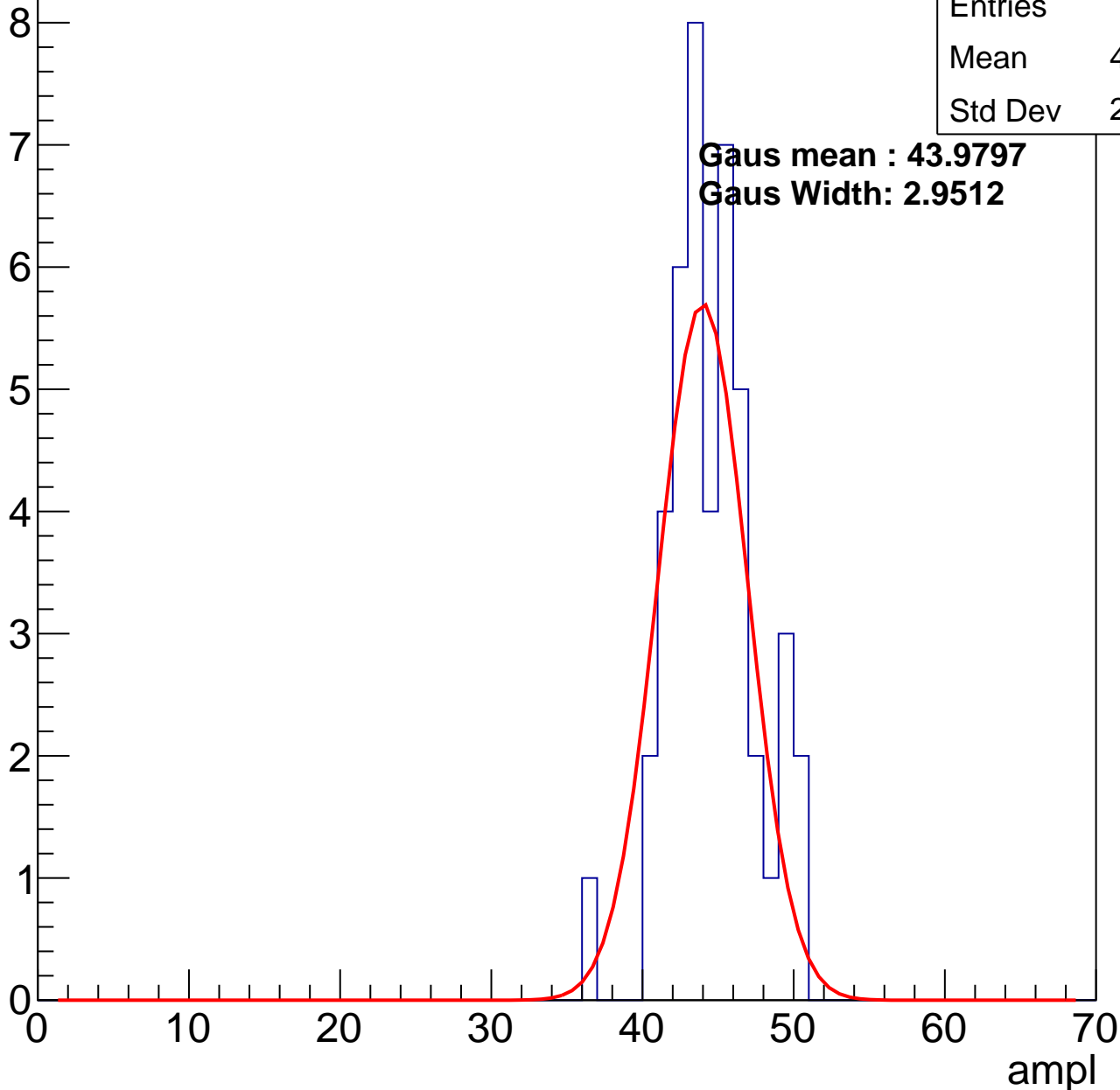
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	44.13
Std Dev	2.872

Gaus mean : 43.9797

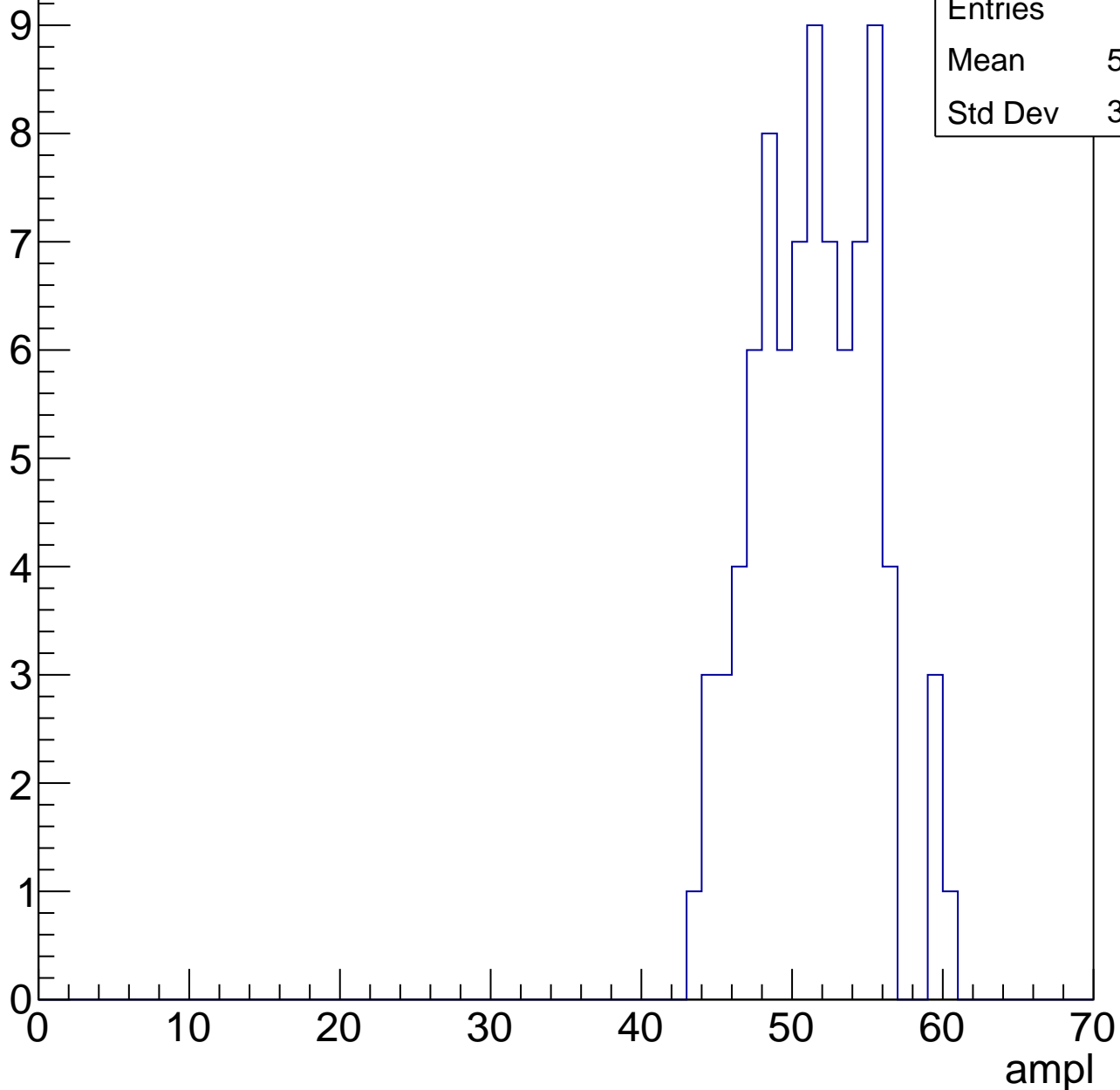
Gaus Width: 2.9512



B1L102S, U22-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

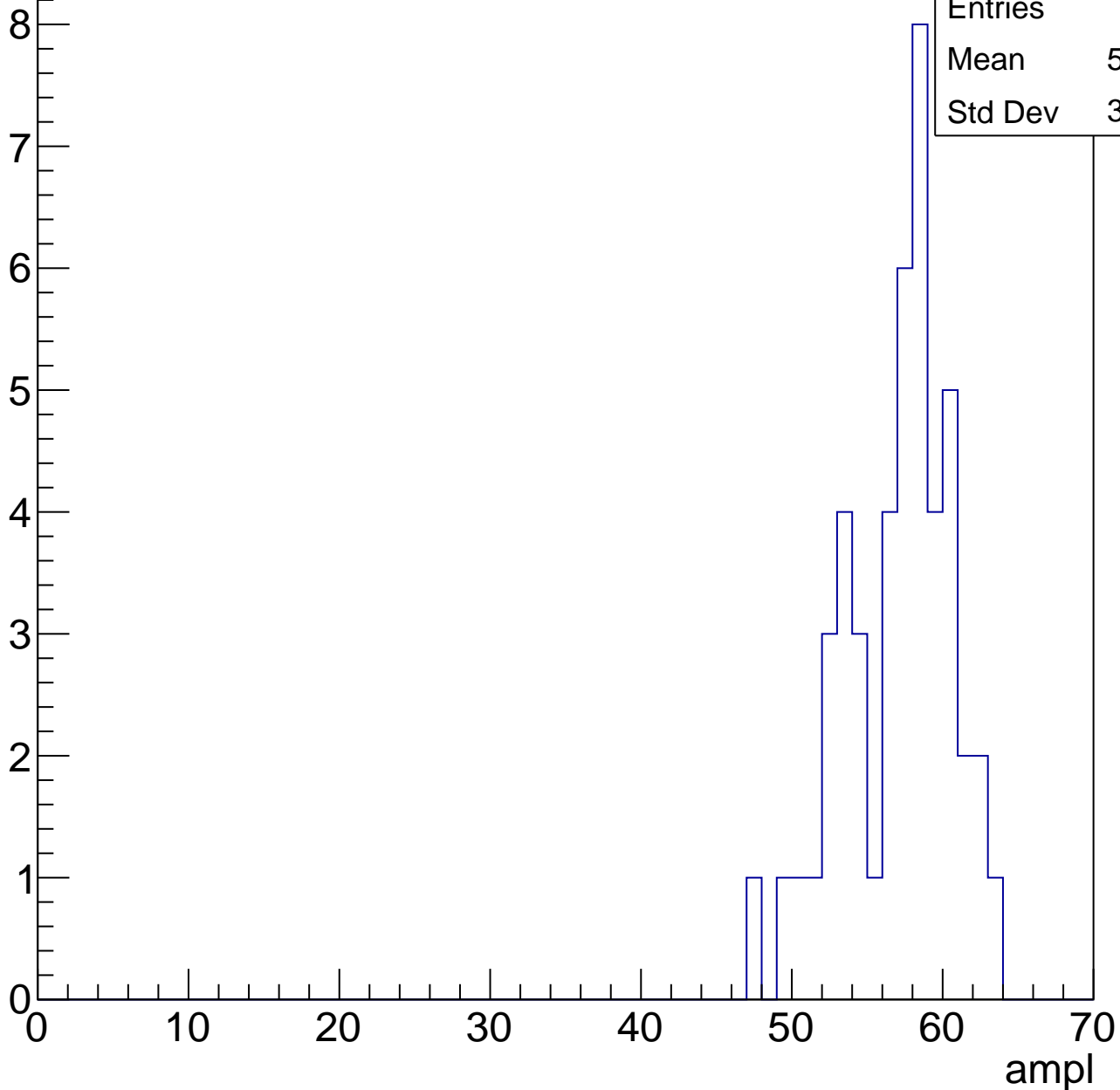


B1L102S, U22-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	56.53
Std Dev	3.572

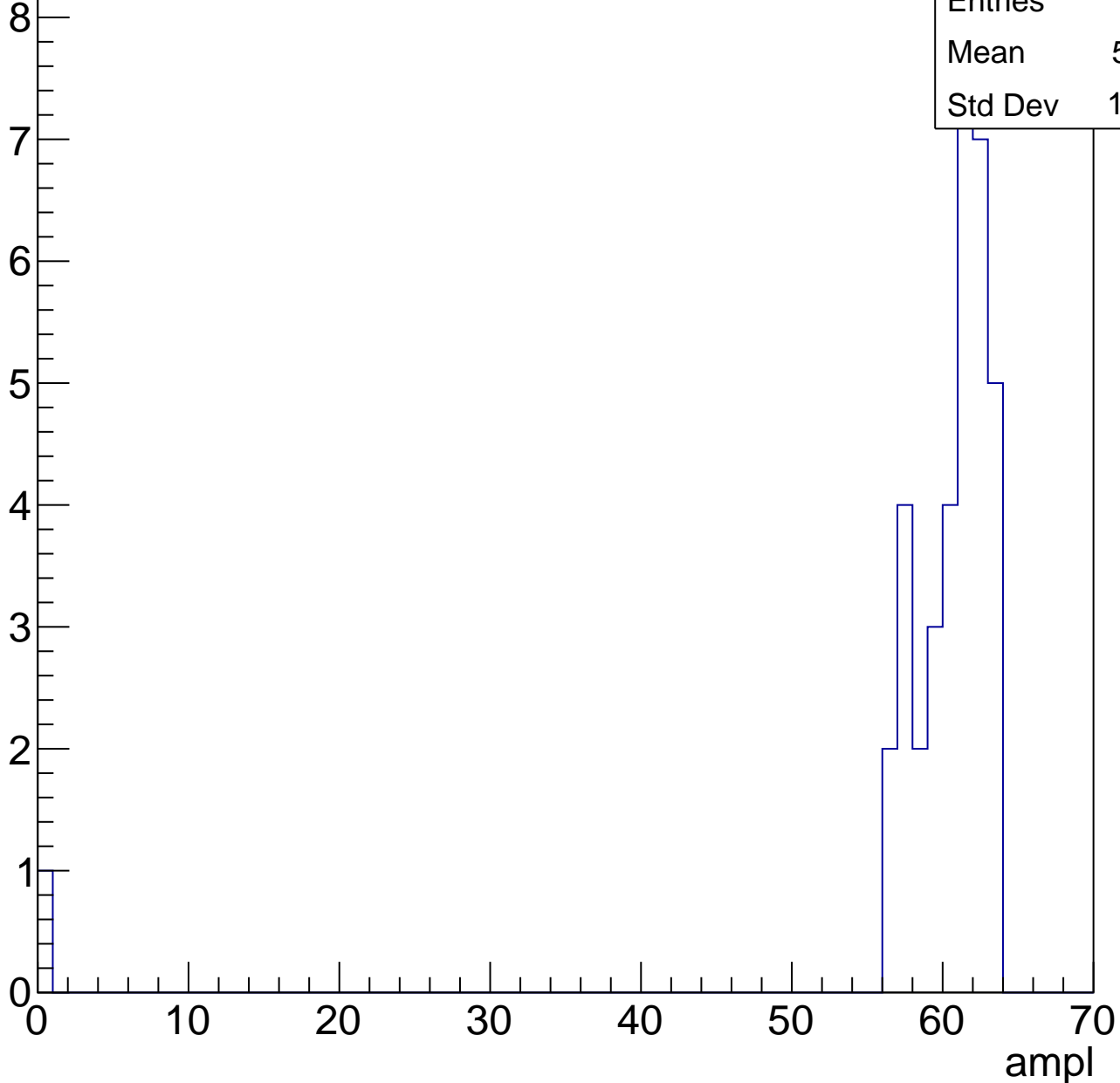


B1L102S, U22-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

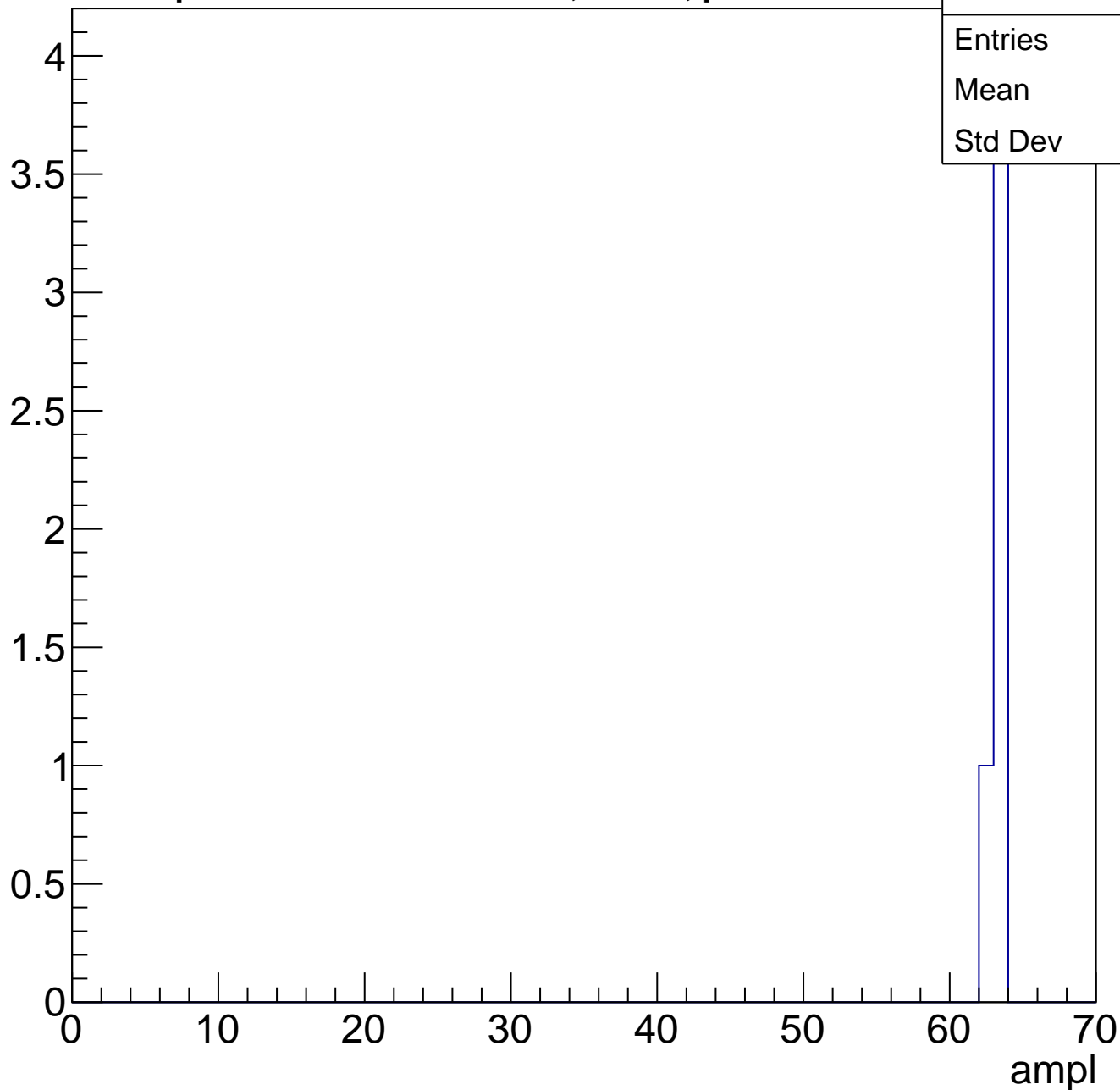
Entries	36
Mean	58.61
Std Dev	10.12



B1L102S, U22-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U22-ch119, adc0

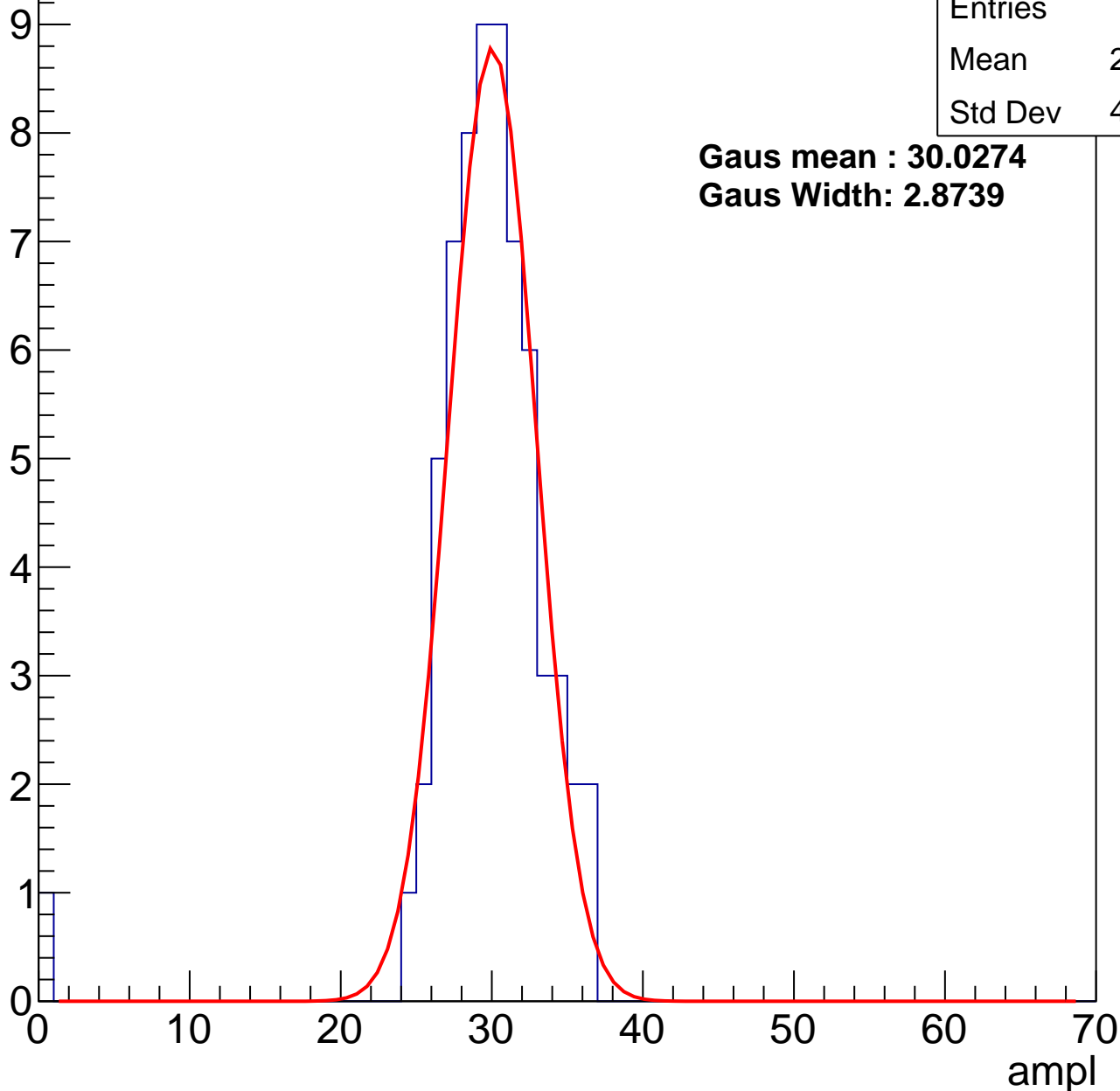
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.23
Std Dev	4.573

Gaus mean : 30.0274

Gaus Width: 2.8739



B1L102S, U22-ch119, adc1

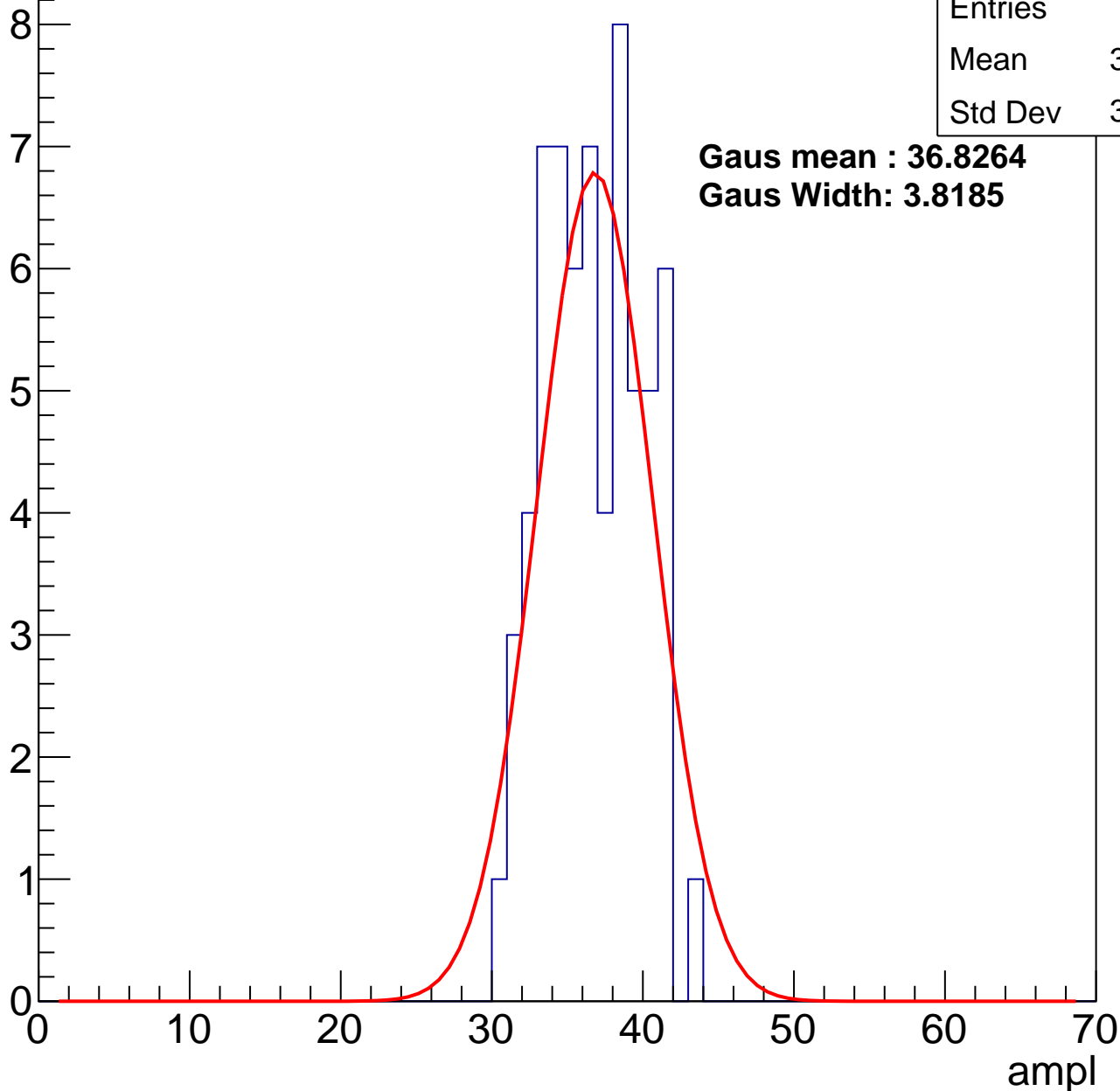
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.22
Std Dev	3.135

Gaus mean : 36.8264

Gaus Width: 3.8185



B1L102S, U22-ch119, adc2

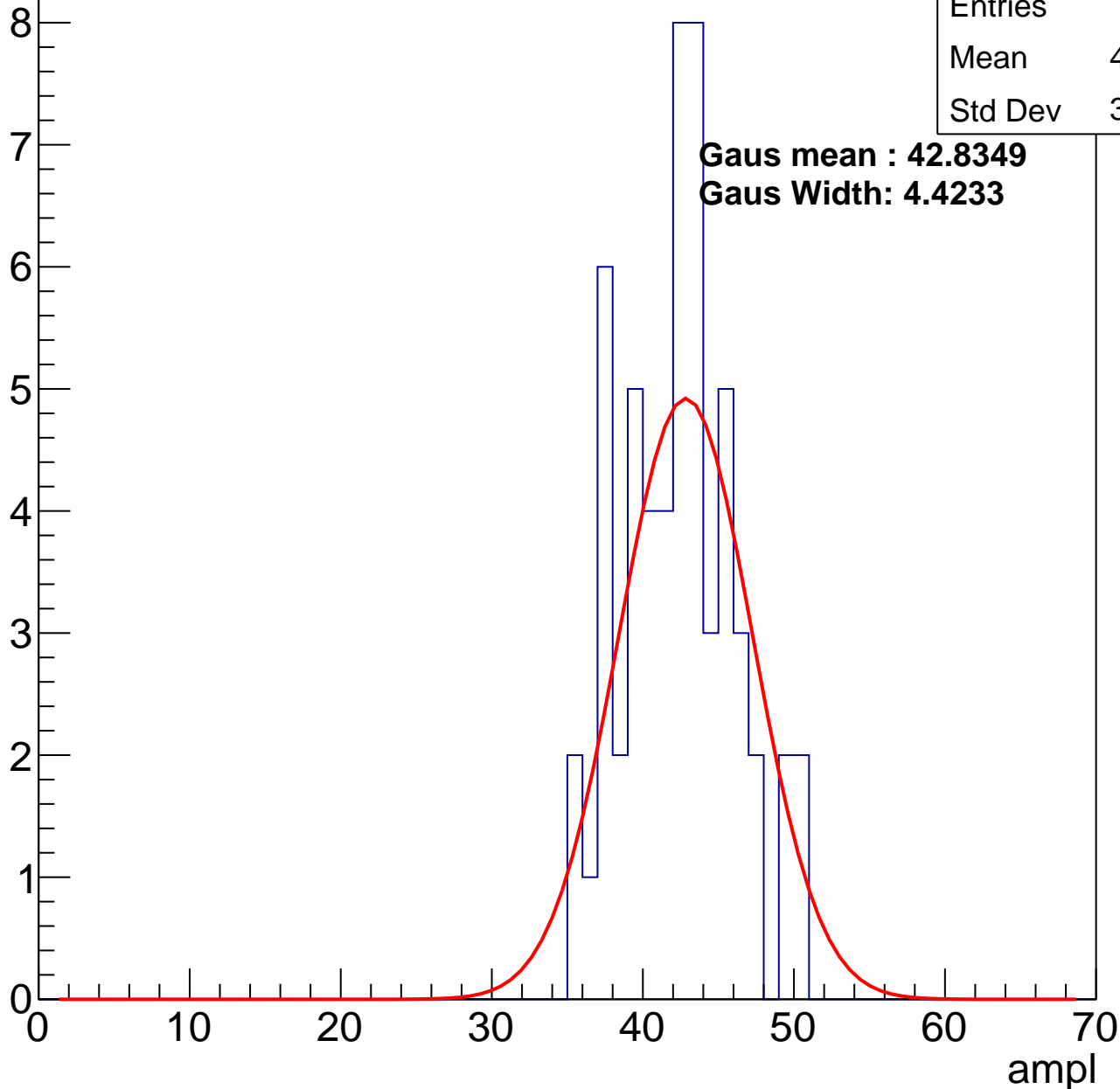
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.93
Std Dev	3.675

Gaus mean : 42.8349

Gaus Width: 4.4233

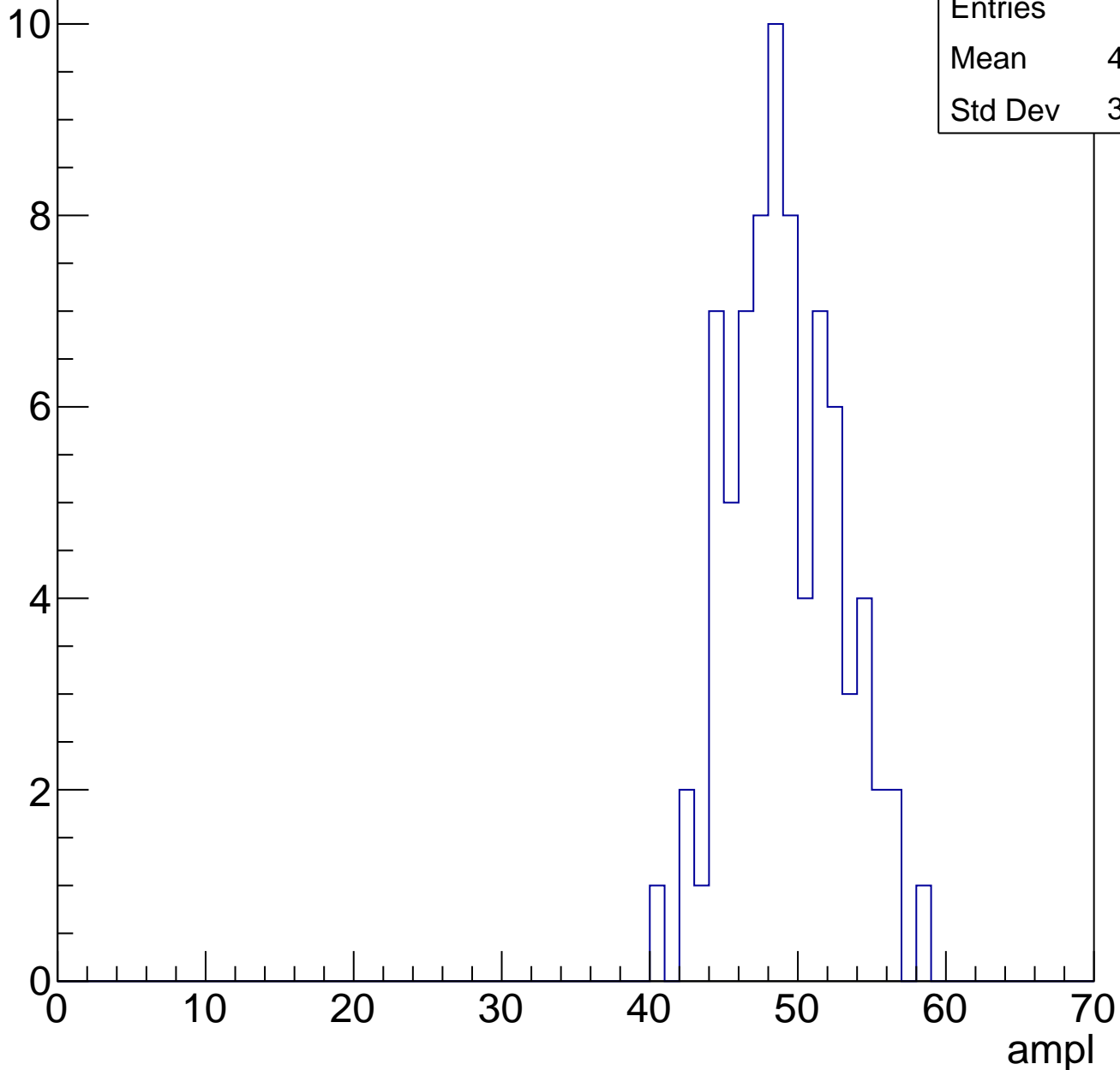


B1L102S, U22-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	48.64
Std Dev	3.669

Entry

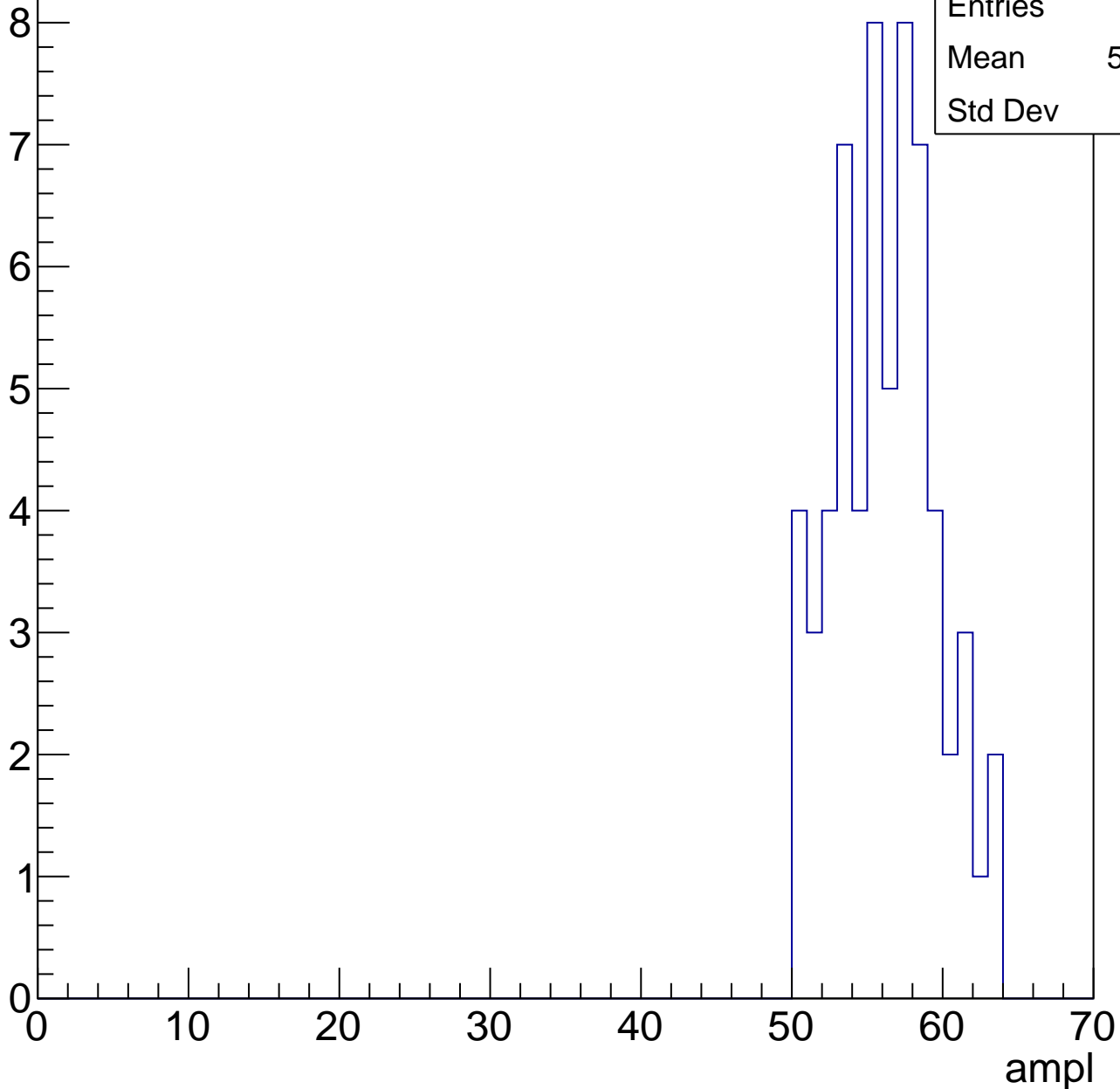


B1L102S, U22-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.76
Std Dev	3.31

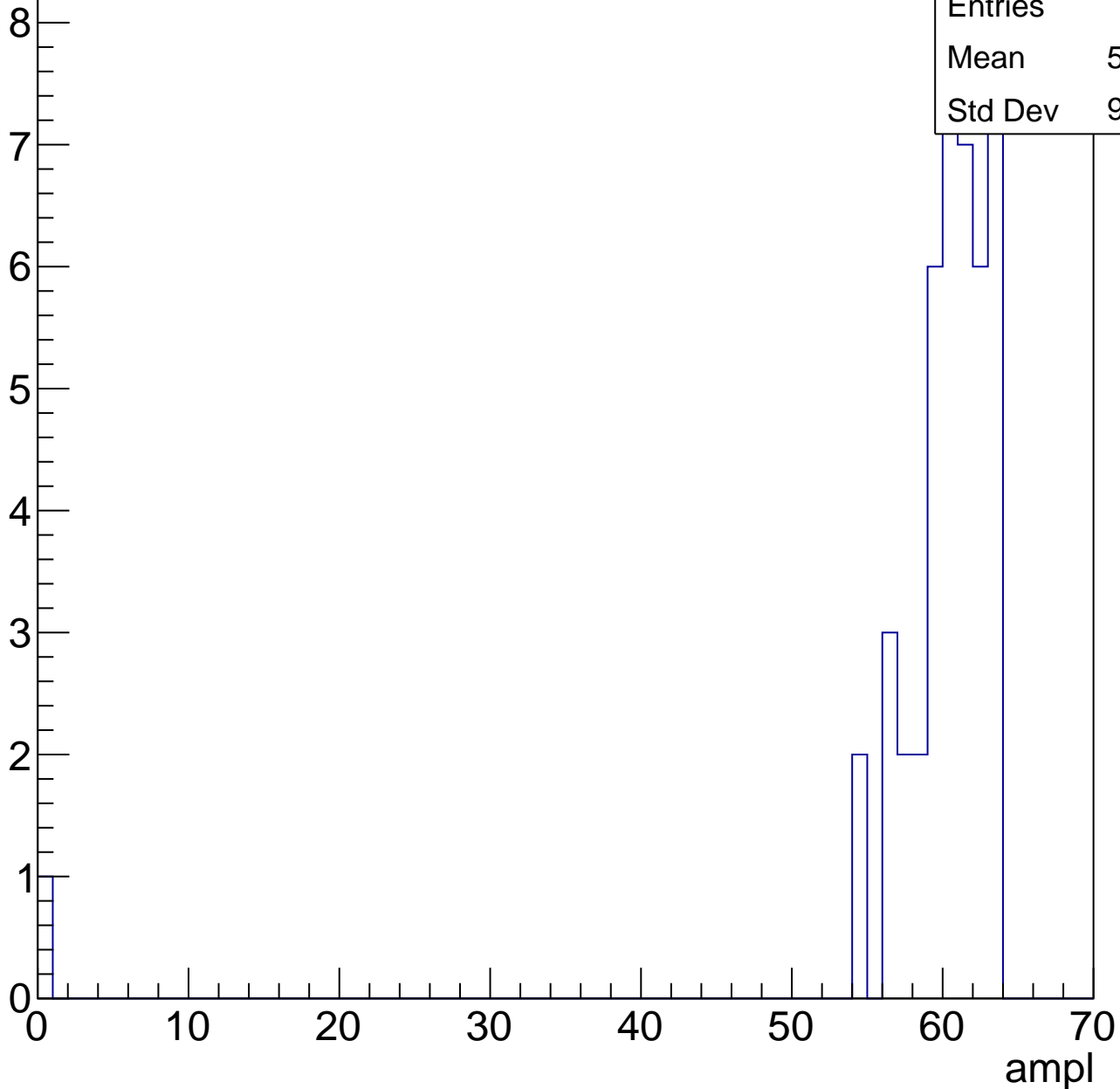


B1L102S, U22-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.73
Std Dev	9.169



B1L102S, U22-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L102S, U22-ch120, adc0

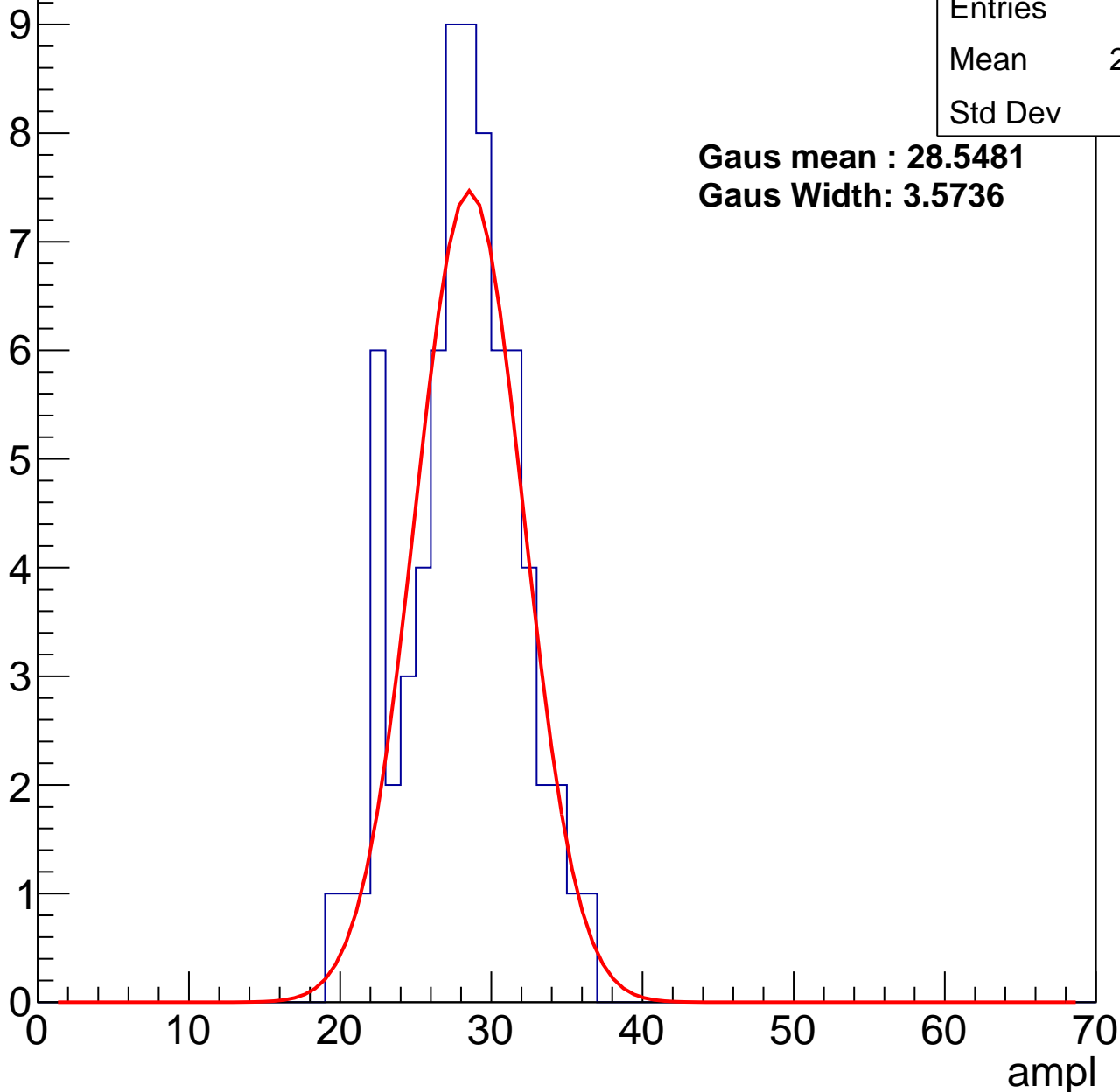
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.67
Std Dev	3.64

Gaus mean : 28.5481

Gaus Width: 3.5736



B1L102S, U22-ch120, adc1

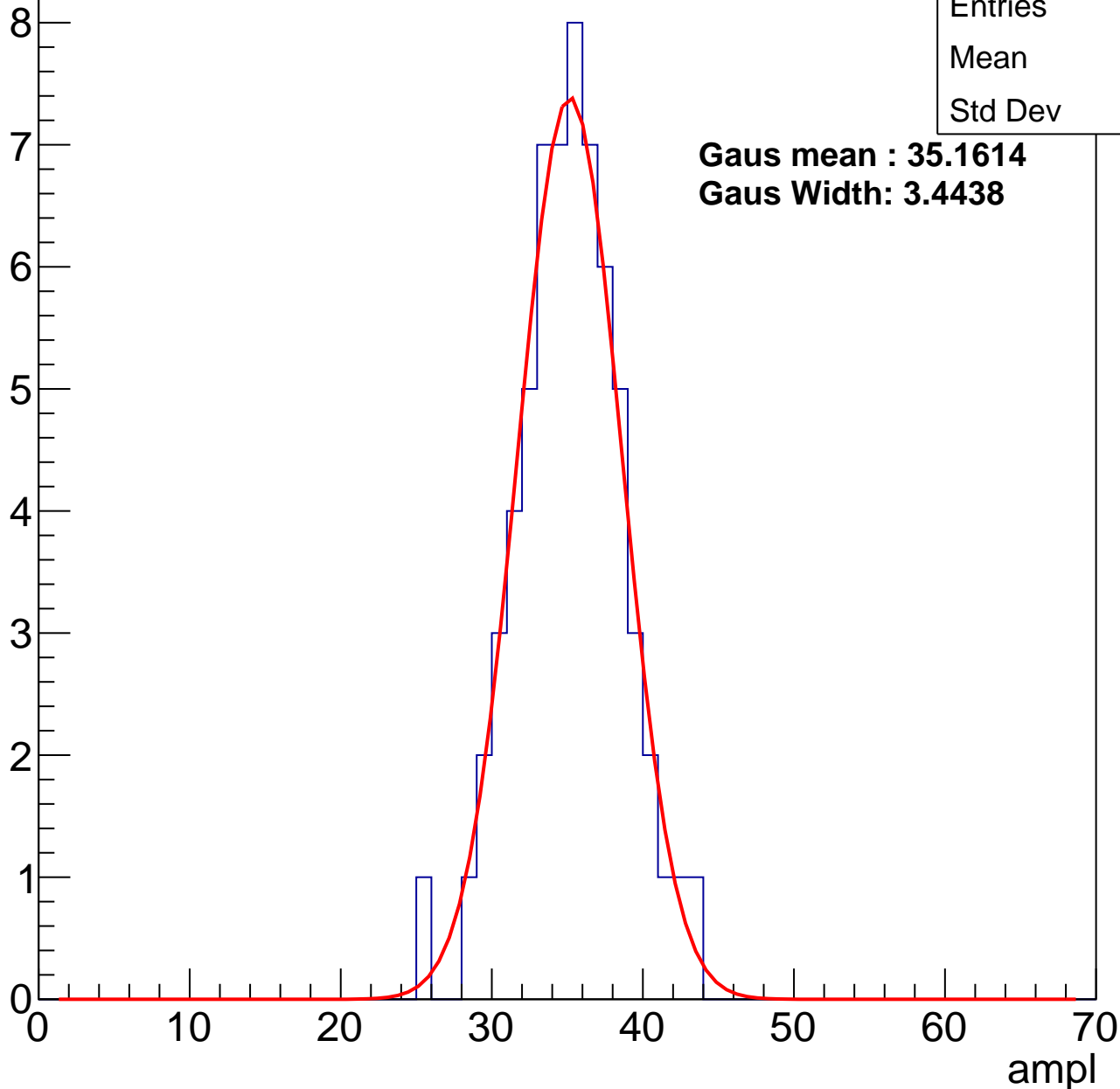
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	34.7
Std Dev	3.44

Gaus mean : 35.1614

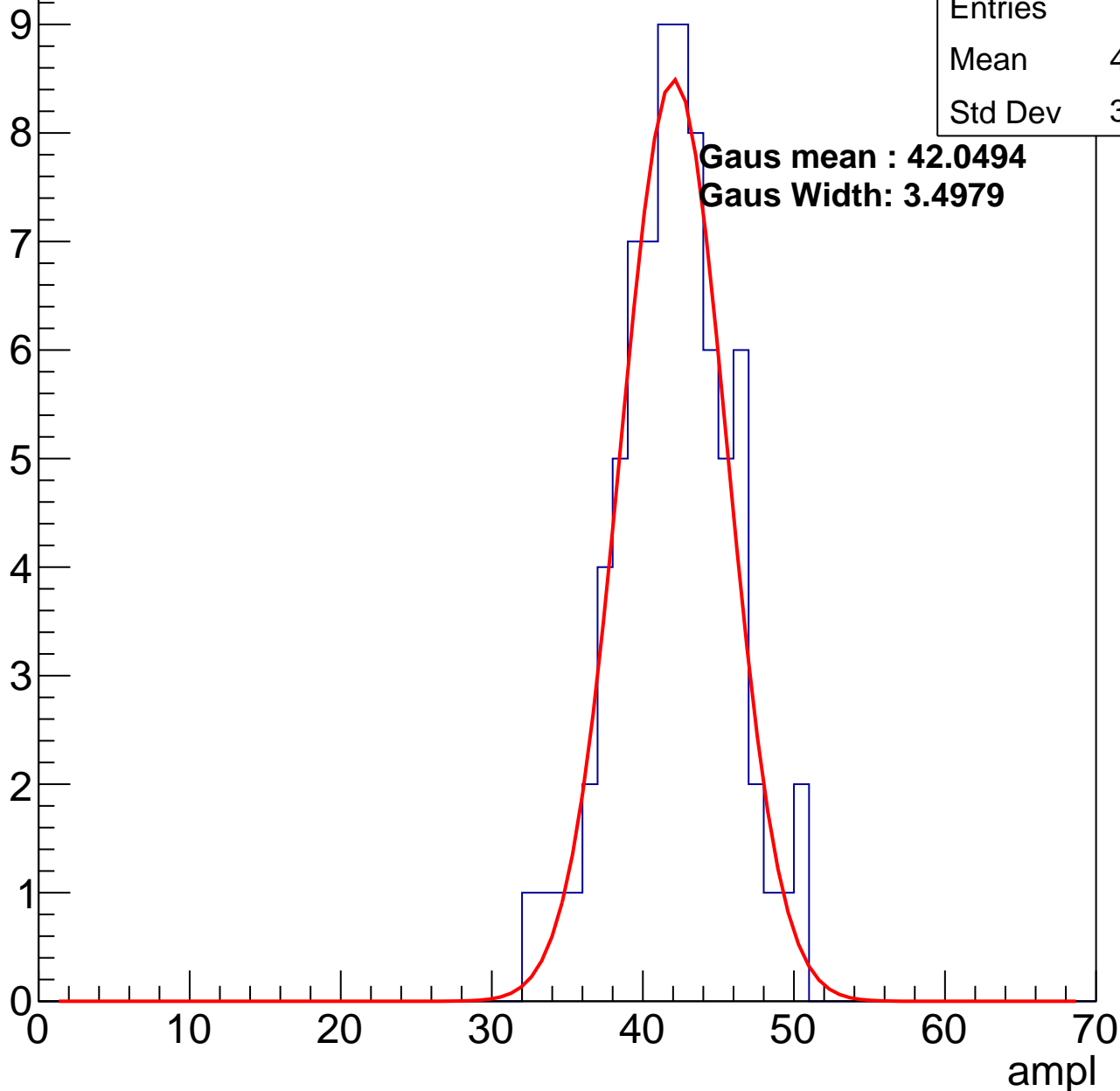
Gaus Width: 3.4438



B1L102S, U22-ch120, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

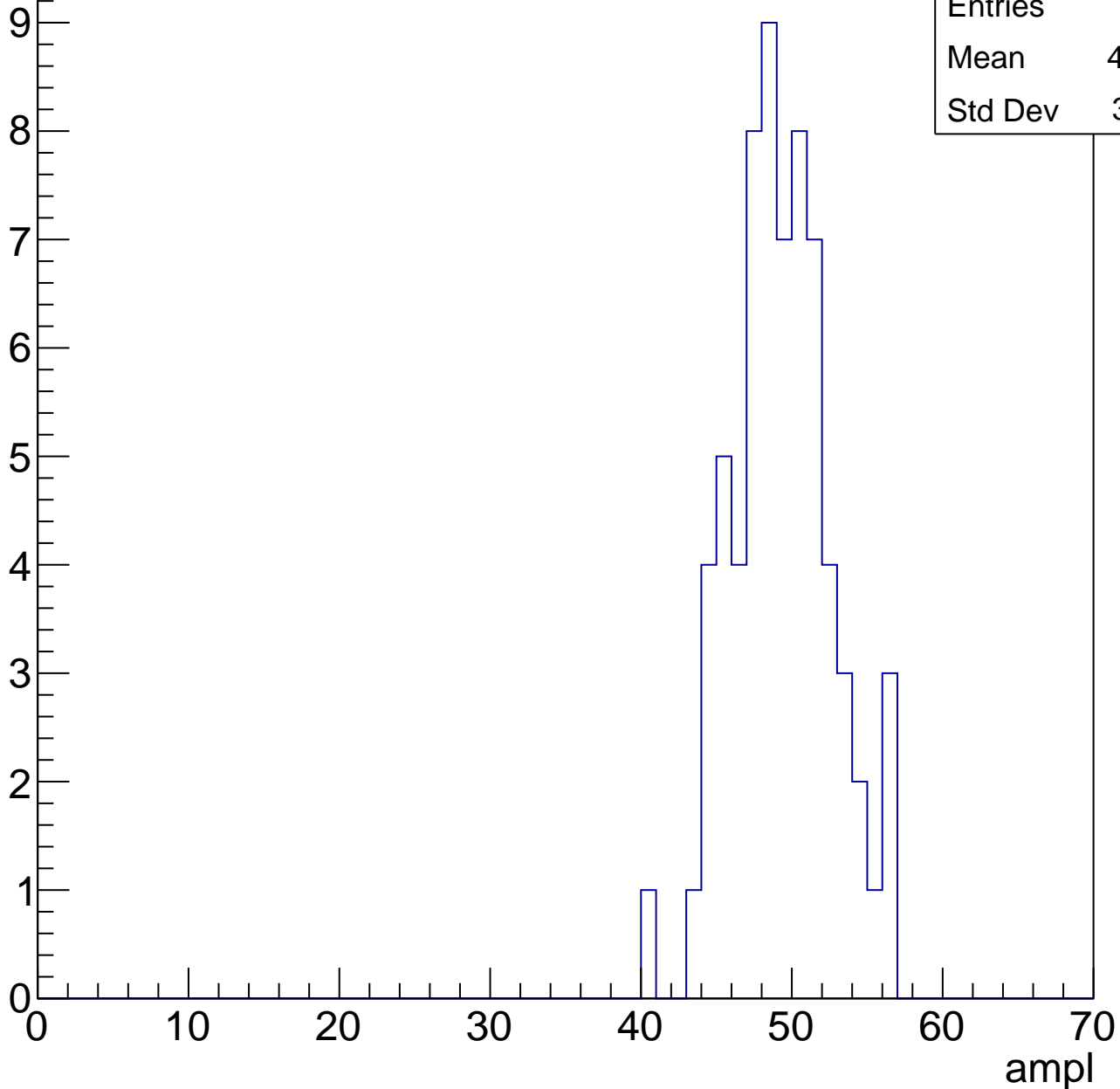


B1L102S, U22-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.87
Std Dev	3.291

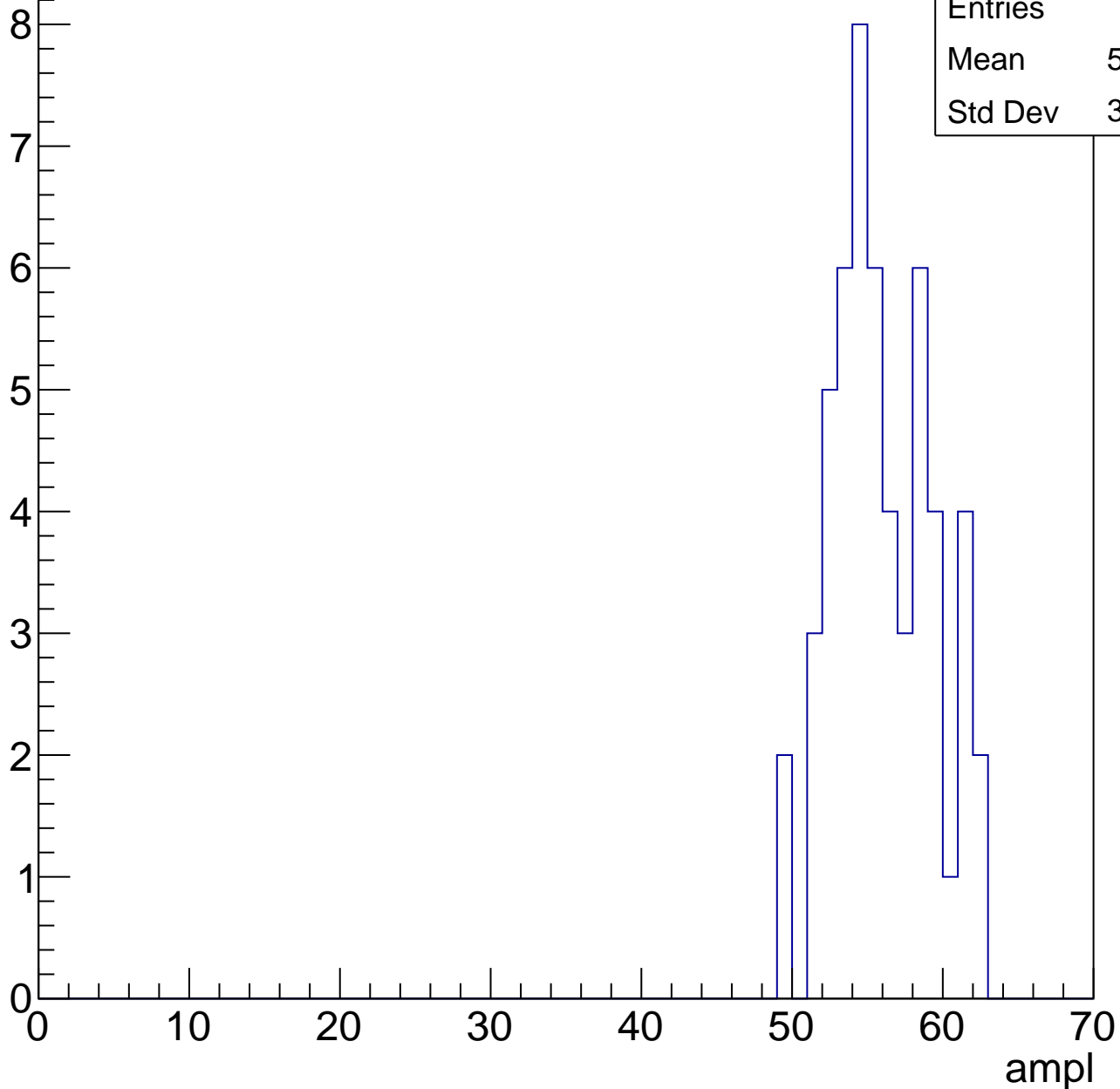


B1L102S, U22-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.52
Std Dev	3.287

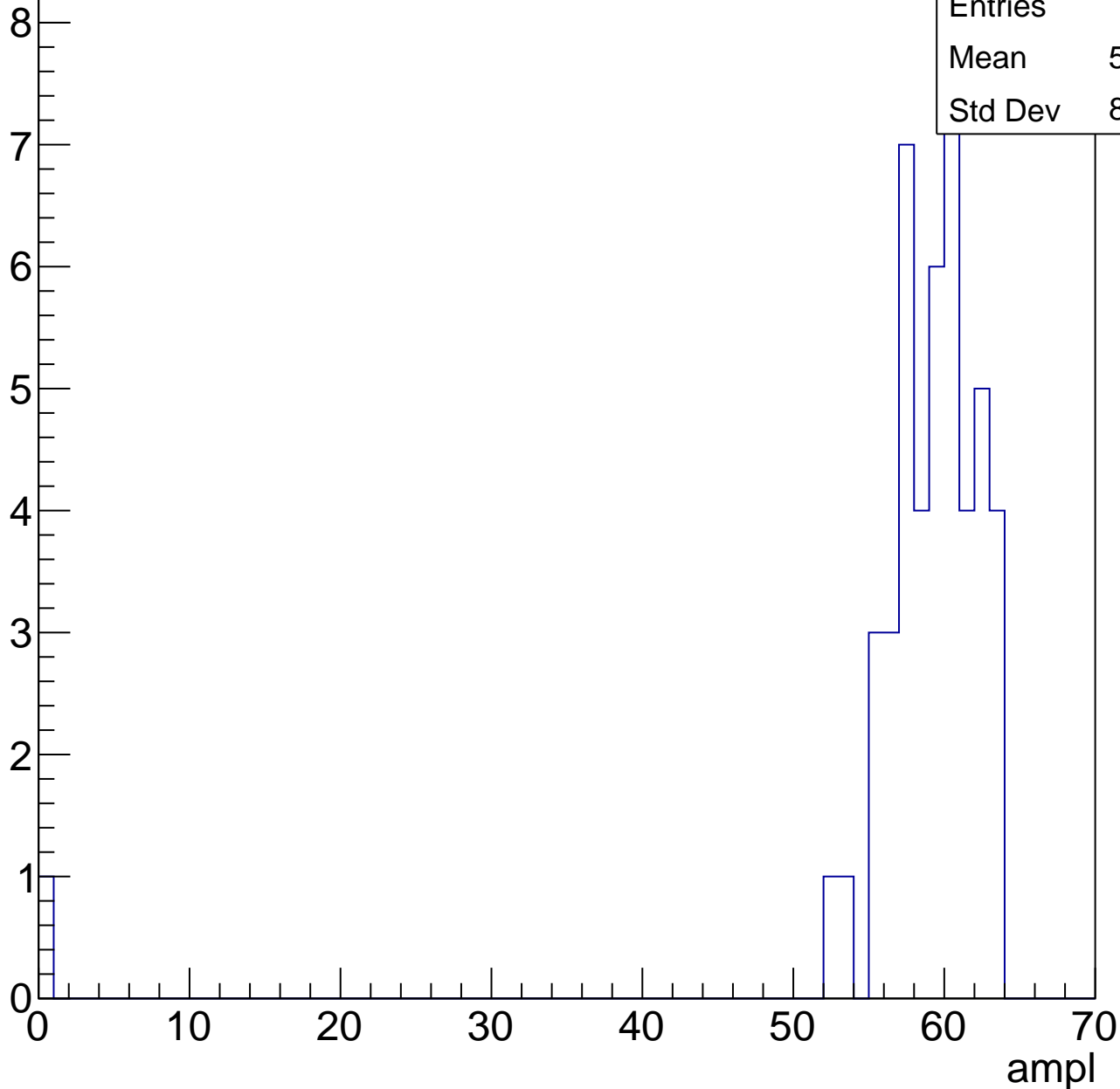


B1L102S, U22-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.64
Std Dev	8.895

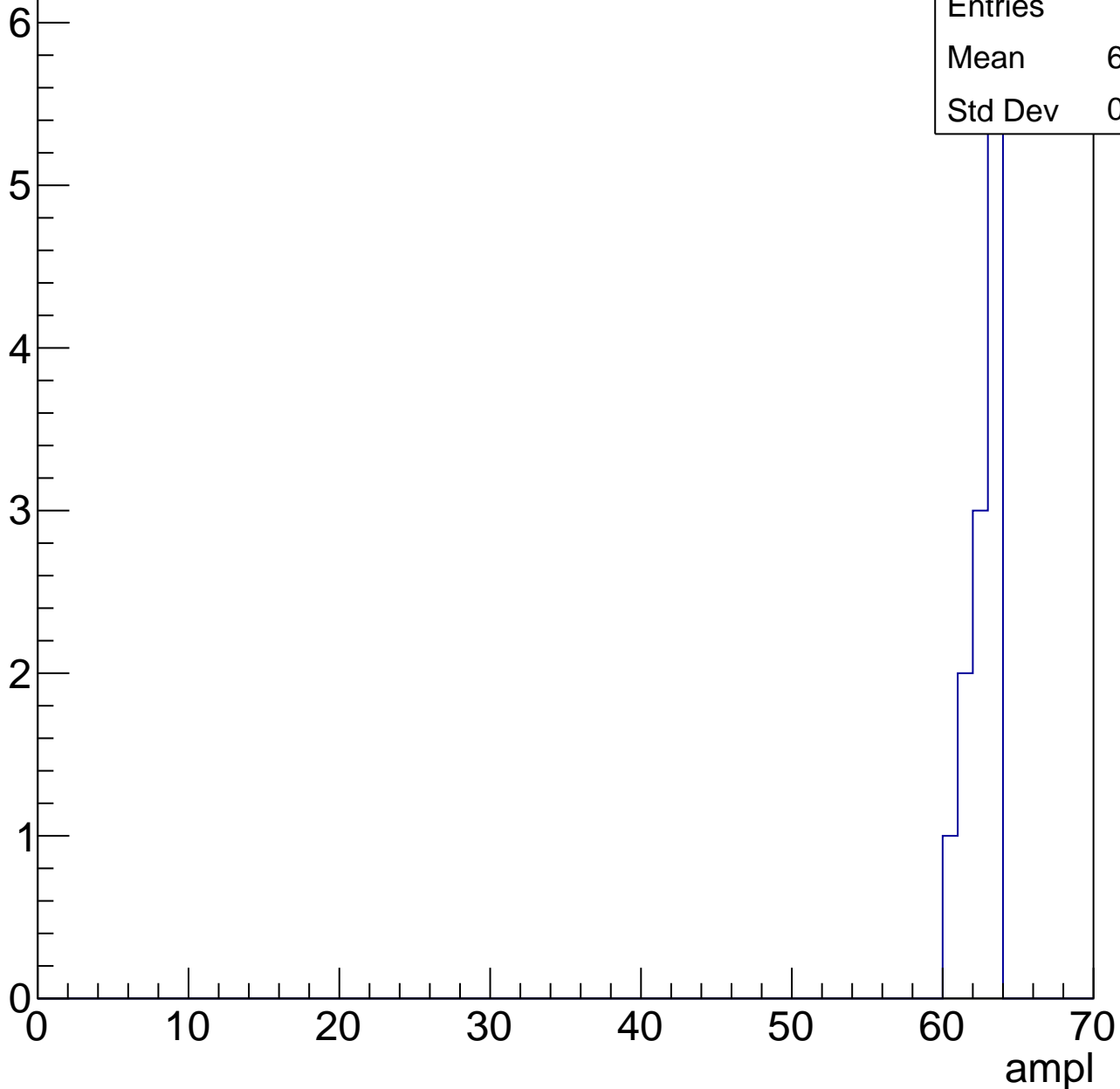


B1L102S, U22-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	62.17
Std Dev	0.986



B1L102S, U22-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch121, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	28.44
Std Dev	3.586

Gaus mean : 28.7567

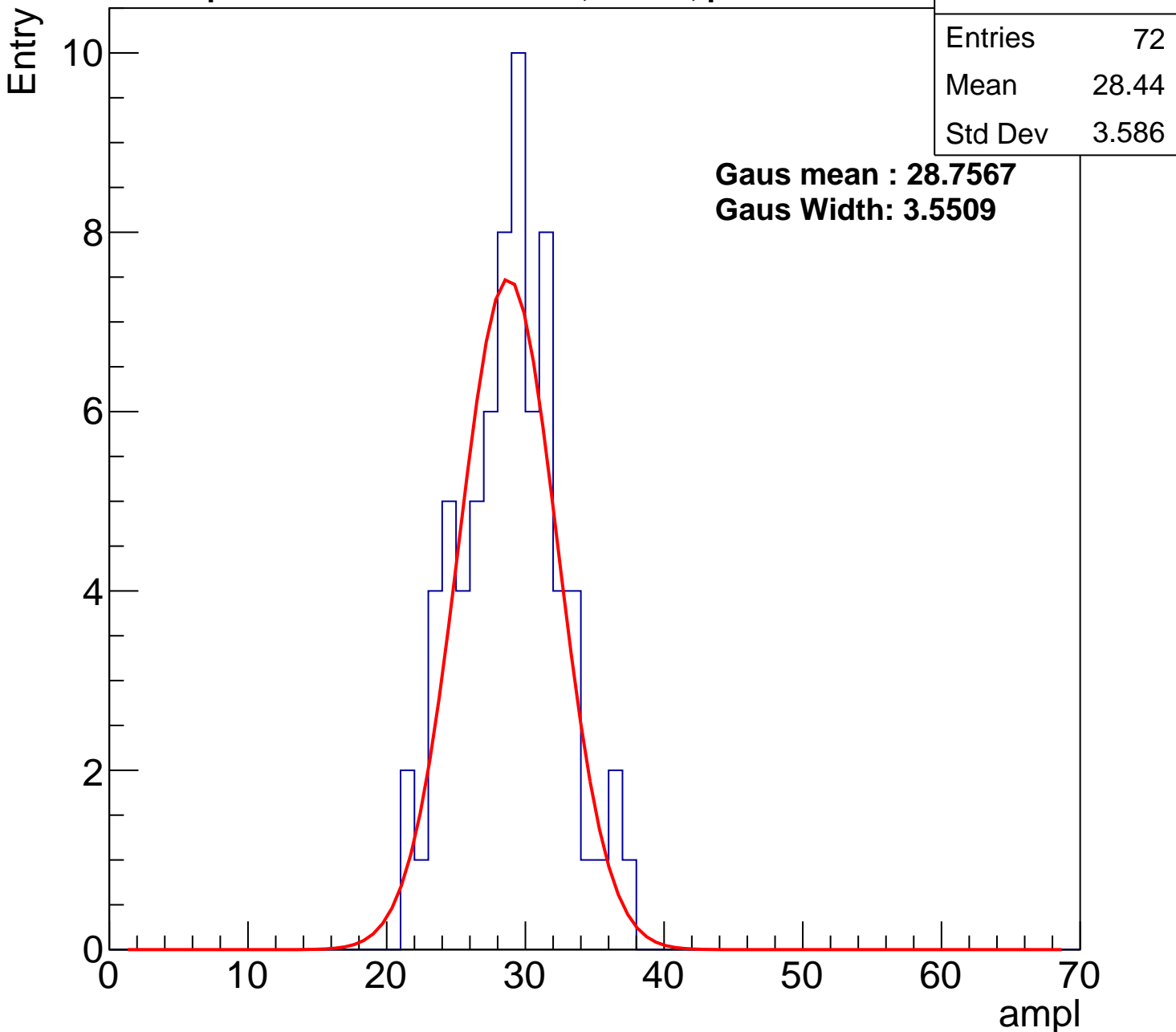
Gaus Width: 3.5509

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch121, adc1

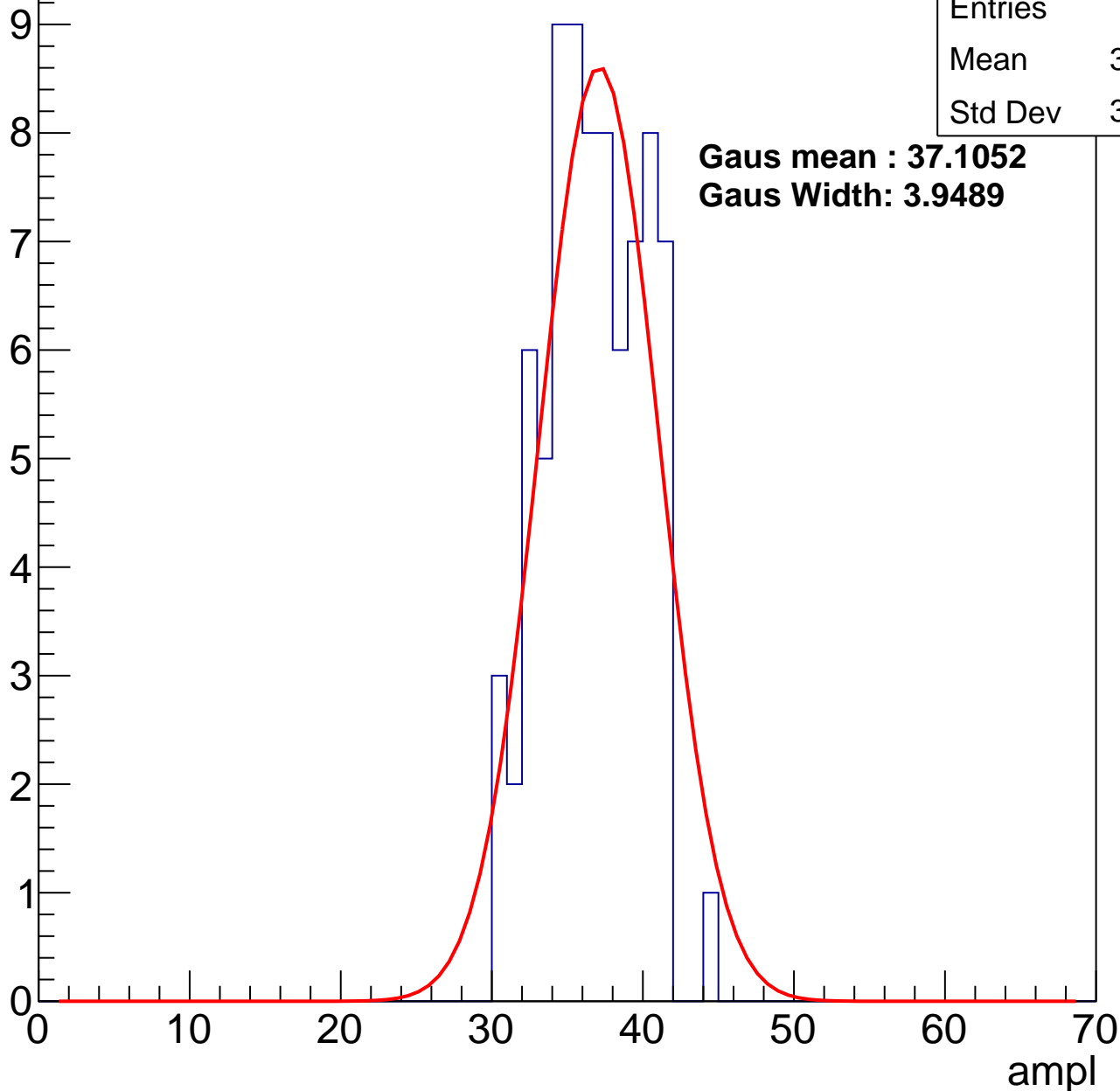
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.28
Std Dev	3.182

Gaus mean : 37.1052

Gaus Width: 3.9489



B1L102S, U22-ch121, adc2

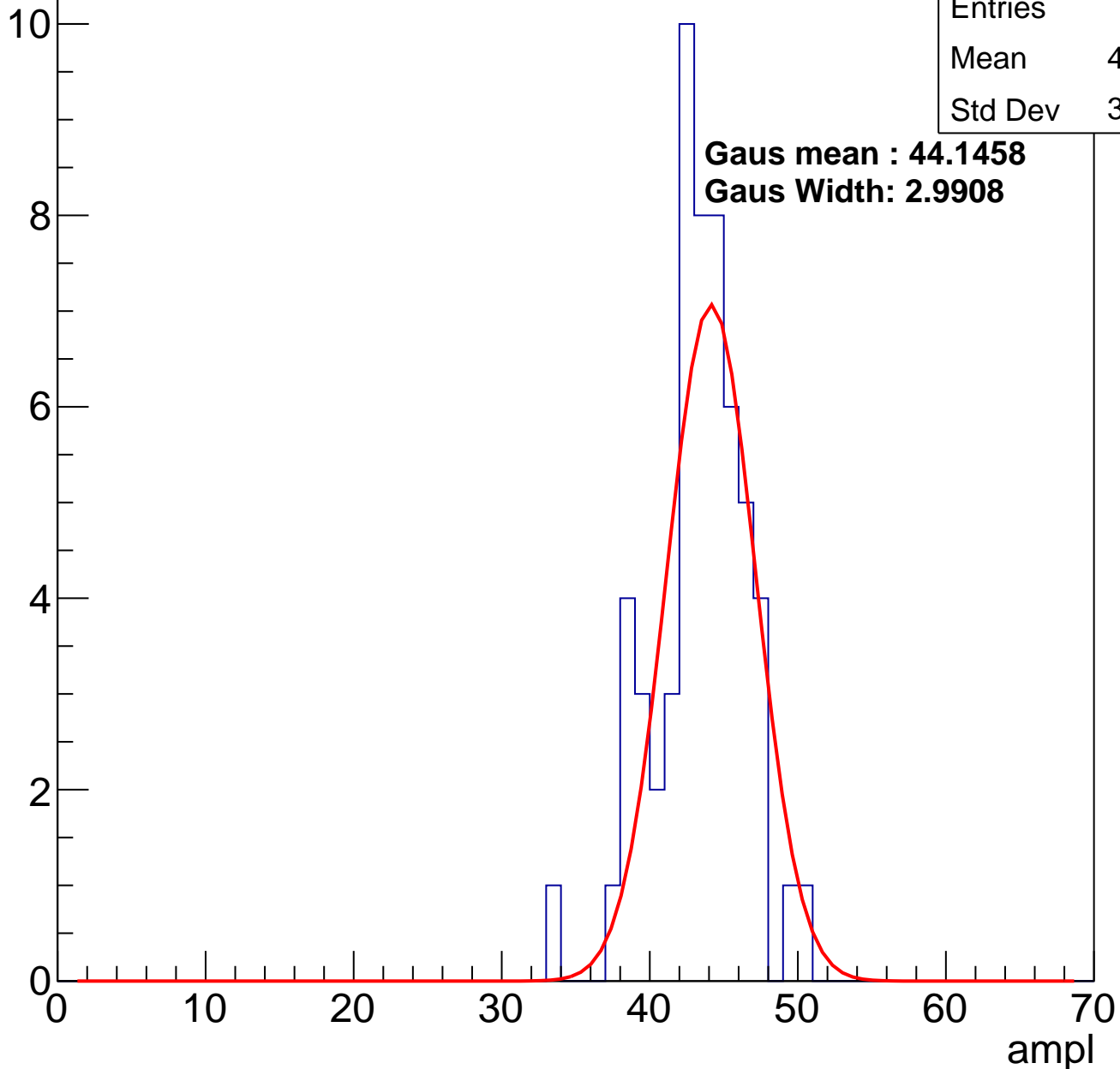
calib_packv5_042523_0143.root, FC#11, port A2

Entries	57
Mean	42.89
Std Dev	3.105

Gaus mean : 44.1458

Gaus Width: 2.9908

Entry

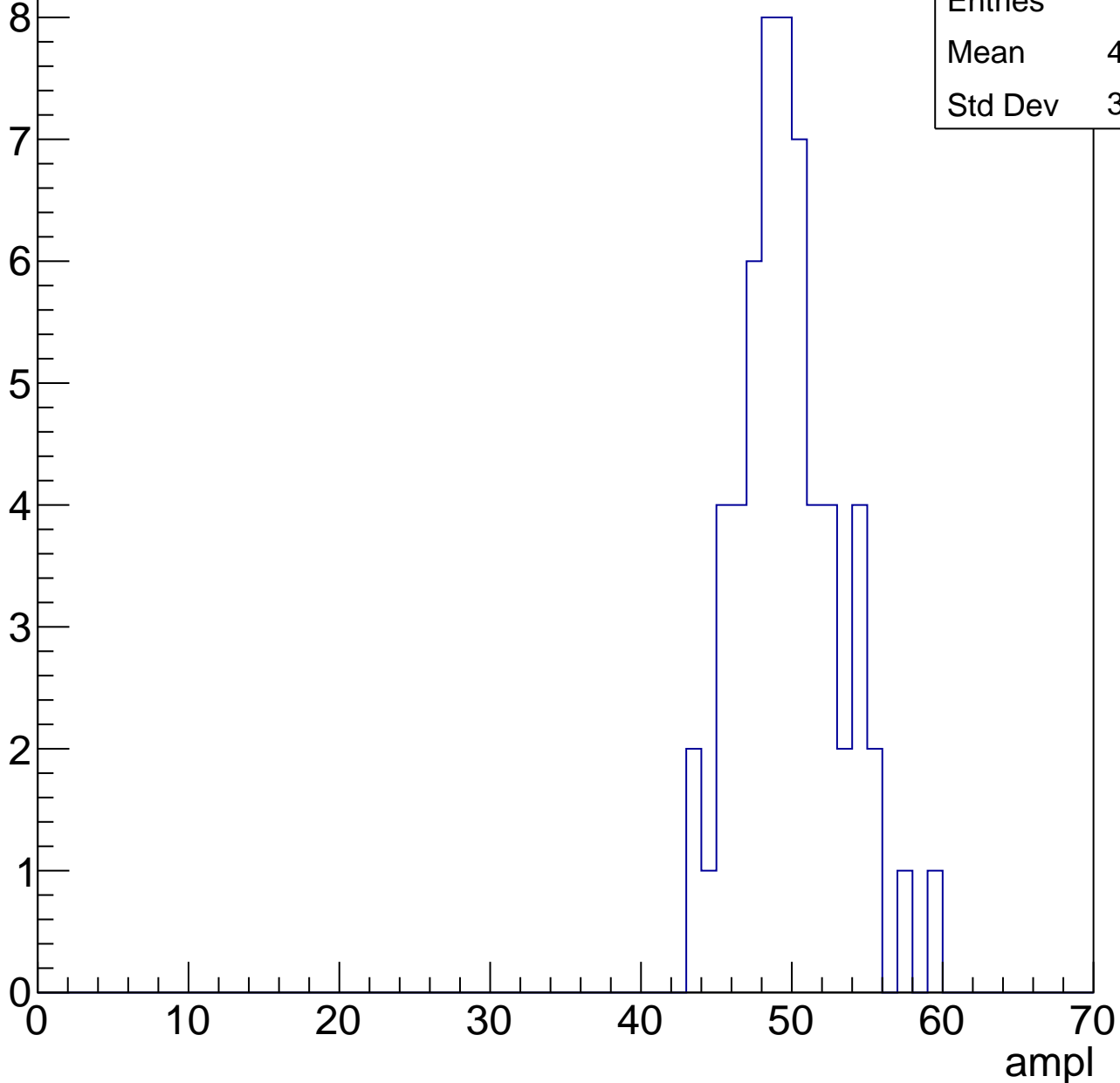


B1L102S, U22-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	49.34
Std Dev	3.345

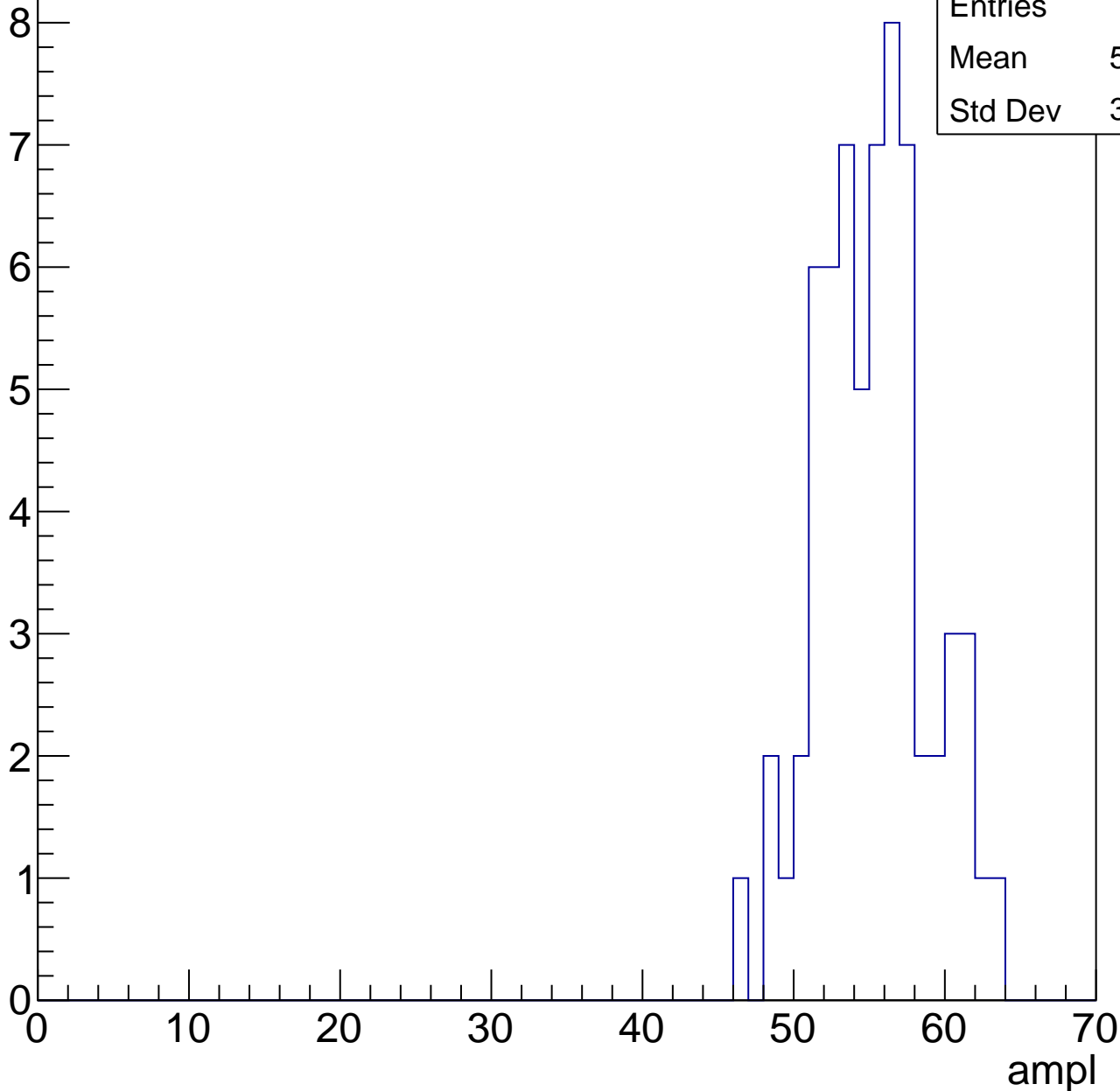


B1L102S, U22-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	54.75
Std Dev	3.593



B1L102S, U22-ch121, adc5

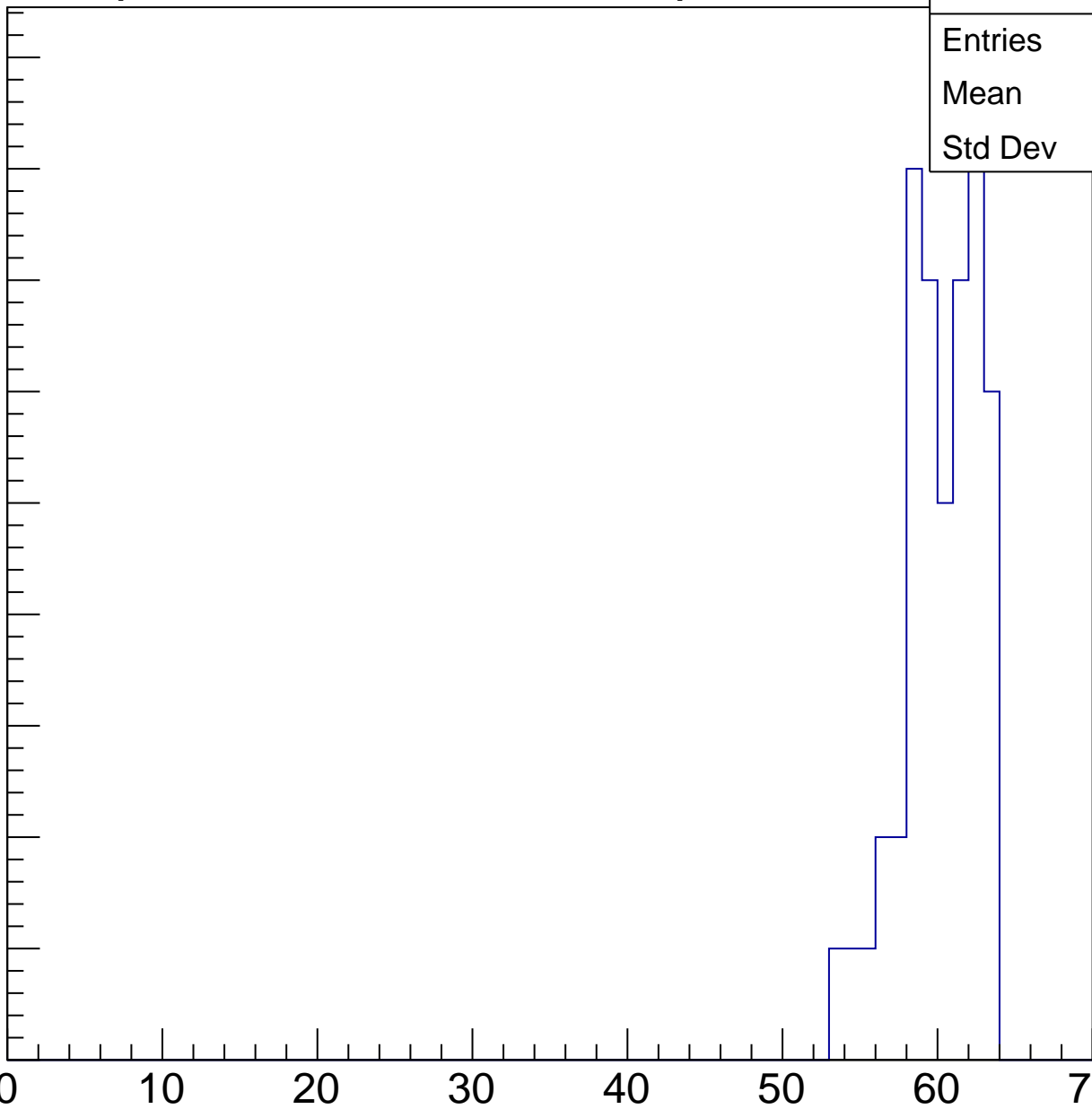
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.76
Std Dev	2.446

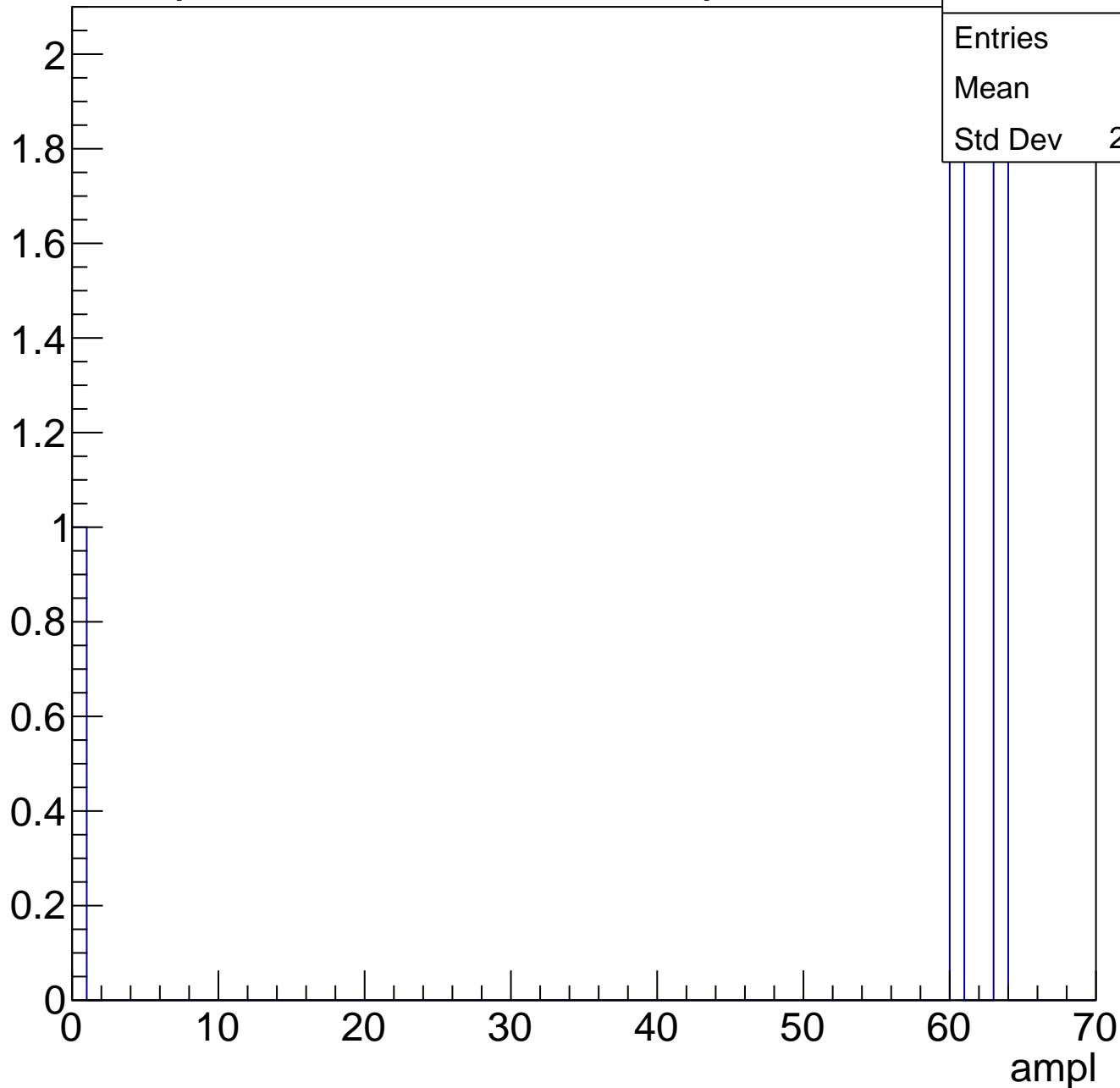
ampl



B1L102S, U22-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

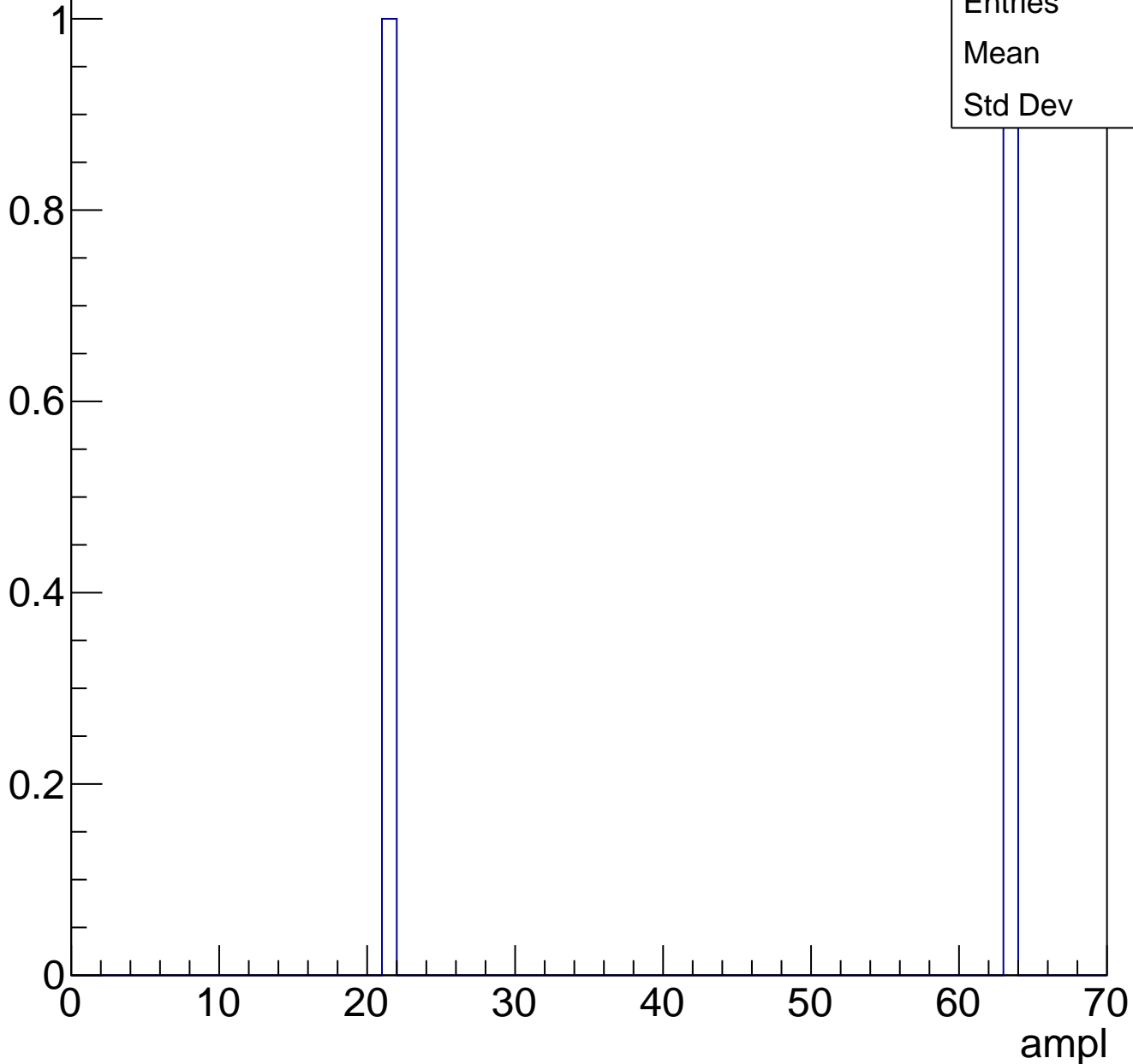
Entry



B1L102S, U22-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U22-ch122, adc0

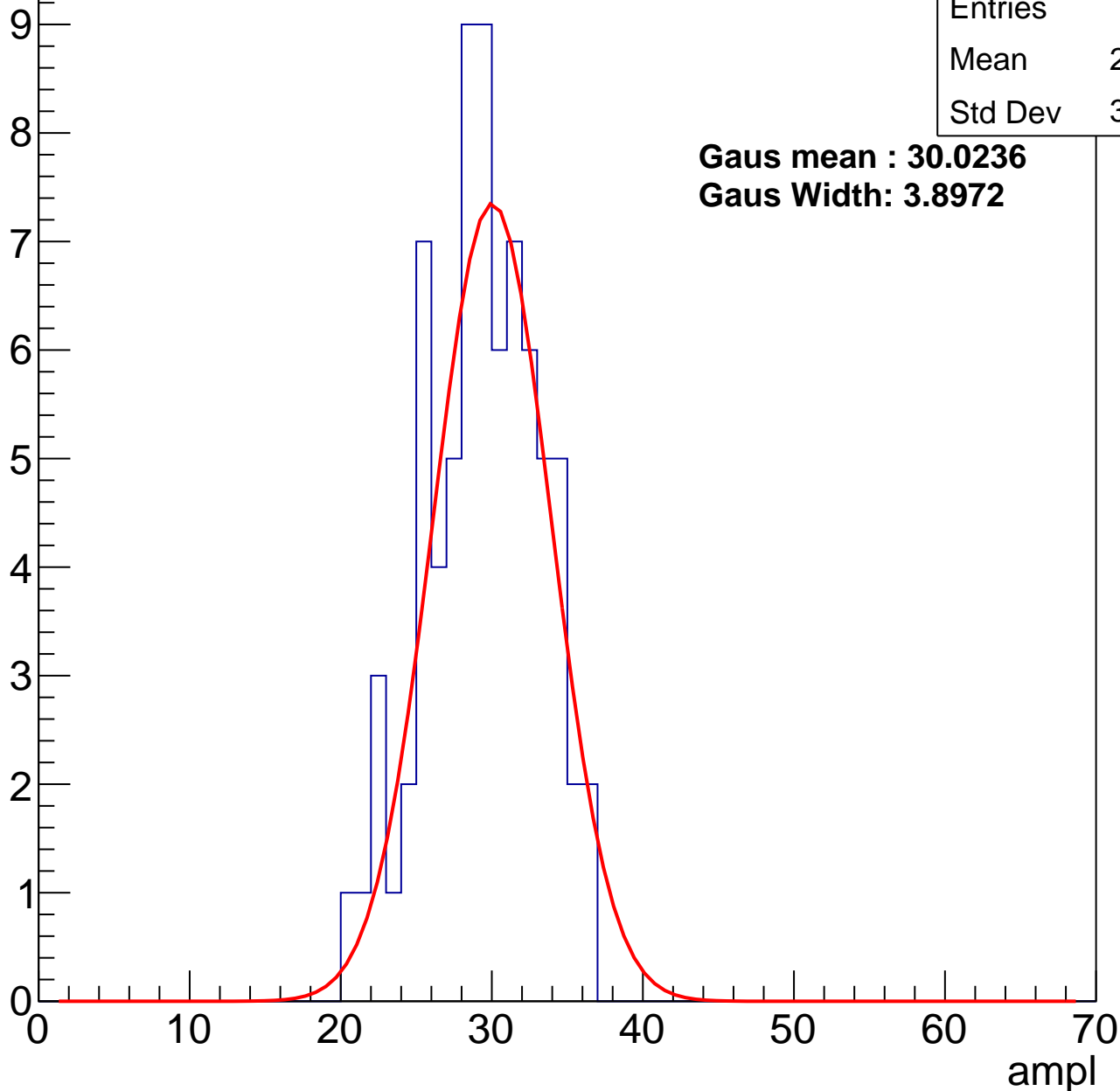
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.95
Std Dev	3.684

Gaus mean : 30.0236

Gaus Width: 3.8972



B1L102S, U22-ch122, adc1

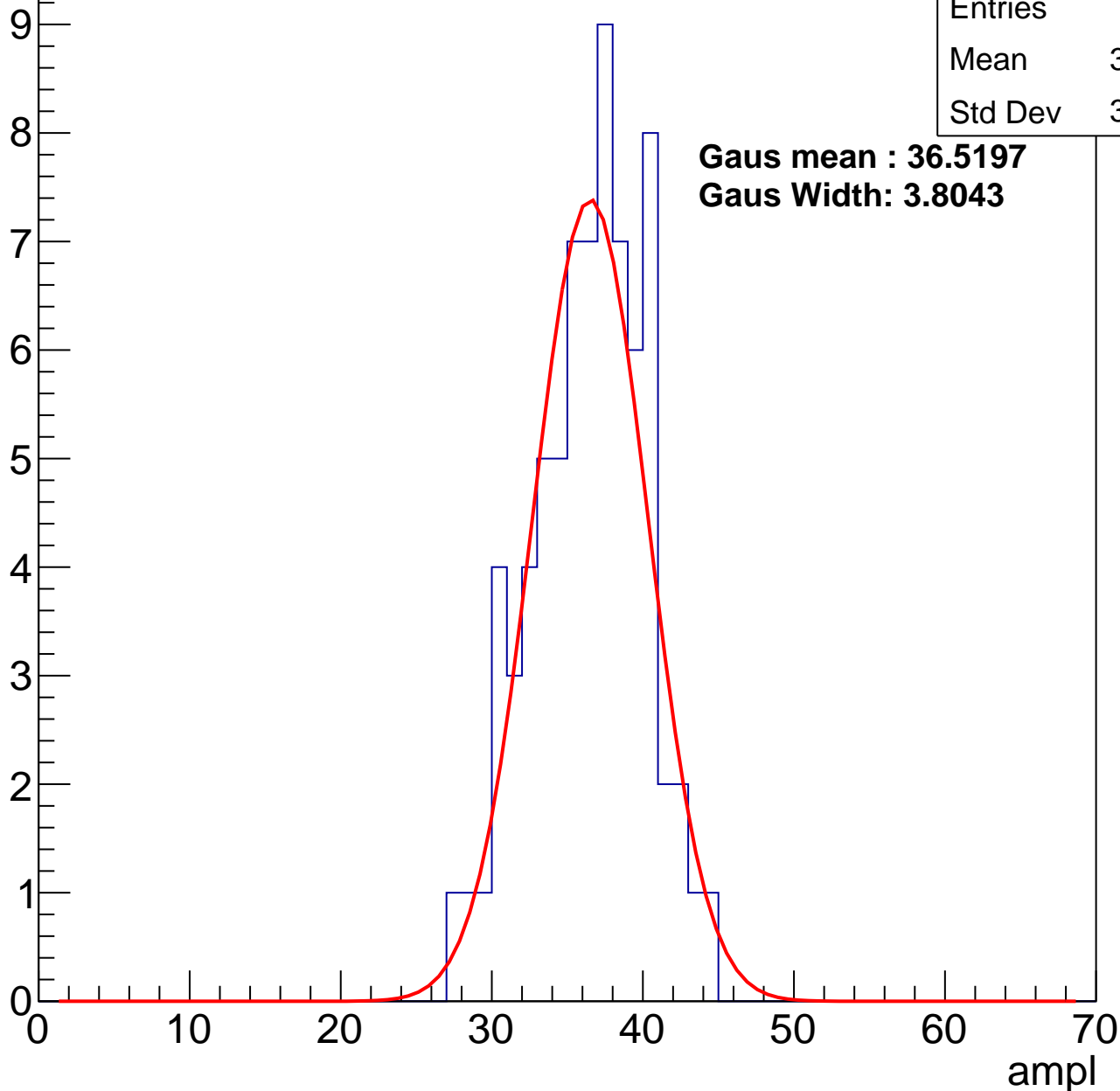
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.99
Std Dev	3.682

Gaus mean : 36.5197

Gaus Width: 3.8043



B1L102S, U22-ch122, adc2

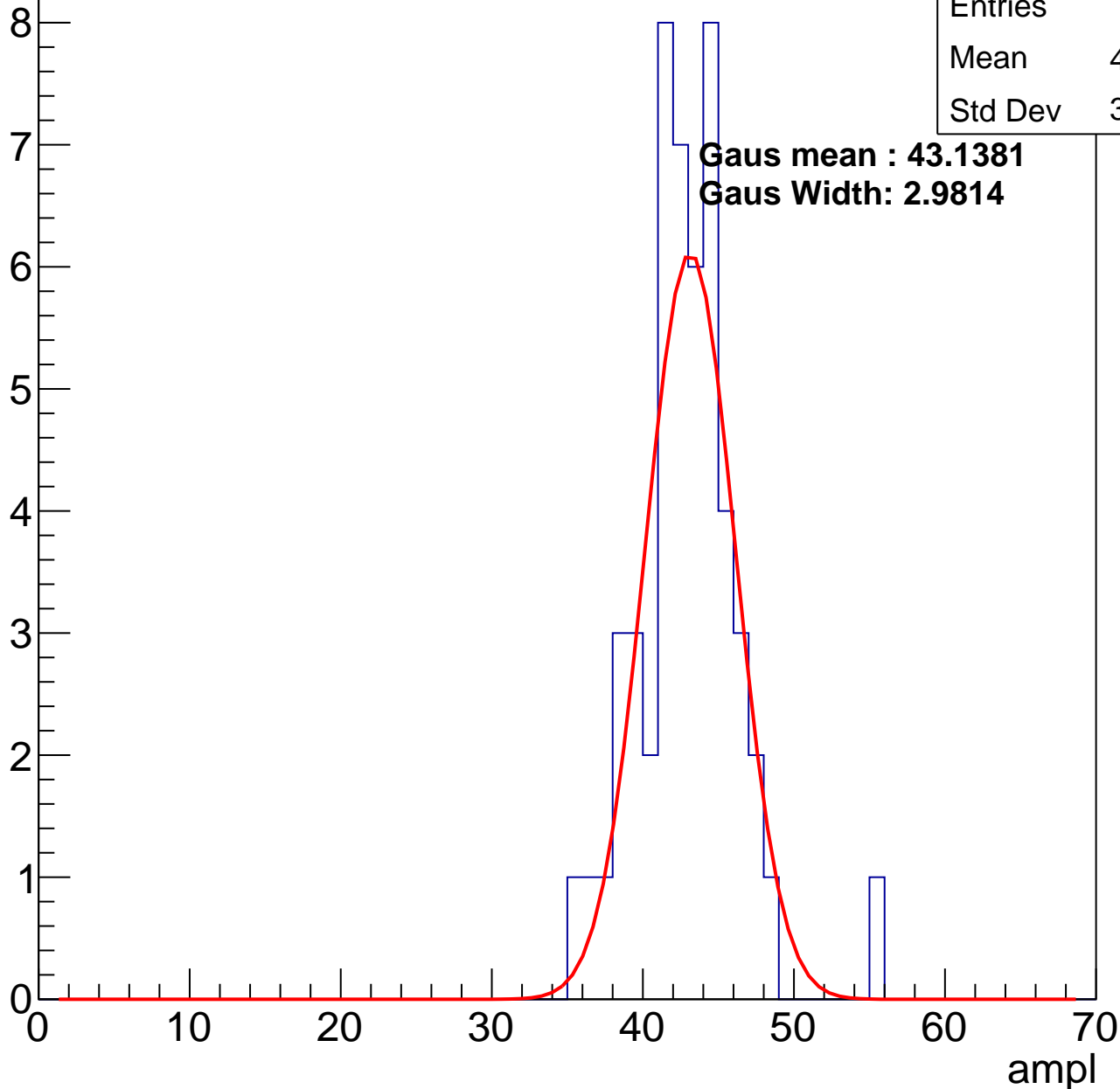
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.47
Std Dev	3.333

Gaus mean : 43.1381

Gaus Width: 2.9814

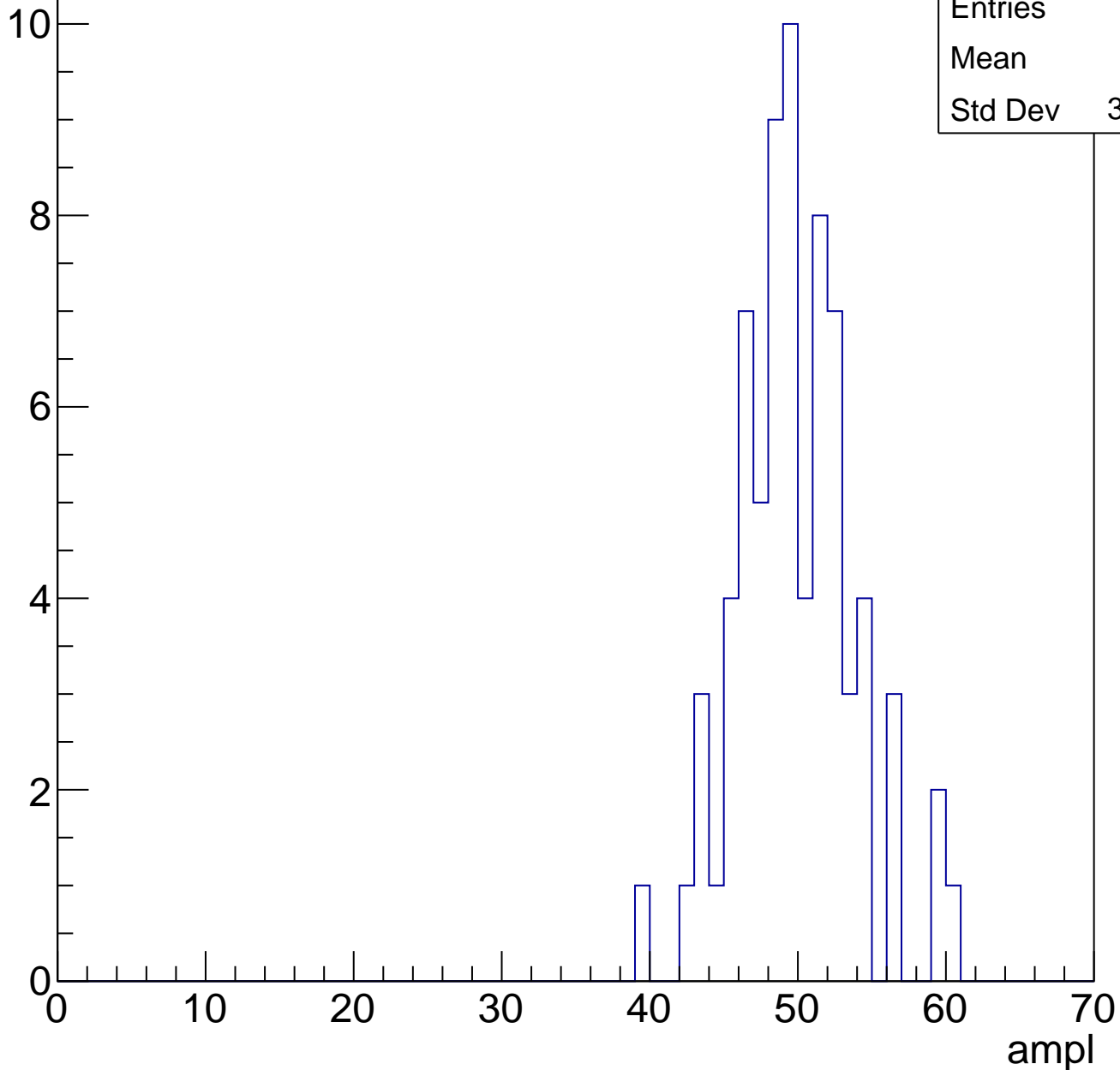


B1L102S, U22-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	49.4
Std Dev	3.954

Entry

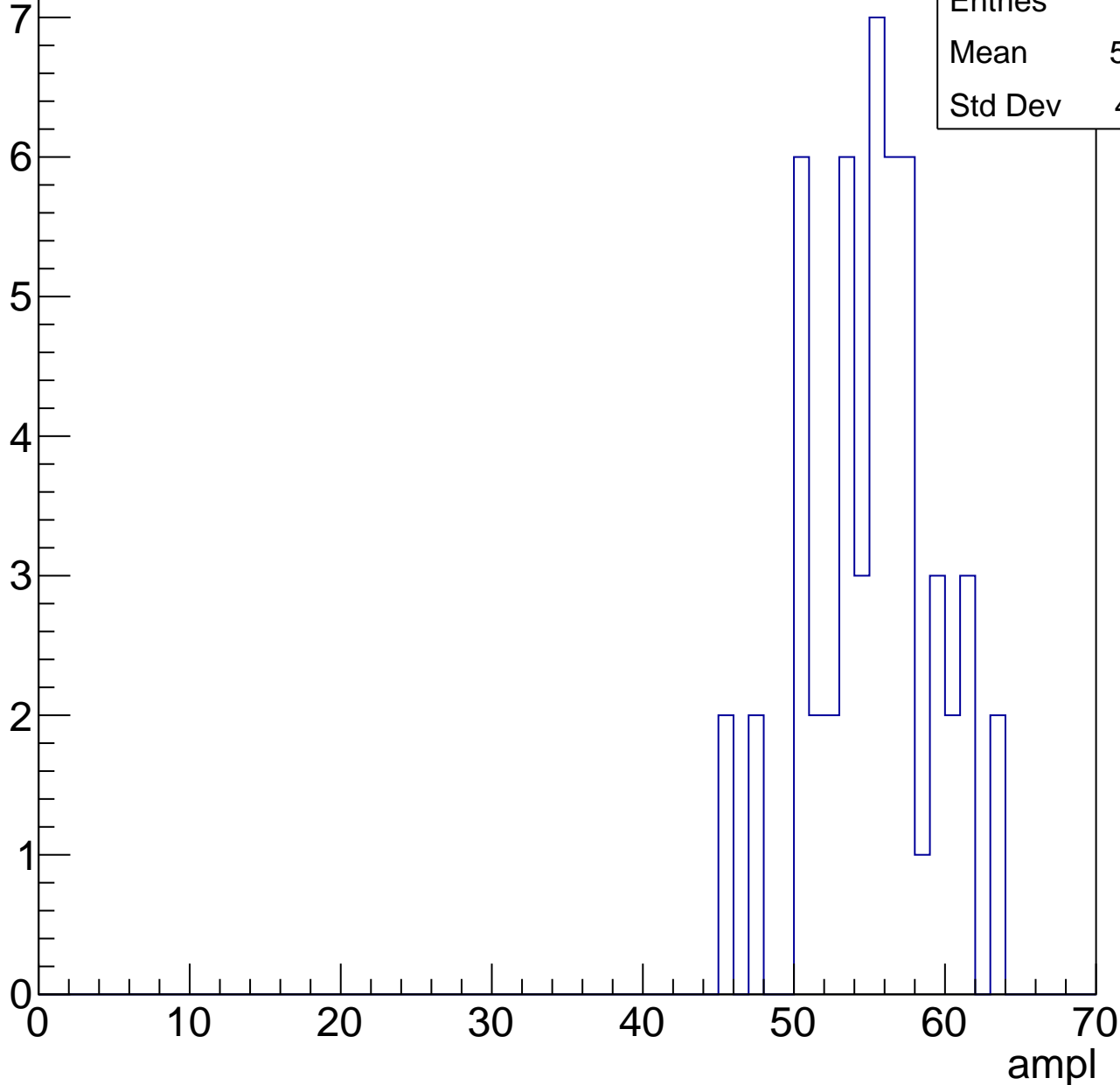


B1L102S, U22-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.66
Std Dev	4.171

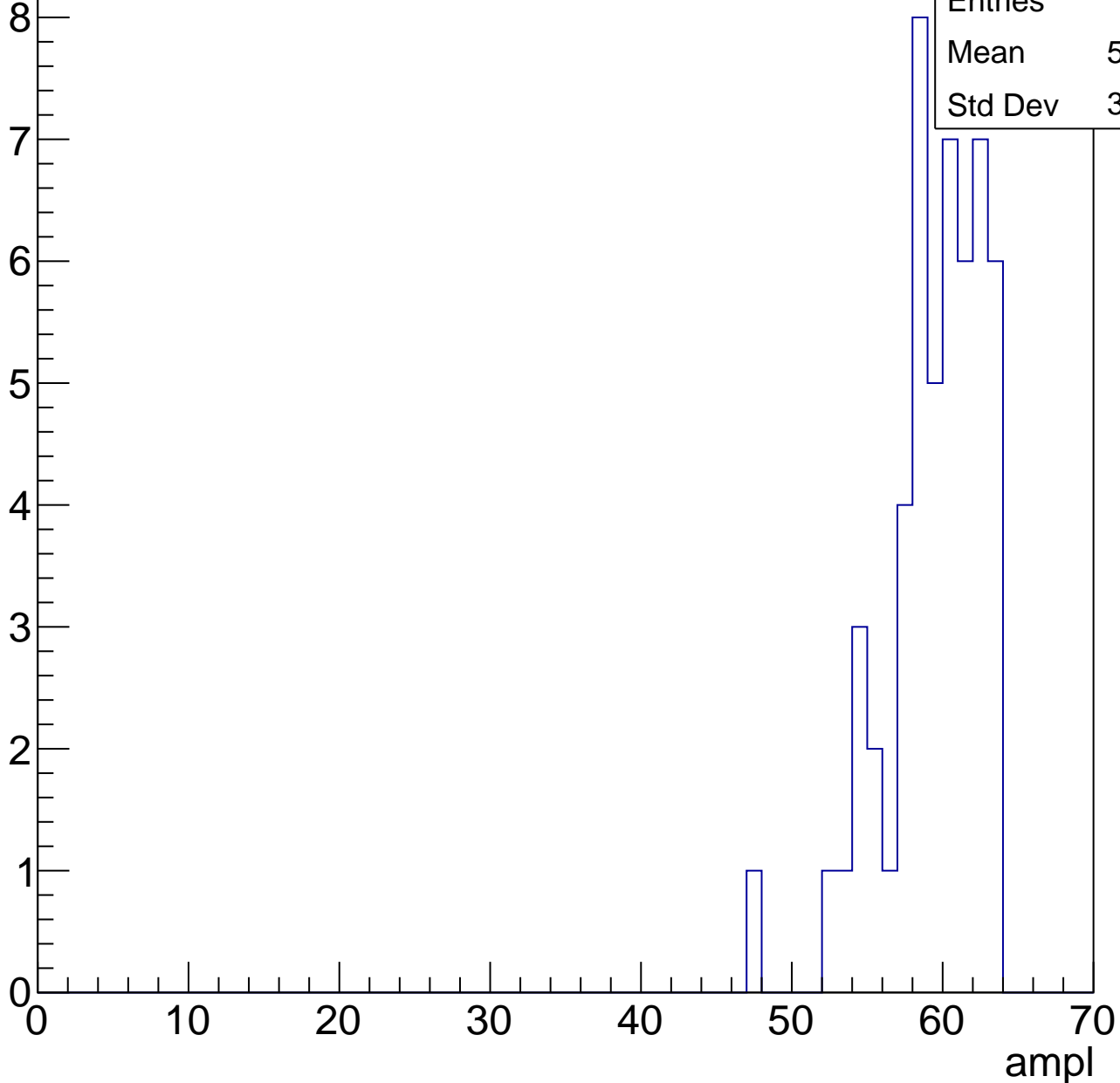


B1L102S, U22-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

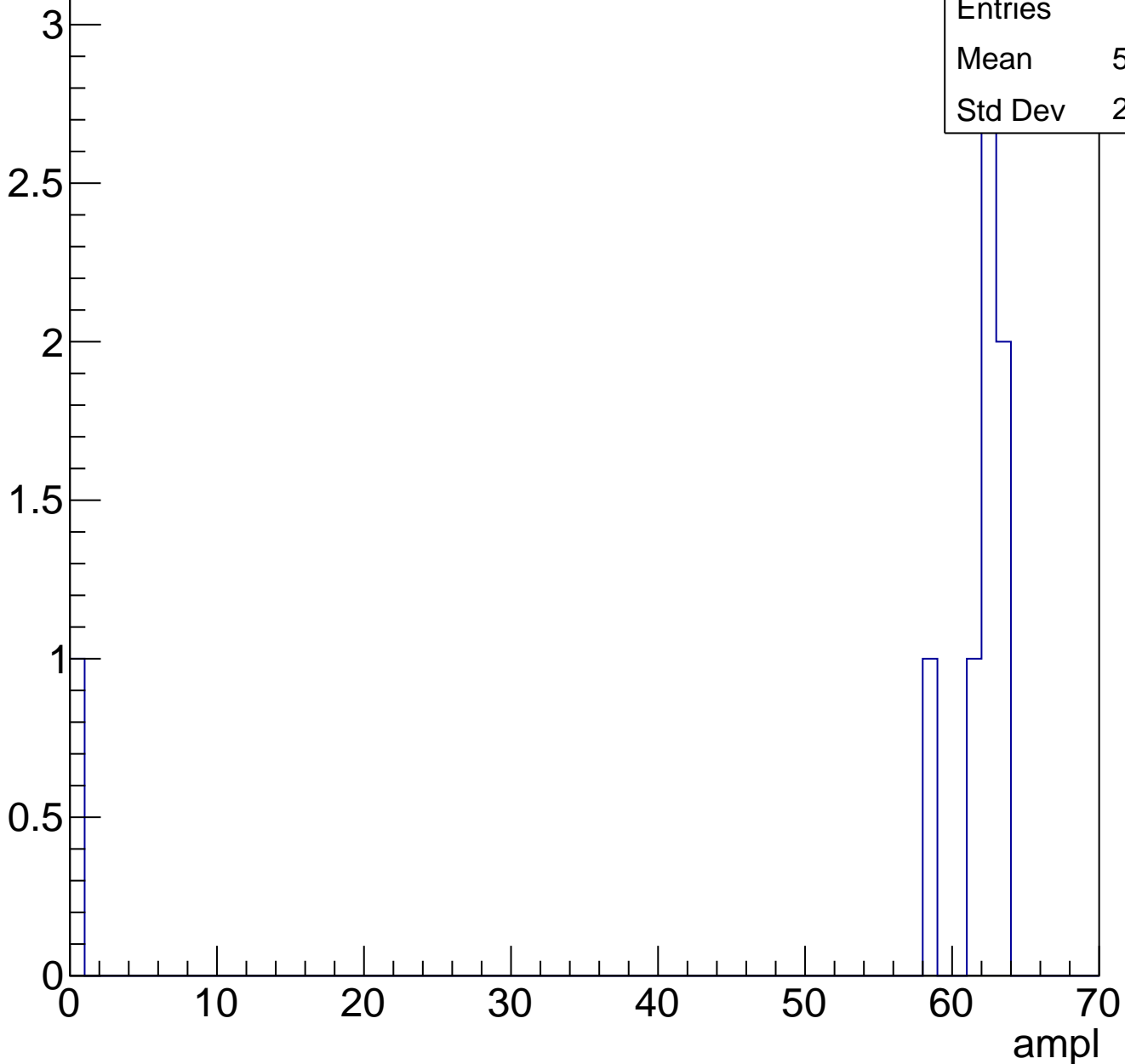
Entries	52
Mean	58.94
Std Dev	3.278



B1L102S, U22-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

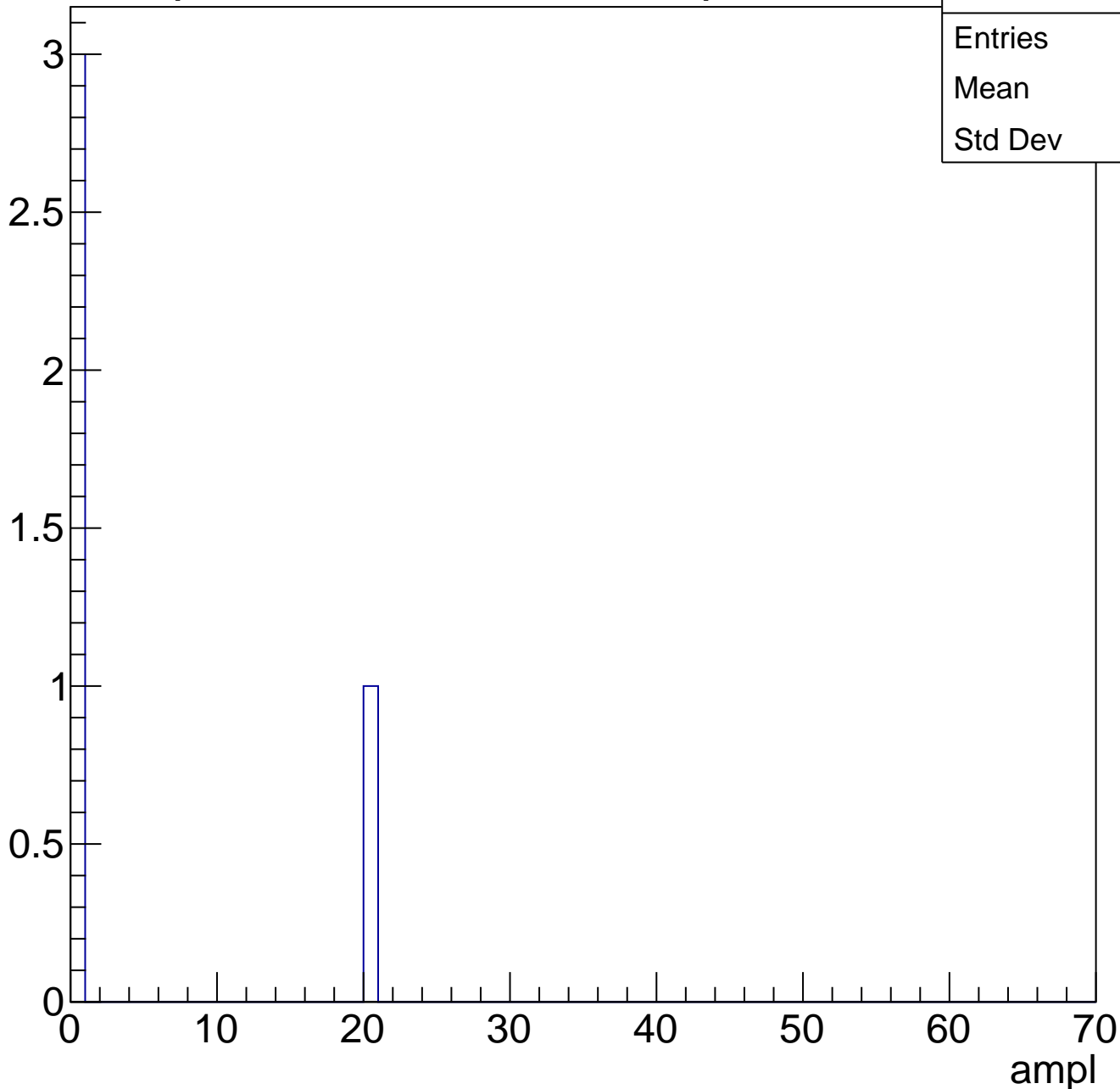
Entry



B1L102S, U22-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	5
Std Dev	8.66

B1L102S, U22-ch123, adc0

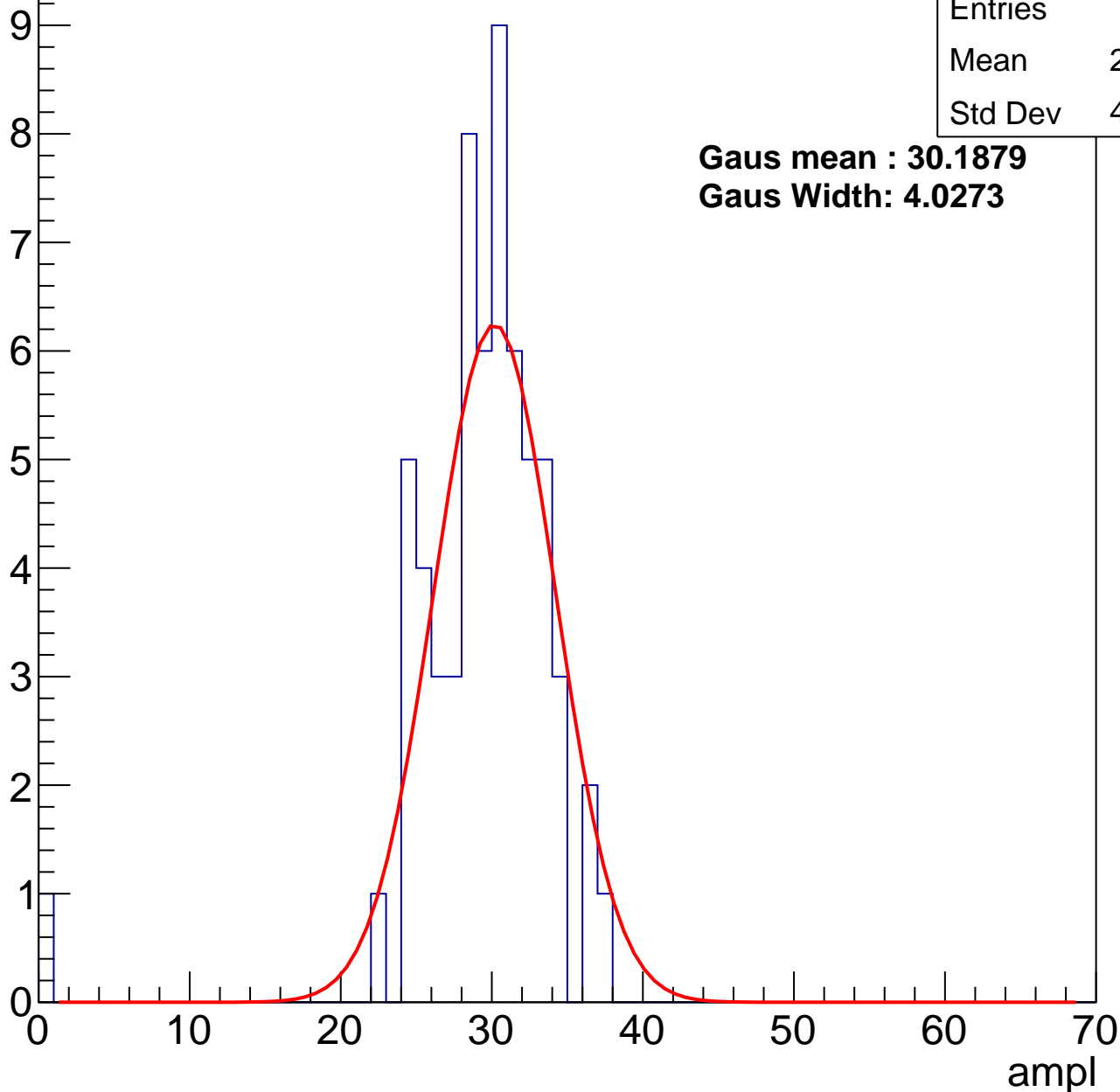
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.89
Std Dev	4.952

Gaus mean : 30.1879

Gaus Width: 4.0273



B1L102S, U22-ch123, adc1

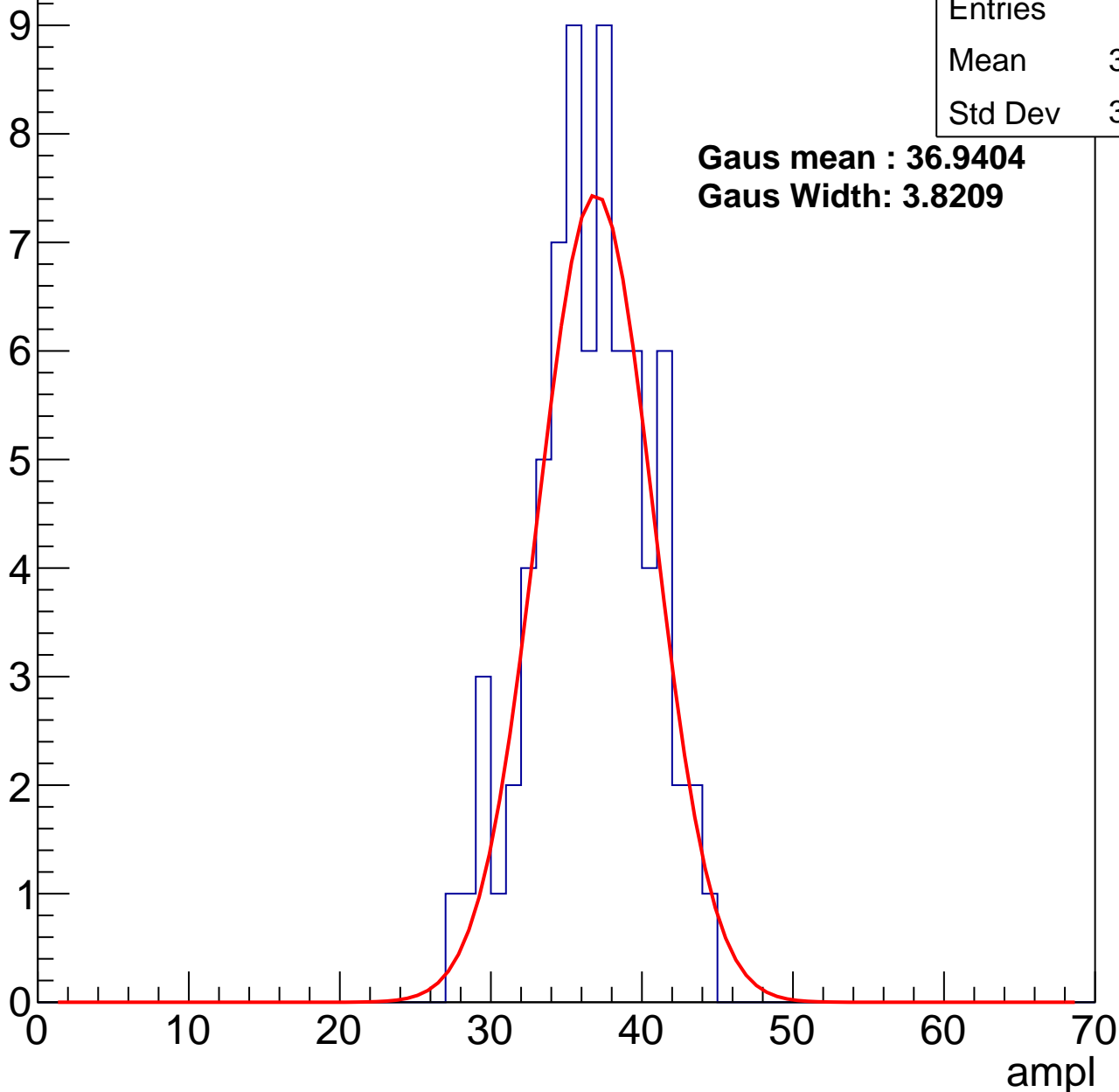
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.15
Std Dev	3.755

Gaus mean : 36.9404

Gaus Width: 3.8209



B1L102S, U22-ch123, adc2

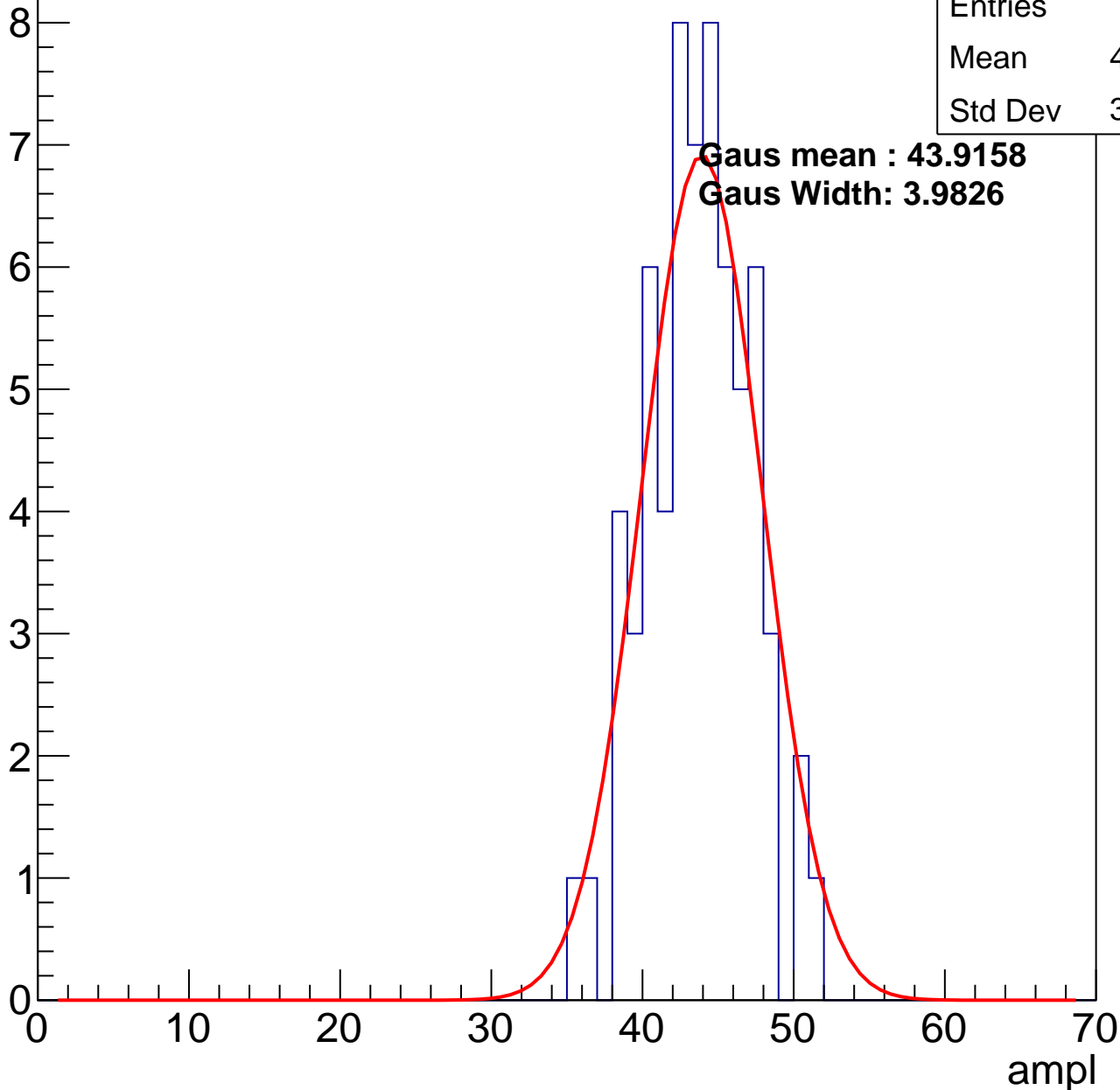
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.23
Std Dev	3.387

Gaus mean : 43.9158

Gaus Width: 3.9826

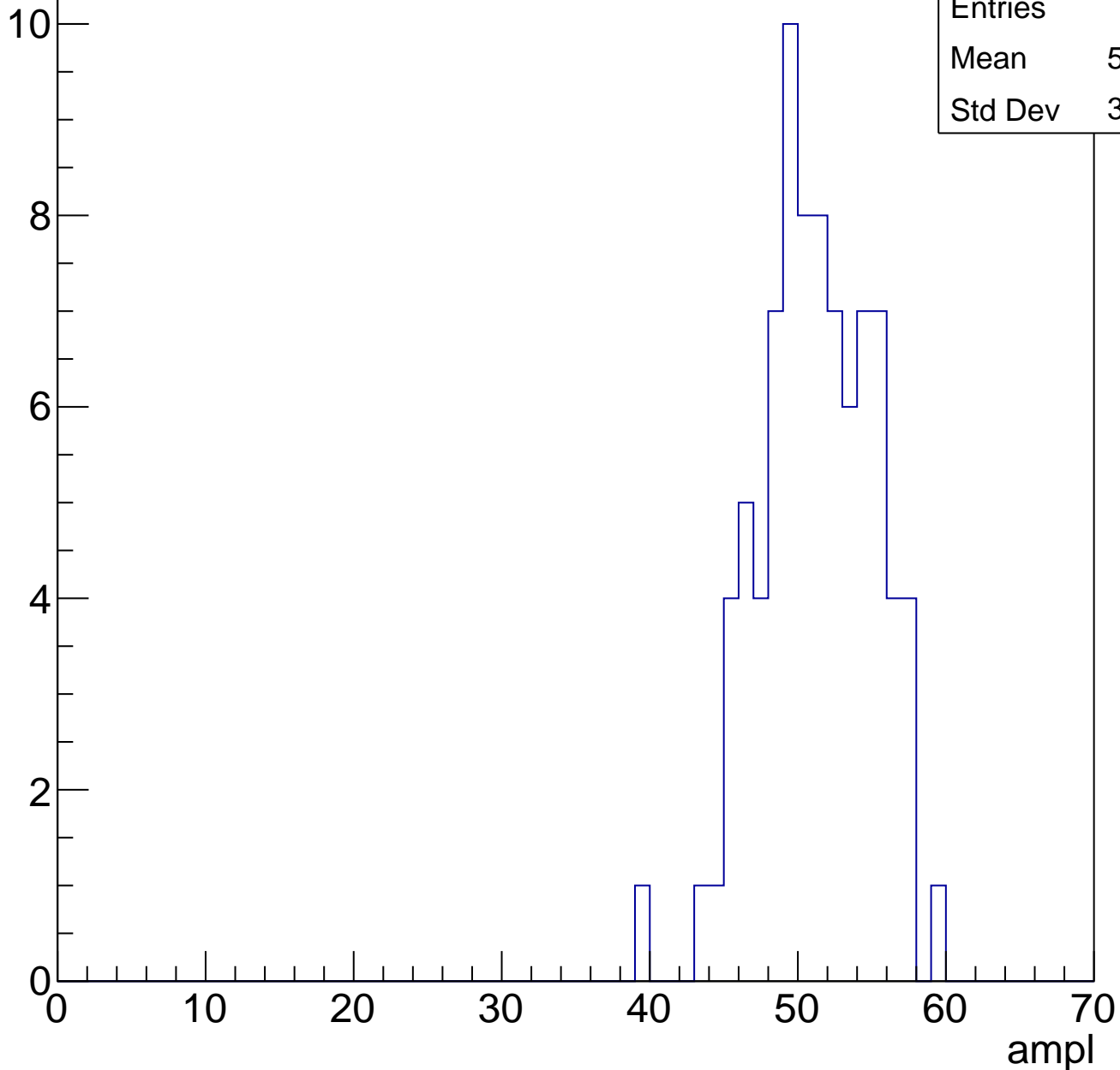


B1L102S, U22-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	50.75
Std Dev	3.773

Entry

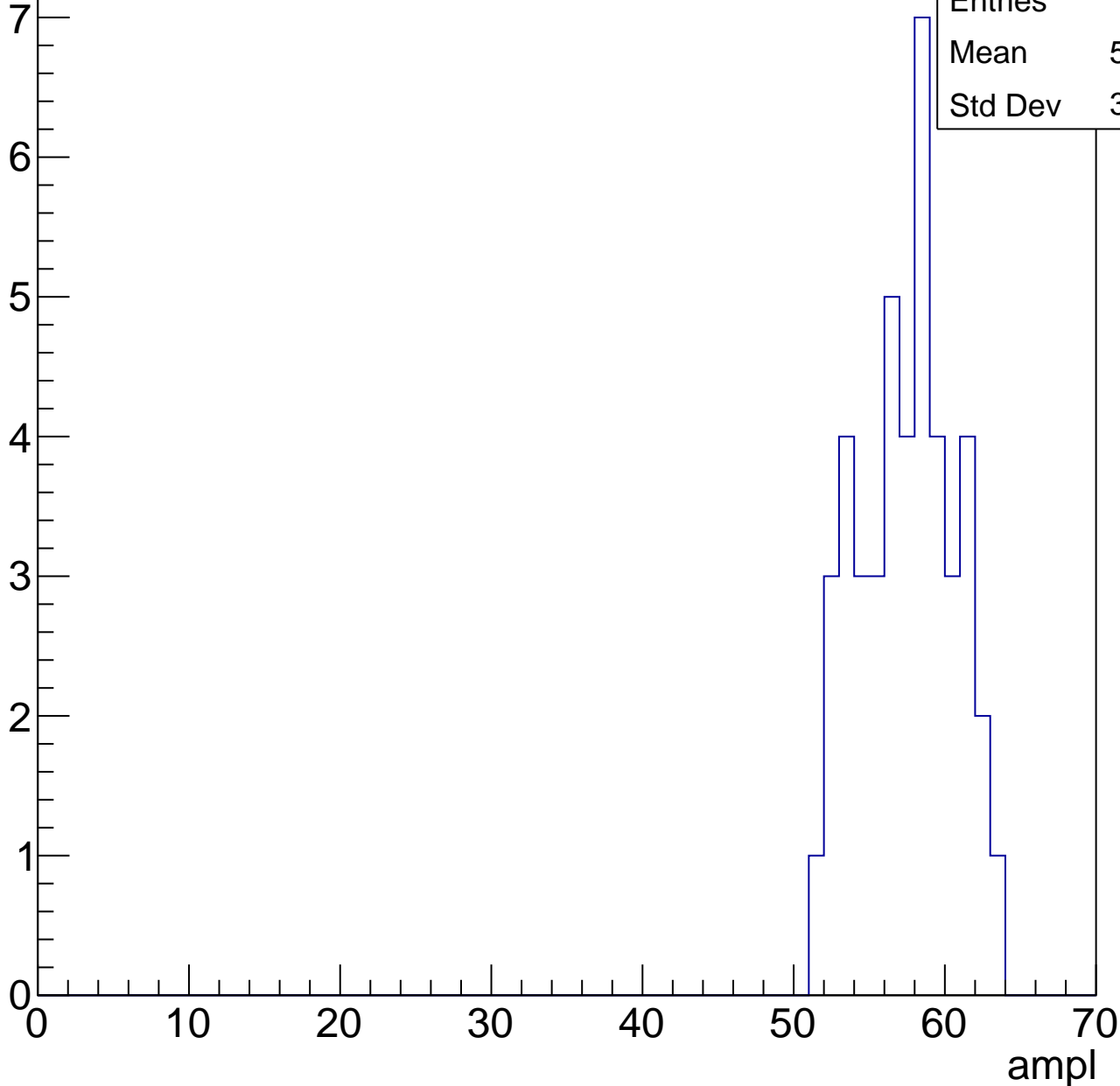


B1L102S, U22-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

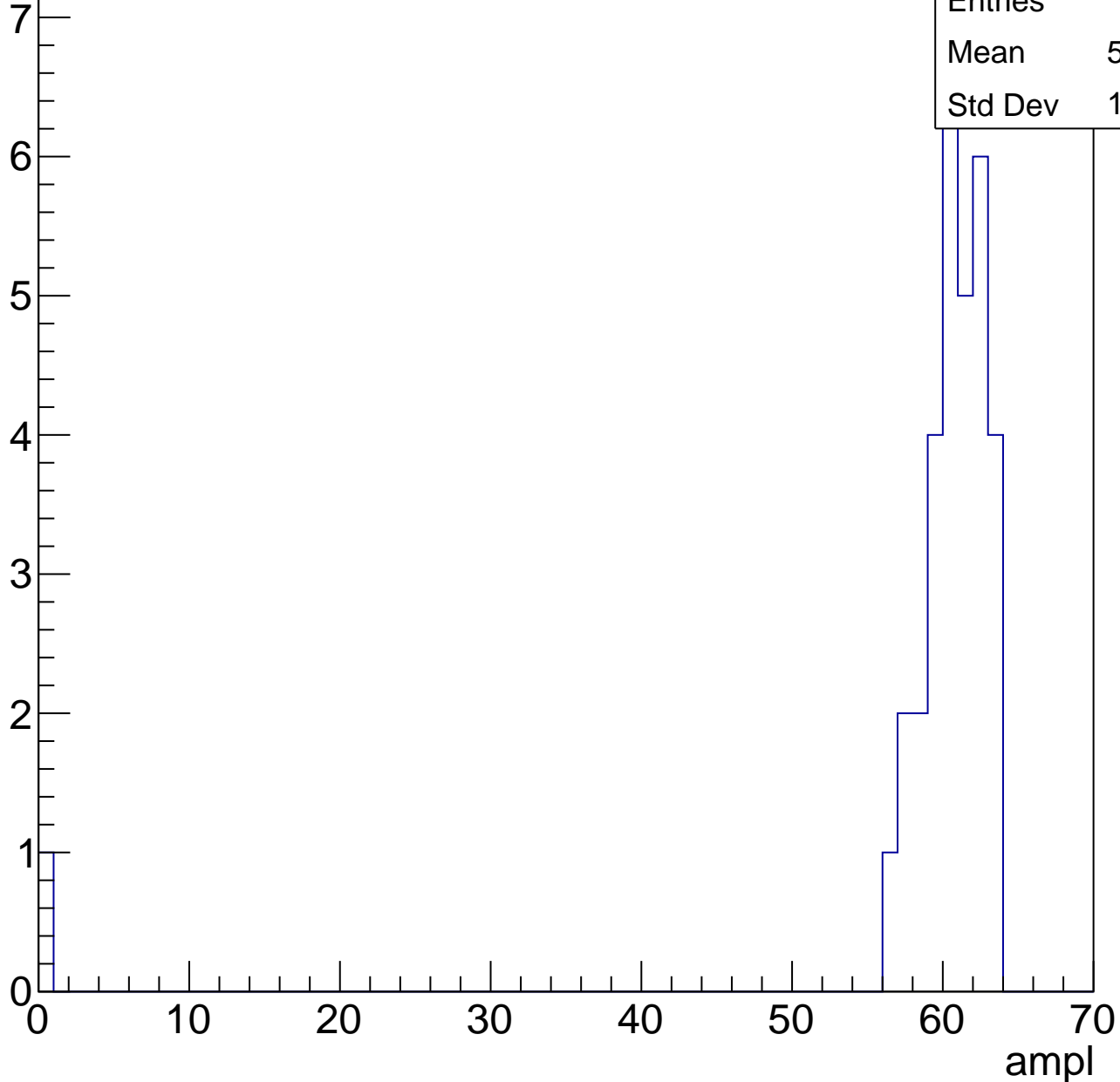
Entries	44
Mean	56.98
Std Dev	3.086



B1L102S, U22-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



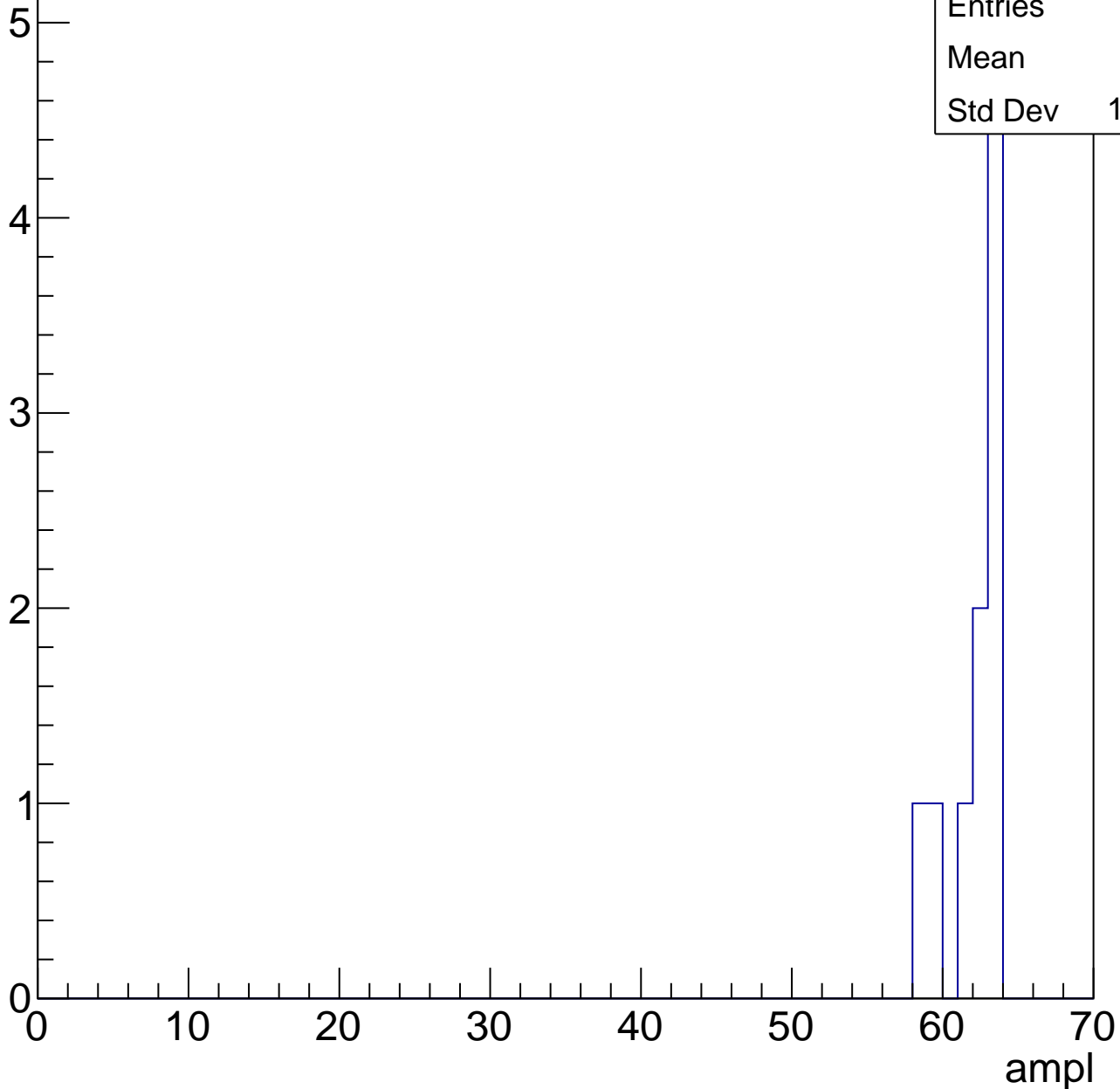
Entries	32
Mean	58.47
Std Dev	10.66

B1L102S, U22-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	61.7
Std Dev	1.735



B1L102S, U22-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch124, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	94
Mean	27.81
Std Dev	5.663

Gaus mean : 29.4026

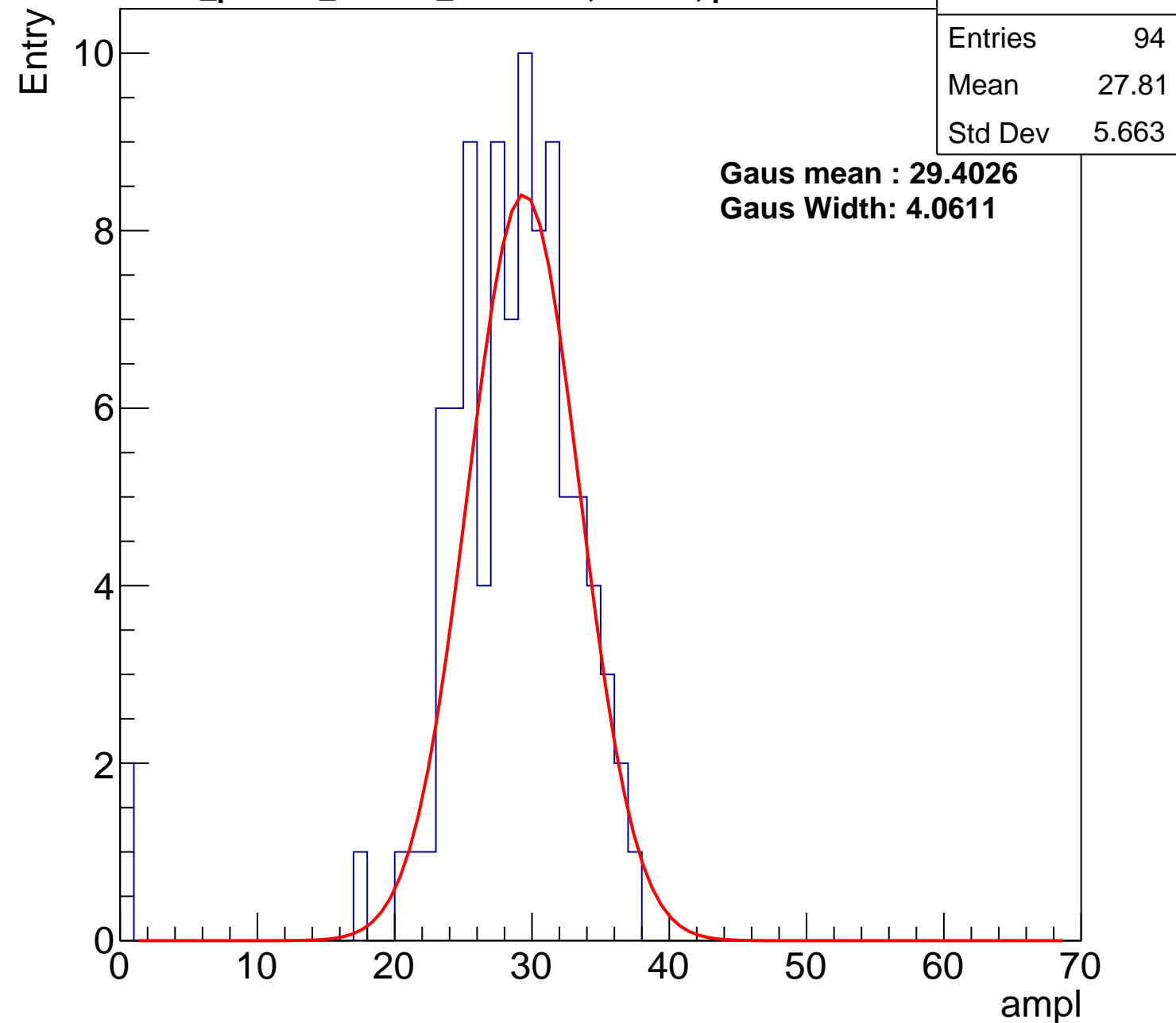
Gaus Width: 4.0611

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U22-ch124, adc1

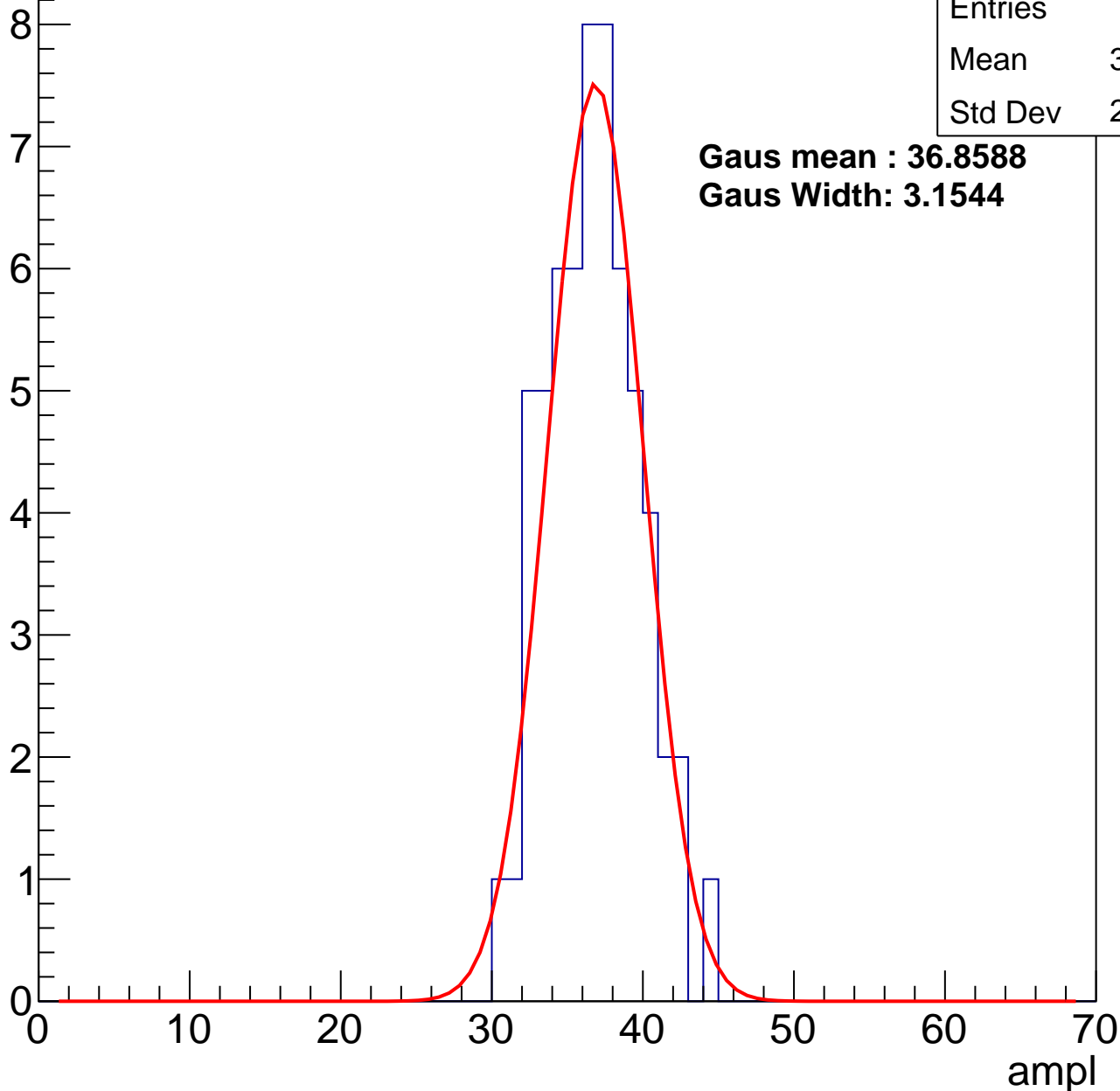
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	36.28
Std Dev	2.995

Gaus mean : 36.8588

Gaus Width: 3.1544



B1L102S, U22-ch124, adc2

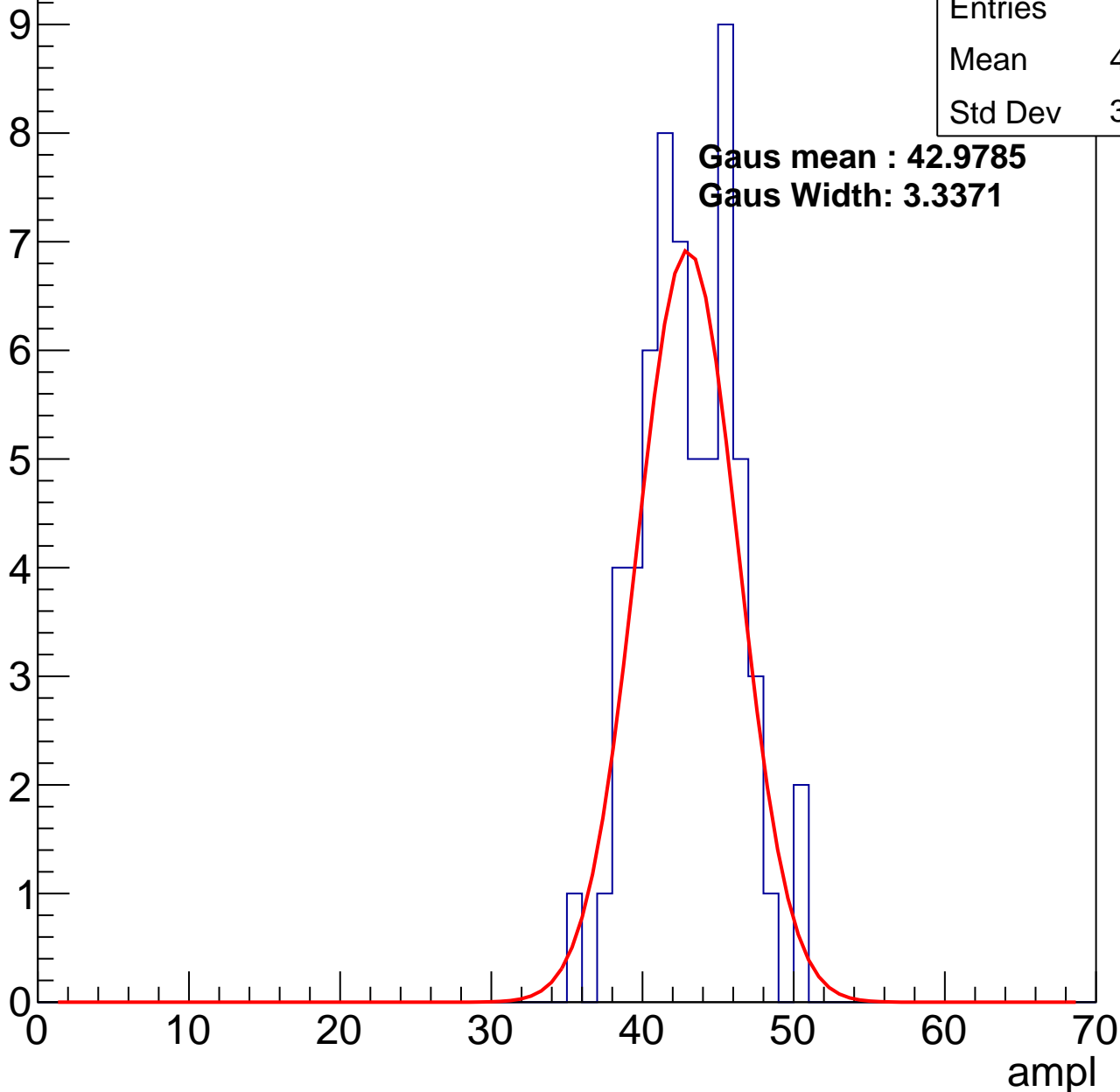
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.64
Std Dev	3.152

Gaus mean : 42.9785

Gaus Width: 3.3371

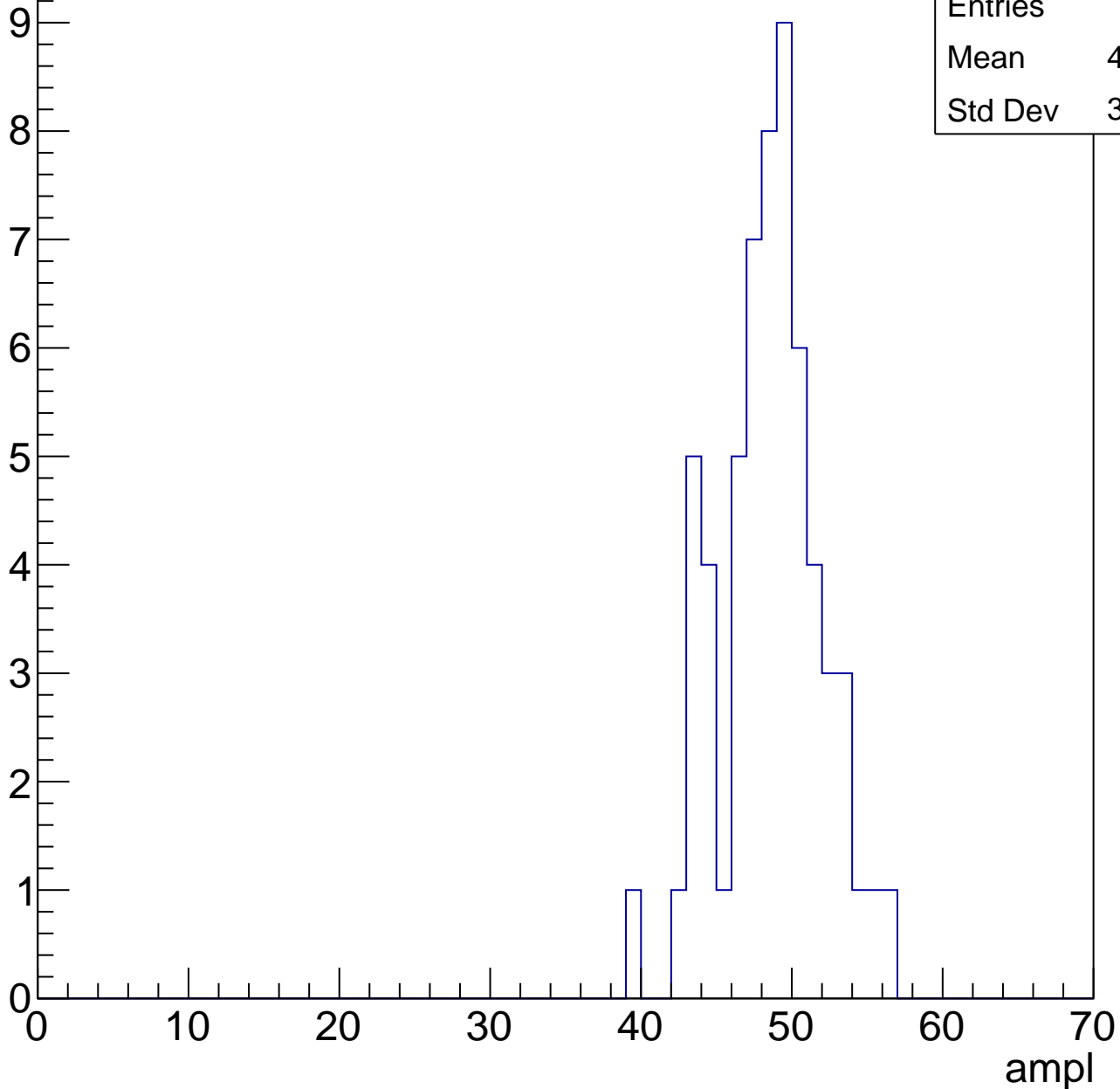


B1L102S, U22-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	48.08
Std Dev	3.373

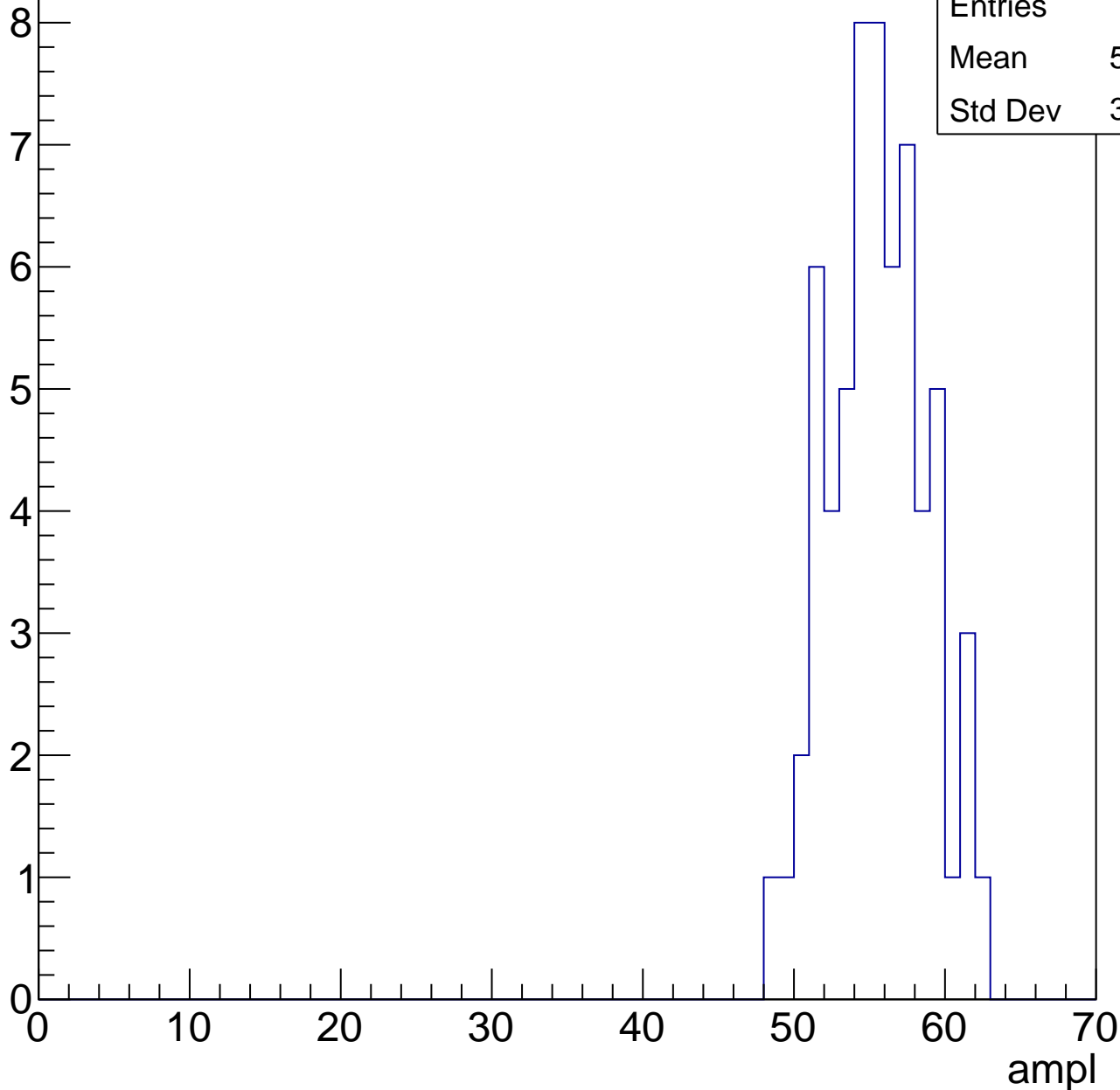


B1L102S, U22-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.08
Std Dev	3.179

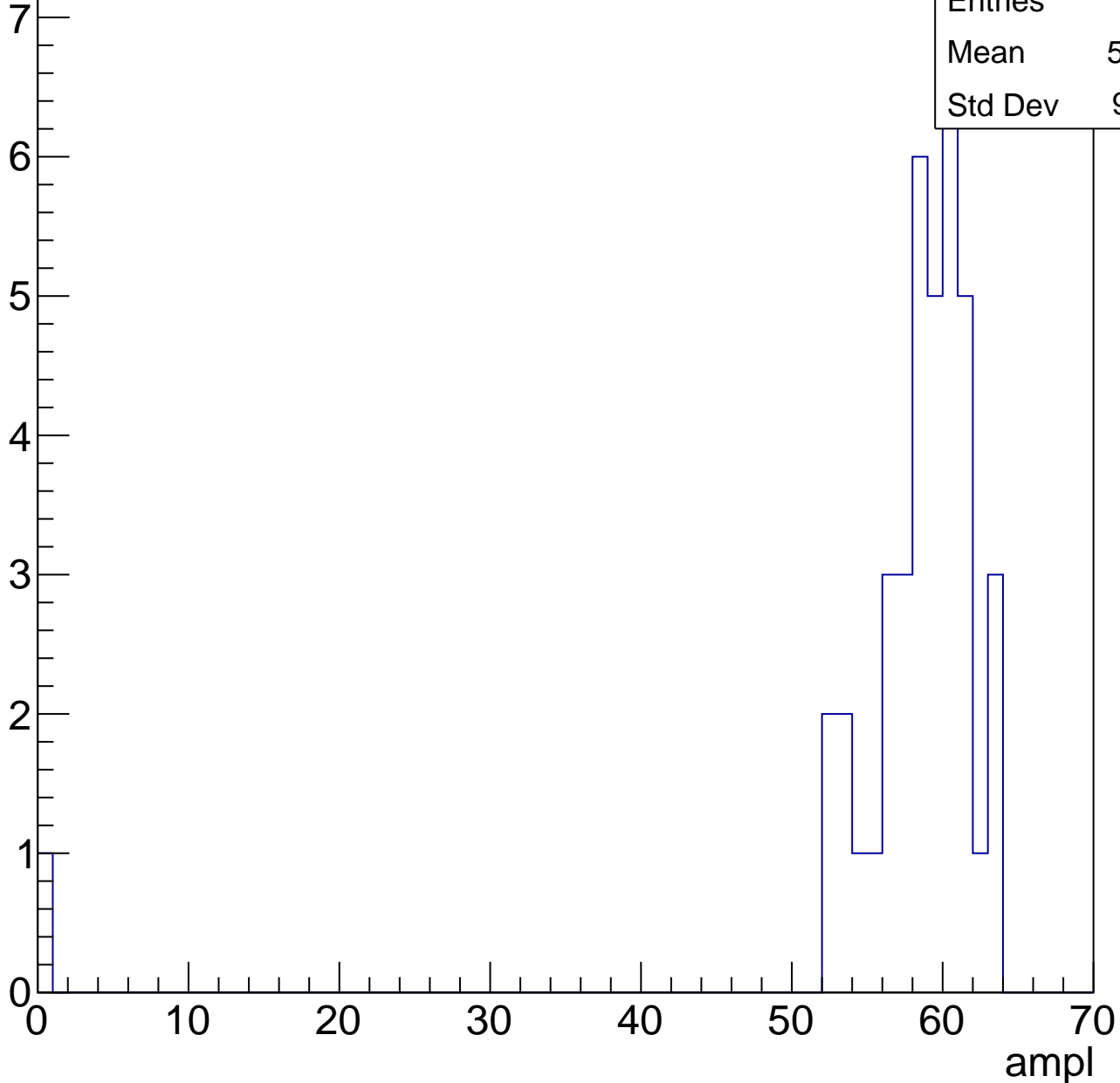


B1L102S, U22-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	56.92
Std Dev	9.551

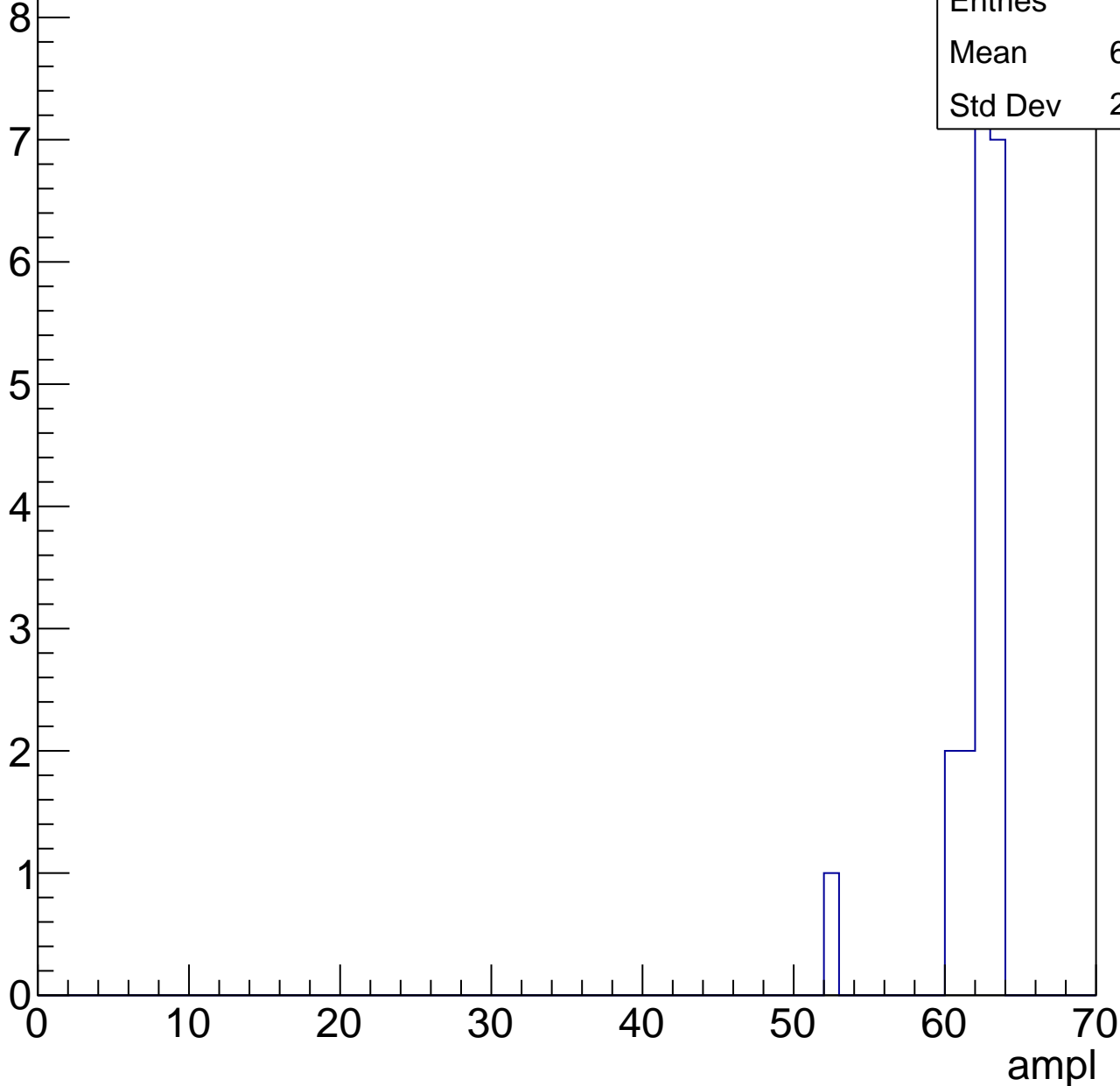


B1L102S, U22-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	61.55
Std Dev	2.376



B1L102S, U22-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U22-ch125, adc0

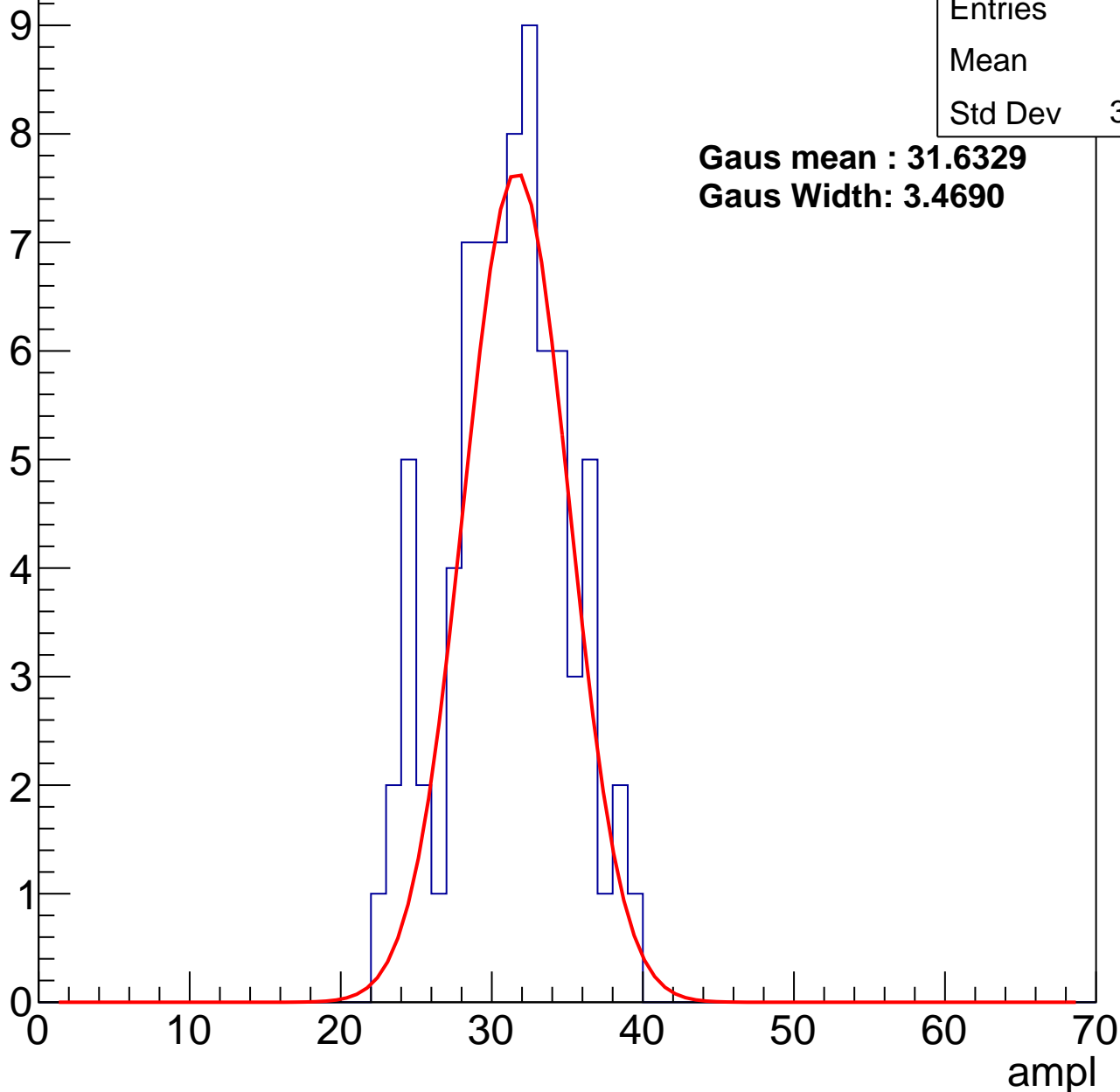
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	30.6
Std Dev	3.879

Gaus mean : 31.6329

Gaus Width: 3.4690



B1L102S, U22-ch125, adc1

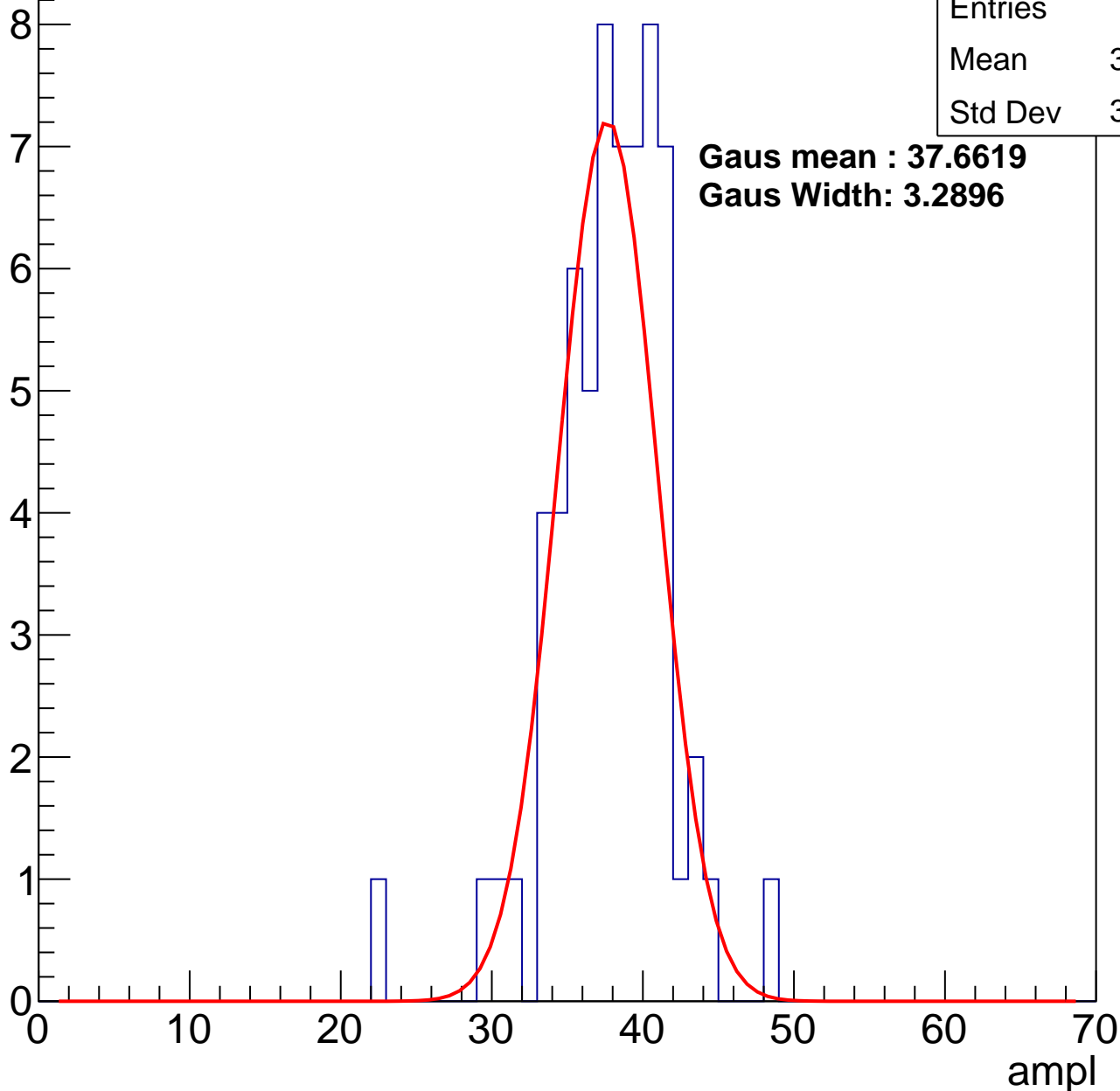
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	37.42
Std Dev	3.879

Gaus mean : 37.6619

Gaus Width: 3.2896



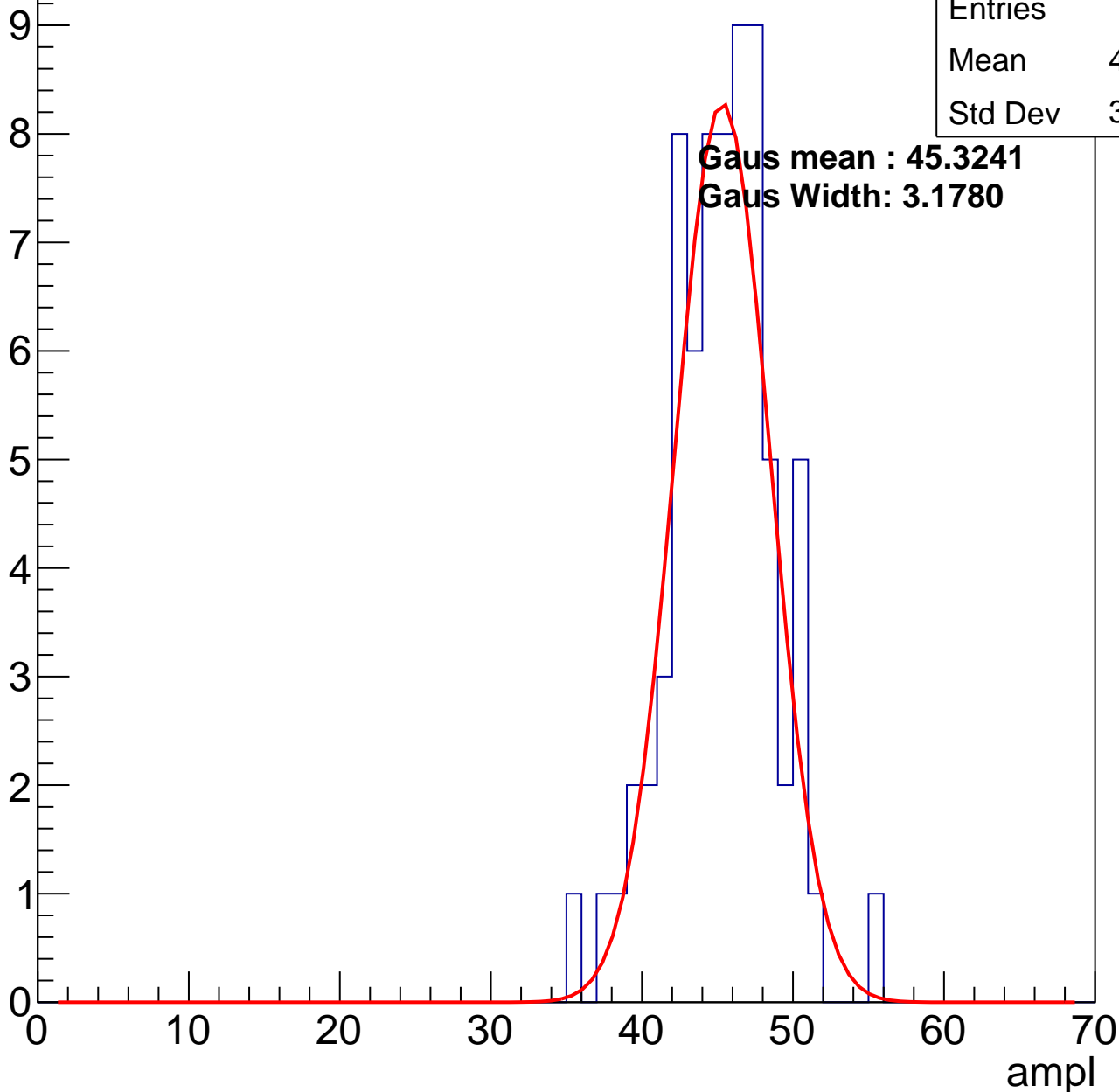
B1L102S, U22-ch125, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	44.83
Std Dev	3.472

Gaus mean : 45.3241
Gaus Width: 3.1780

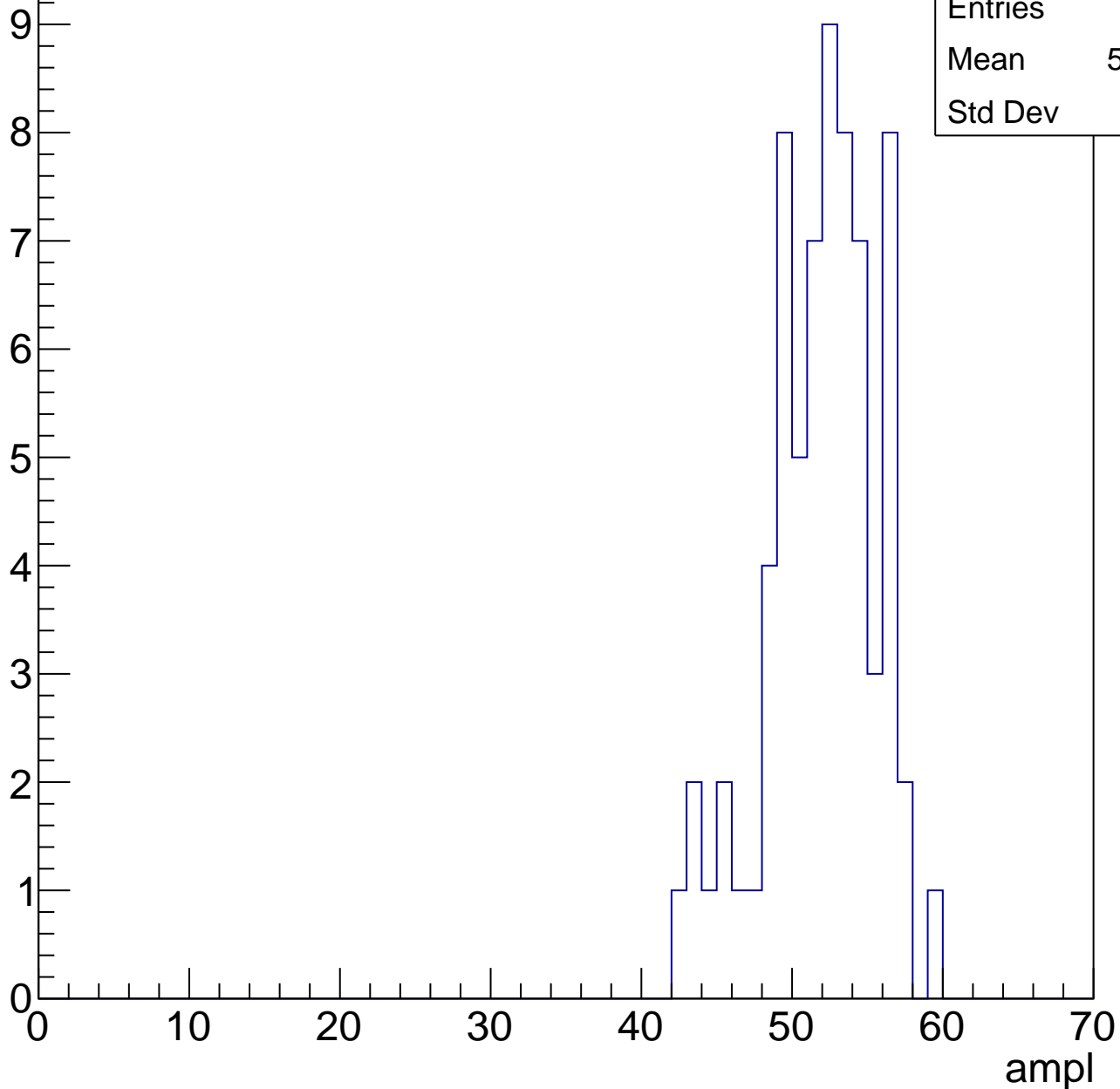


B1L102S, U22-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	51.46
Std Dev	3.62

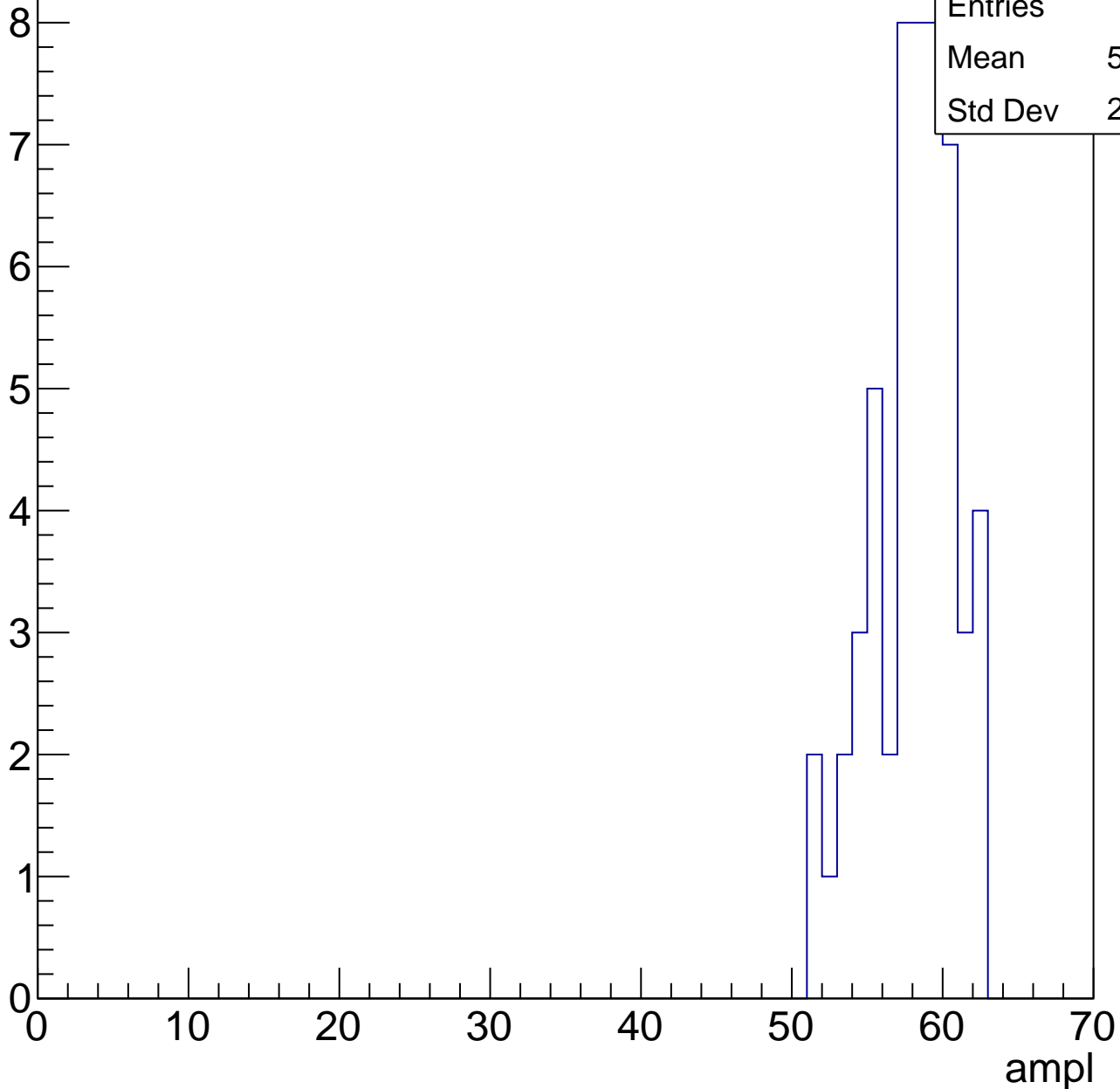


B1L102S, U22-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.58
Std Dev	2.784

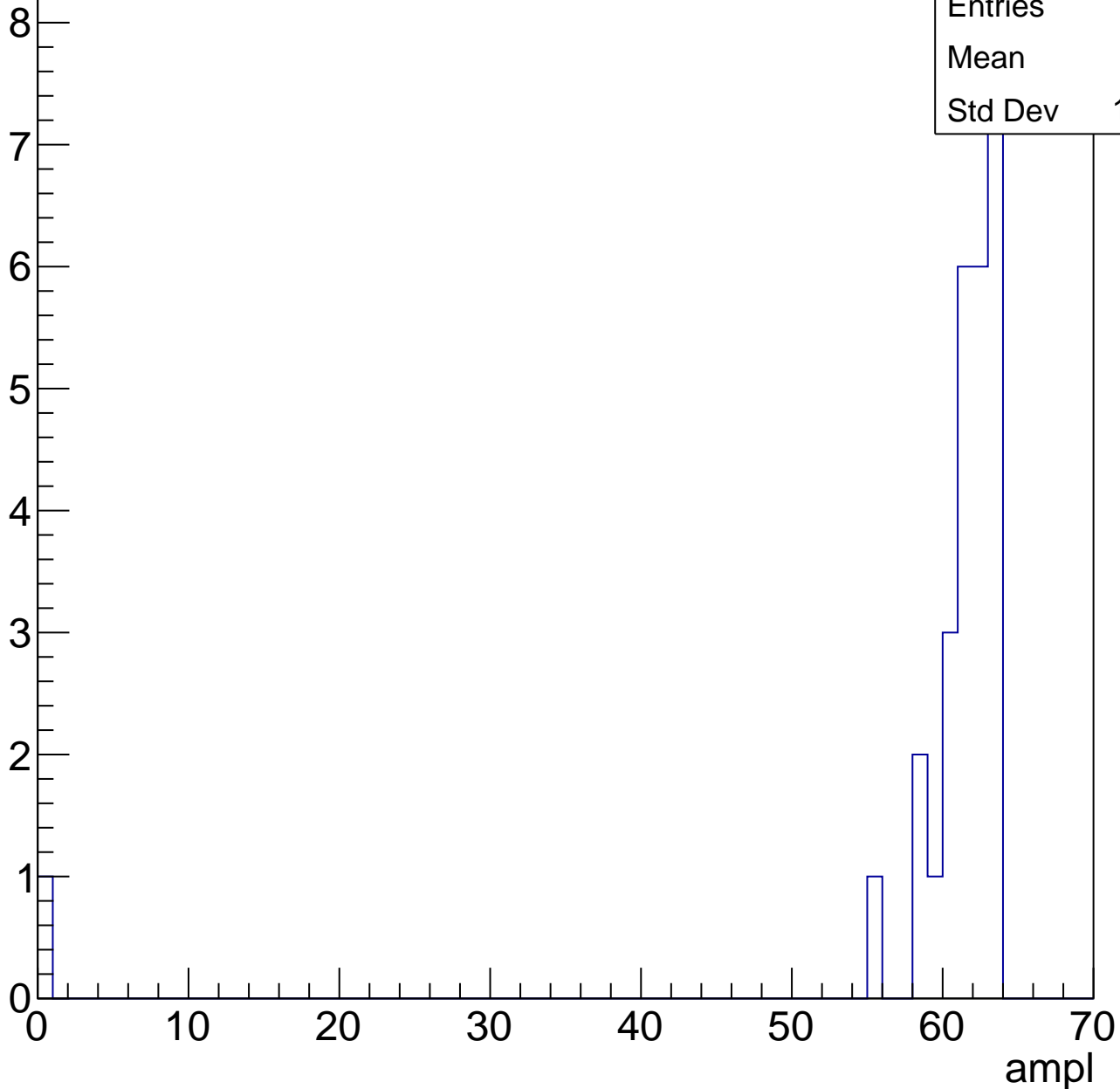


B1L102S, U22-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	59
Std Dev	11.51



B1L102S, U22-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L102S, U22-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U22-ch126, adc0

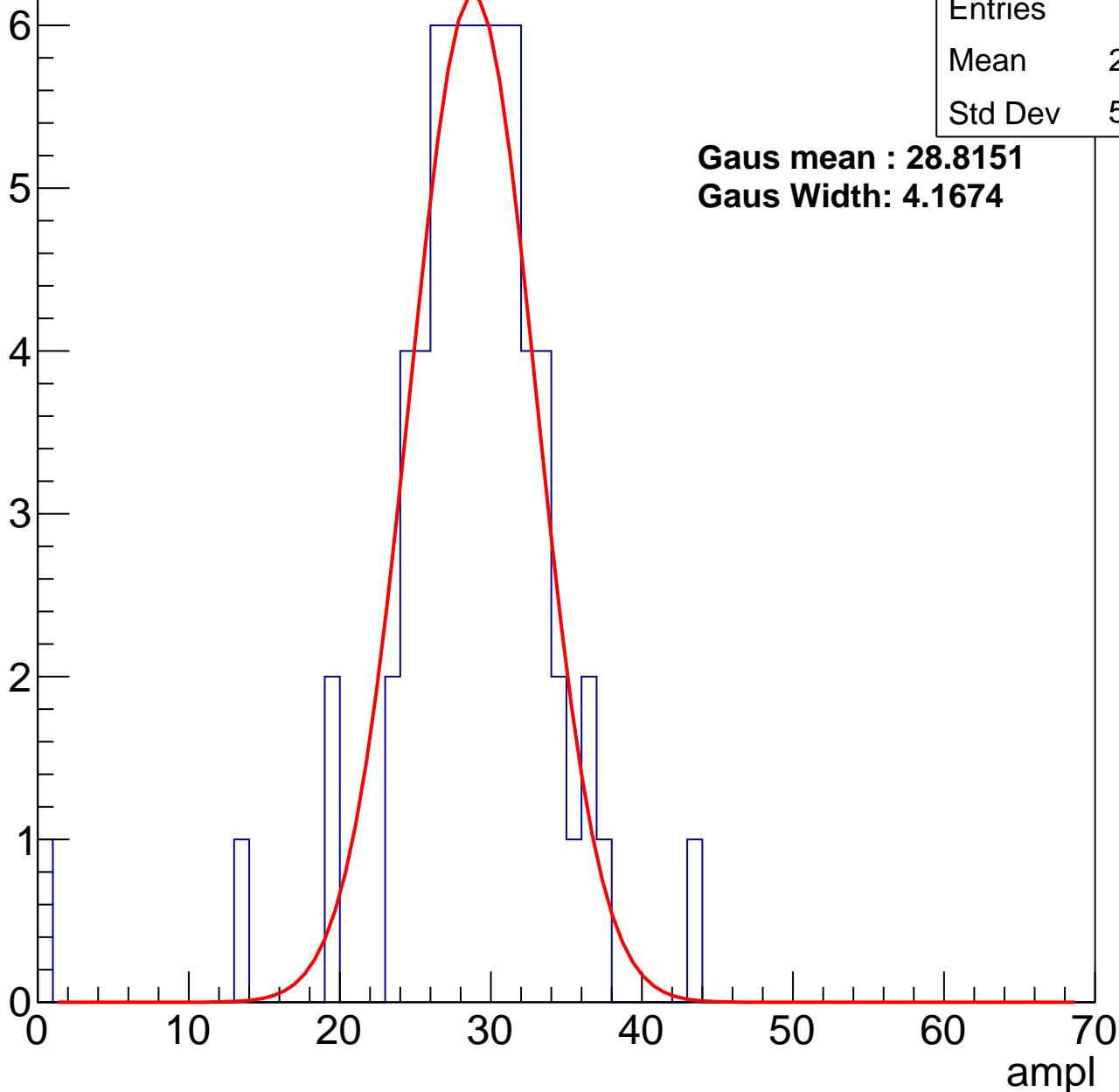
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	28.22
Std Dev	5.753

Gaus mean : 28.8151

Gaus Width: 4.1674



B1L102S, U22-ch126, adc1

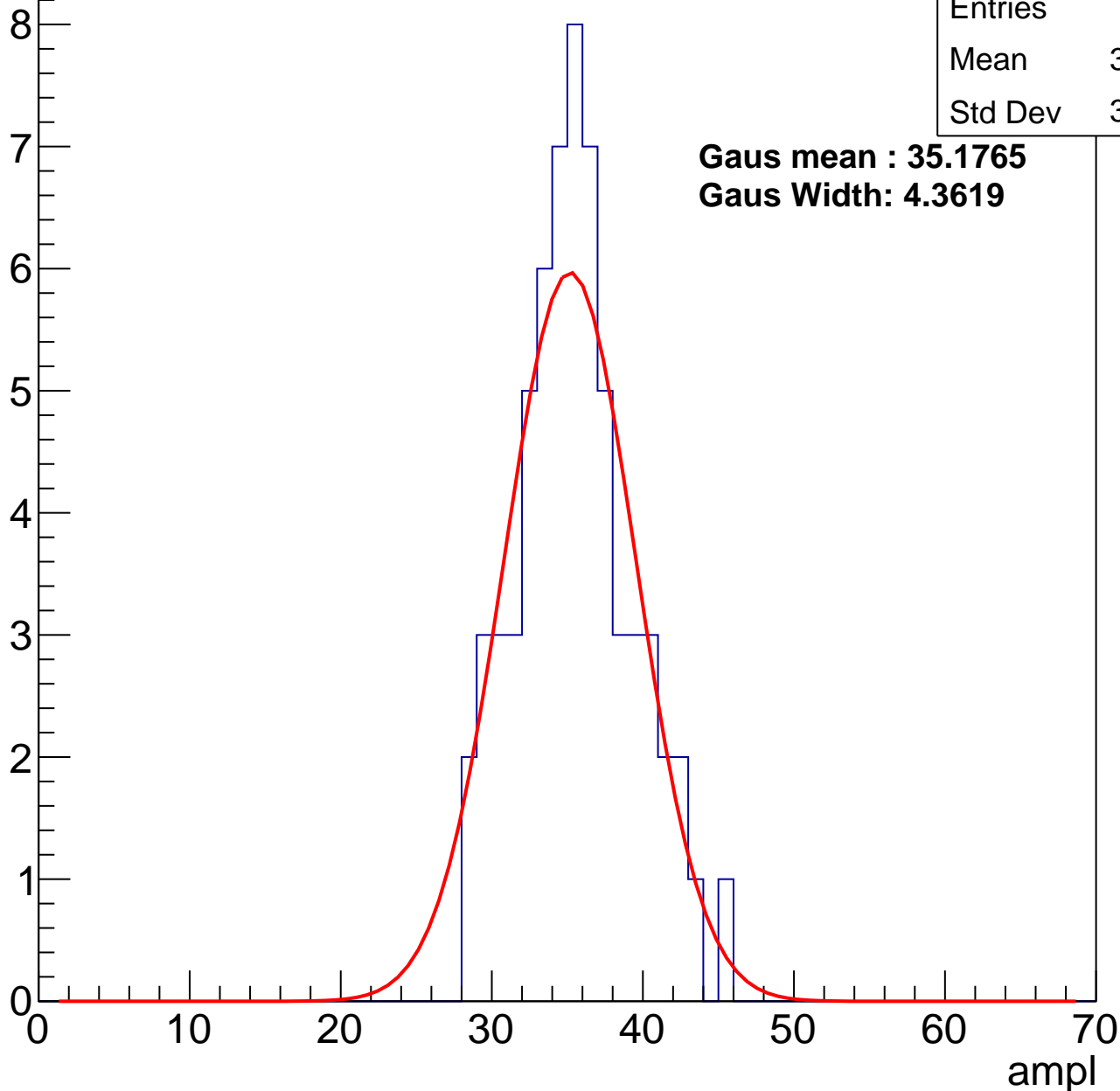
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.06
Std Dev	3.783

Gaus mean : 35.1765

Gaus Width: 4.3619



B1L102S, U22-ch126, adc2

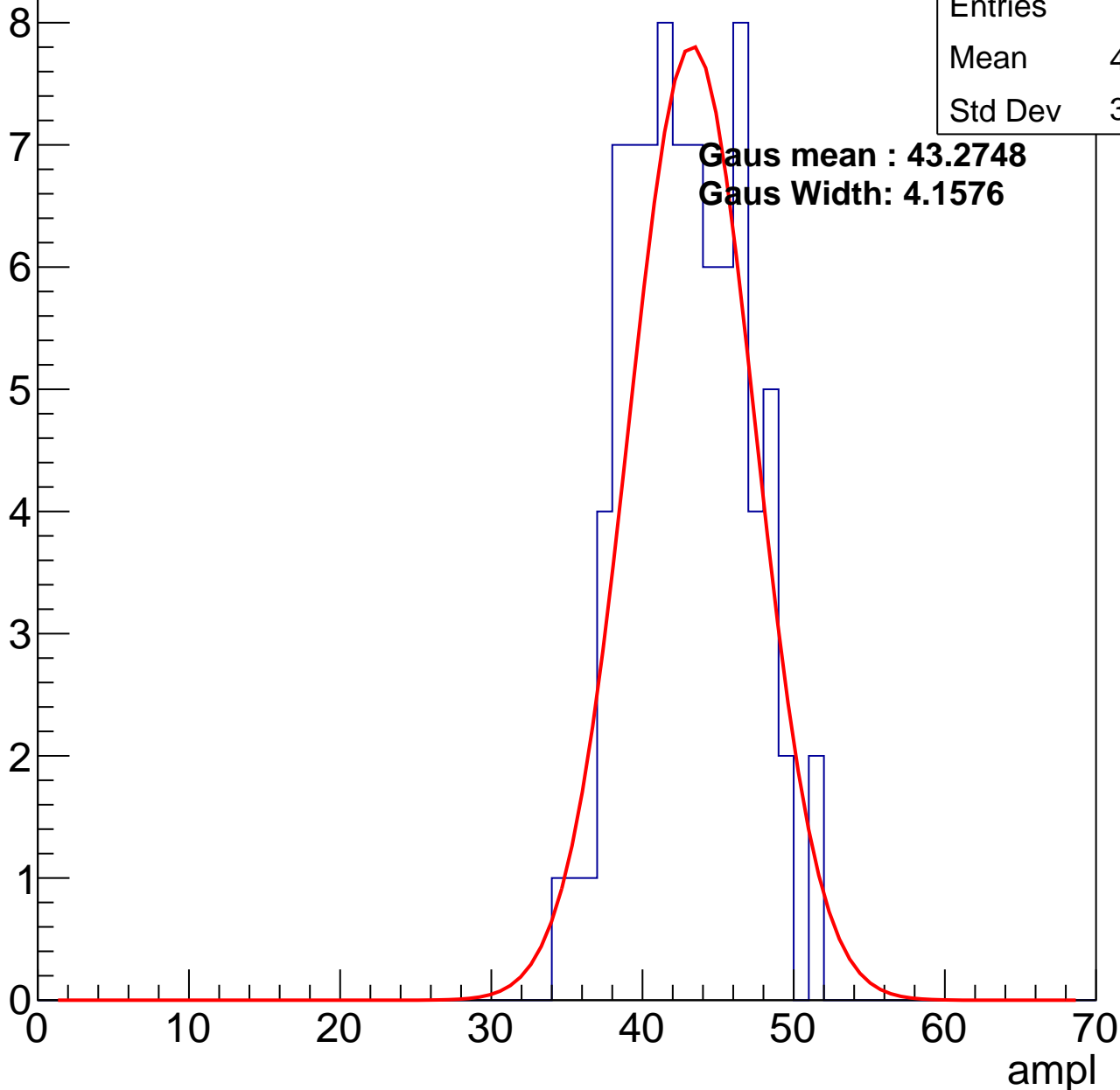
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	42.47
Std Dev	3.794

Gaus mean : 43.2748

Gaus Width: 4.1576

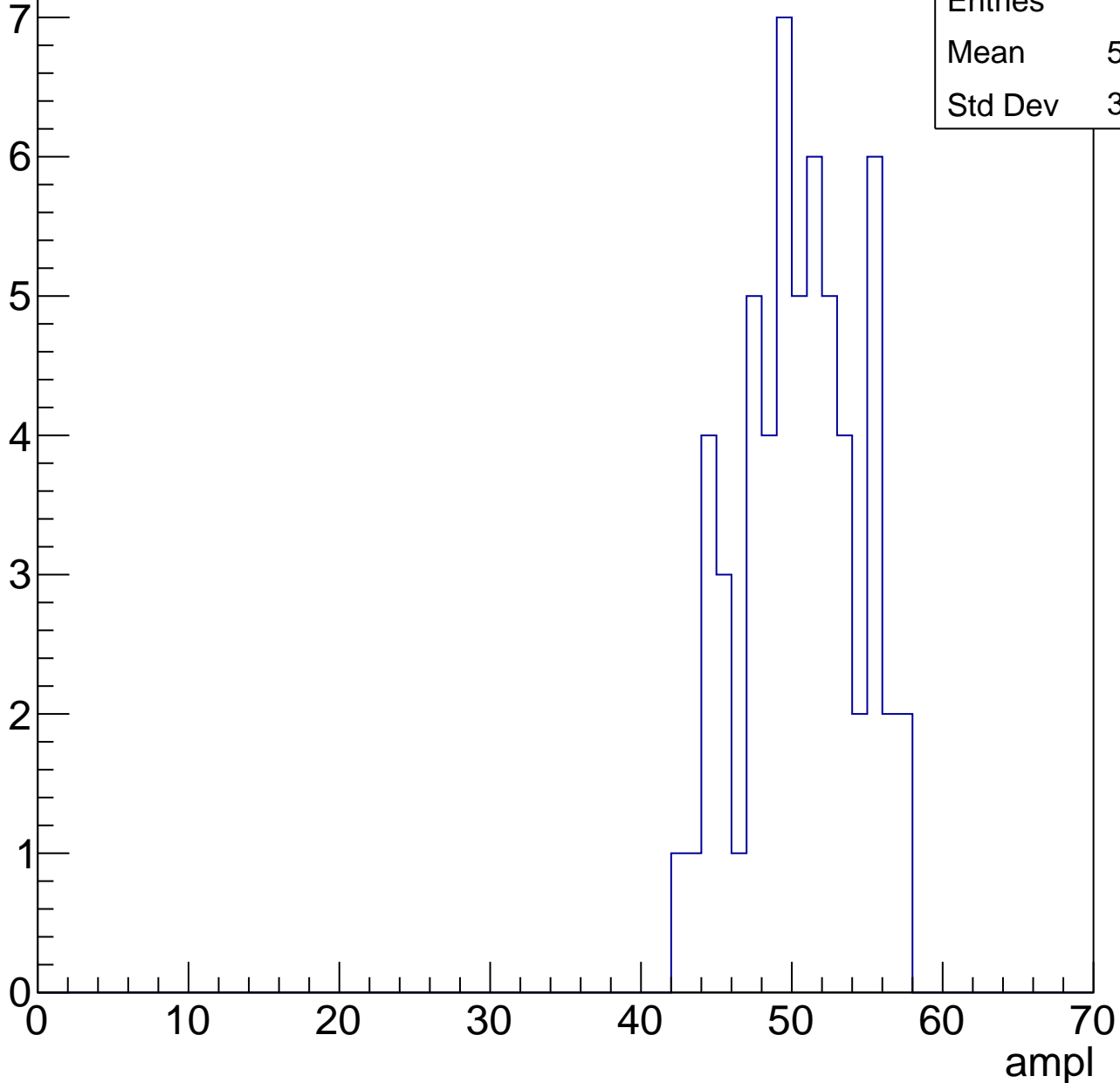


B1L102S, U22-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

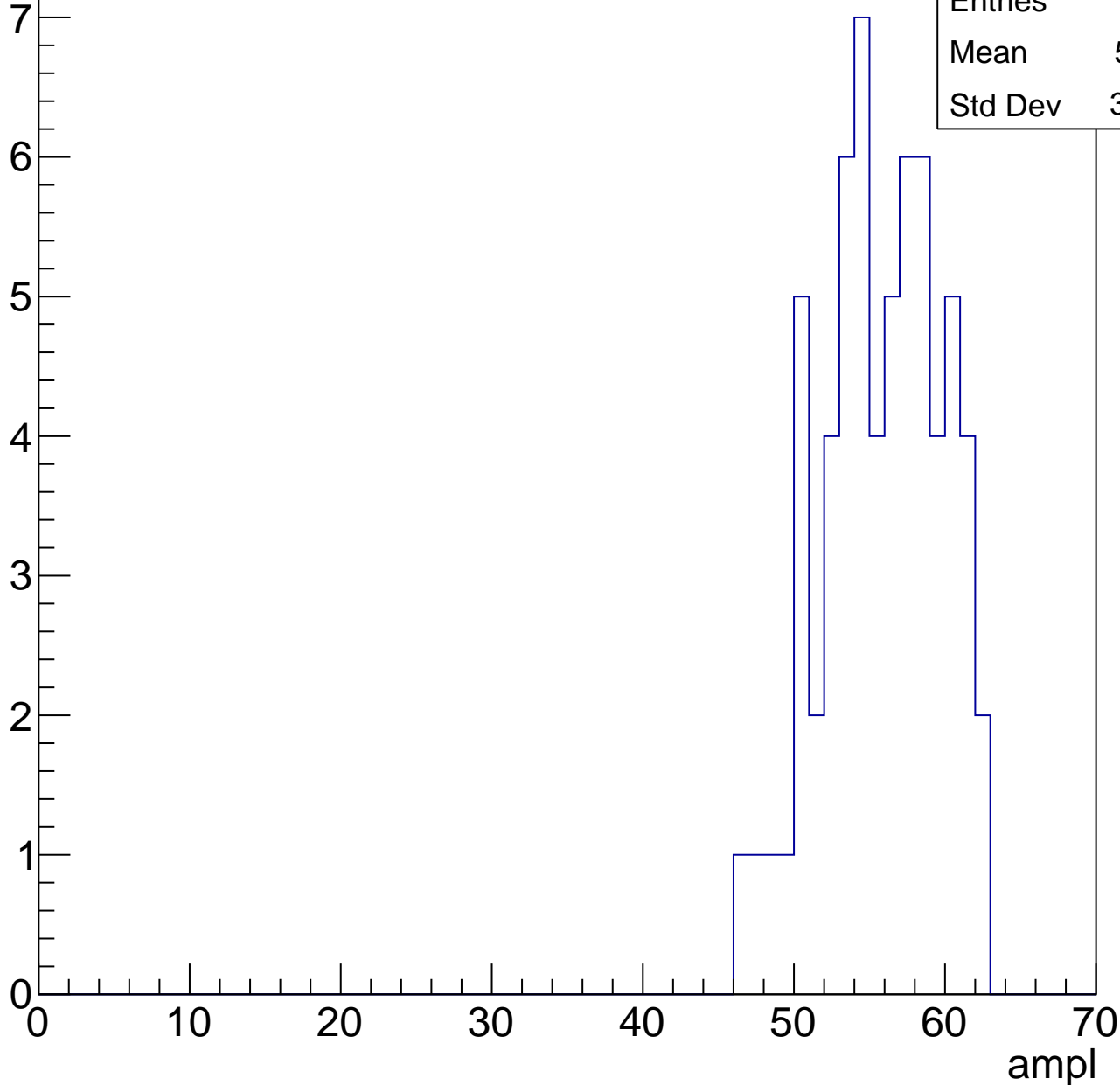
Entries	58
Mean	50.07
Std Dev	3.782



B1L102S, U22-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



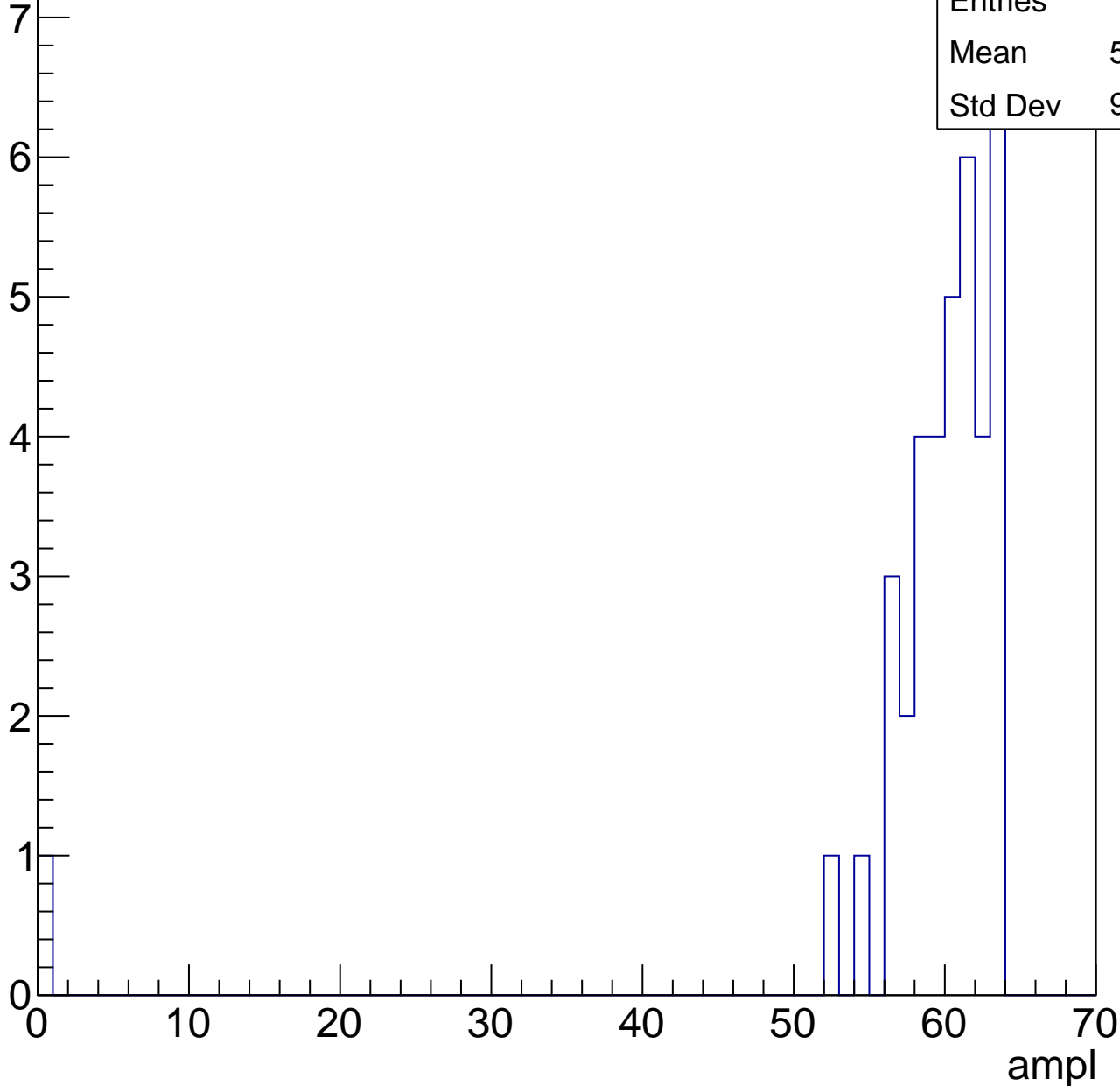
Entries	64
Mean	55.31
Std Dev	3.877

B1L102S, U22-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.18
Std Dev	9.928



B1L102S, U22-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.67
Std Dev	1.491

ampl

0 10 20 30 40 50 60 70

0 0.5 1 1.5 2 2.5 3 3.5 4

B1L102S, U22-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U22-ch127, adc0

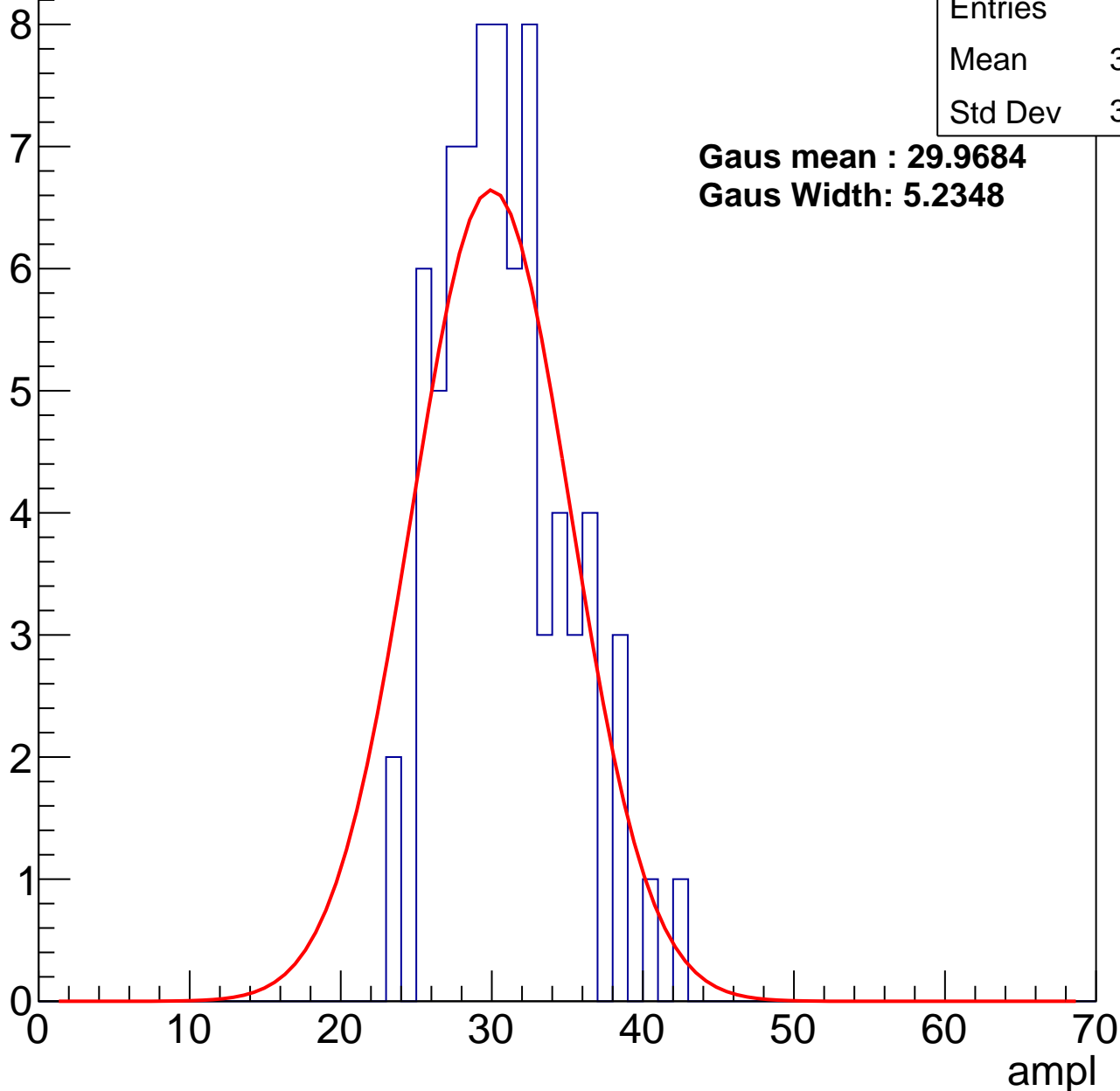
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	30.33
Std Dev	3.978

Gaus mean : 29.9684

Gaus Width: 5.2348



B1L102S, U22-ch127, adc1

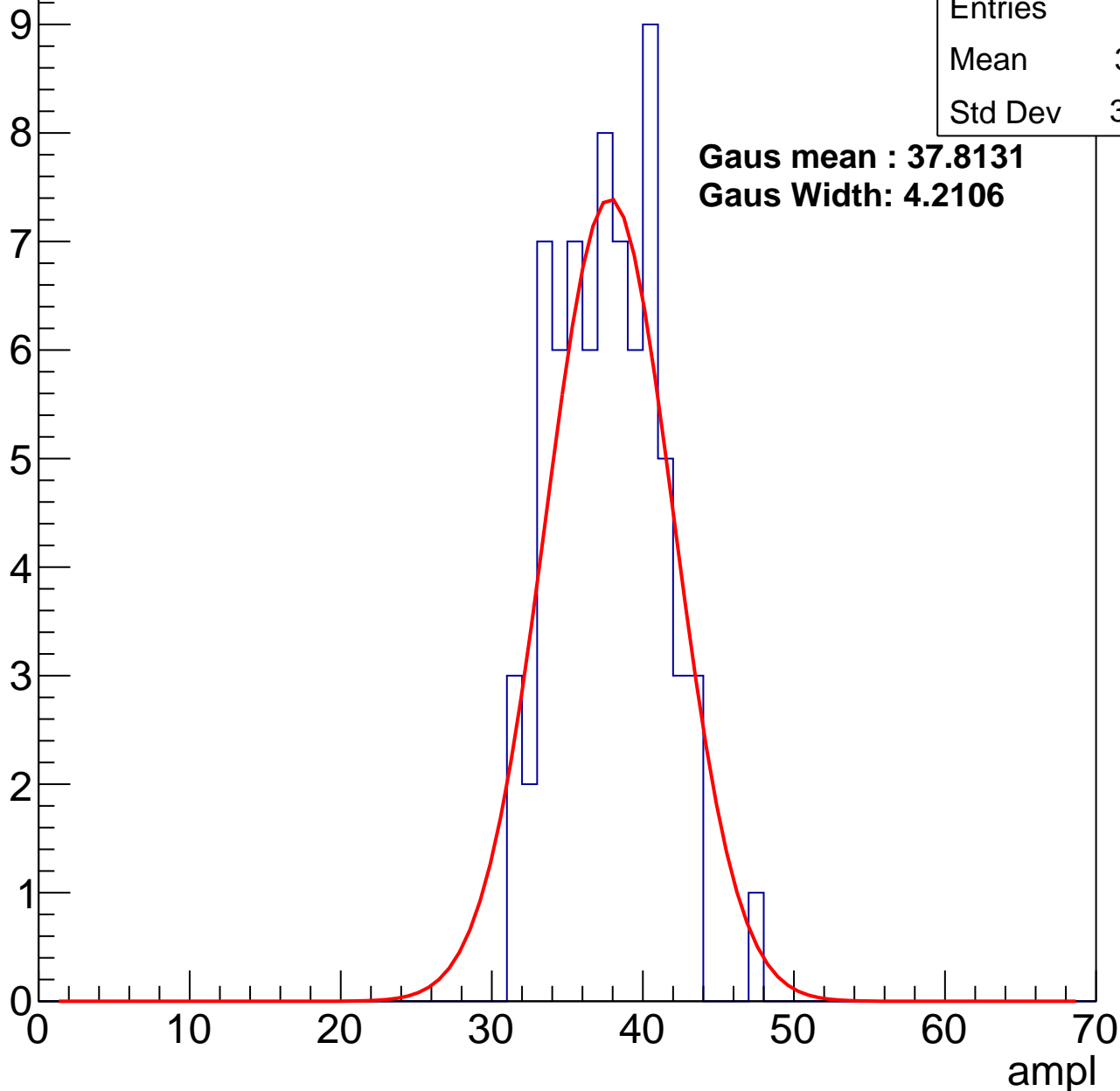
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	37.21
Std Dev	3.372

Gaus mean : 37.8131

Gaus Width: 4.2106



B1L102S, U22-ch127, adc2

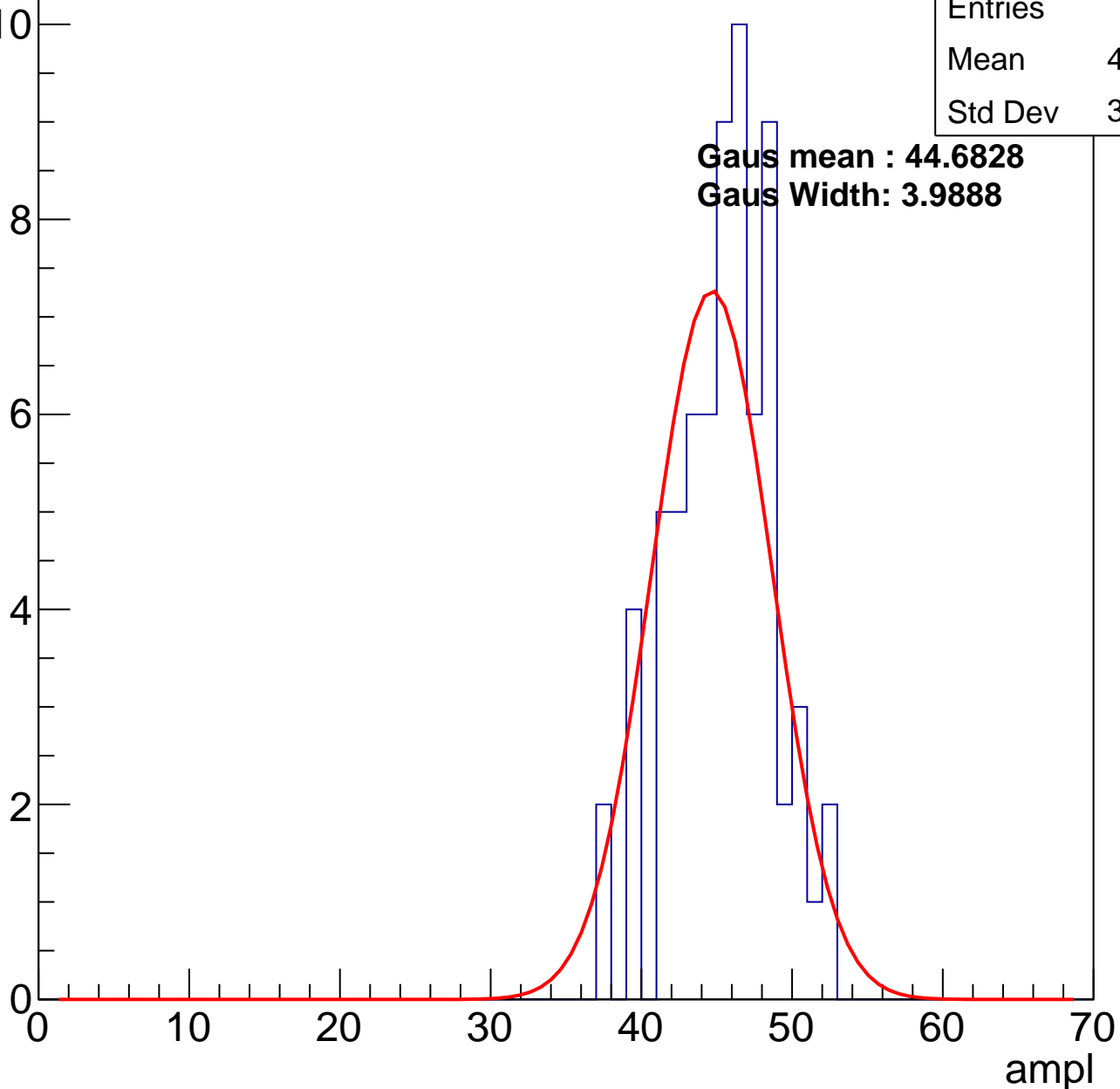
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	44.99
Std Dev	3.344

Gaus mean : 44.6828

Gaus Width: 3.9888

Entry

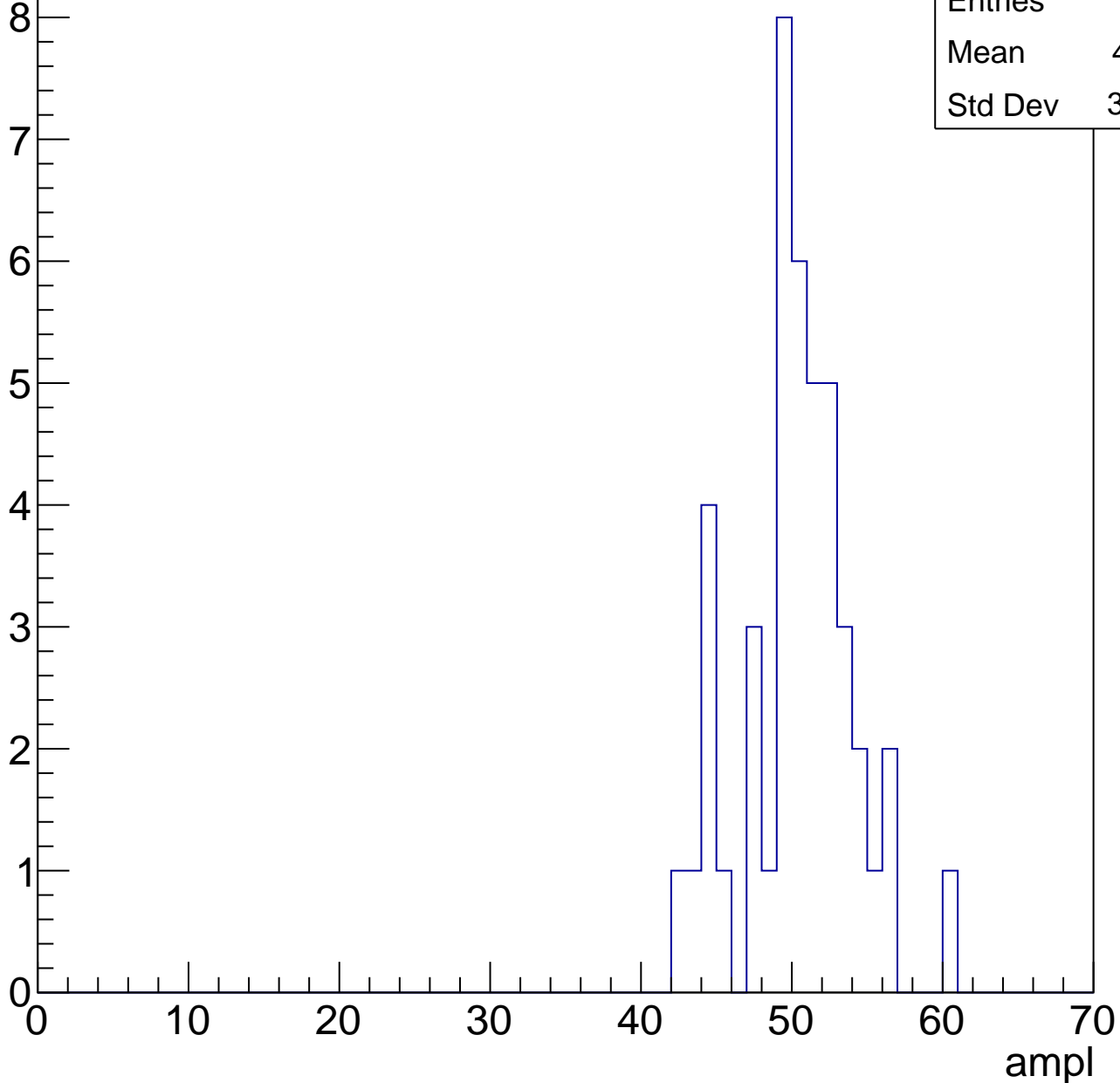


B1L102S, U22-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	49.91
Std Dev	3.698

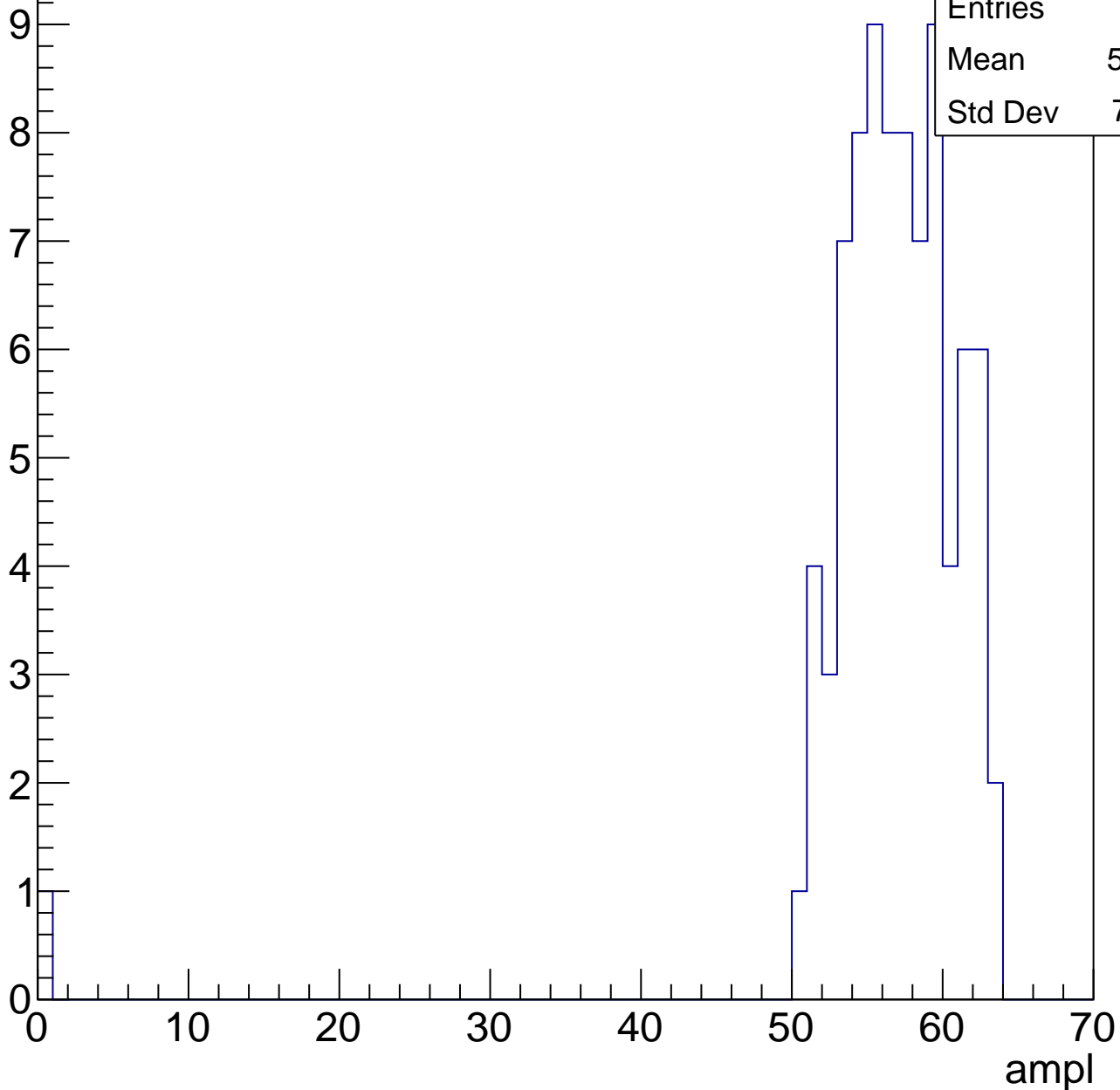


B1L102S, U22-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	56.06
Std Dev	7.001

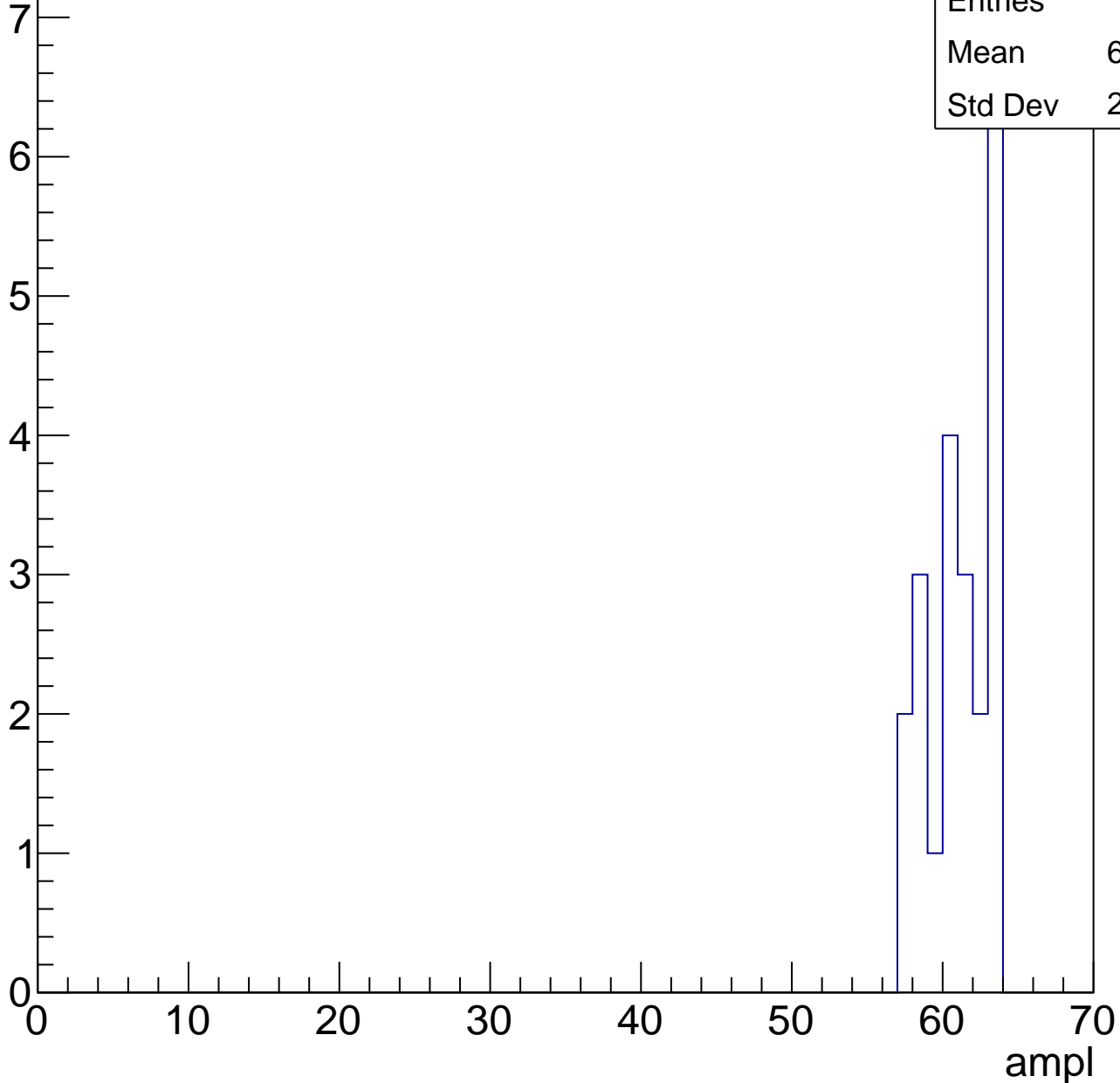


B1L102S, U22-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	22
Mean	60.68
Std Dev	2.076



B1L102S, U22-ch127, adc6

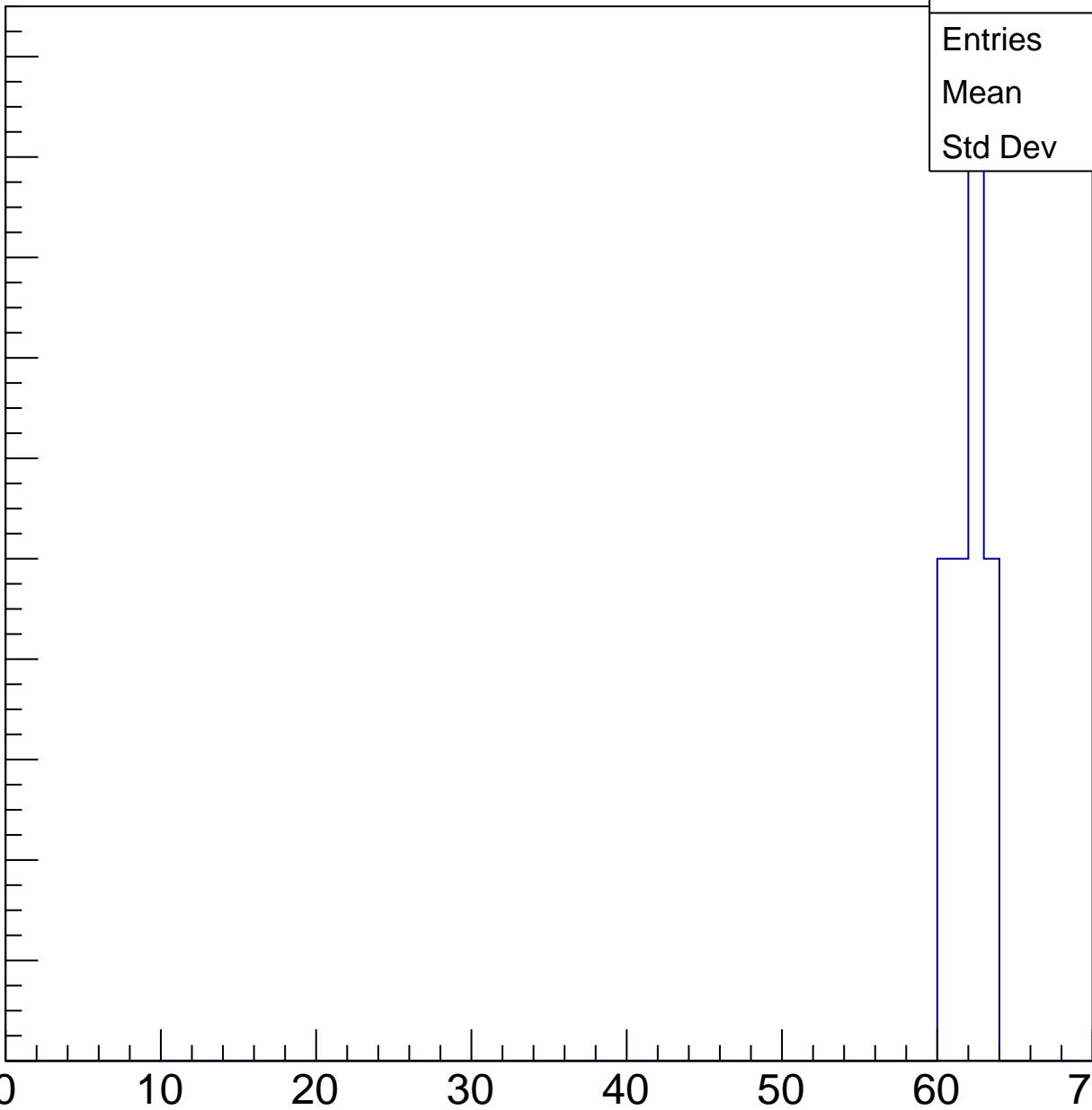
calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.02

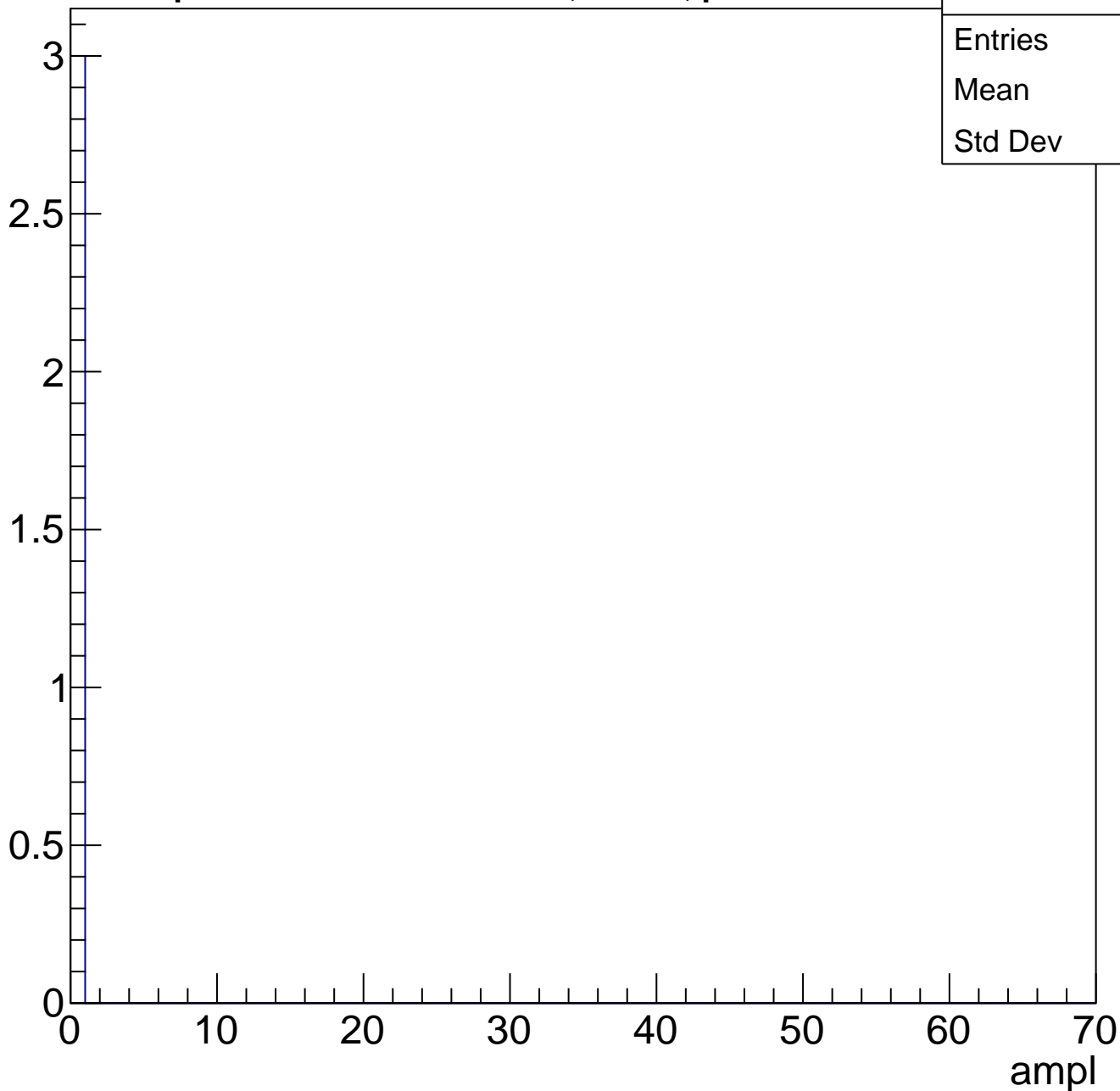
ampl



B1L102S, U22-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

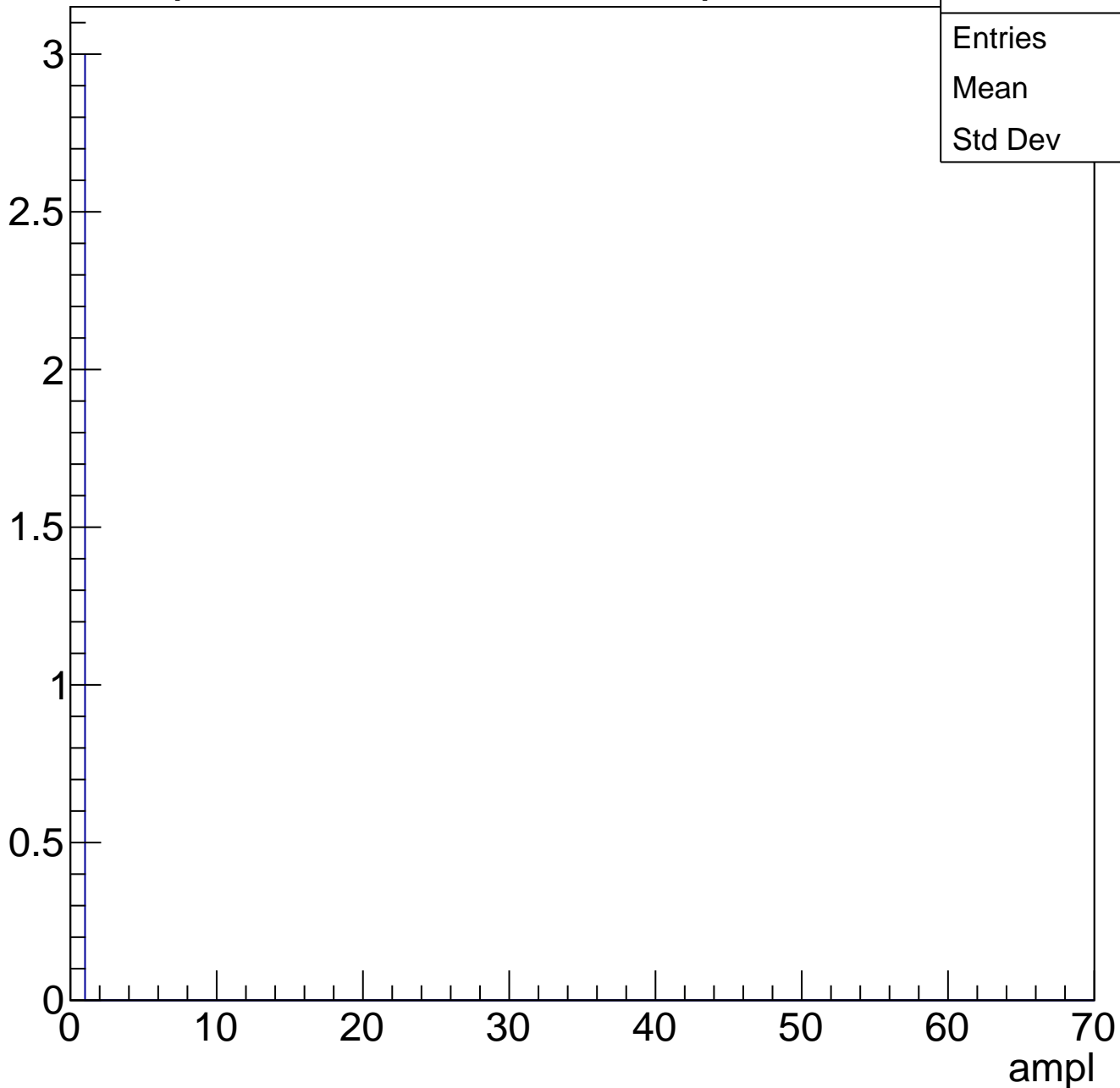


Entries	3
Mean	0
Std Dev	0

B1L102S, U22-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0