

B1L103S, U24-ch0, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	122
Mean	20.42
Std Dev	12.07

Gaus mean : 27.7225

Gaus Width: 3.8327

Entry

25

20

15

10

5

0

0

10

20

30

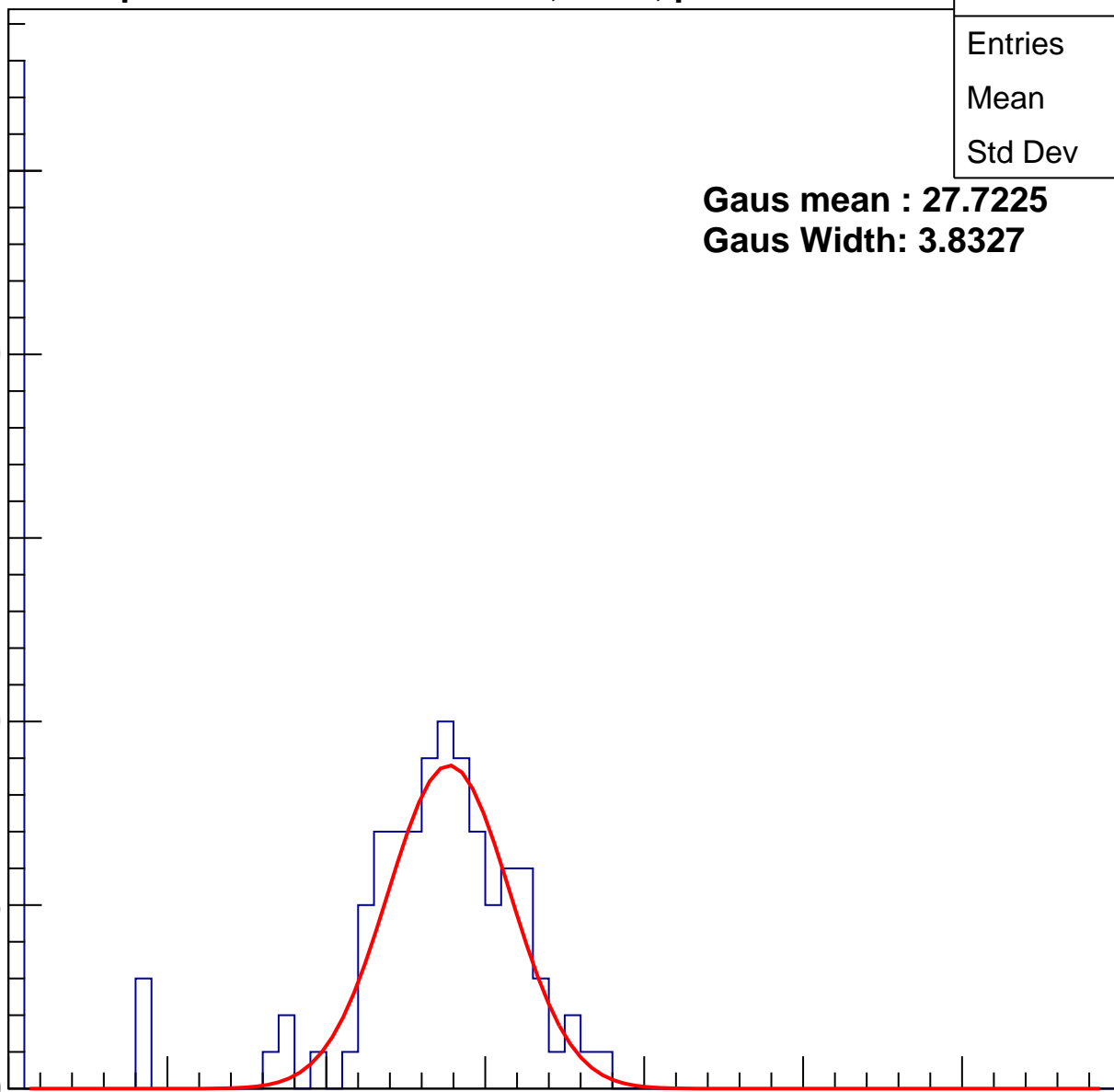
40

50

60

70

ampl



B1L103S, U24-ch0, adc1

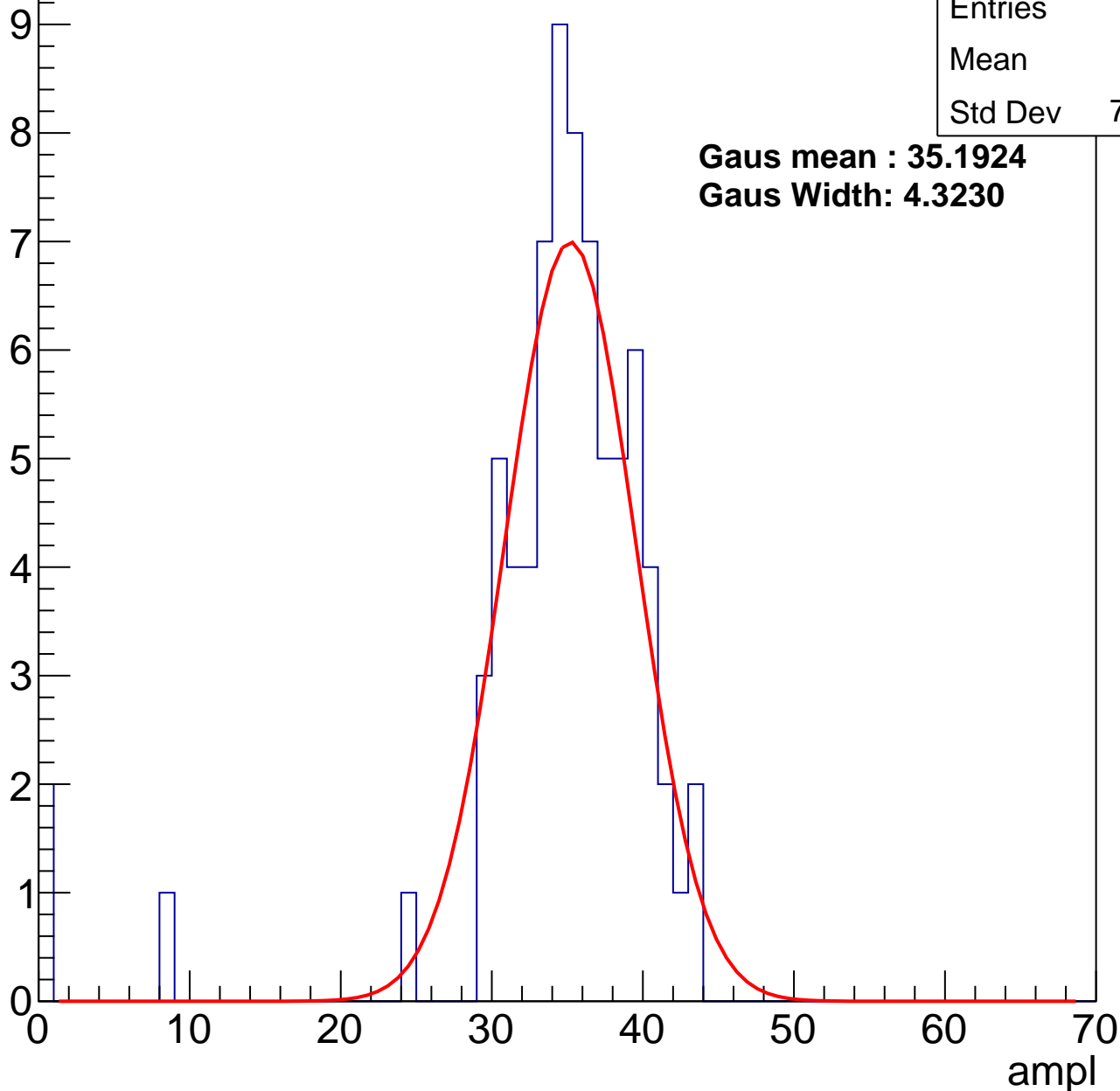
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	33.8
Std Dev	7.325

Gaus mean : 35.1924

Gaus Width: 4.3230



B1L103S, U24-ch0, adc2

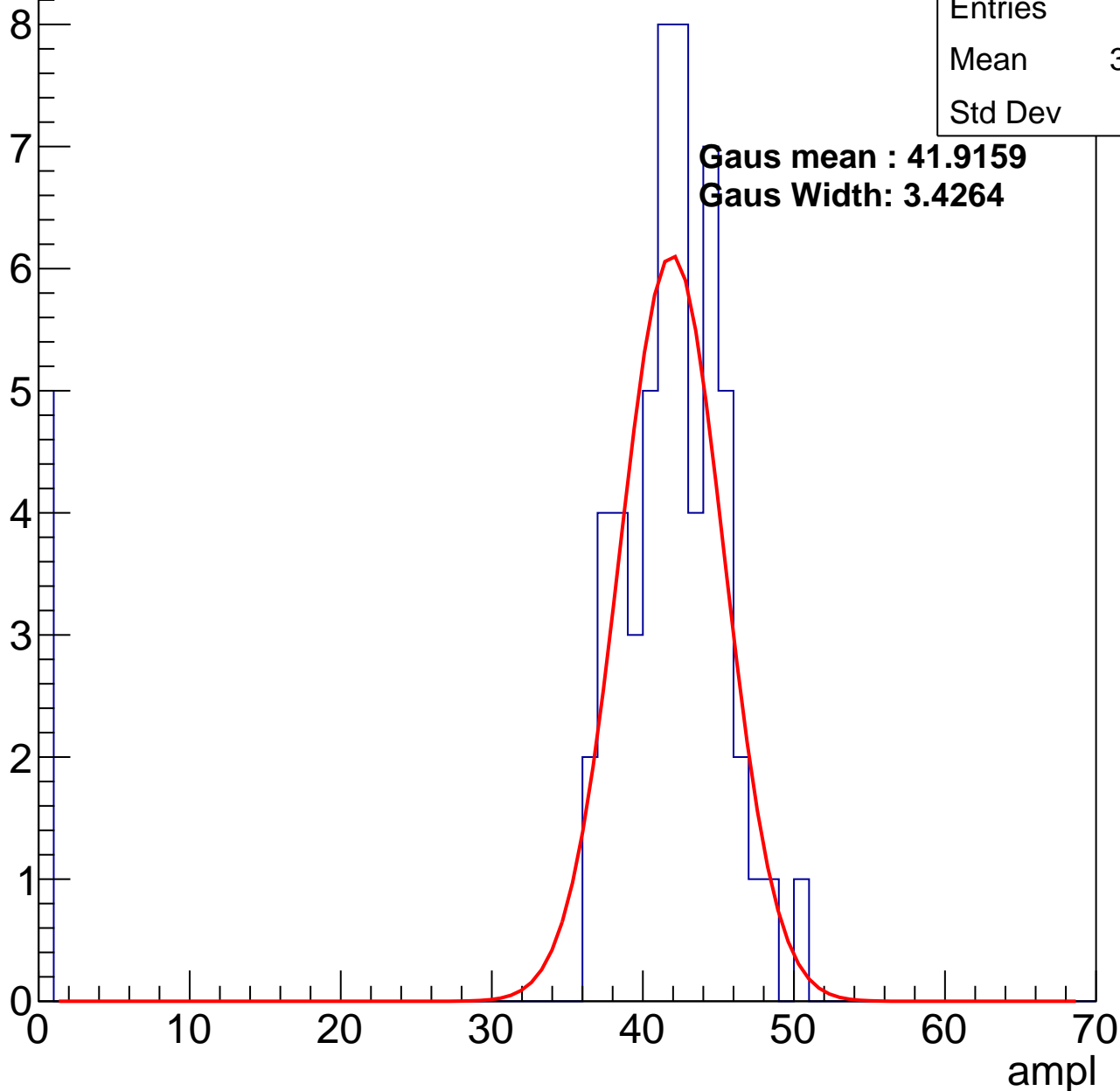
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	38.25
Std Dev	11.9

Gaus mean : 41.9159

Gaus Width: 3.4264



B1L103S, U24-ch0, adc3

calib_packv5_041523_1651.root, FC#0, port C2

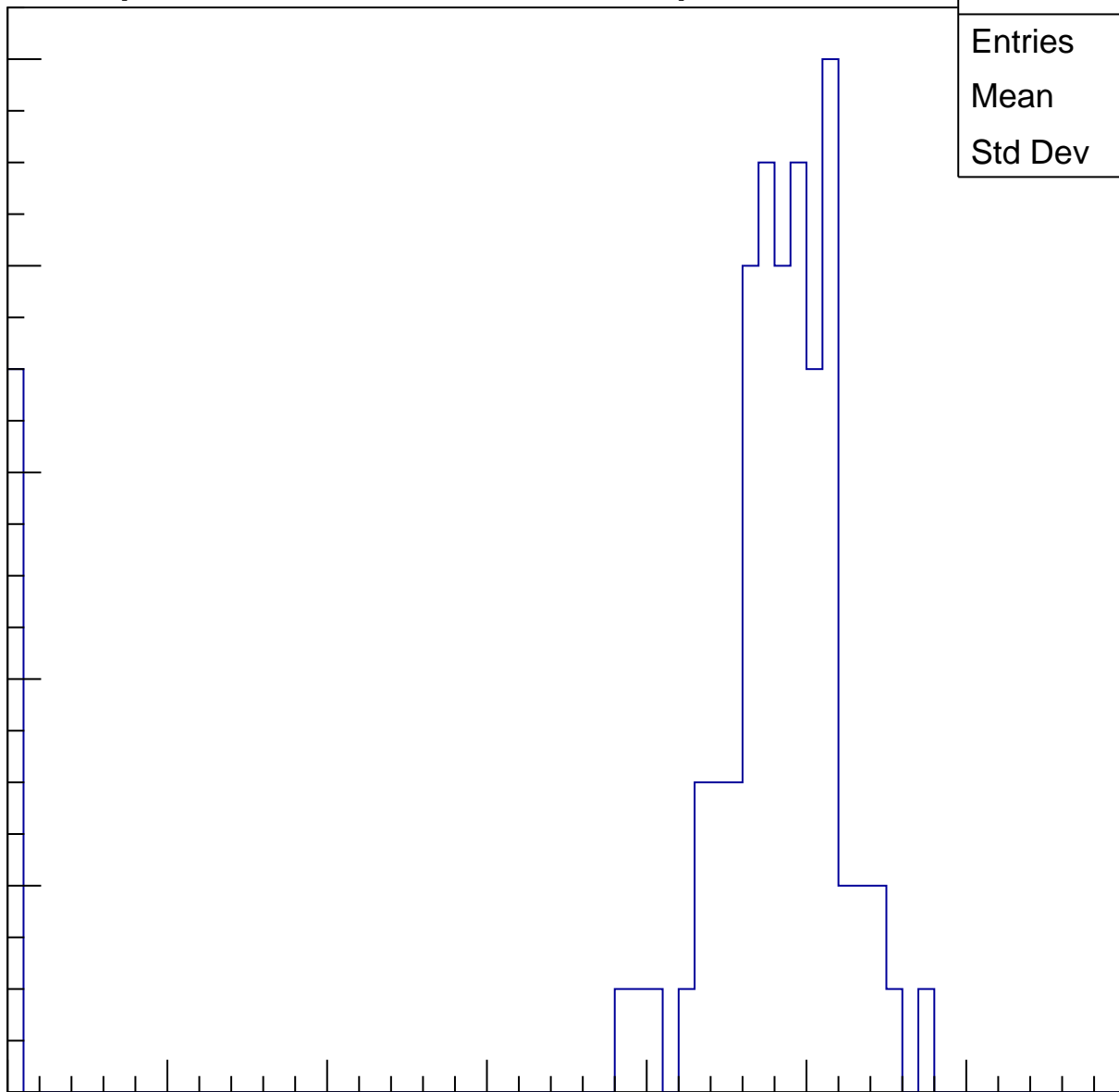
Entries	79
Mean	43.81
Std Dev	14.06

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

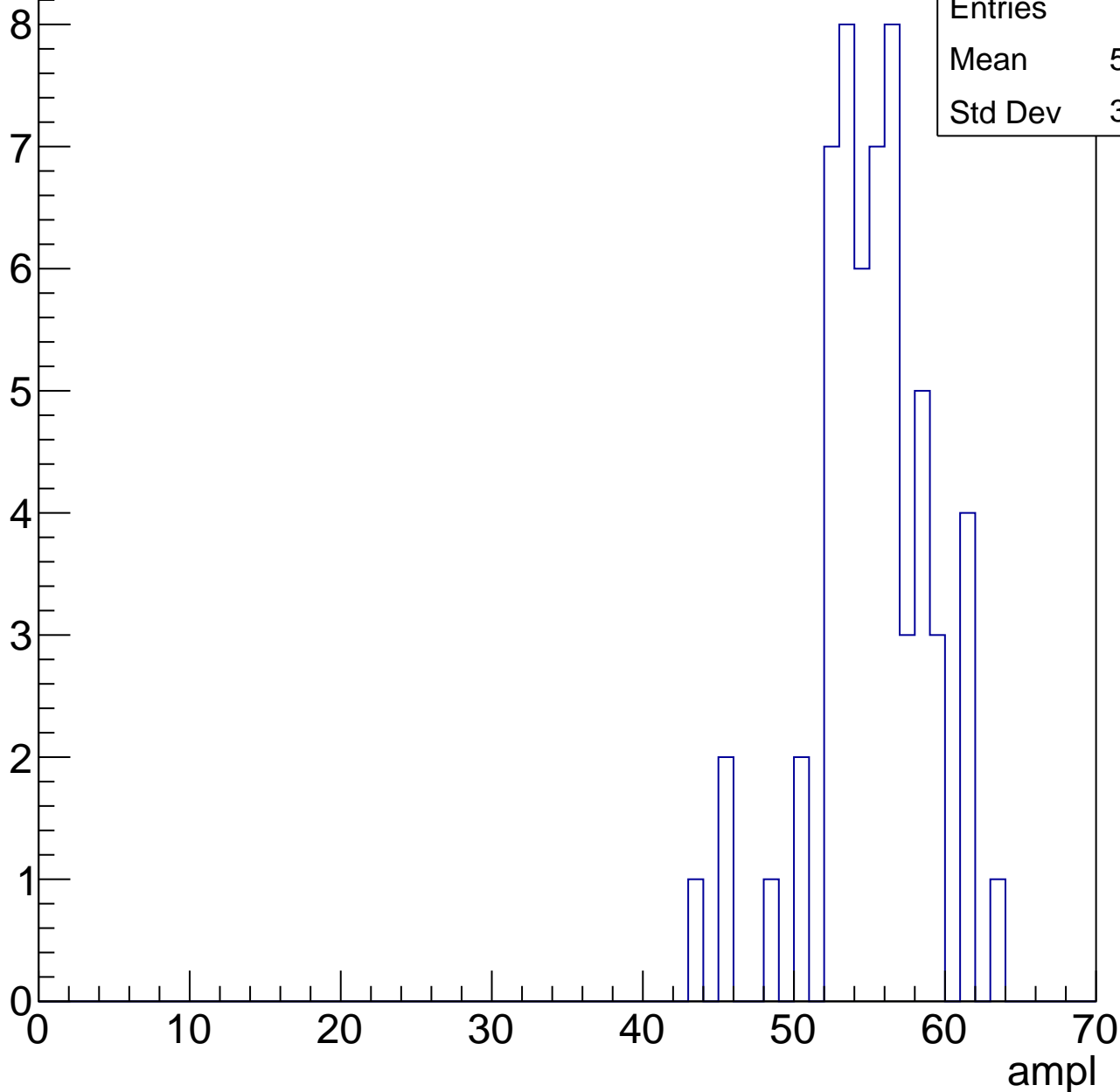


B1L103S, U24-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	54.67
Std Dev	3.848

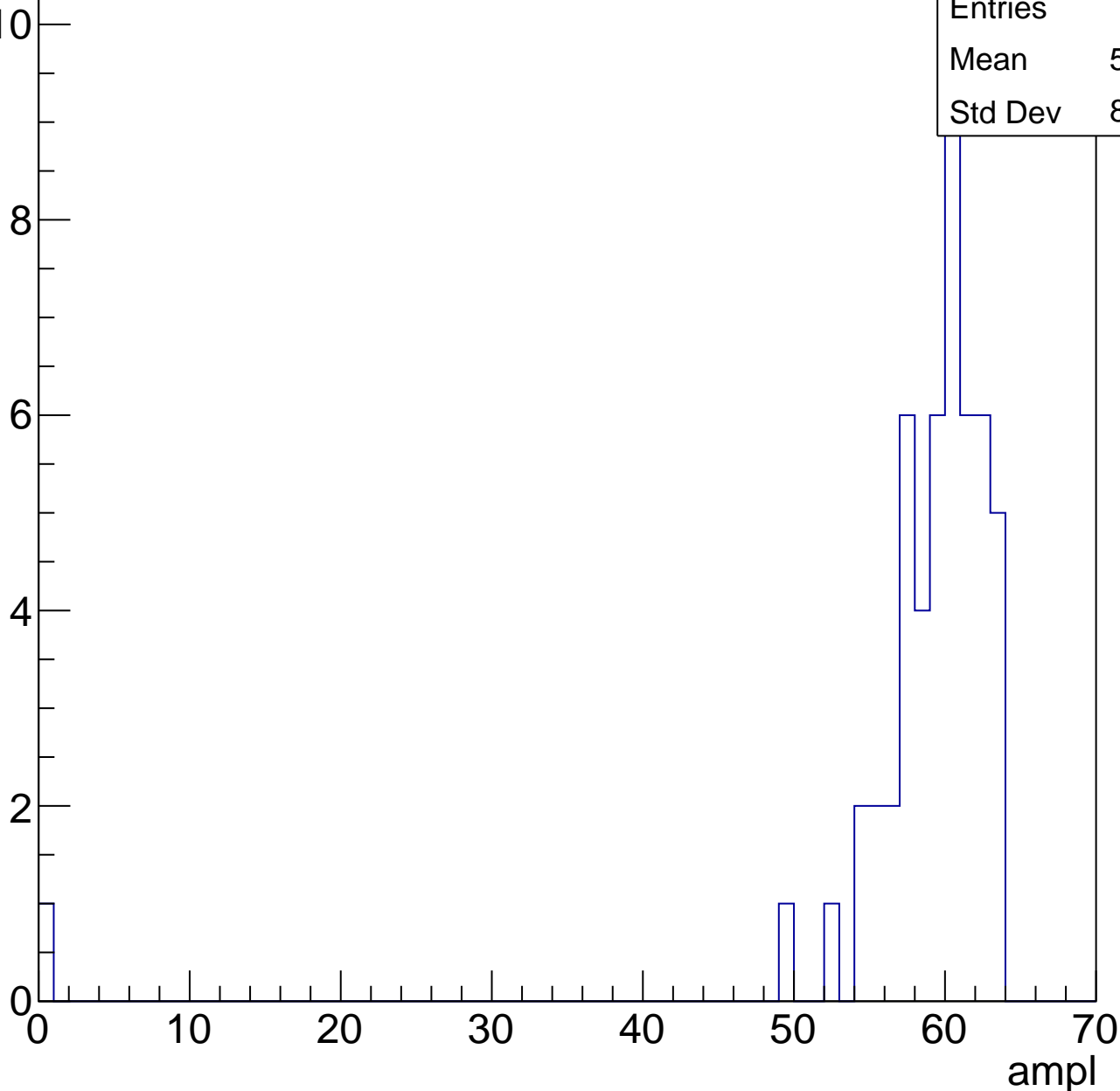


B1L103S, U24-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

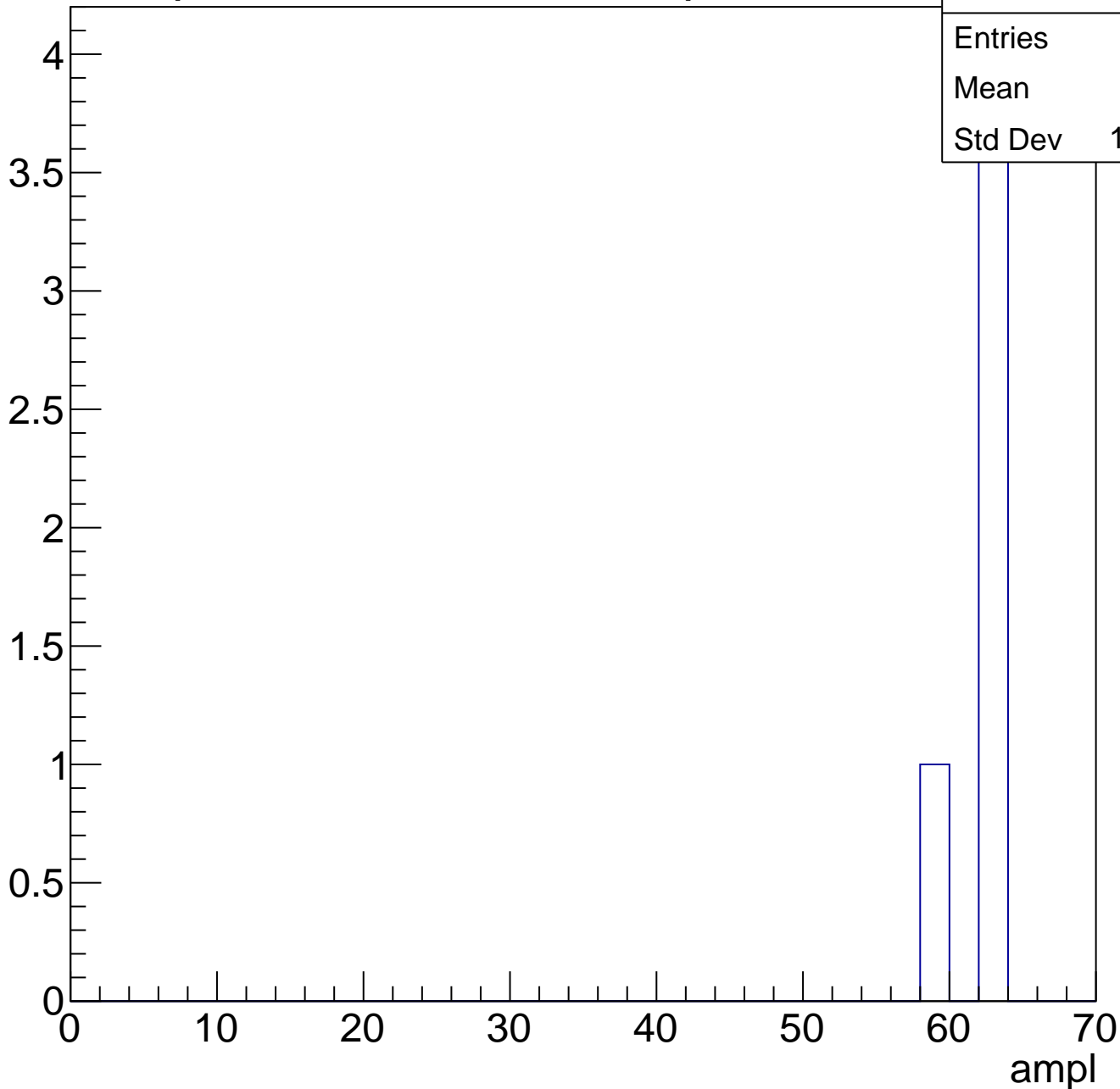
Entries	52
Mean	57.92
Std Dev	8.622



B1L103S, U24-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

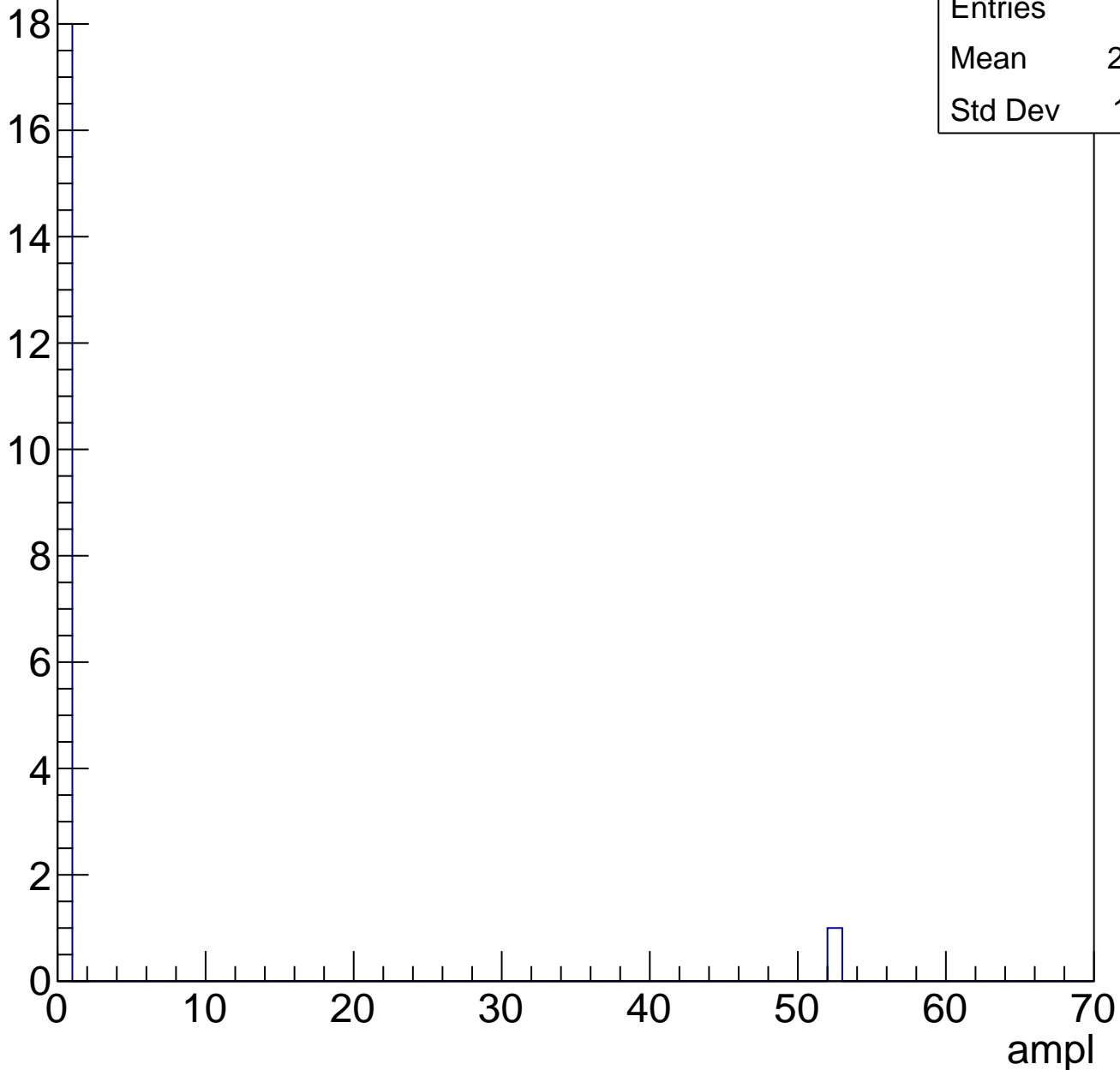


B1L103S, U24-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	2.737
Std Dev	11.61

Entry



B1L103S, U24-ch1, adc0

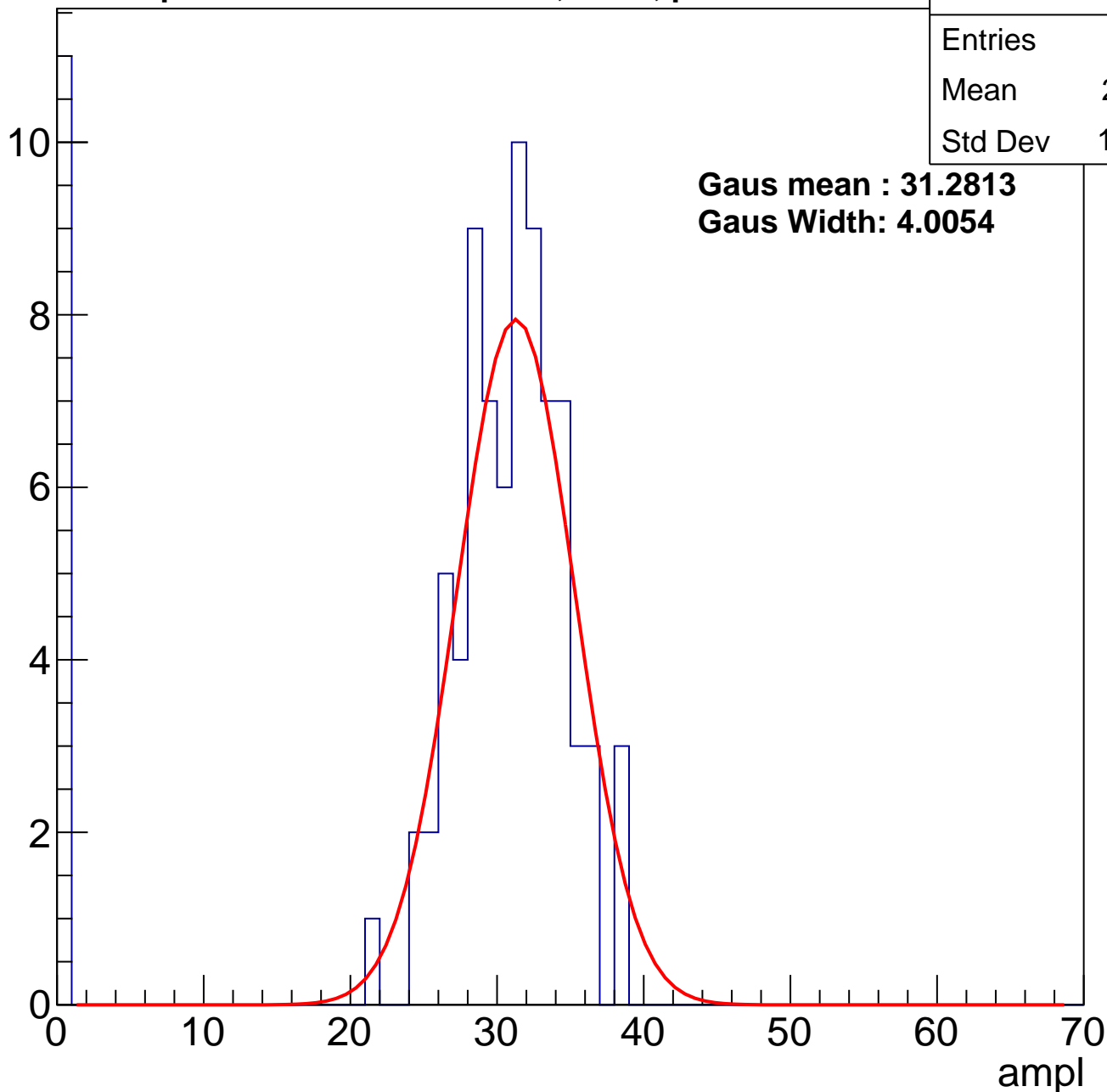
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	26.81
Std Dev	10.57

Gaus mean : 31.2813

Gaus Width: 4.0054

Entry



B1L103S, U24-ch1, adc1

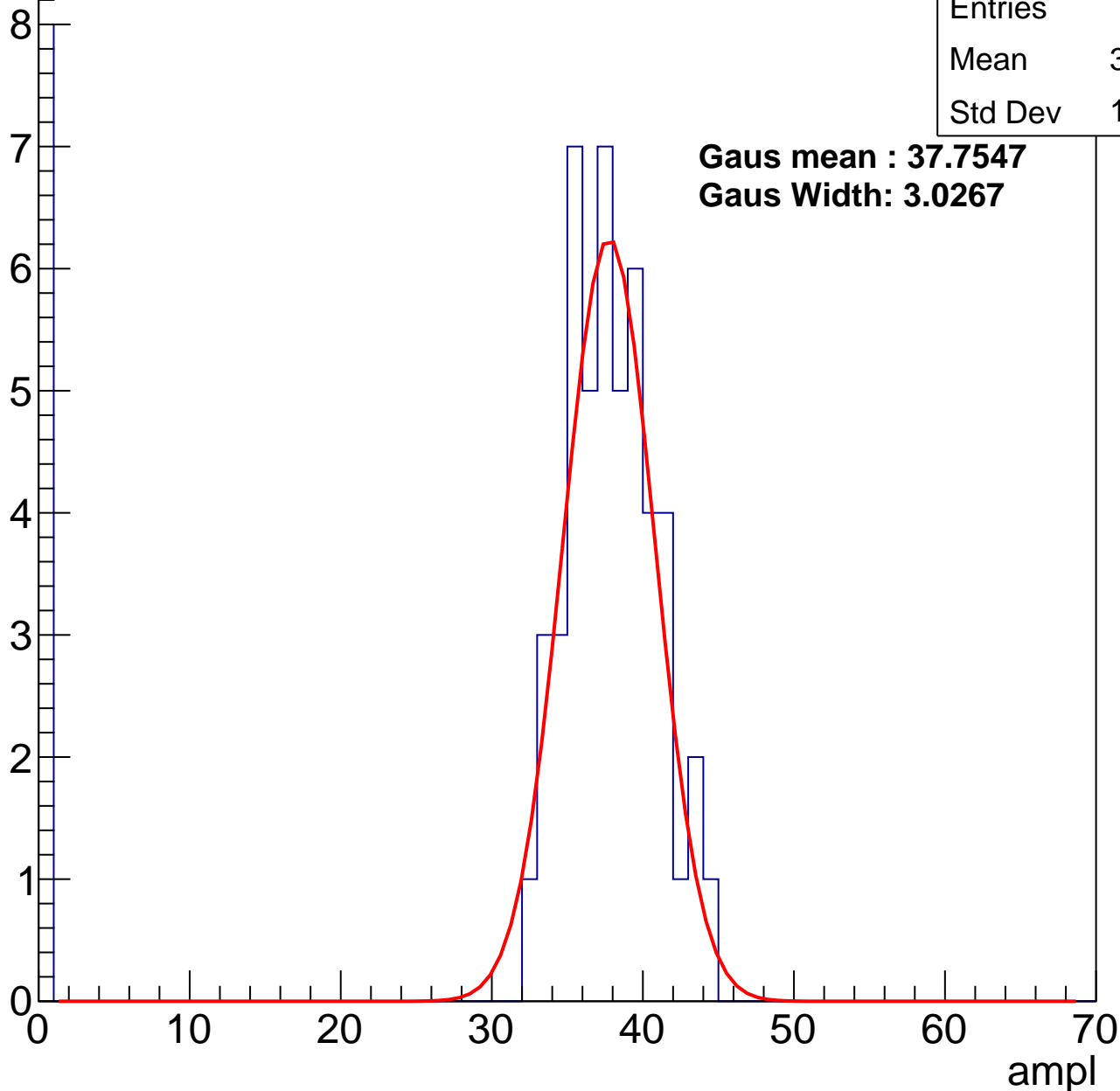
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	32.23
Std Dev	13.29

Gaus mean : 37.7547

Gaus Width: 3.0267



B1L103S, U24-ch1, adc2

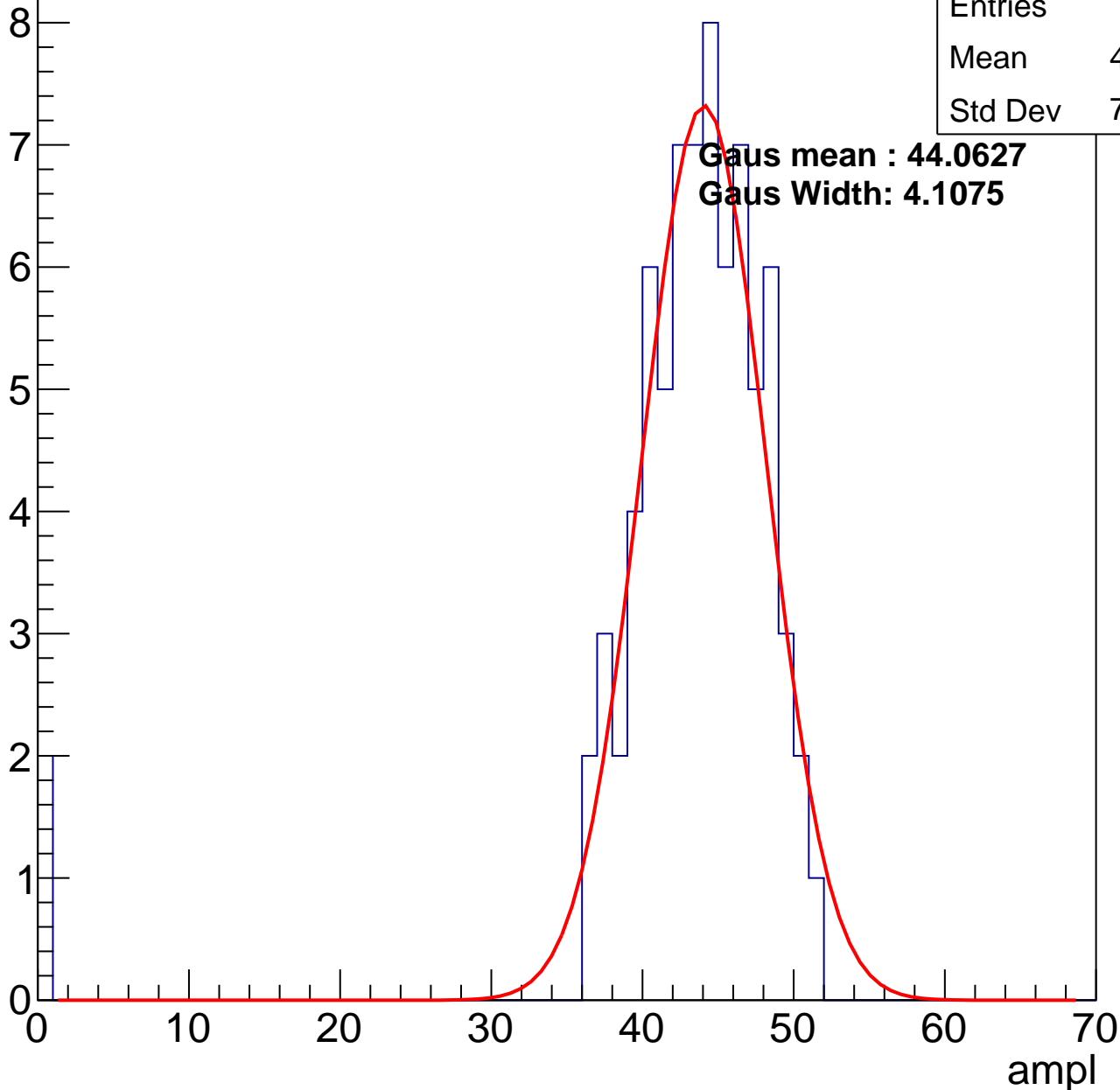
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	42.37
Std Dev	7.834

Gaus mean : 44.0627

Gaus Width: 4.1075

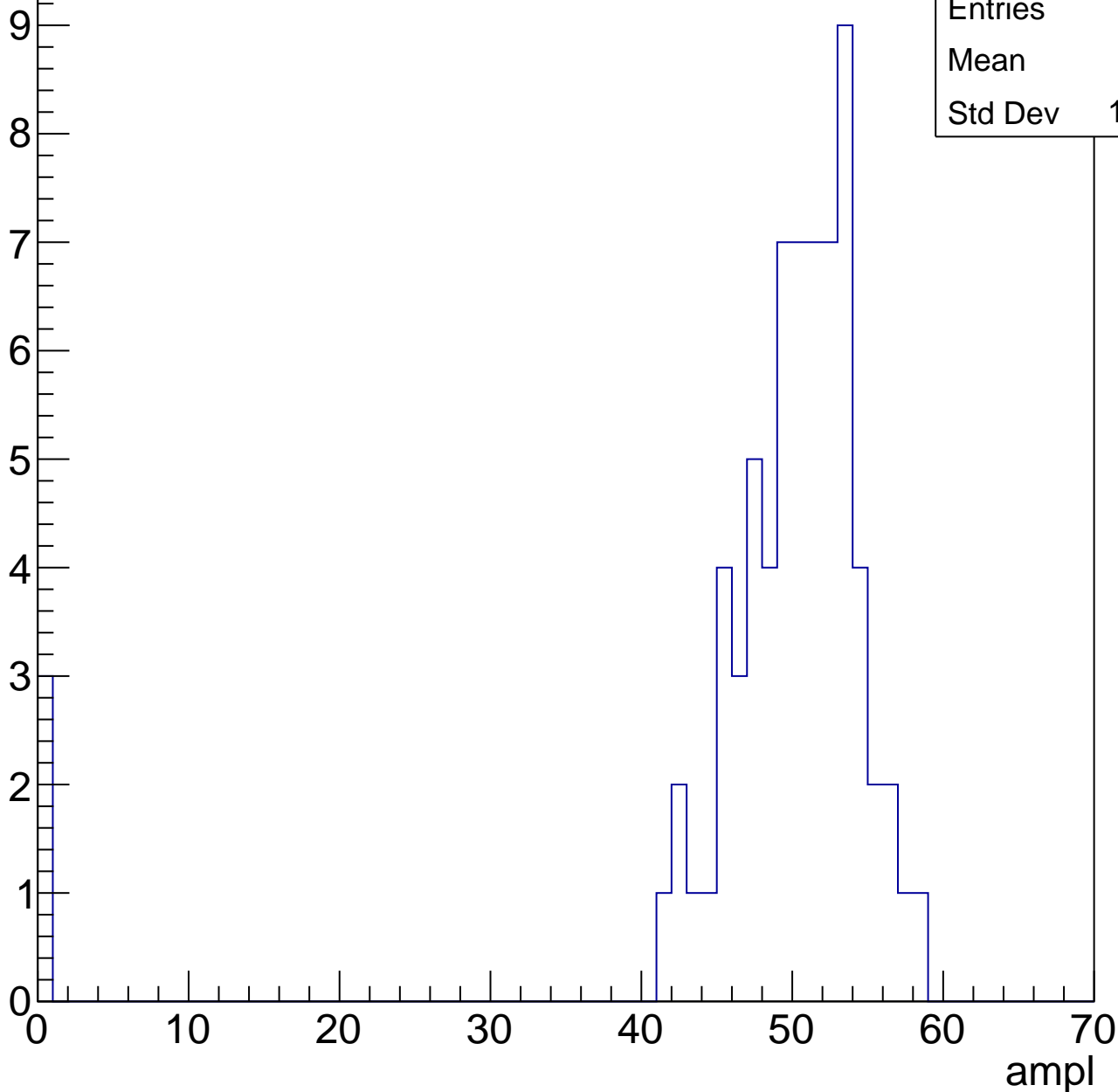


B1L103S, U24-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	47.9
Std Dev	10.69

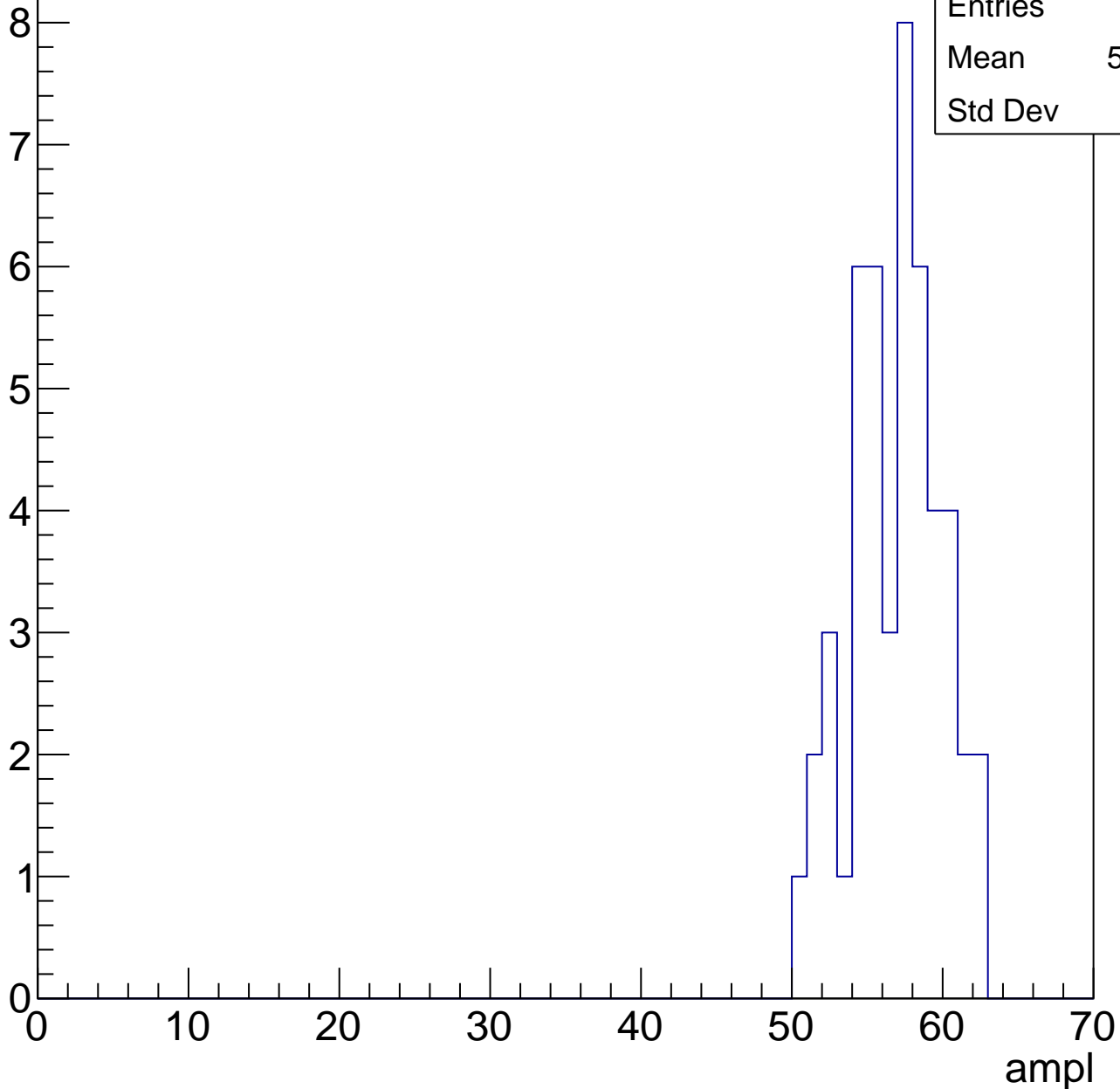


B1L103S, U24-ch1, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	56.44
Std Dev	2.95

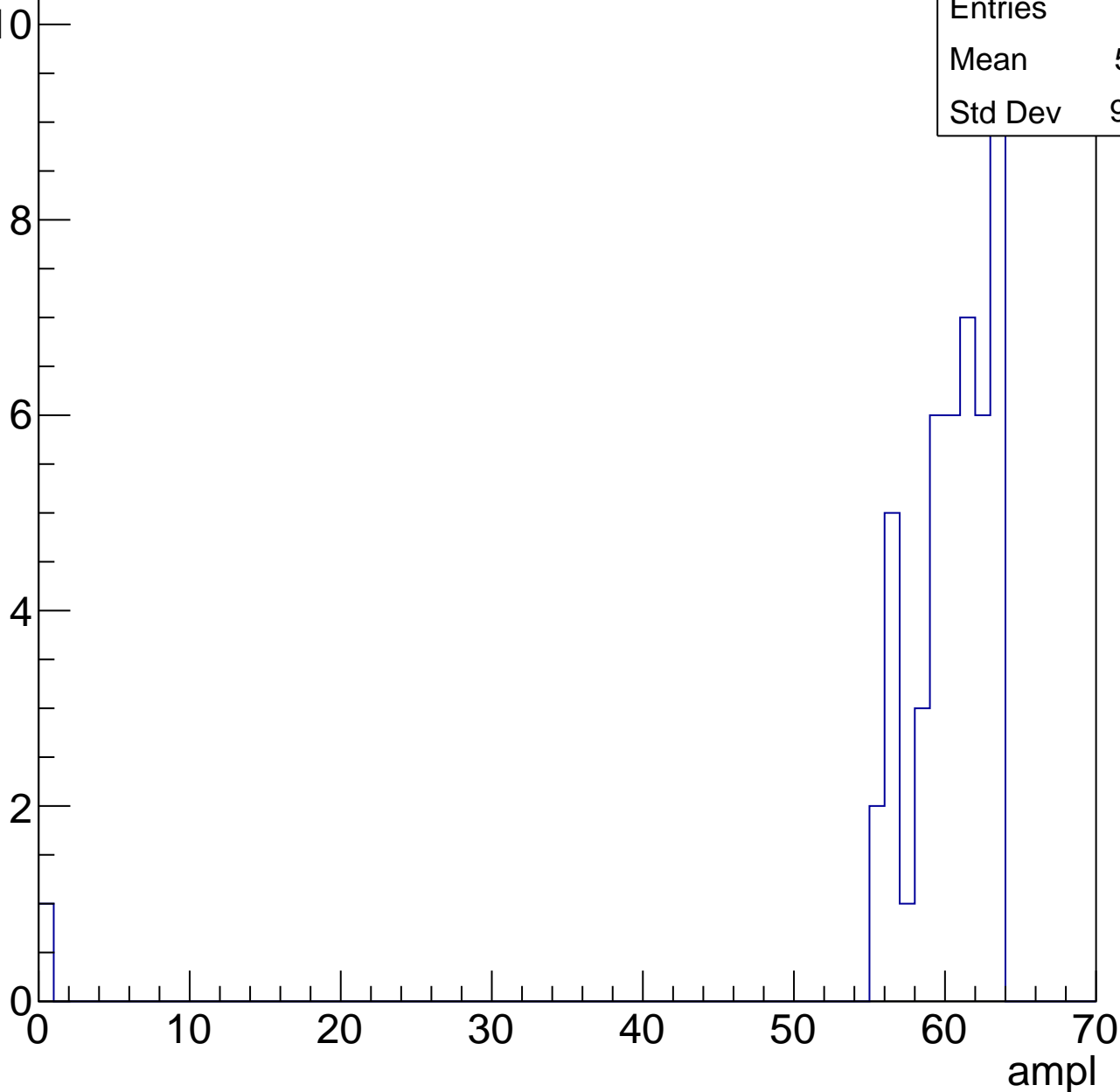


B1L103S, U24-ch1, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	58.81
Std Dev	9.005



B1L103S, U24-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U24-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch2, adc0

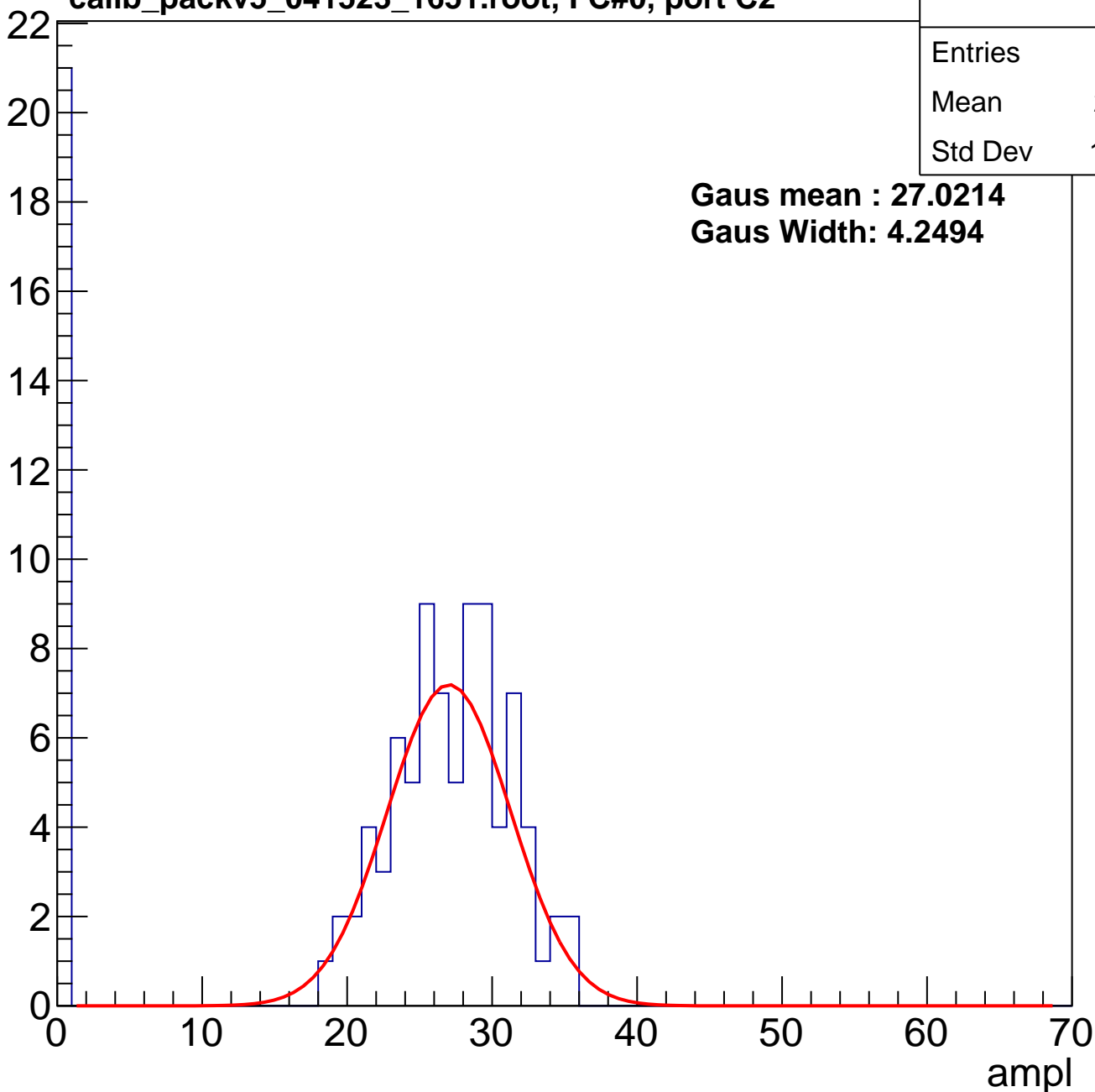
calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	21.31
Std Dev	11.34

Gaus mean : 27.0214

Gaus Width: 4.2494

Entry



B1L103S, U24-ch2, adc1

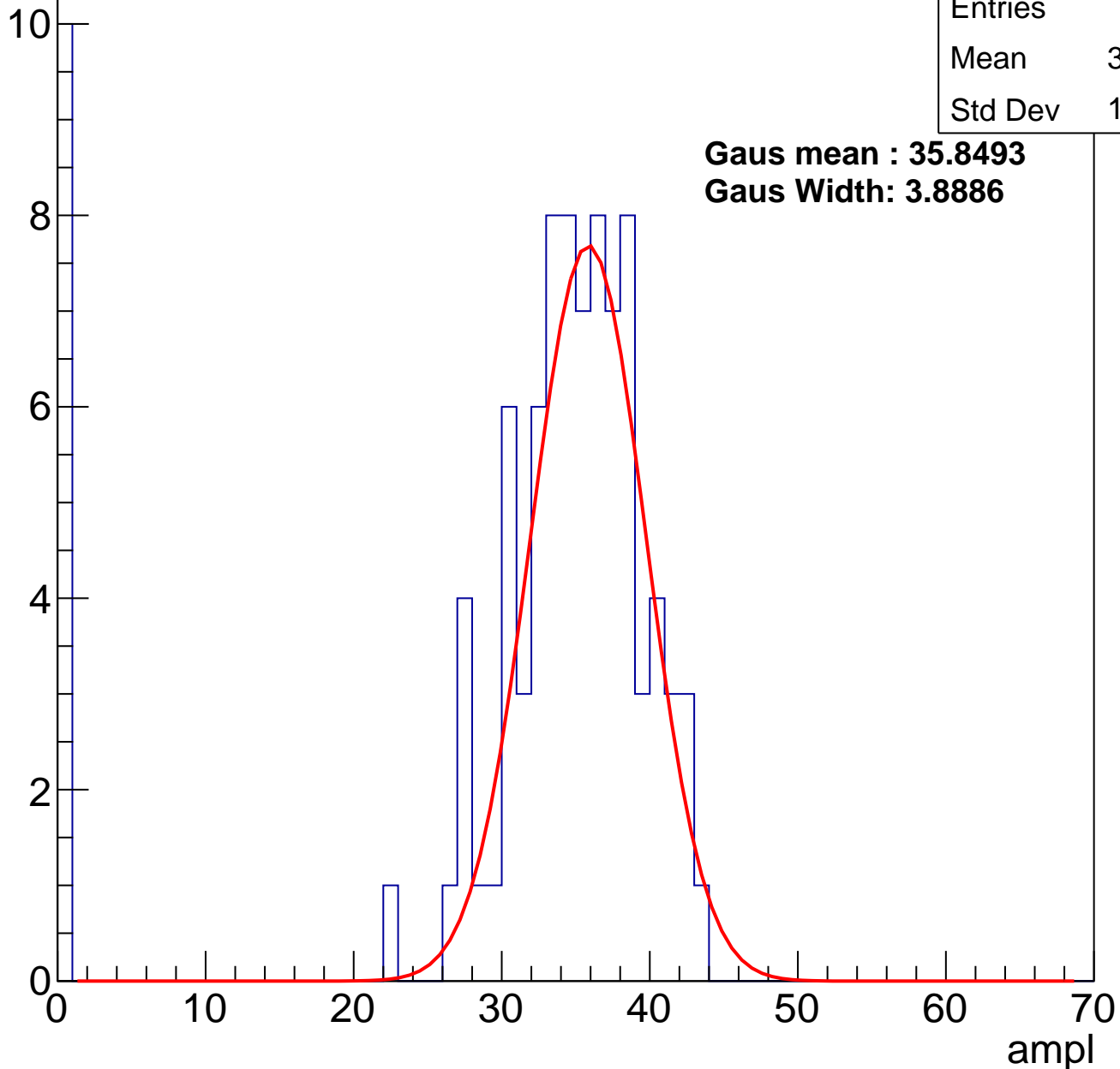
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	30.96
Std Dev	11.45

Gaus mean : 35.8493

Gaus Width: 3.8886

Entry



B1L103S, U24-ch2, adc2

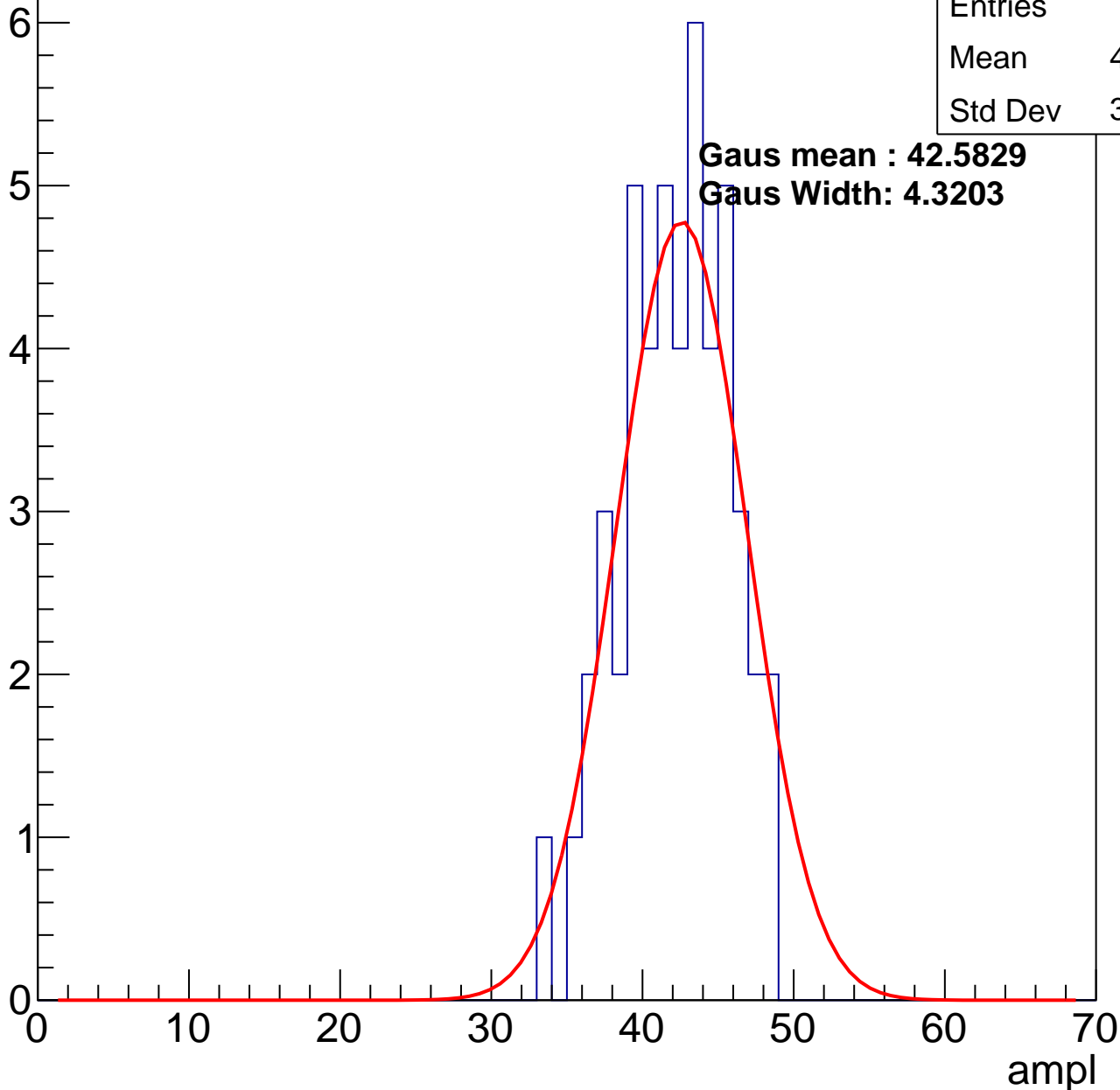
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	41.67
Std Dev	3.519

Gaus mean : 42.5829

Gaus Width: 4.3203

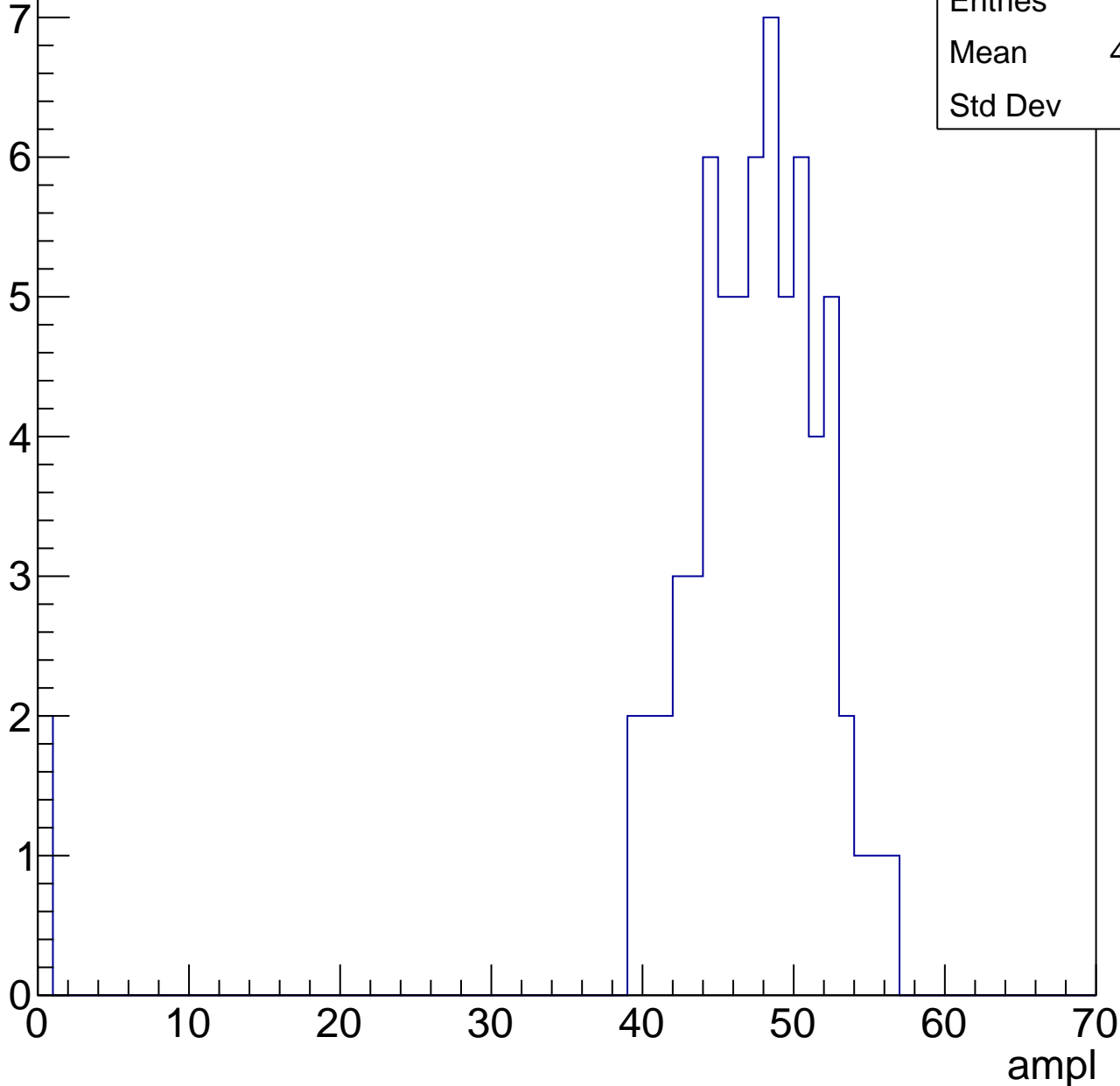


B1L103S, U24-ch2, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	45.76
Std Dev	8.87

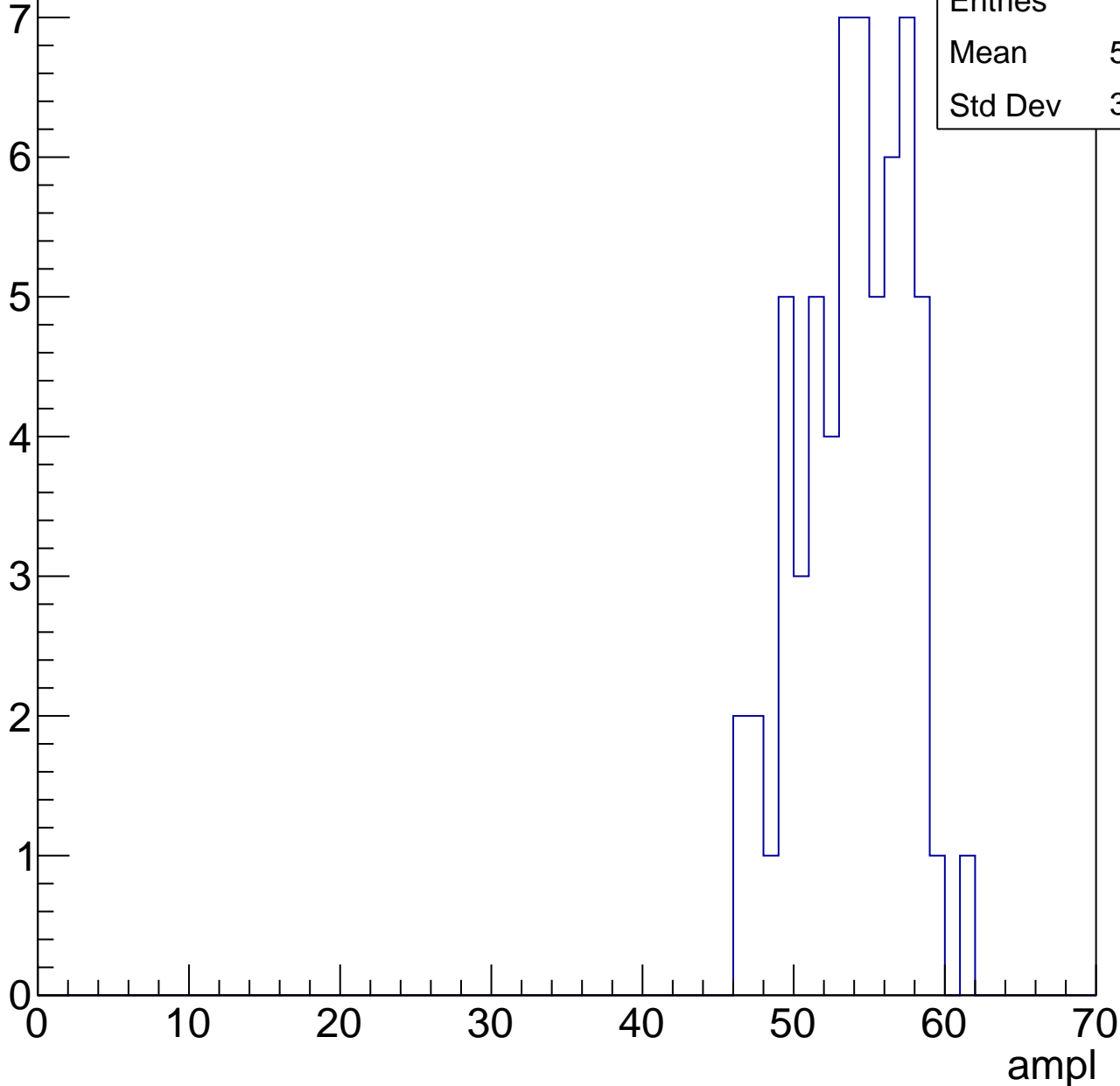


B1L103S, U24-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	53.46
Std Dev	3.462



B1L103S, U24-ch2, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	62
Mean	59.02
Std Dev	3.149

0

2

4

6

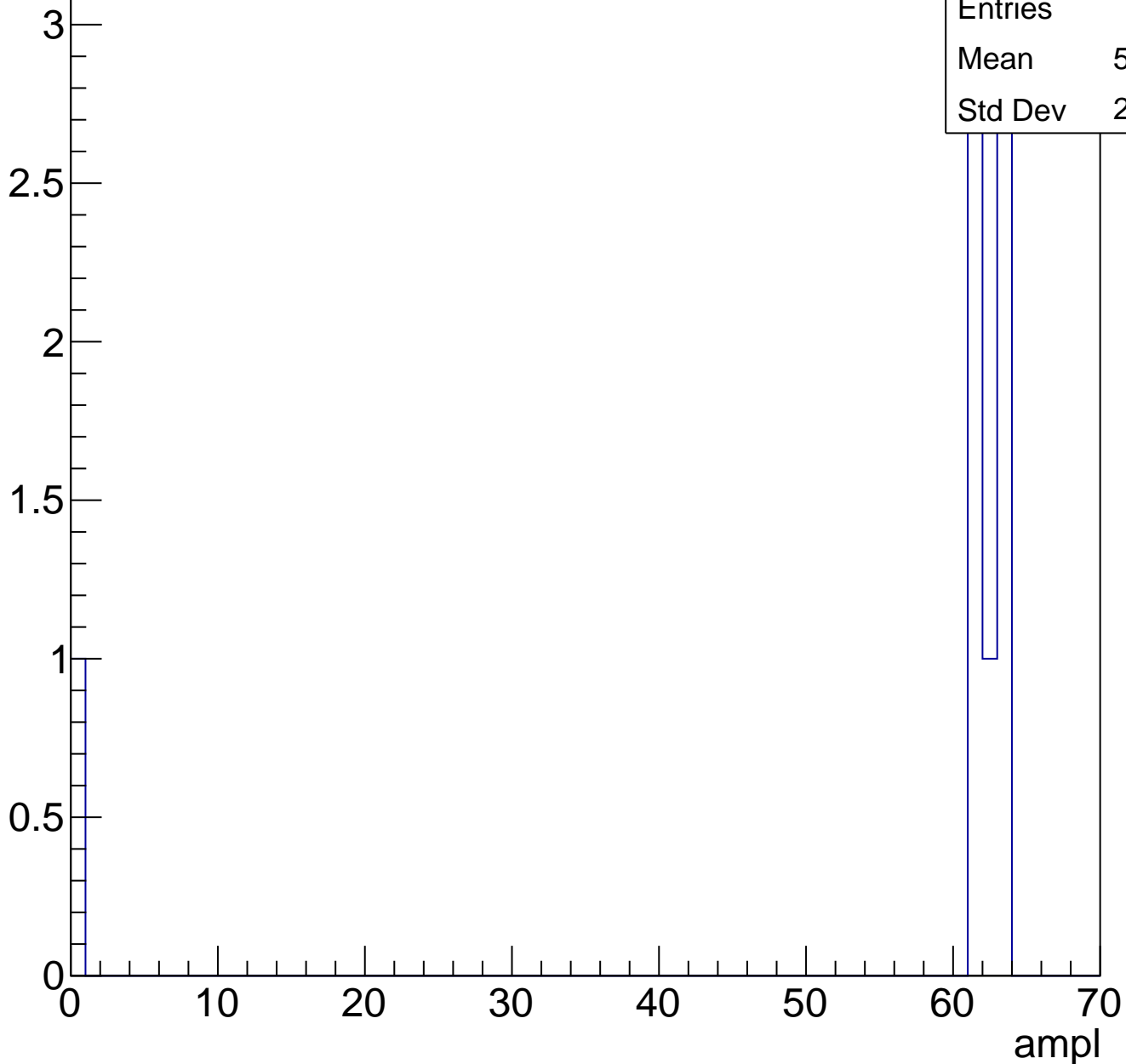
8

10

B1L103S, U24-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch3, adc0

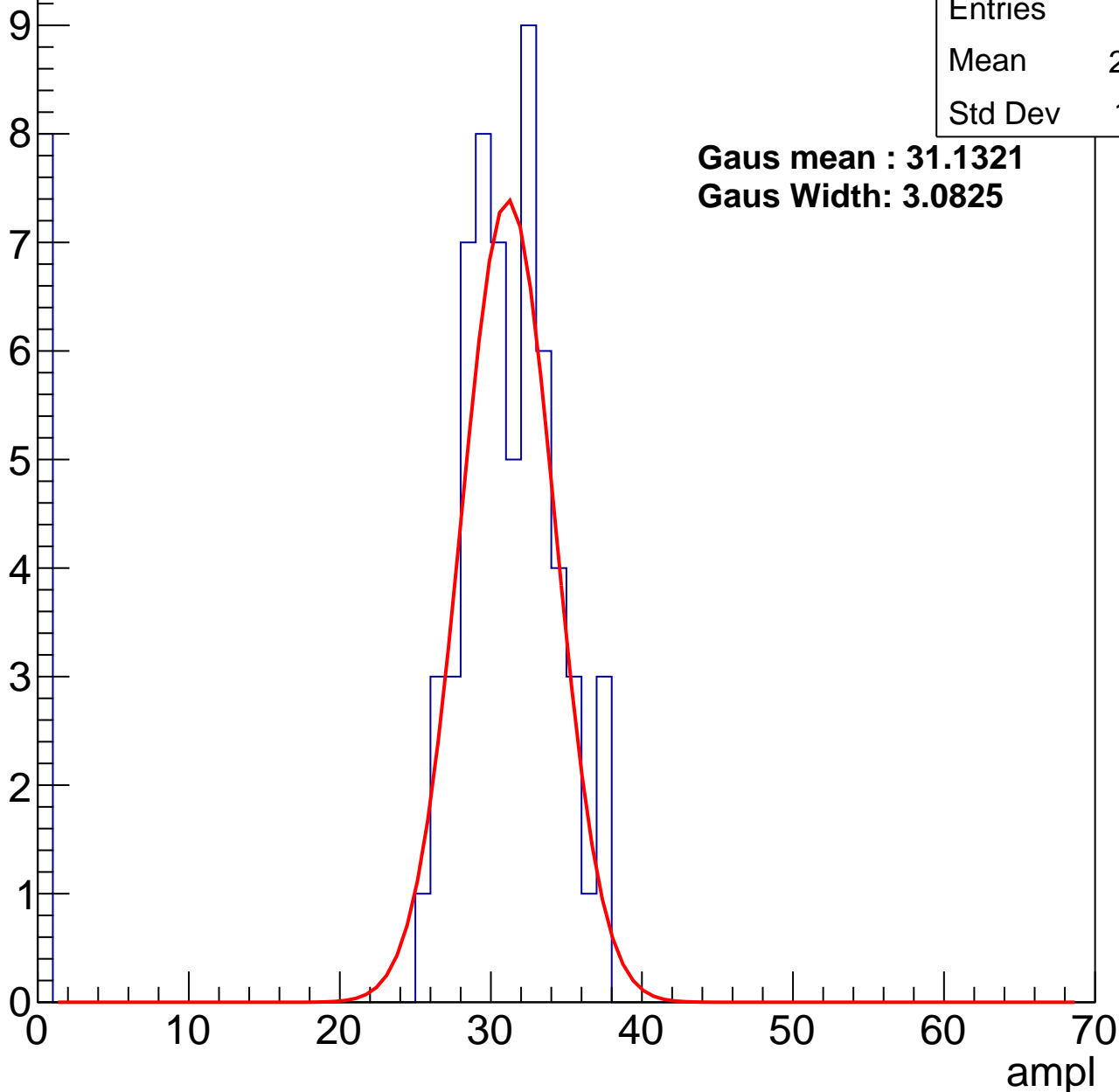
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	27.22
Std Dev	10.31

Gaus mean : 31.1321

Gaus Width: 3.0825



B1L103S, U24-ch3, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	36.23
Std Dev	7.888

Gaus mean : 38.4580

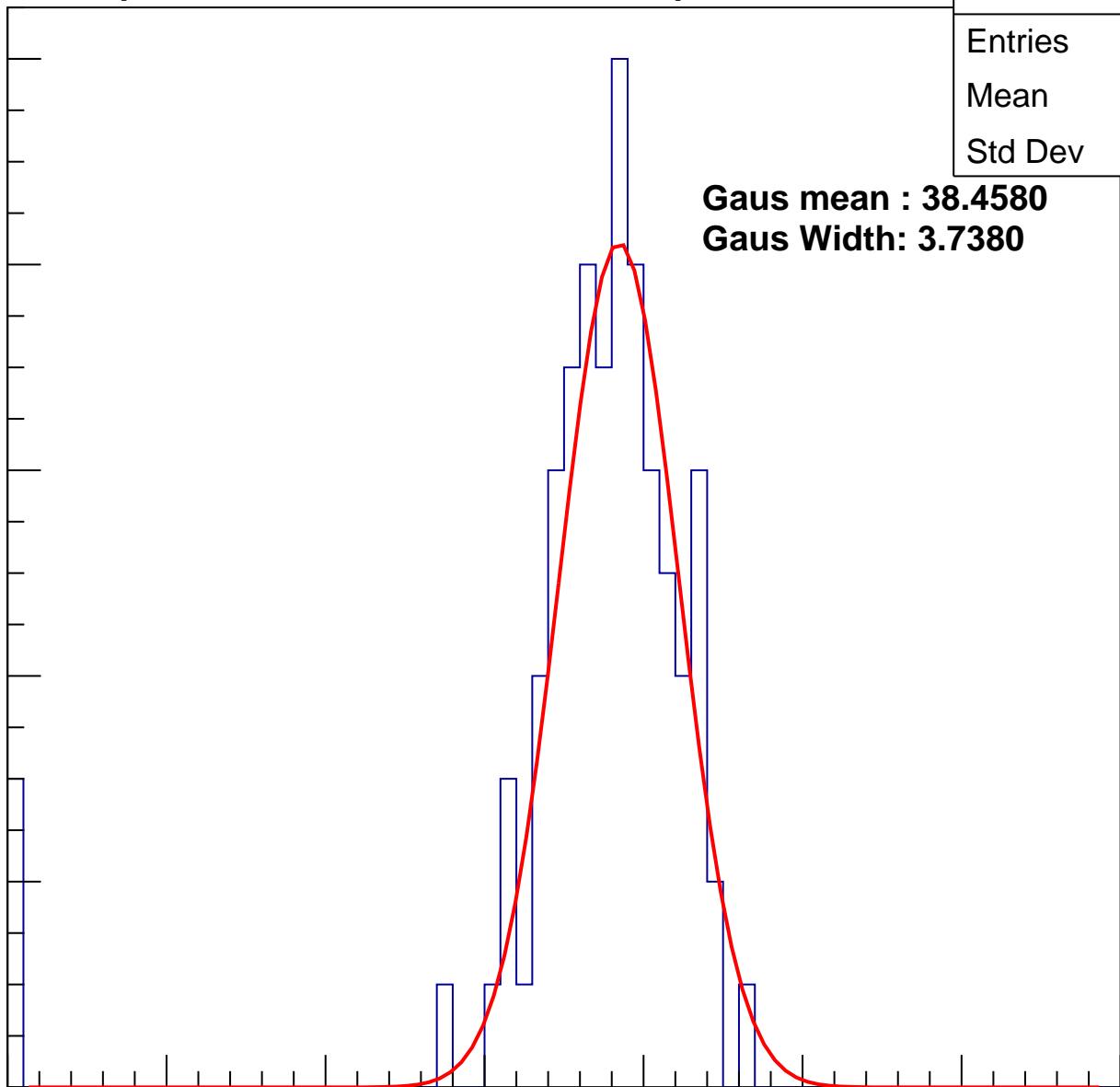
Gaus Width: 3.7380

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch3, adc2

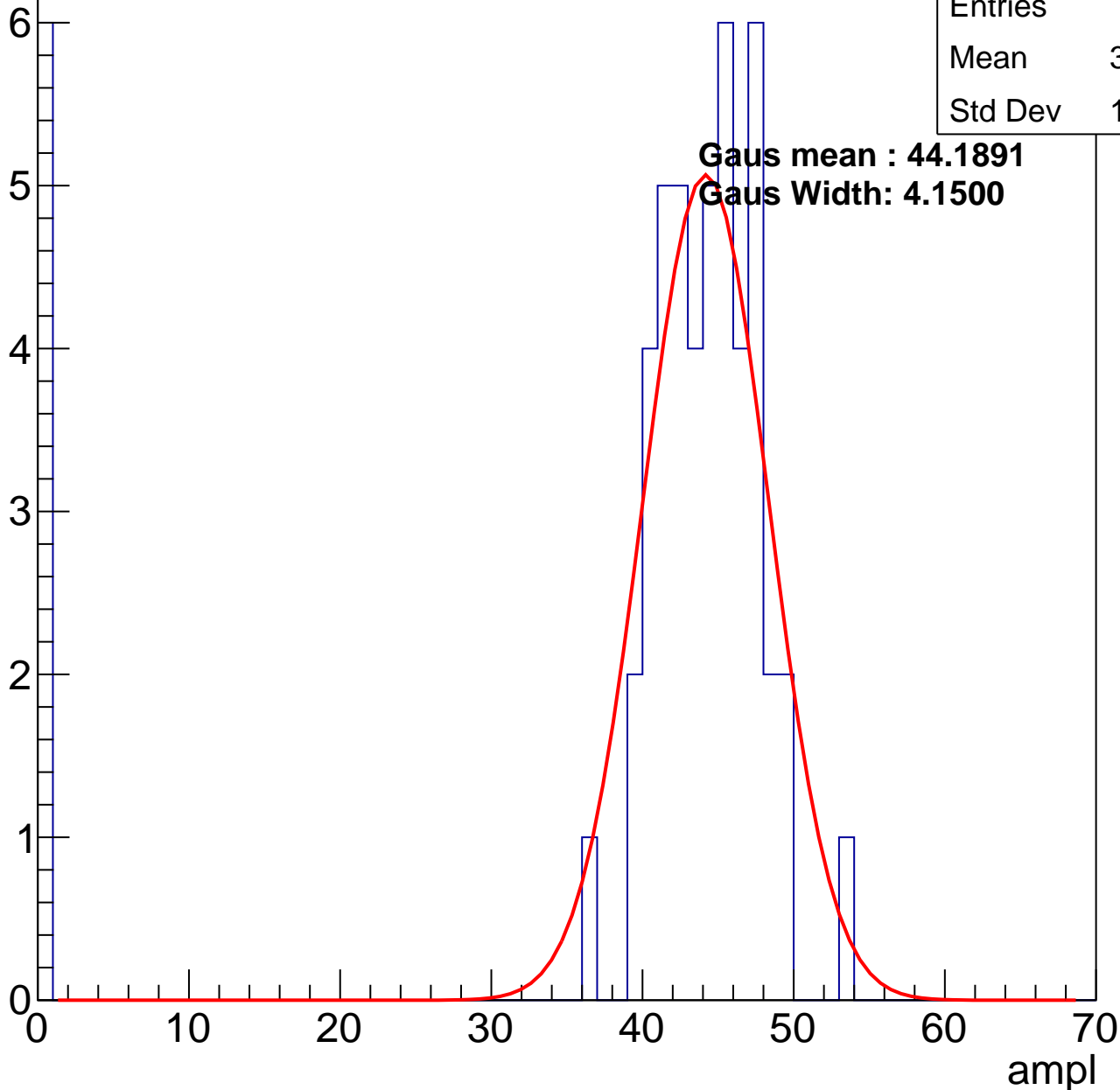
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	38.94
Std Dev	14.24

Gaus mean : 44.1891

Gaus Width: 4.1500



B1L103S, U24-ch3, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	50.26
Std Dev	6.432

Entry

10

8

6

4

2

0

0

10

20

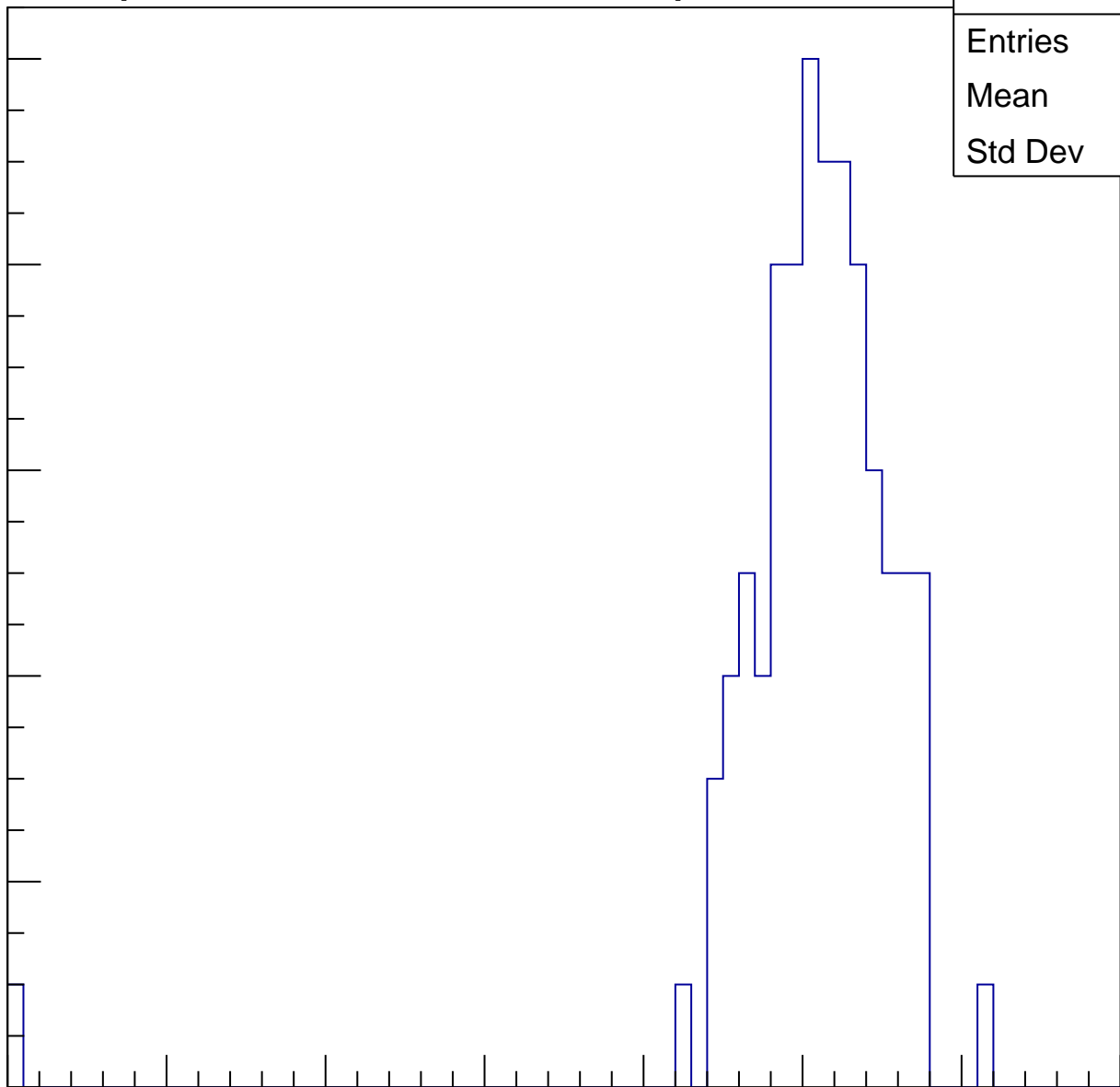
30

40

50

60

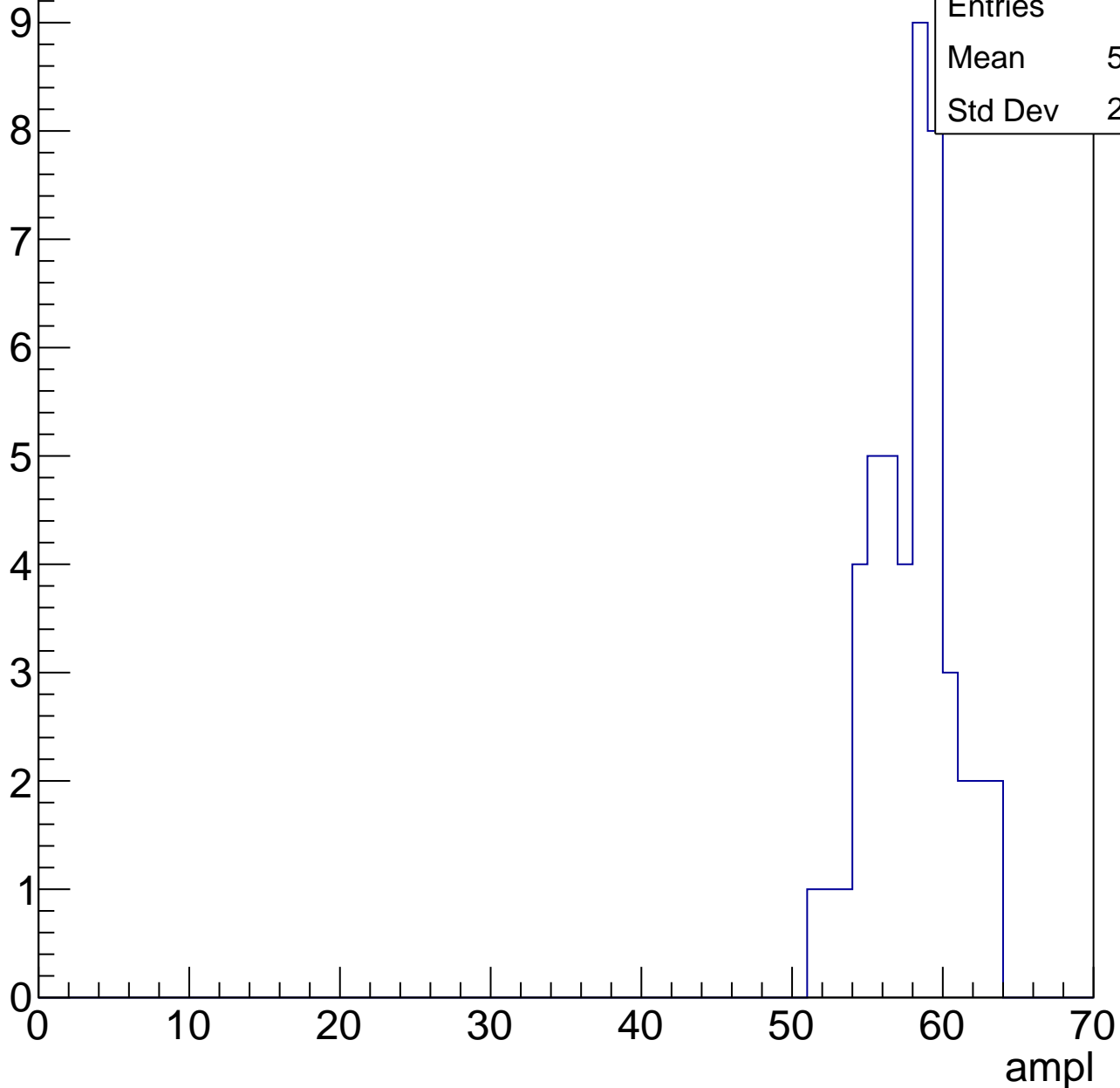
ampl



B1L103S, U24-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	47
Mean	57.47
Std Dev	2.728

B1L103S, U24-ch3, adc5

calib_packv5_041523_1651.root, FC#0, port C2

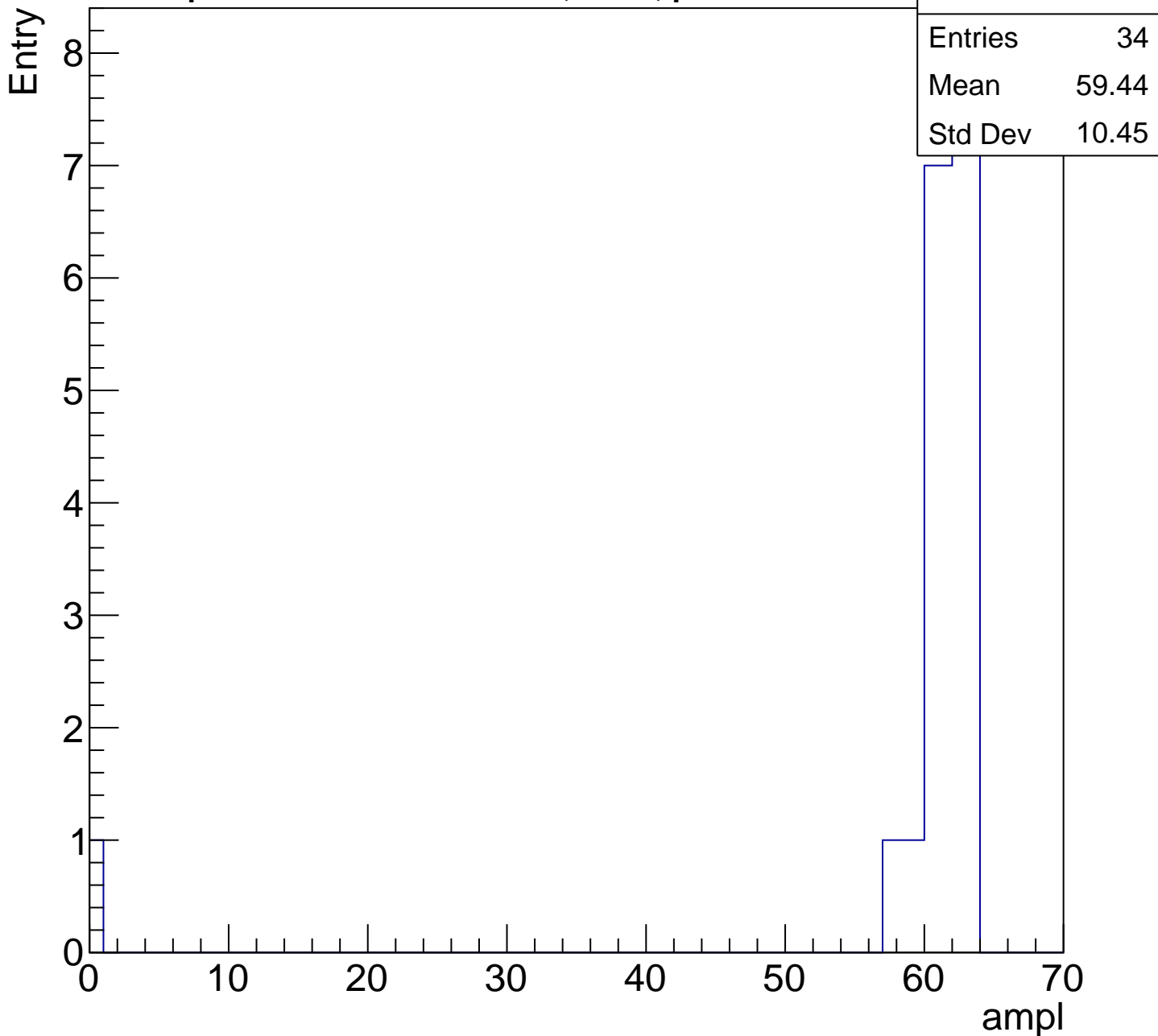
Entry

8
7
6
5
4
3
2
1
0

Entries	34
Mean	59.44
Std Dev	10.45

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	1
Mean	59
Std Dev	0

B1L103S, U24-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch4, adc0

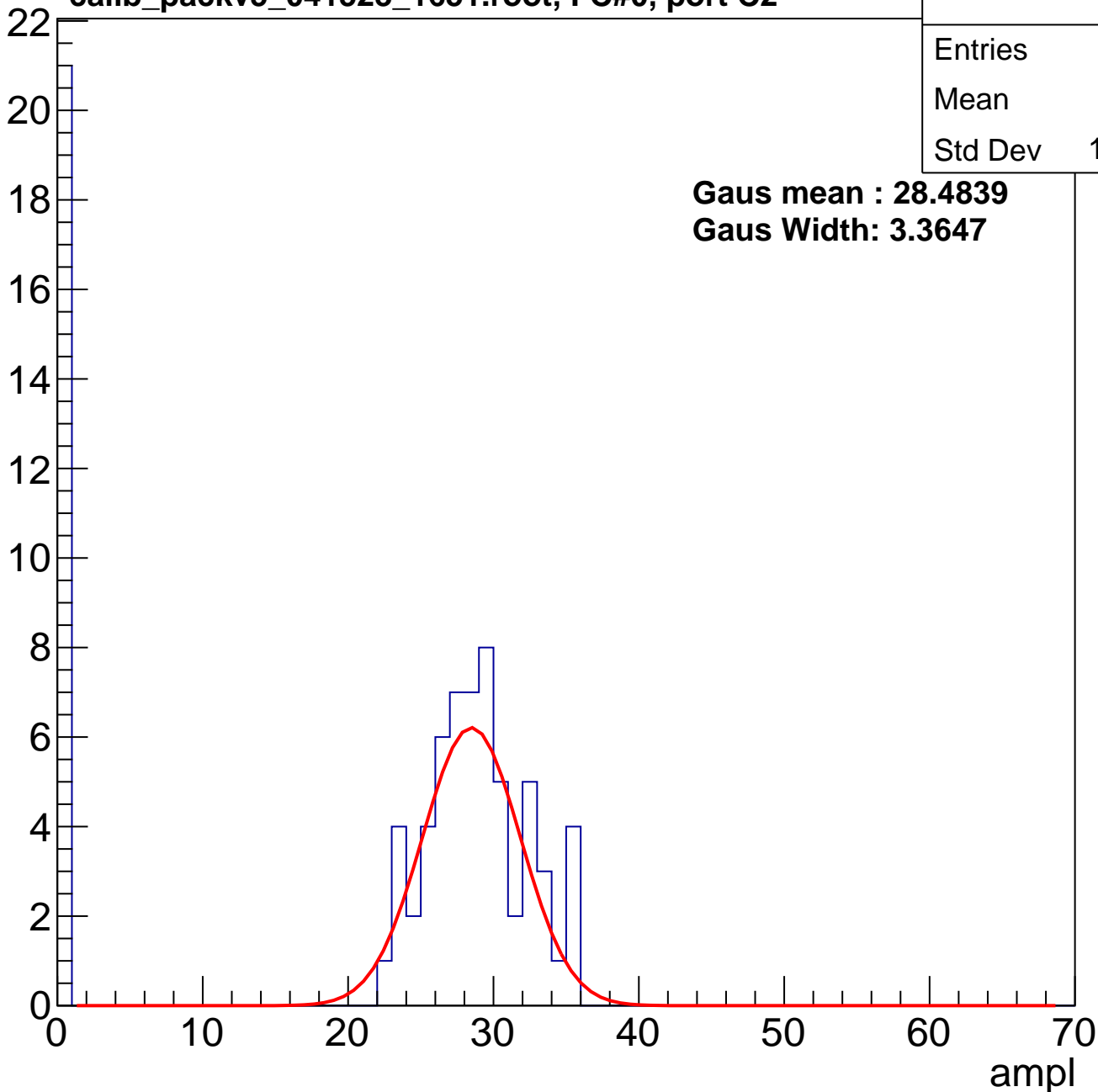
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	21
Std Dev	12.85

Gaus mean : 28.4839

Gaus Width: 3.3647

Entry



B1L103S, U24-ch4, adc1

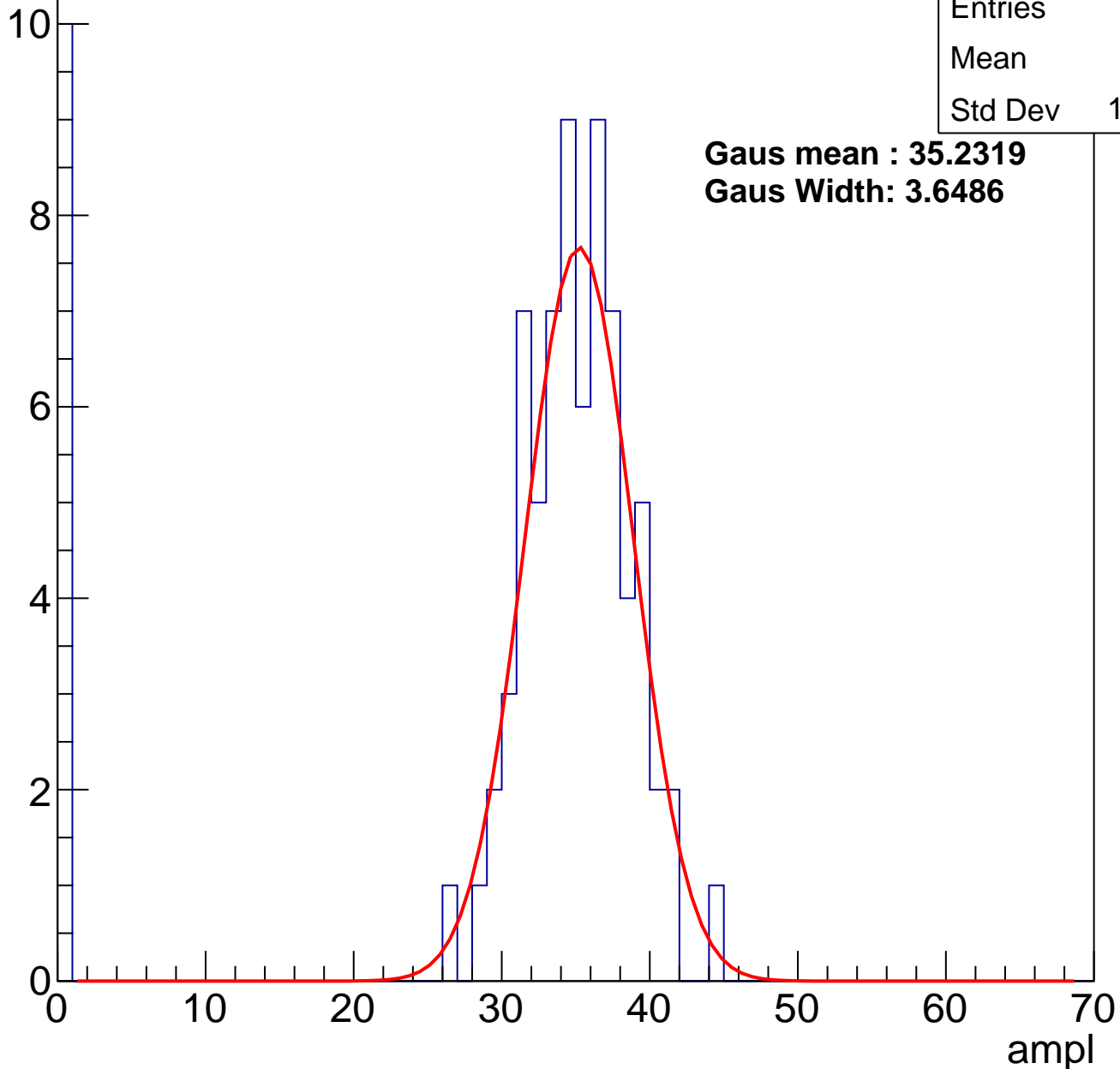
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	30.4
Std Dev	11.84

Gaus mean : 35.2319

Gaus Width: 3.6486

Entry



B1L103S, U24-ch4, adc2

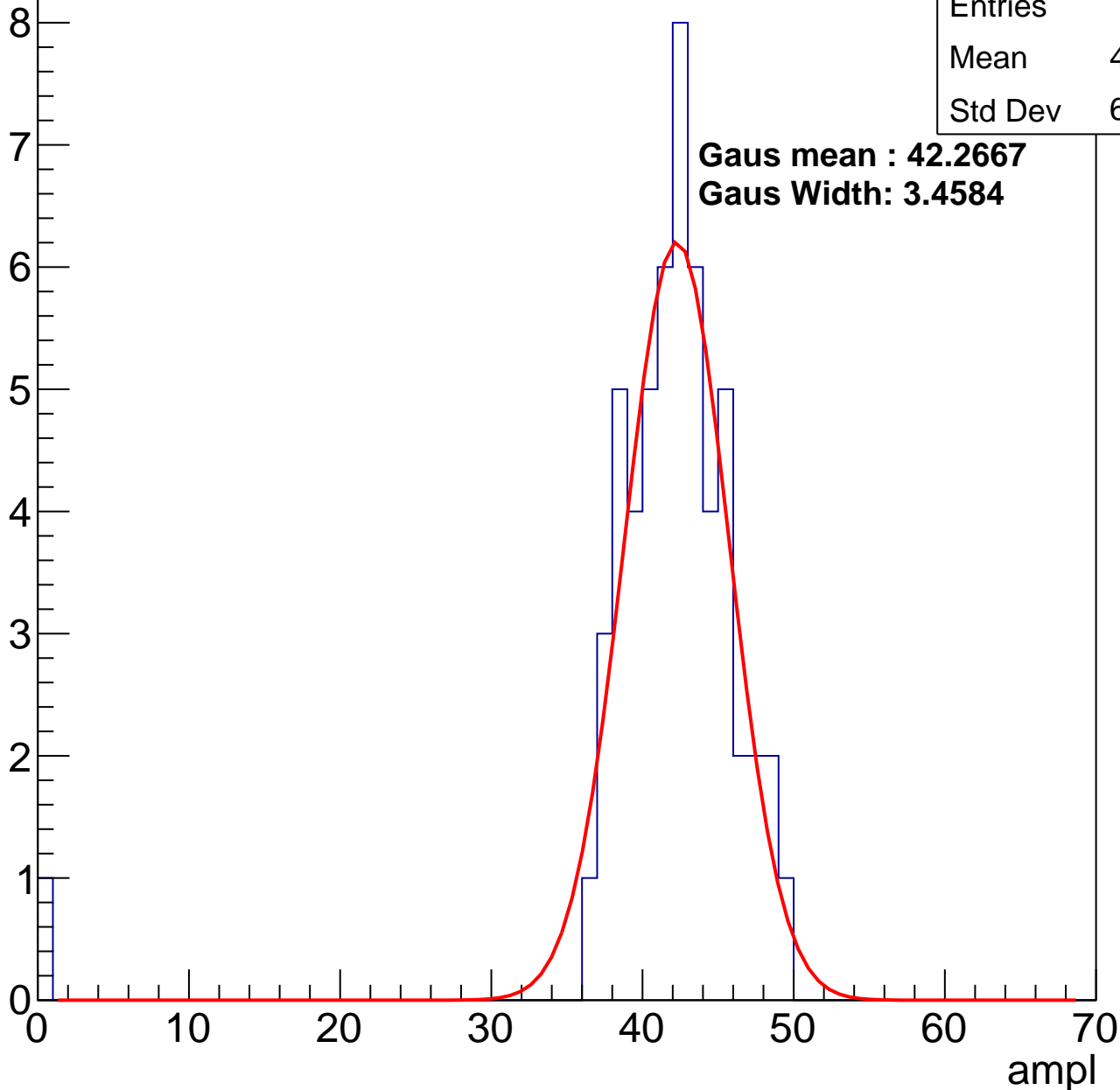
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	41.18
Std Dev	6.396

Gaus mean : 42.2667

Gaus Width: 3.4584

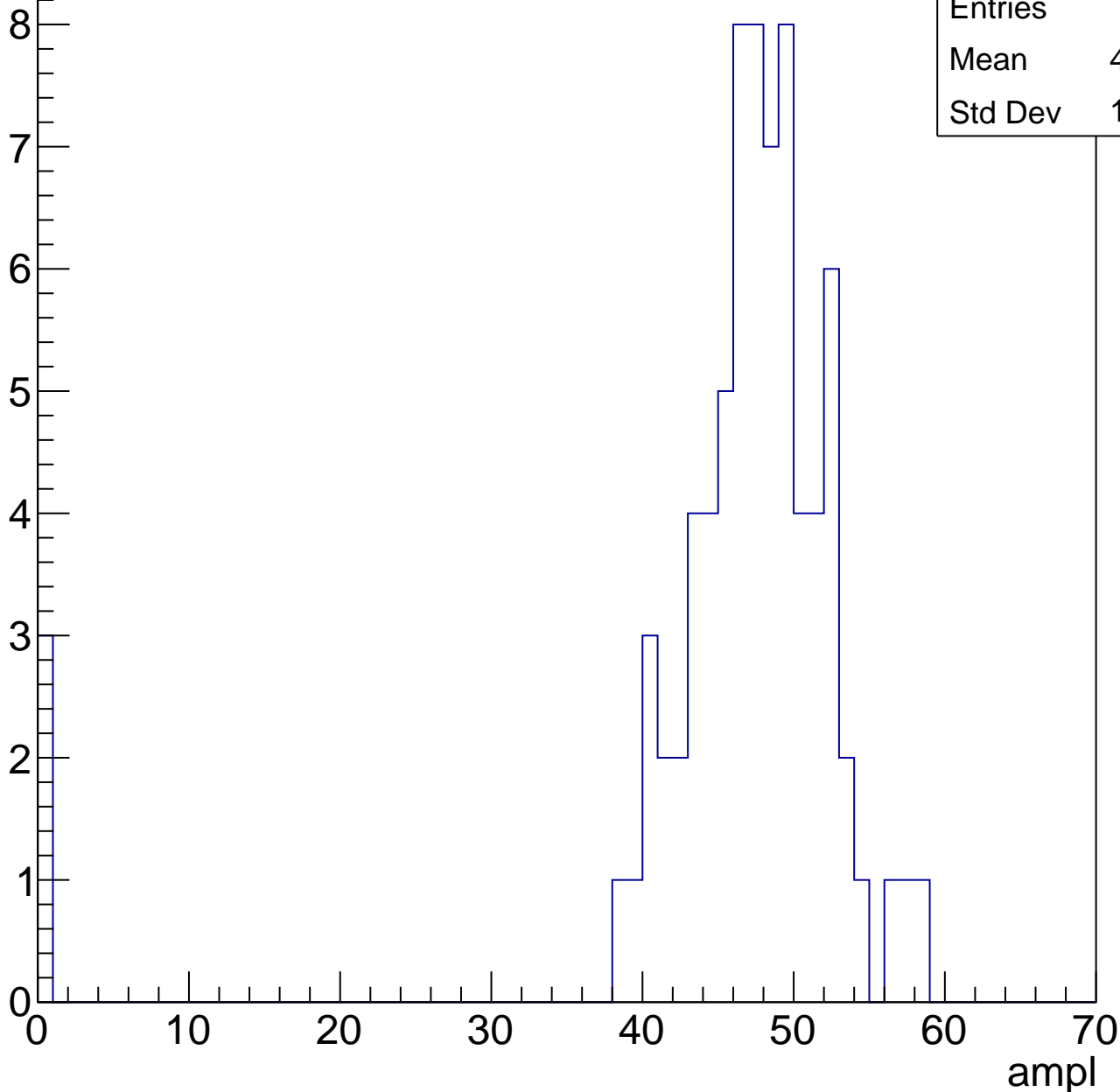


B1L103S, U24-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	45.46
Std Dev	10.07

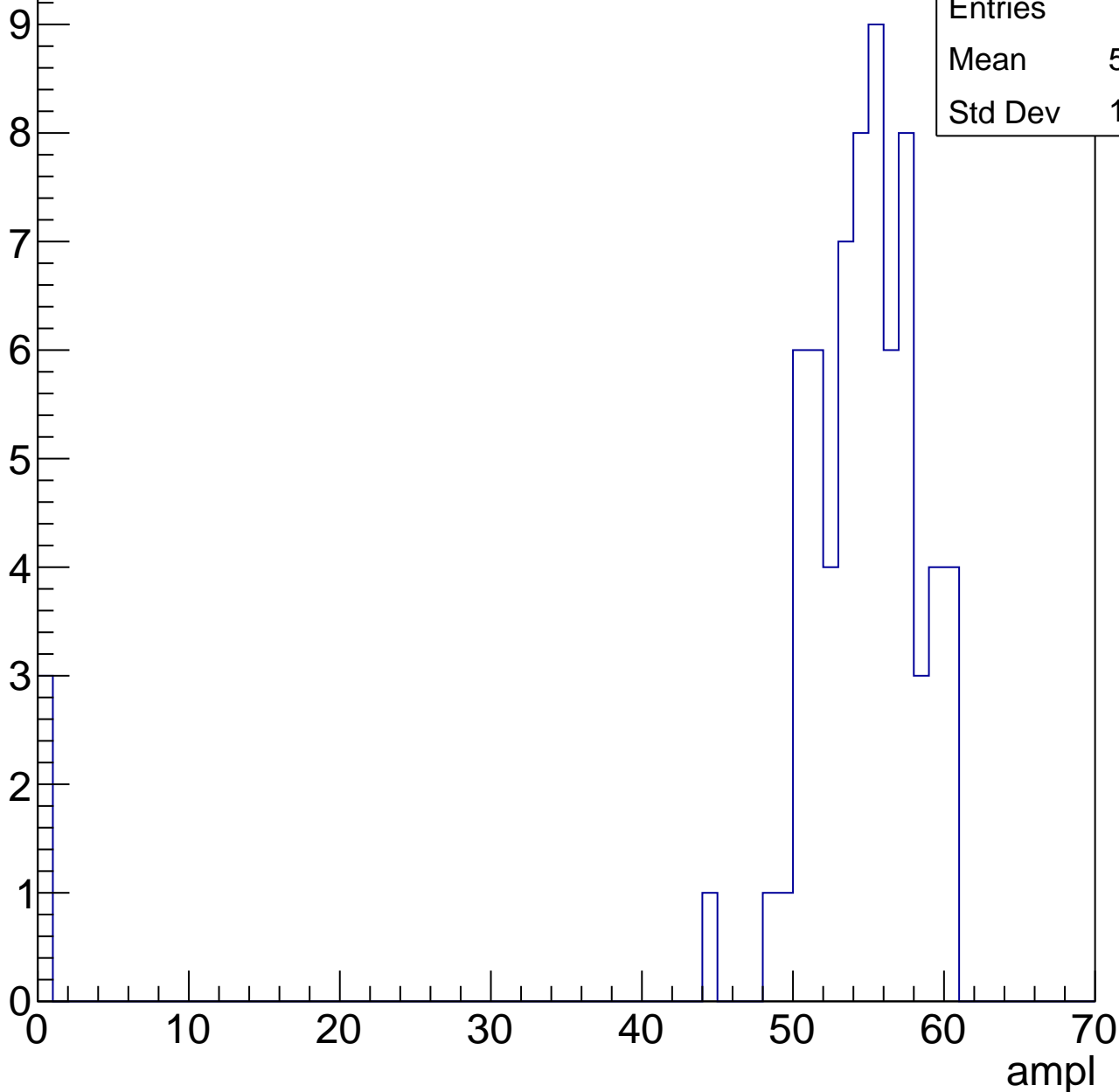


B1L103S, U24-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	52.04
Std Dev	11.39



B1L103S, U24-ch4, adc5

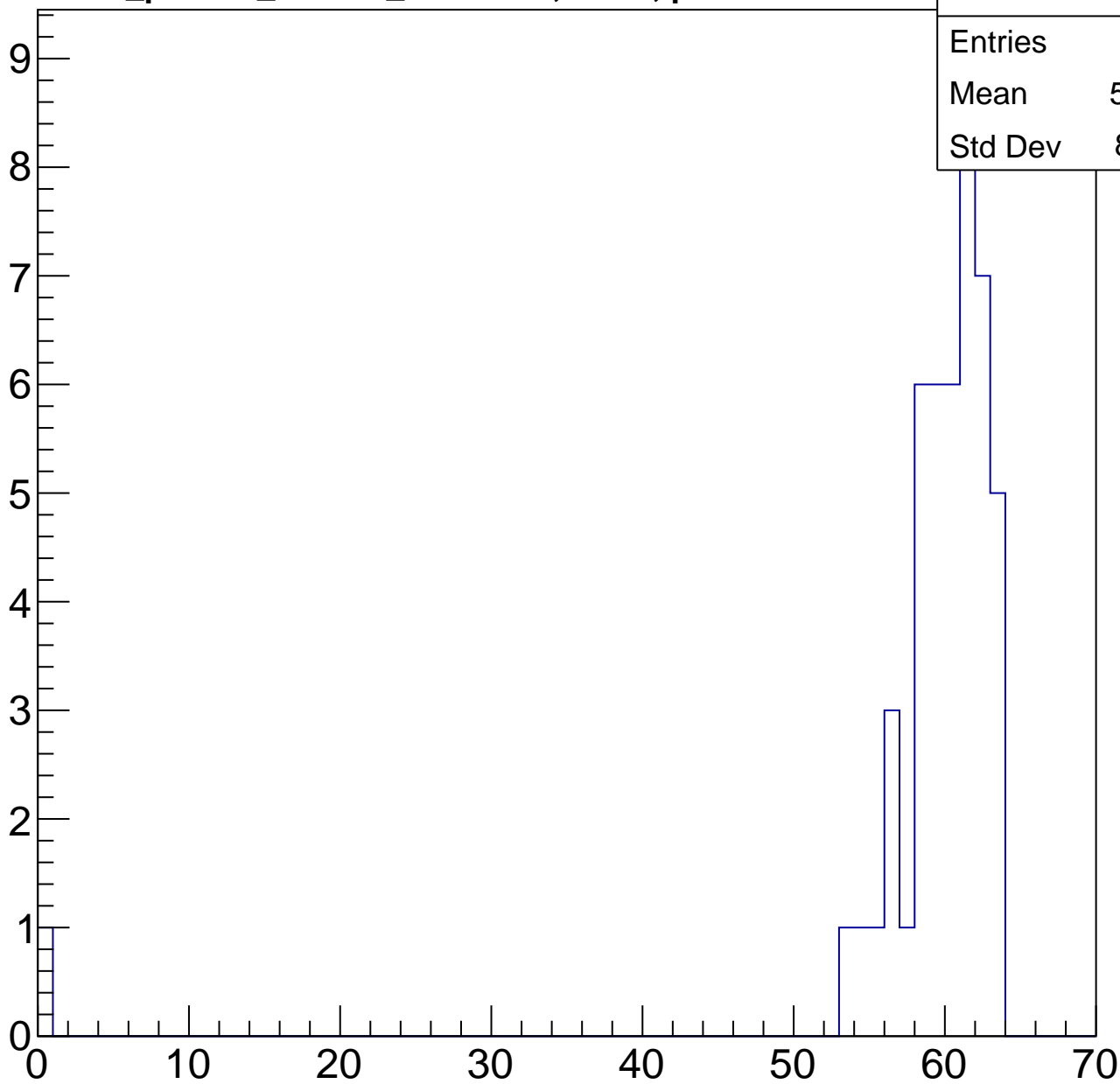
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.45
Std Dev	8.951

ampl

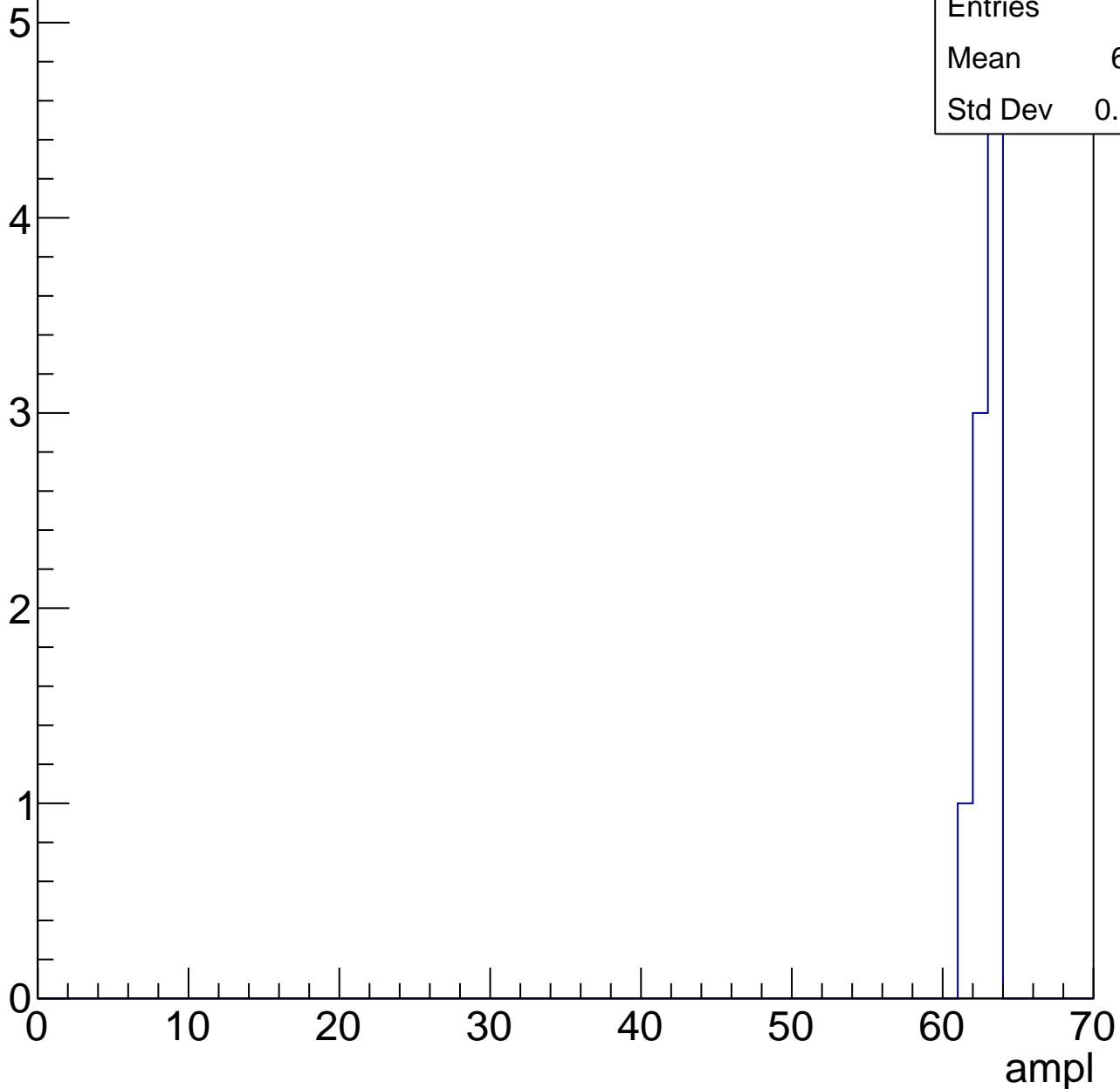


B1L103S, U24-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	62.44
Std Dev	0.6849



B1L103S, U24-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch5, adc0

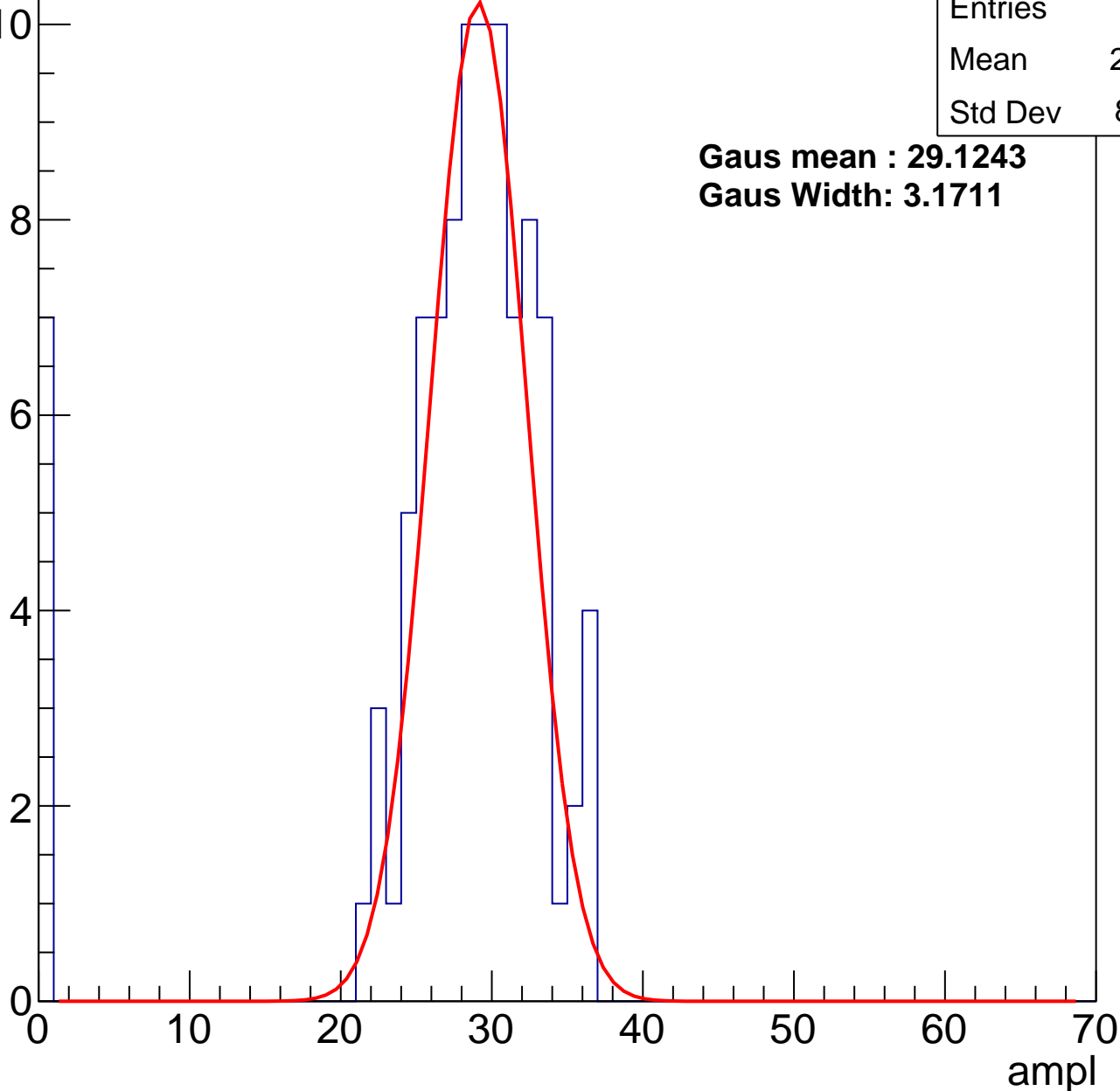
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	98
Mean	26.79
Std Dev	8.151

Gaus mean : 29.1243

Gaus Width: 3.1711



B1L103S, U24-ch5, adc1

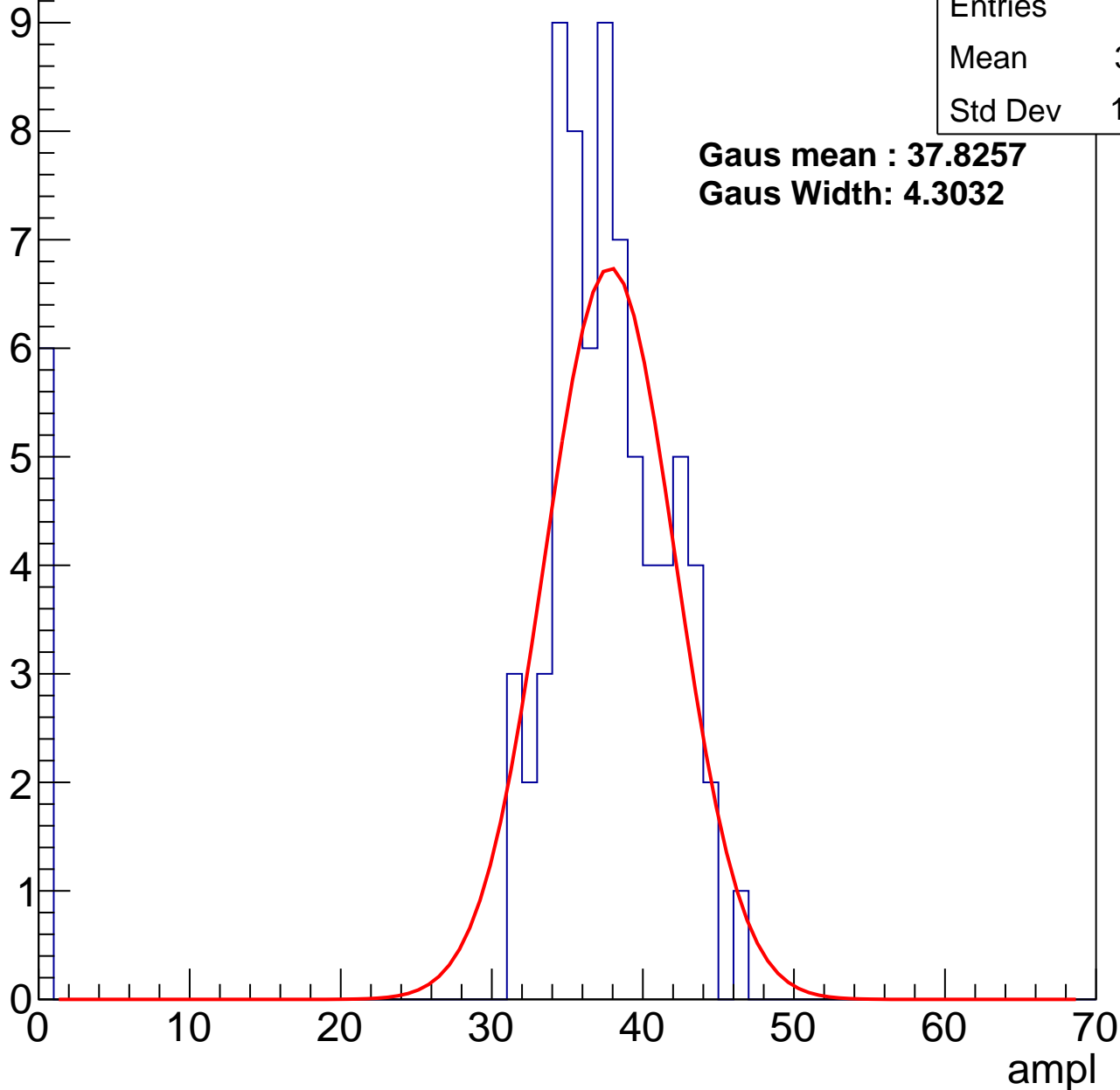
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.51
Std Dev	10.52

Gaus mean : 37.8257

Gaus Width: 4.3032

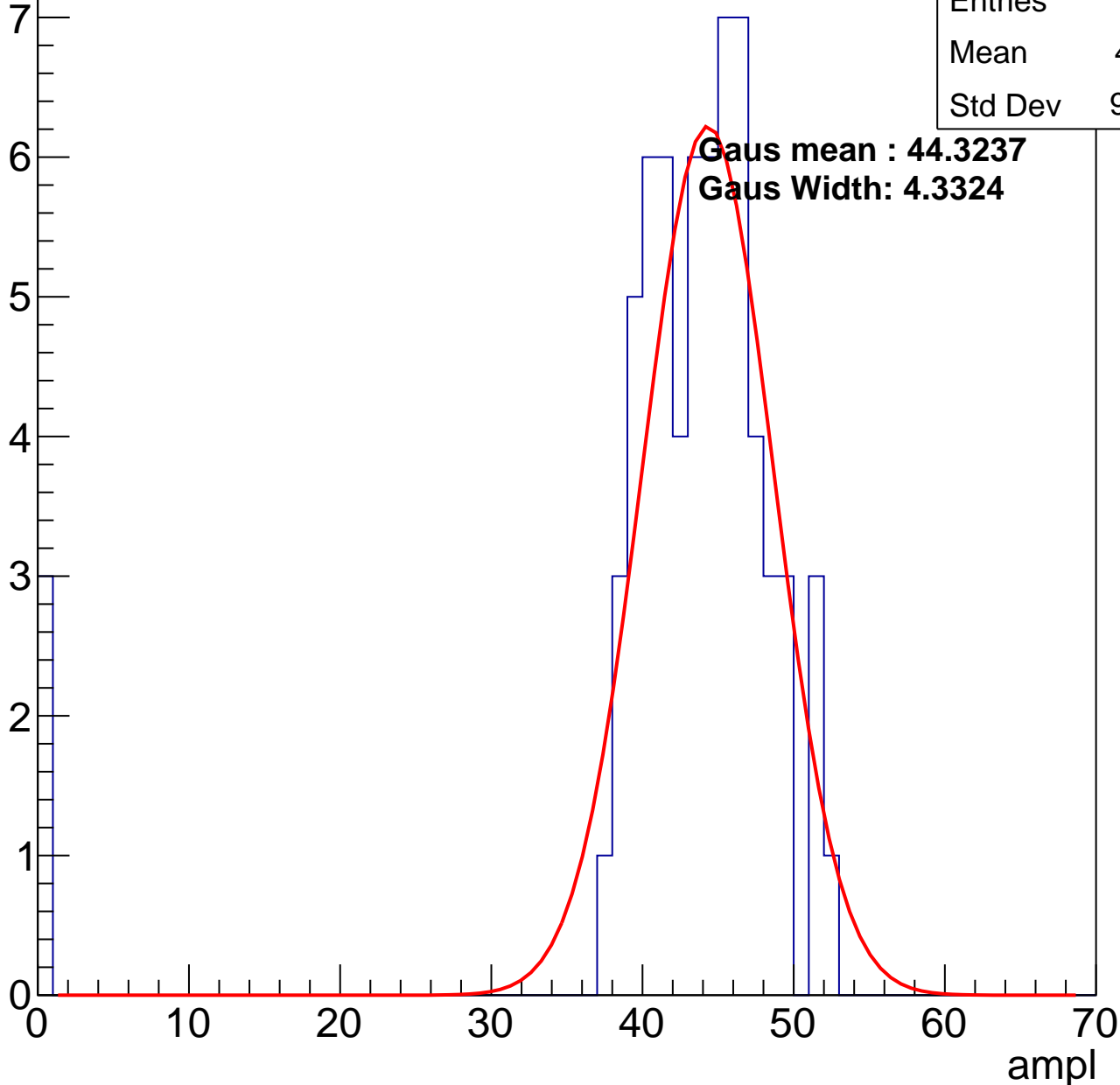


B1L103S, U24-ch5, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.81
Std Dev	9.656



B1L103S, U24-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

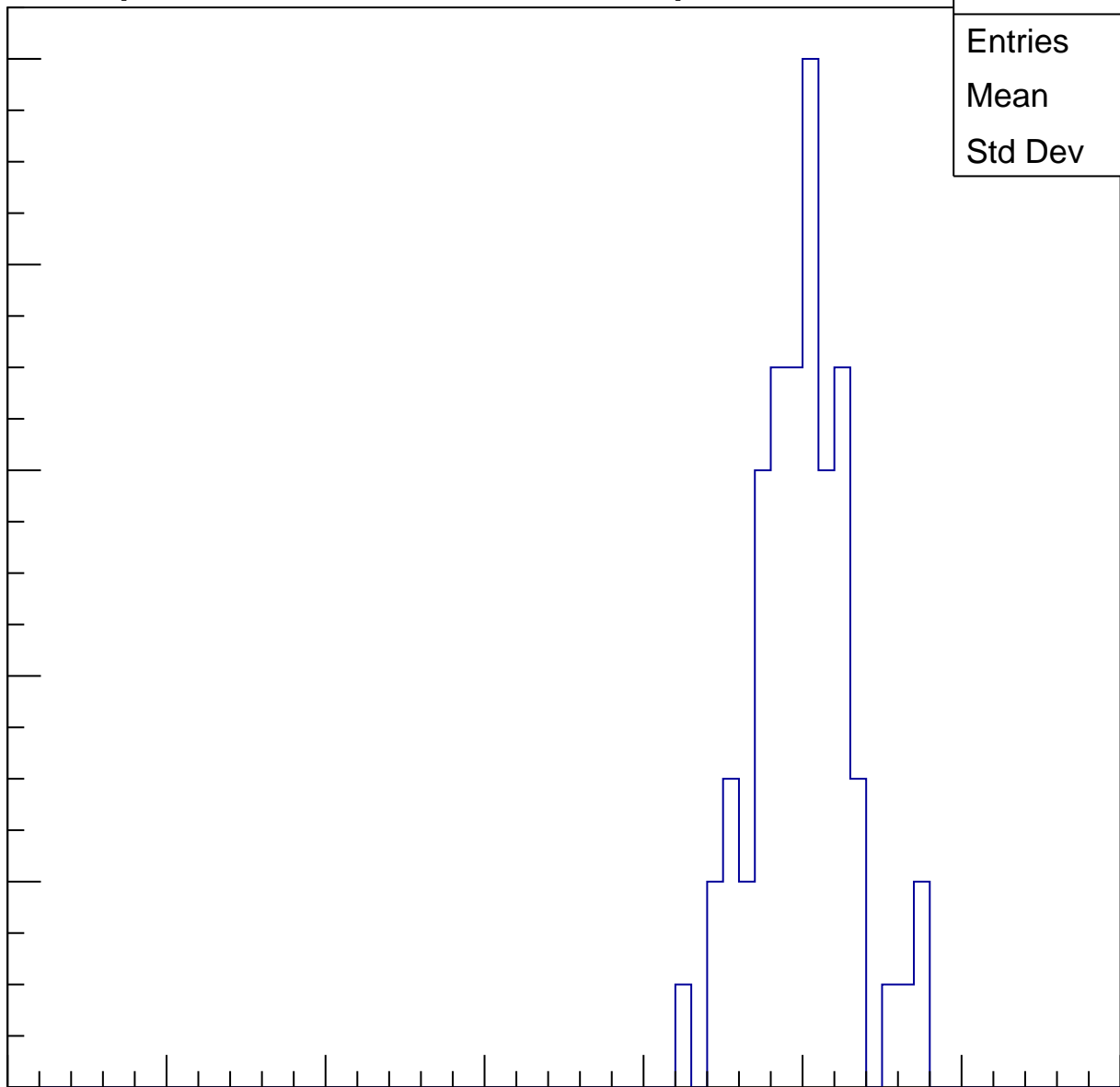
Entries	58
Mean	49.52
Std Dev	3.047

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

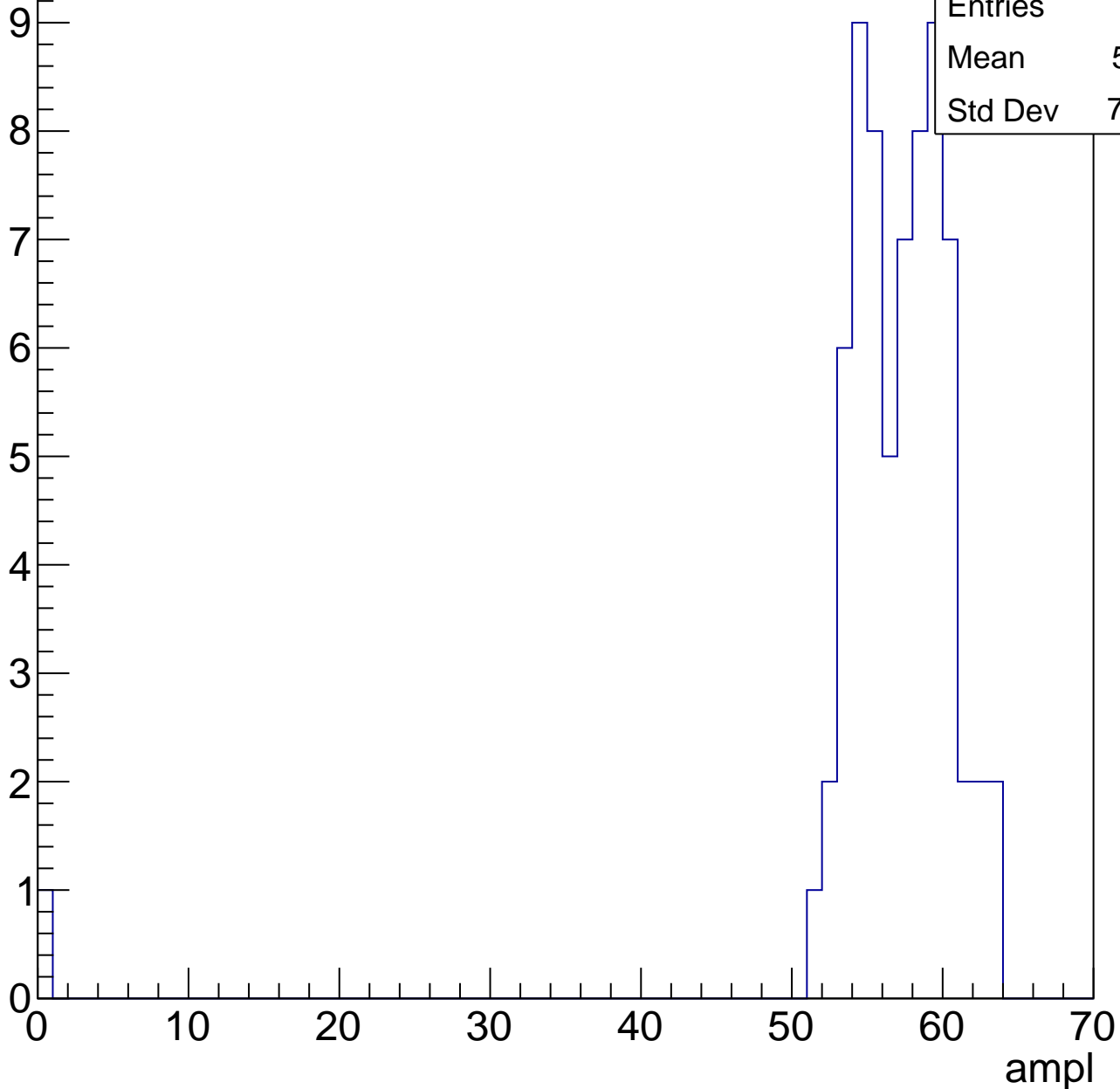


B1L103S, U24-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	56.01
Std Dev	7.365

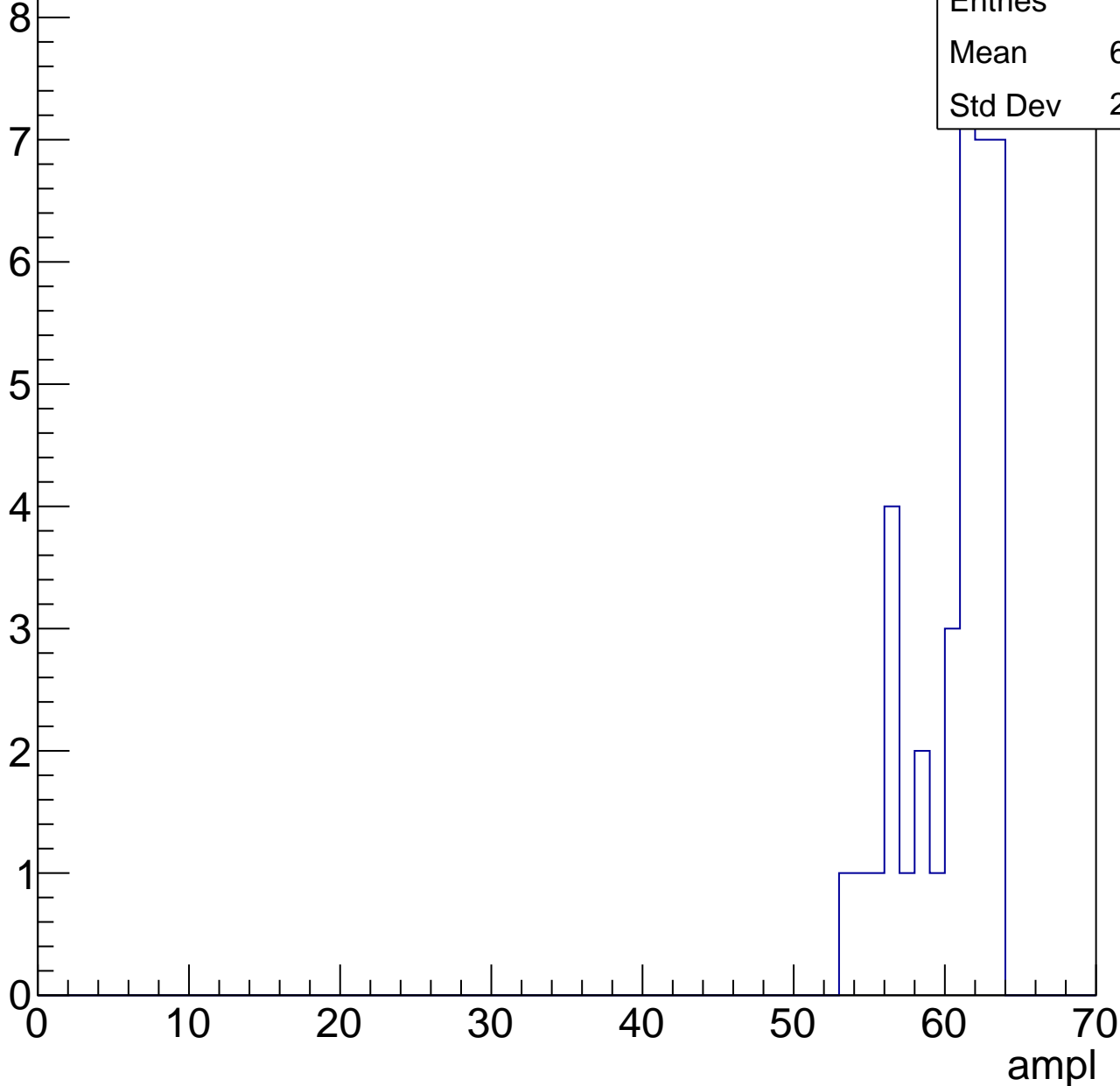


B1L103S, U24-ch5, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	36
Mean	60.03
Std Dev	2.843



B1L103S, U24-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch6, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	20.34
Std Dev	12.38

Gaus mean : 27.9277

Gaus Width: 3.9660

Entry

25

20

15

10

5

0

0

10

20

30

40

50

60

70

ampl

25

20

15

10

5

0

0

10

20

30

40

50

60

70

ampl

B1L103S, U24-ch6, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	28.52
Std Dev	13.24

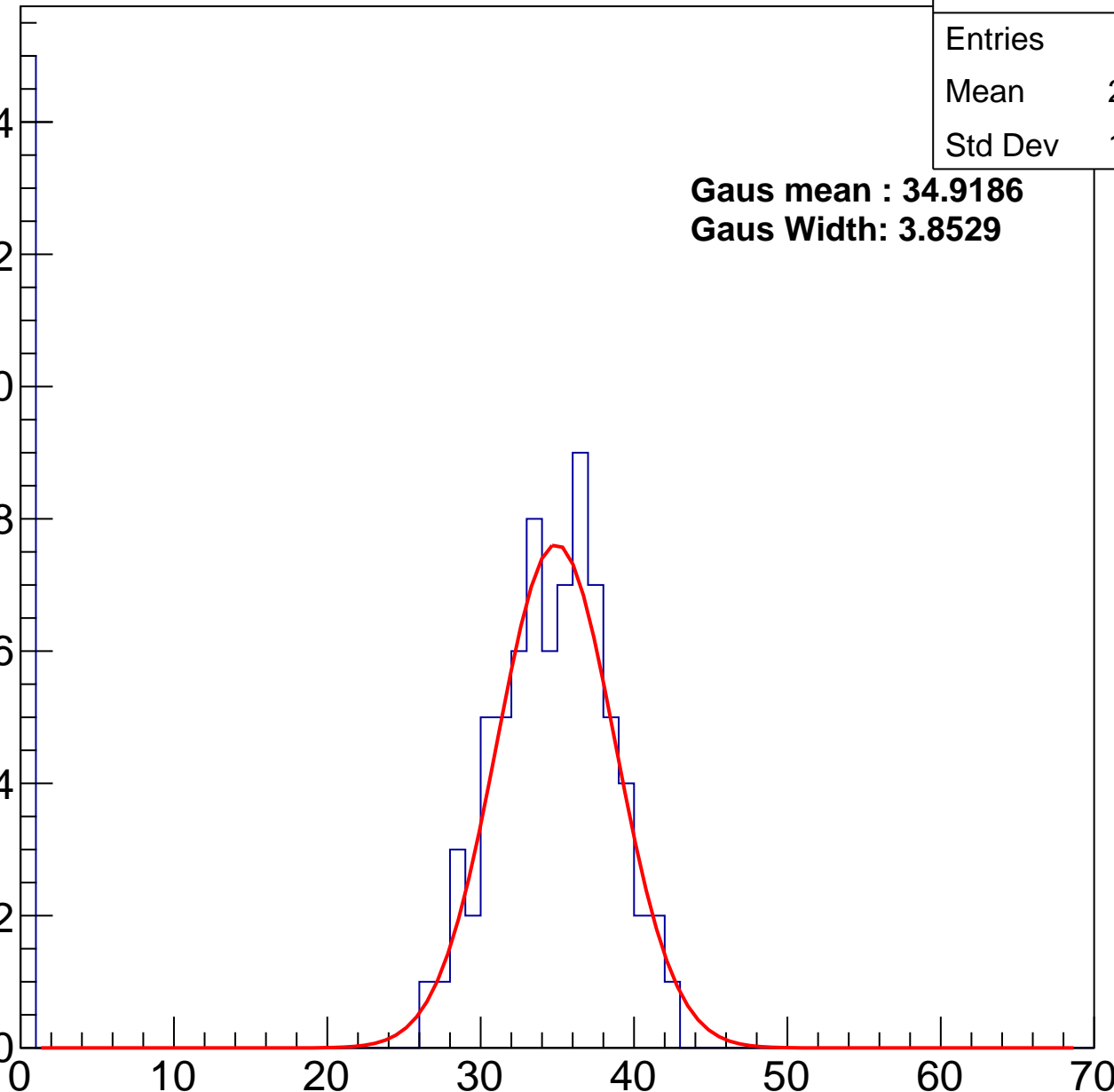
Gaus mean : 34.9186

Gaus Width: 3.8529

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch6, adc2

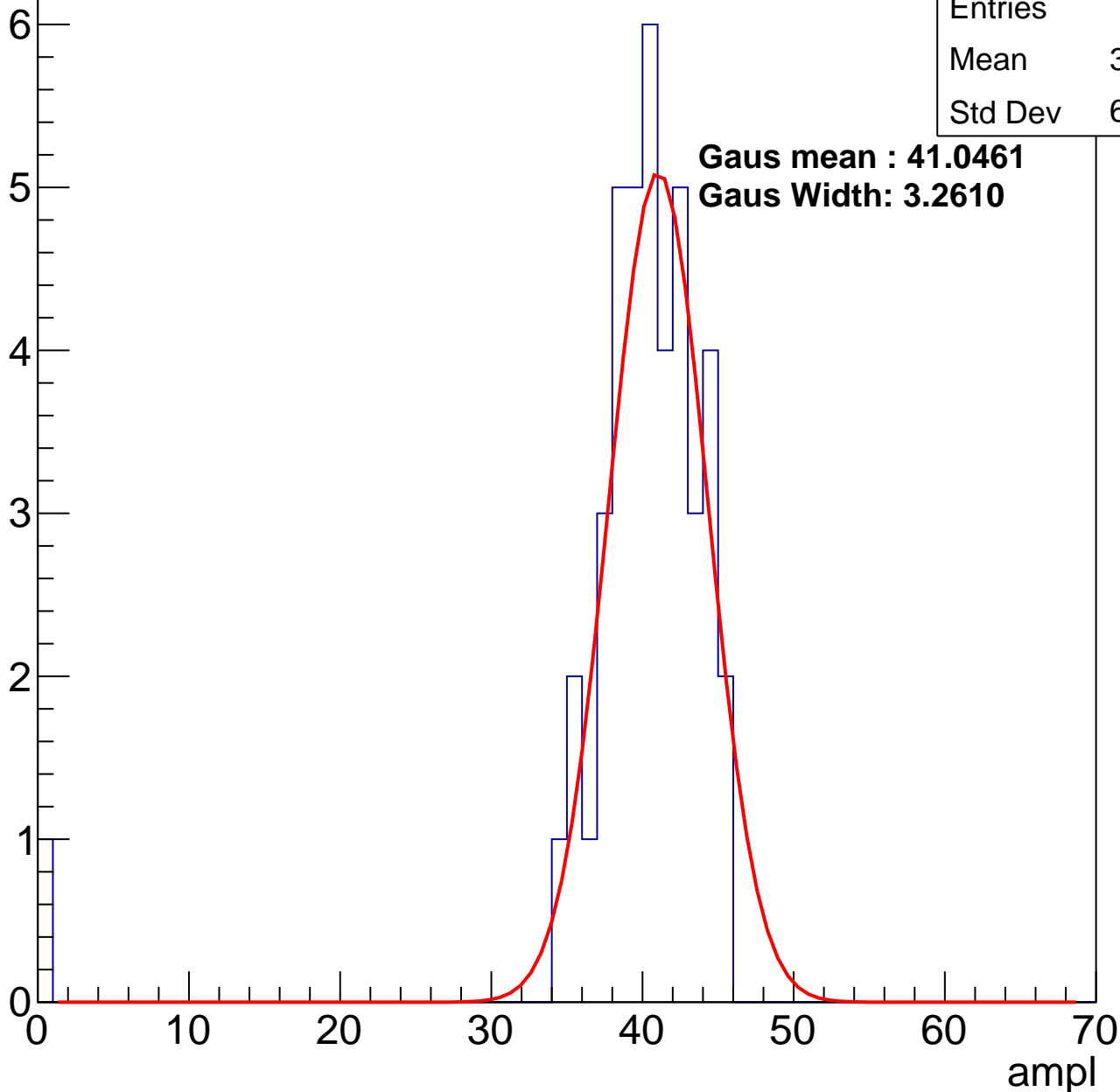
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	39.17
Std Dev	6.708

Gaus mean : 41.0461

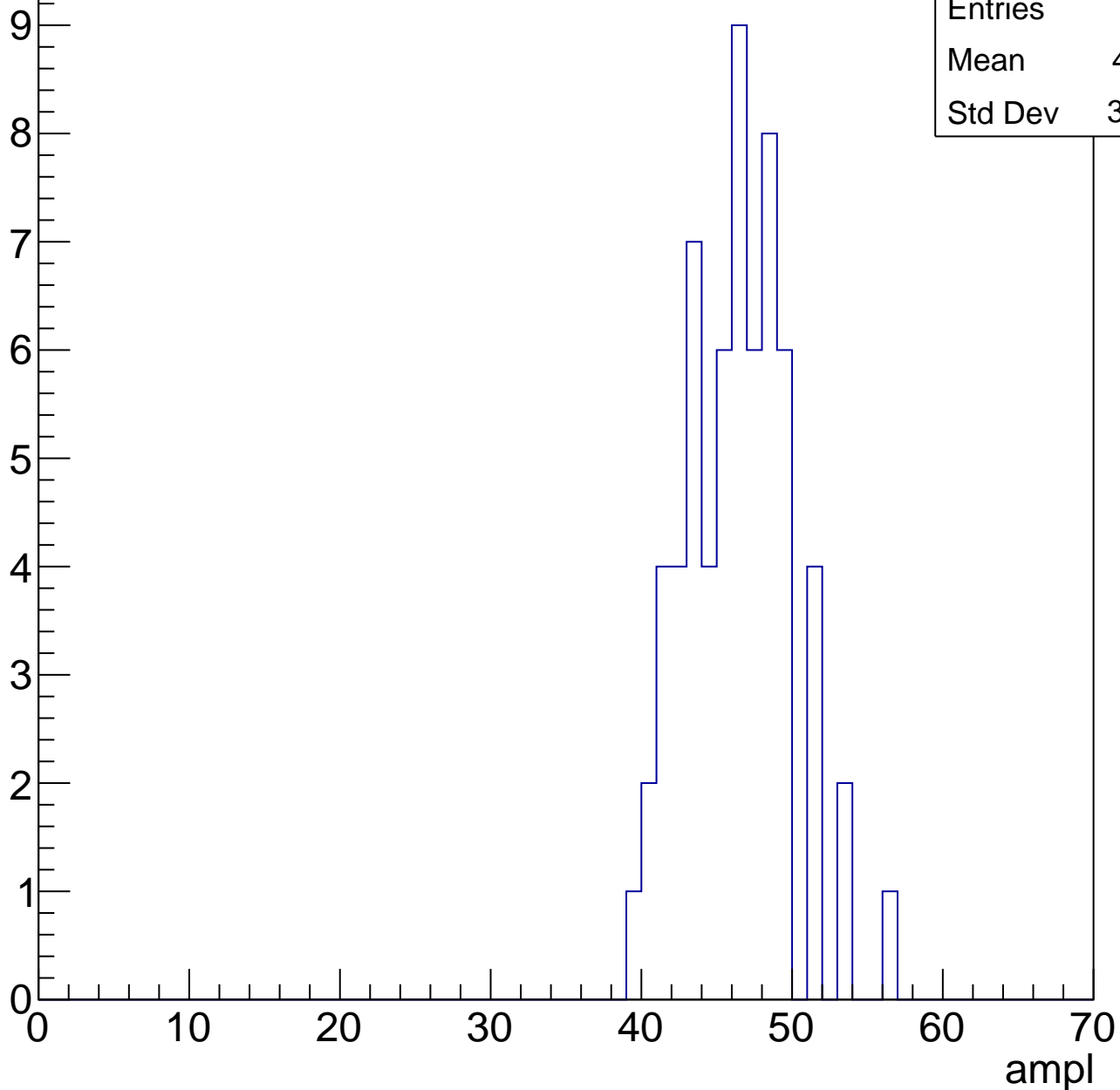
Gaus Width: 3.2610



B1L103S, U24-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

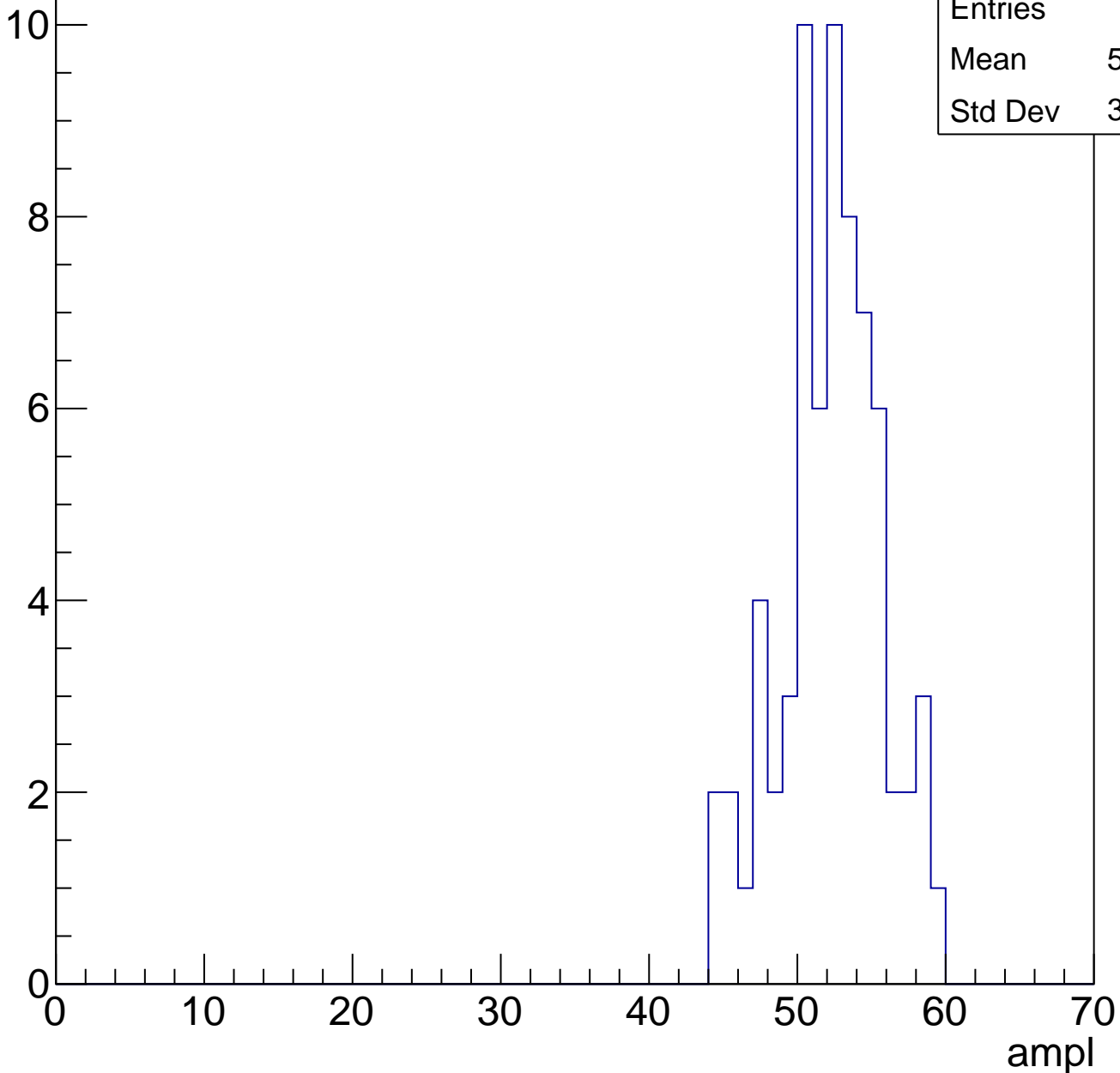


B1L103S, U24-ch6, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

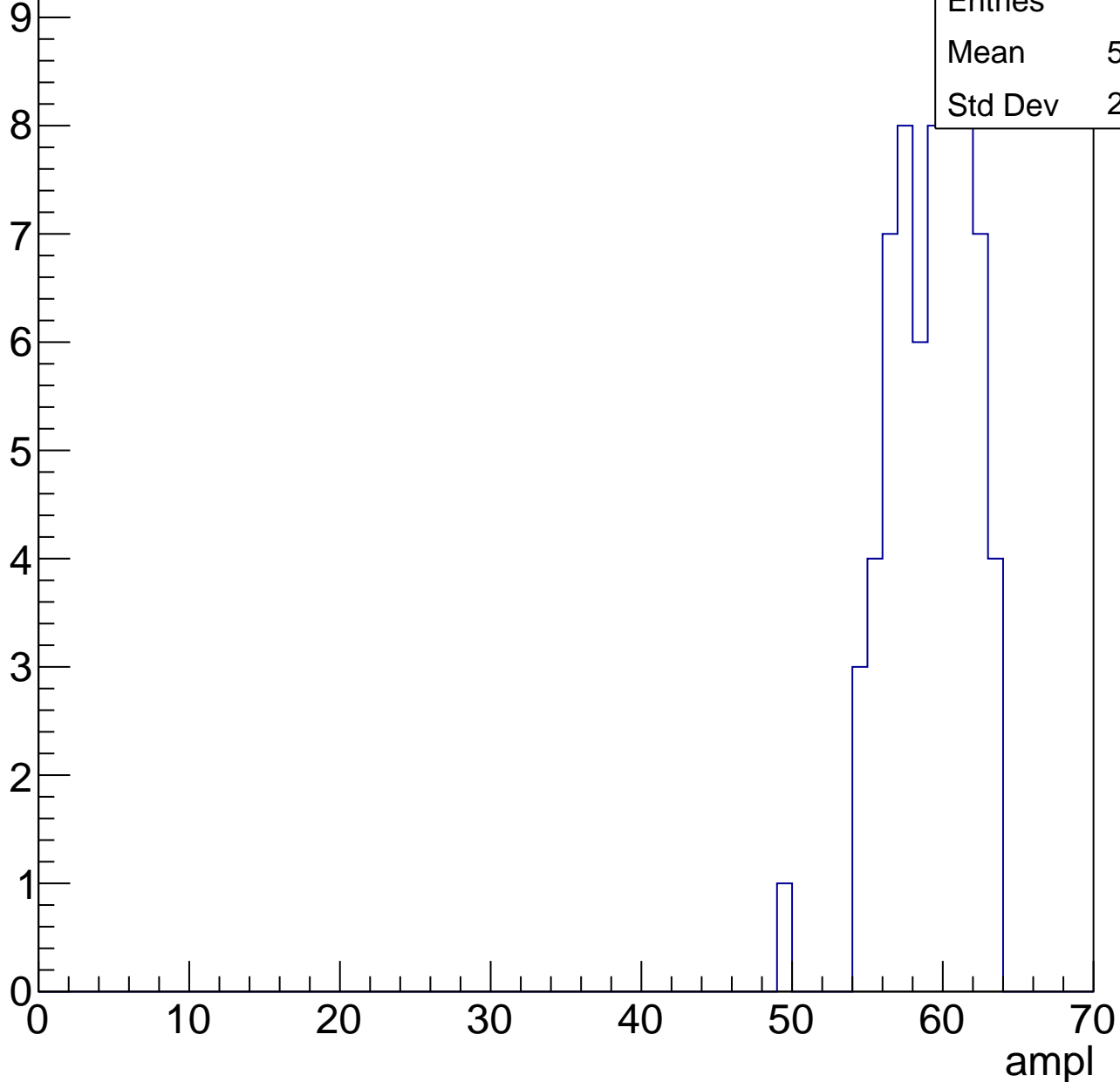
Entries	69
Mean	51.77
Std Dev	3.389



B1L103S, U24-ch6, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

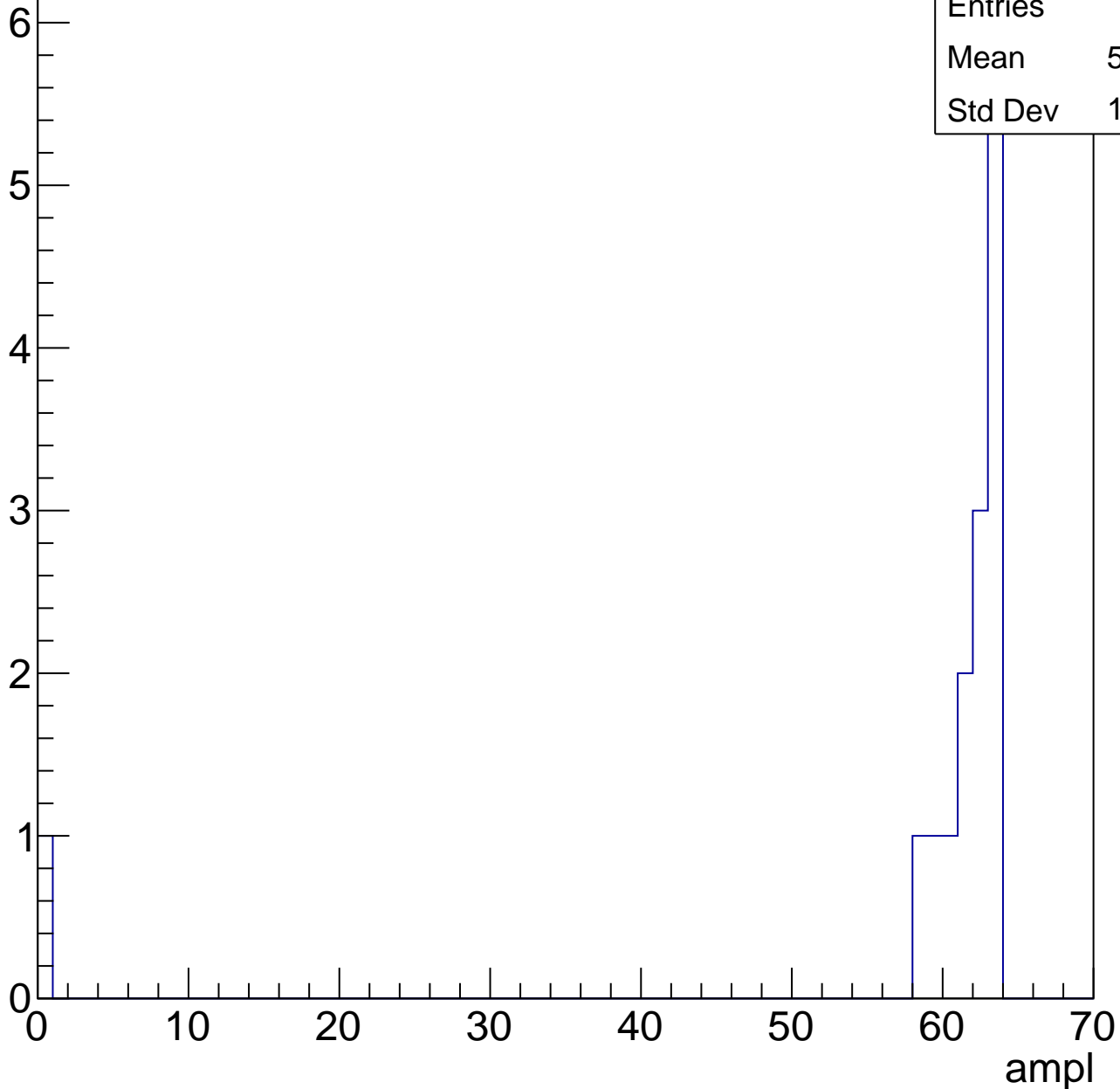


B1L103S, U24-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.53
Std Dev	15.45



B1L103S, U24-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch7, adc0

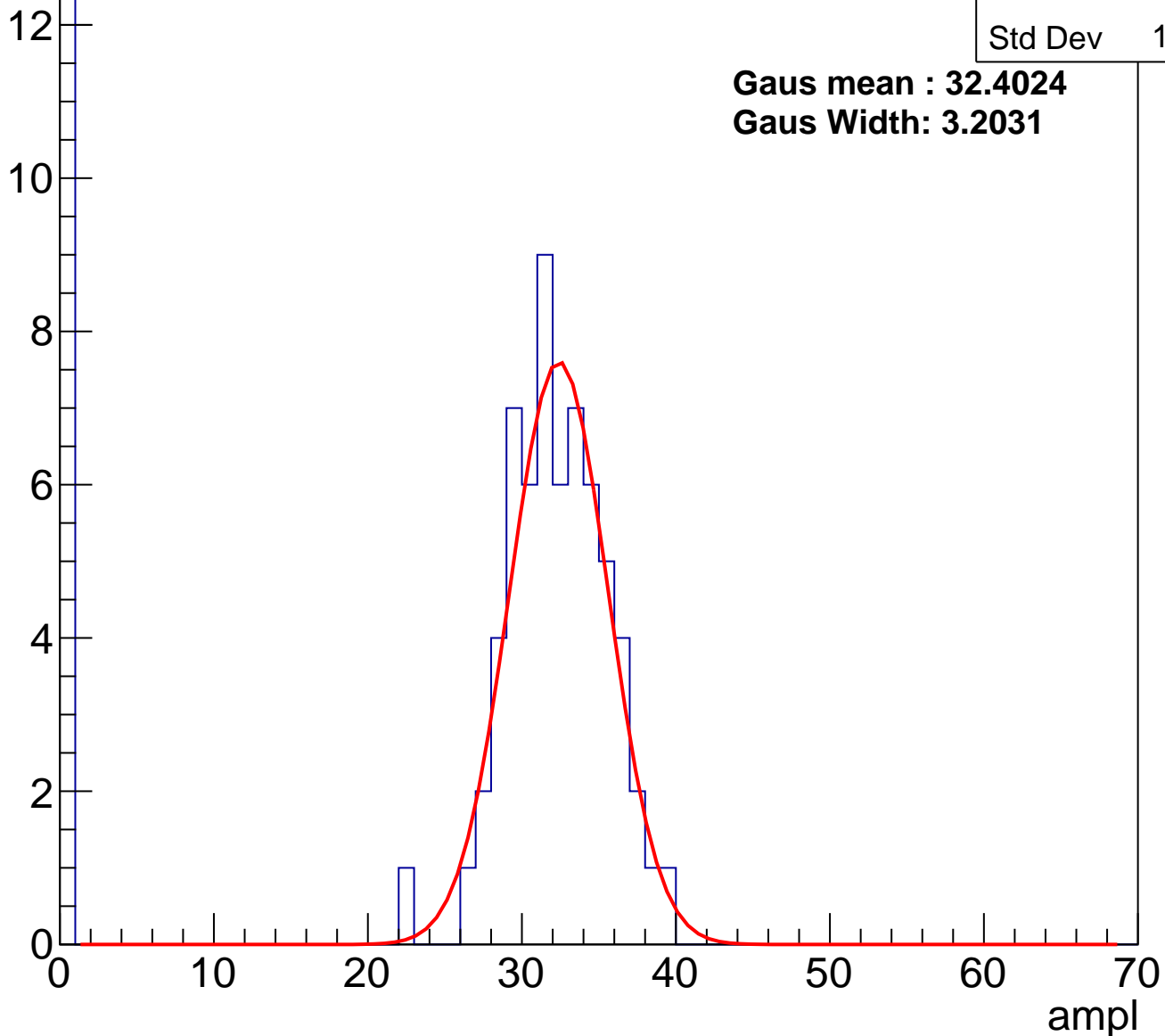
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	26.31
Std Dev	12.38

Gaus mean : 32.4024

Gaus Width: 3.2031



B1L103S, U24-ch7, adc1

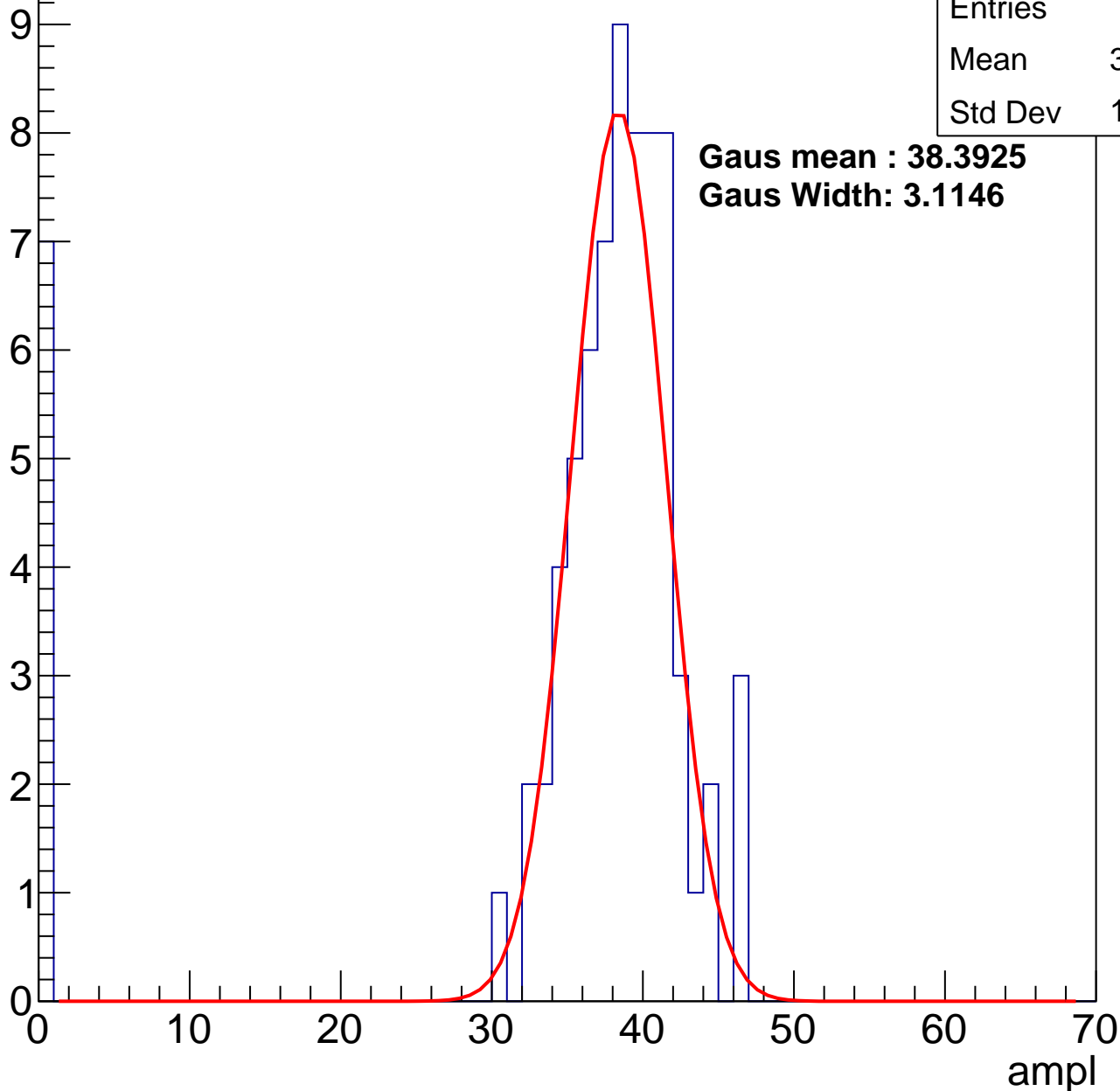
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	34.78
Std Dev	11.52

Gaus mean : 38.3925

Gaus Width: 3.1146



B1L103S, U24-ch7, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	42.86
Std Dev	10.53

Gaus mean : 45.5918

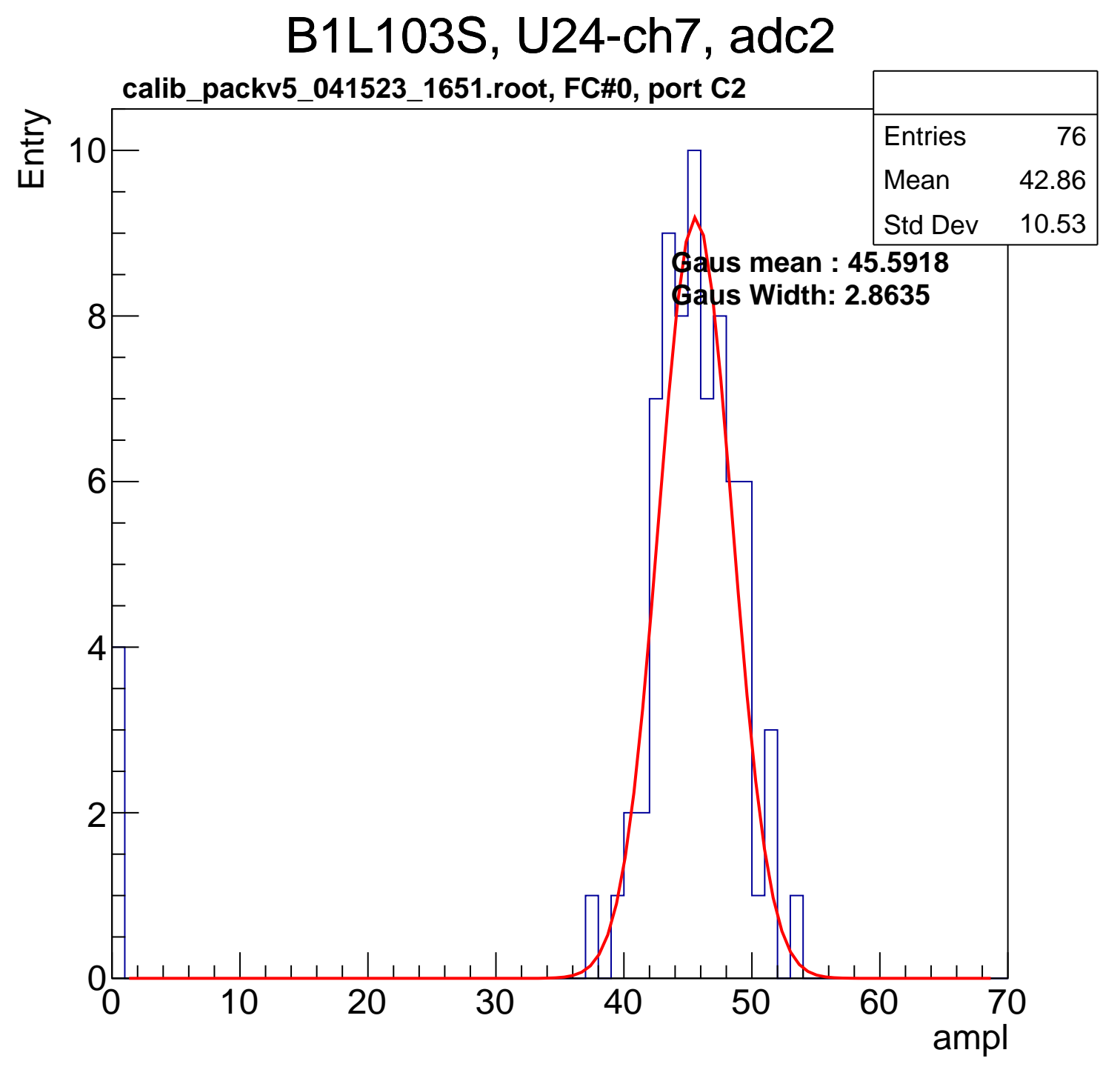
Gaus Width: 2.8635

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

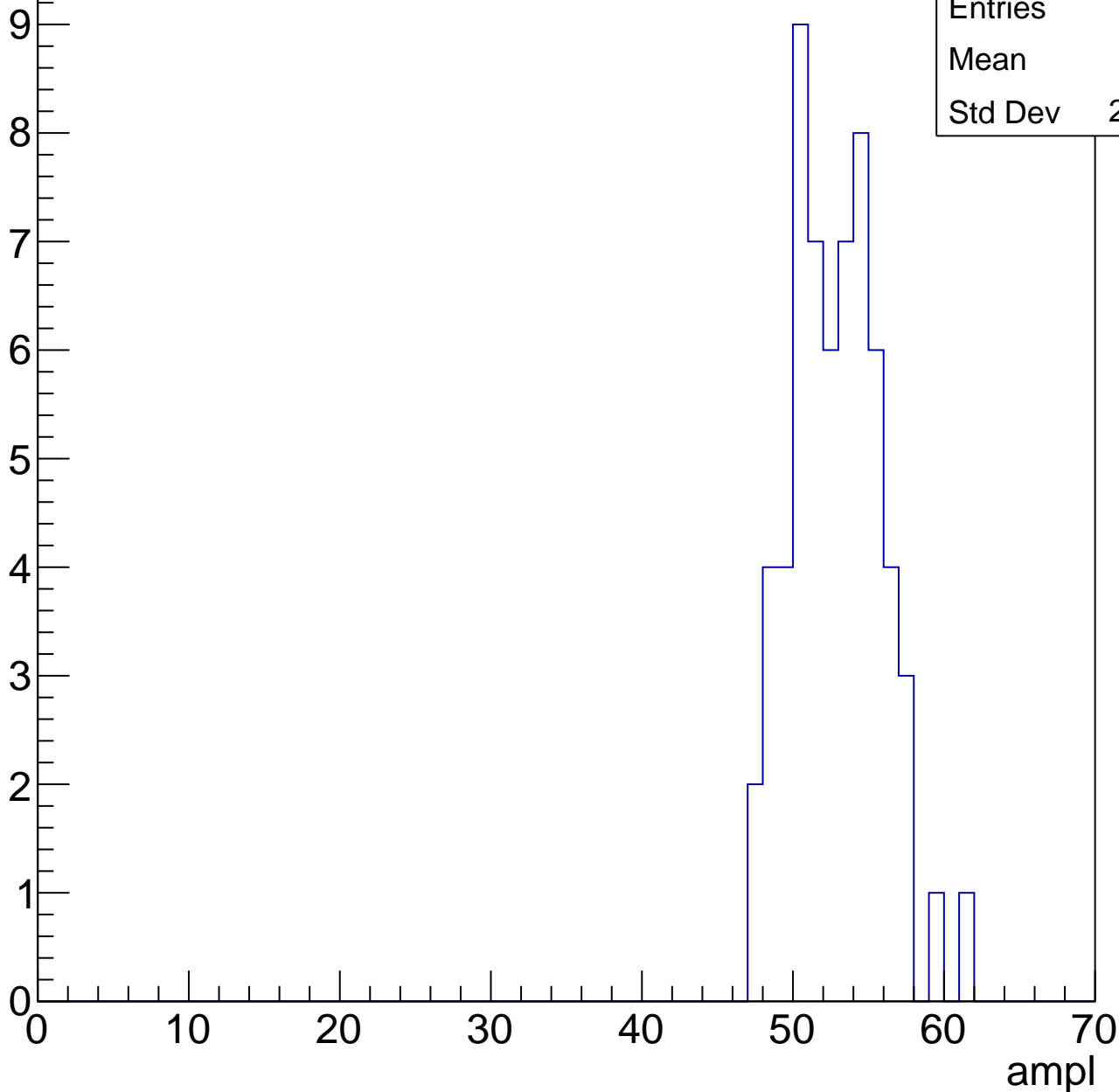


B1L103S, U24-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	52.4
Std Dev	2.965

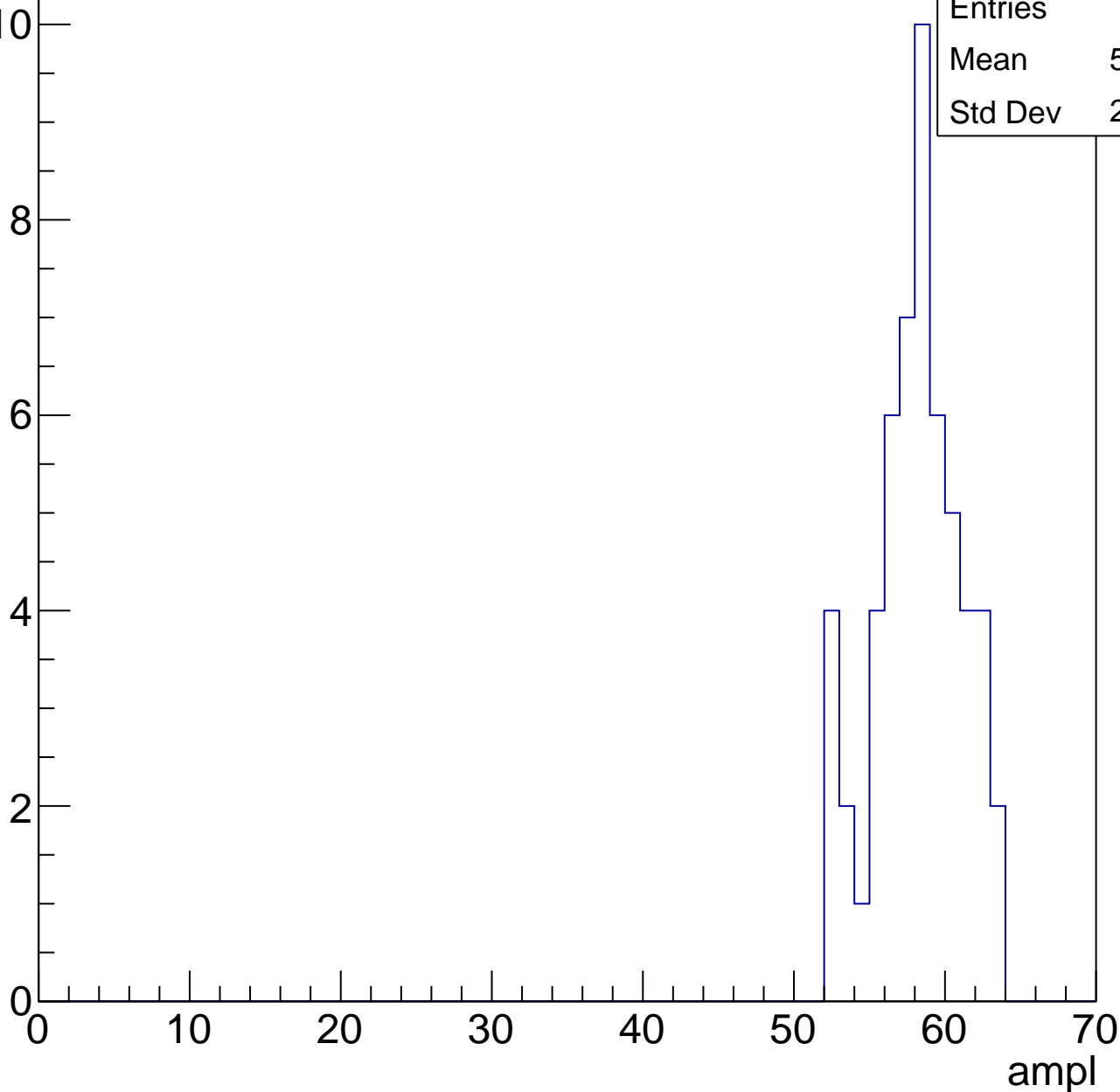


B1L103S, U24-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	57.73
Std Dev	2.857

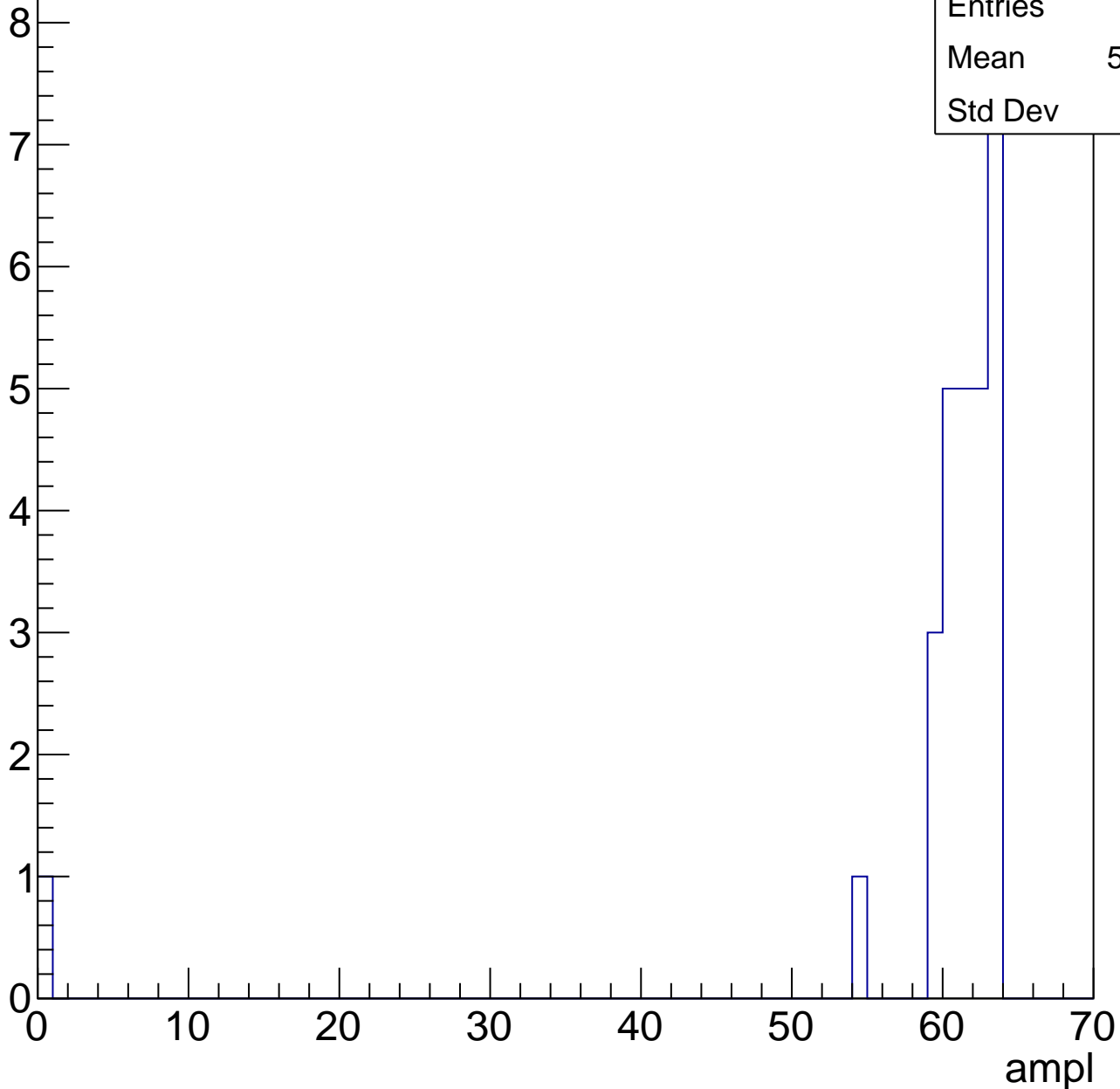


B1L103S, U24-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

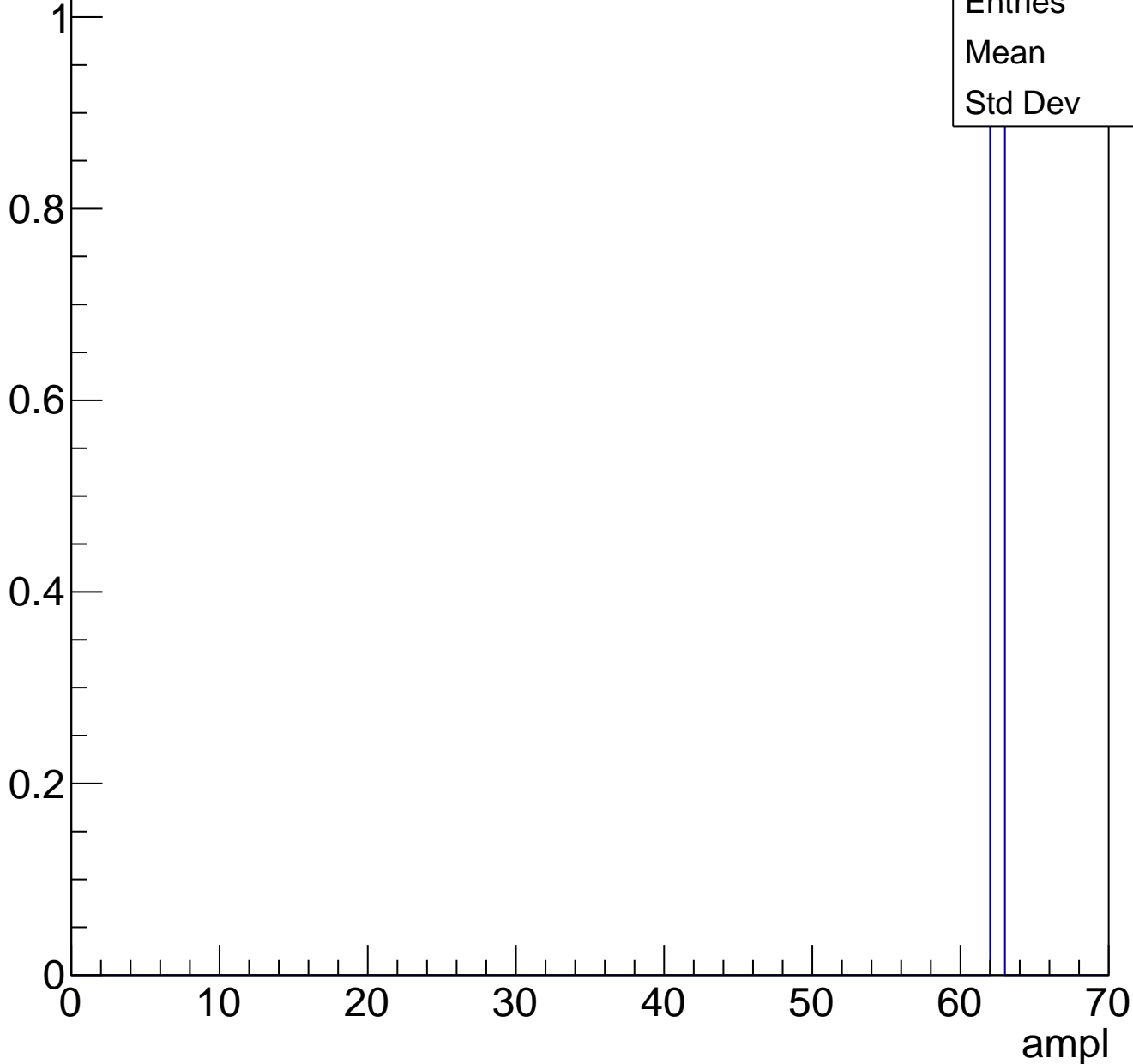
Entries	28
Mean	58.93
Std Dev	11.5



B1L103S, U24-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch7, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

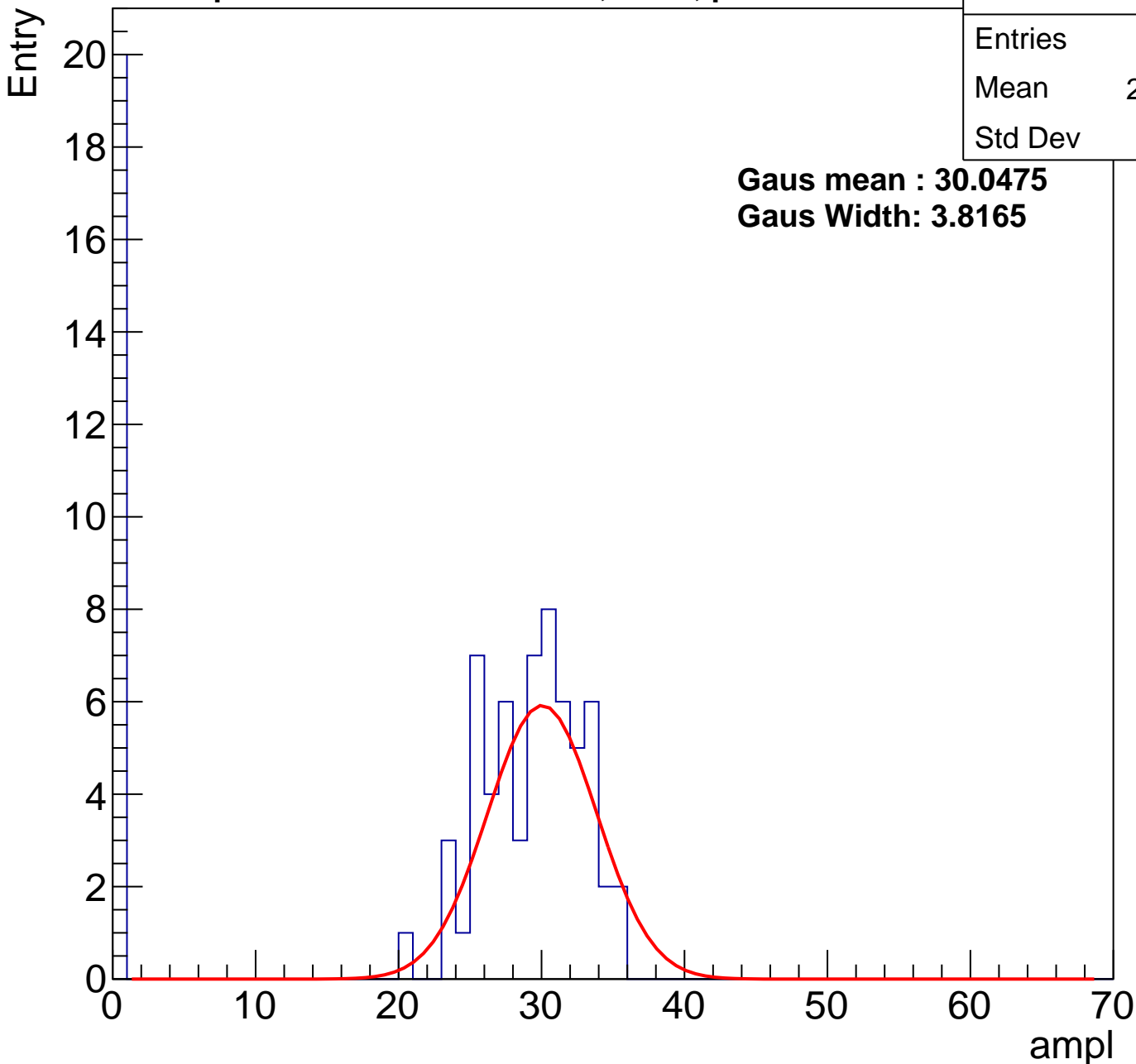
B1L103S, U24-ch8, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	21.77
Std Dev	12.8

Gaus mean : 30.0475

Gaus Width: 3.8165



B1L103S, U24-ch8, adc1

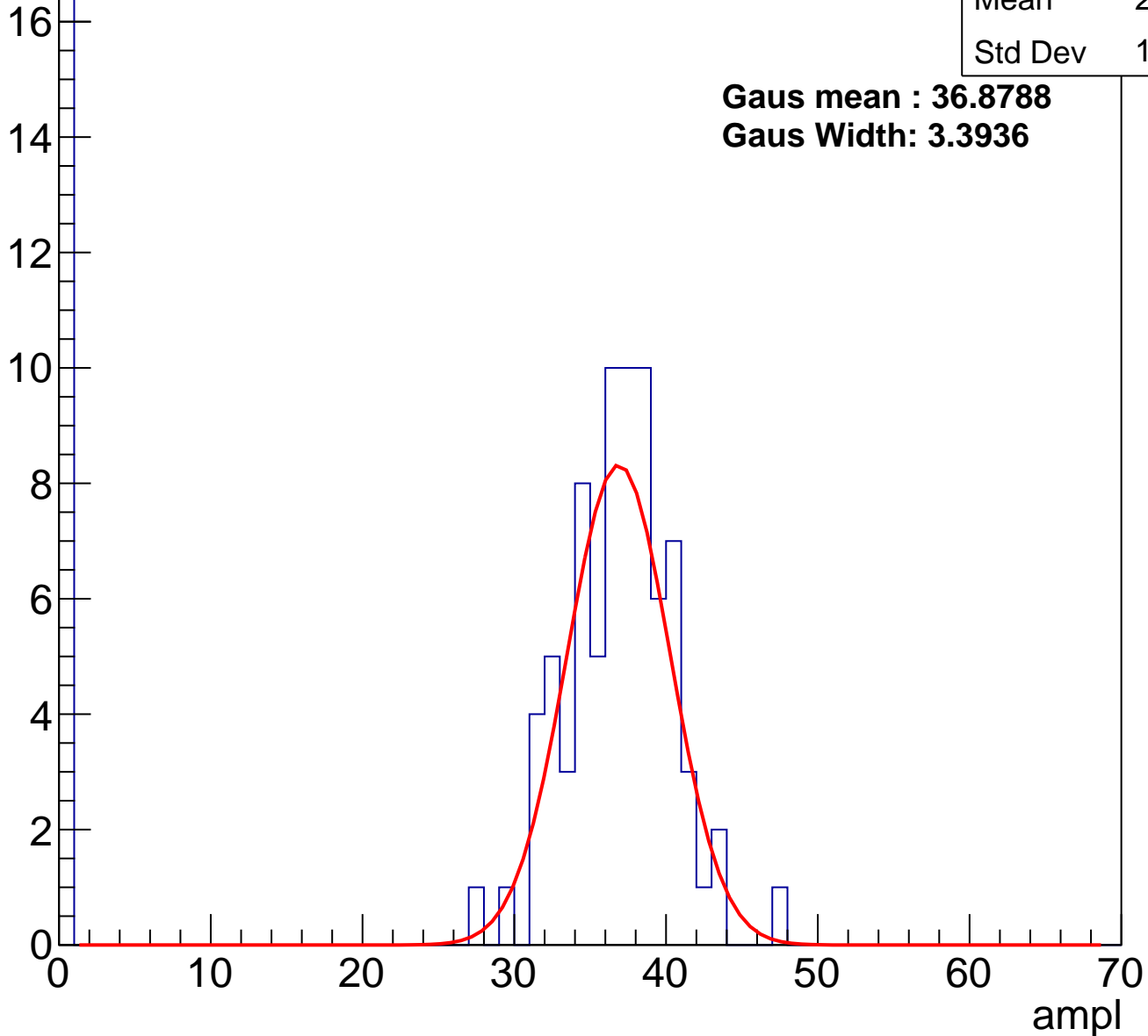
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	29.87
Std Dev	14.37

Gaus mean : 36.8788

Gaus Width: 3.3936

Entry



B1L103S, U24-ch8, adc2

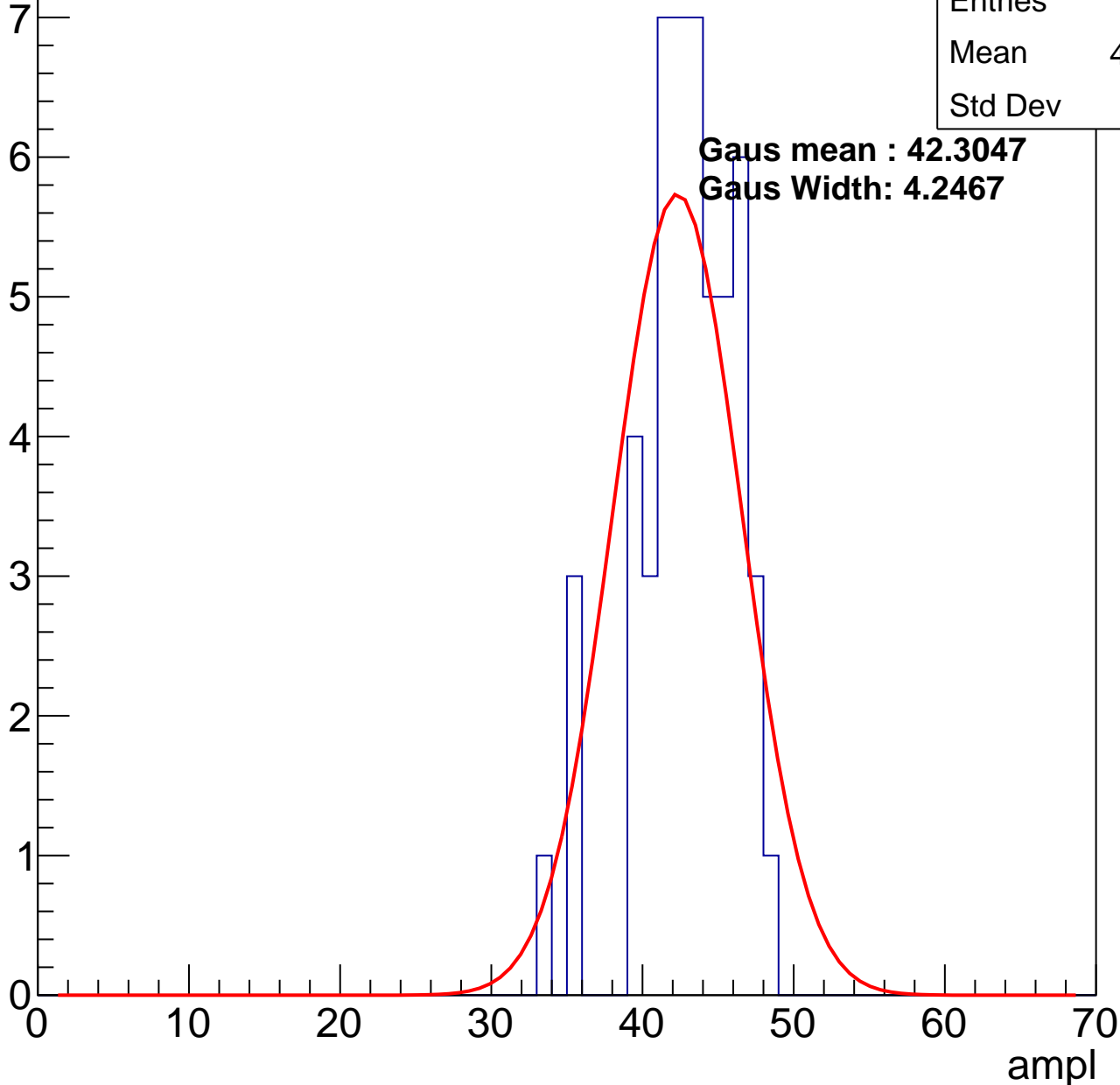
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	42.42
Std Dev	3.26

Gaus mean : 42.3047

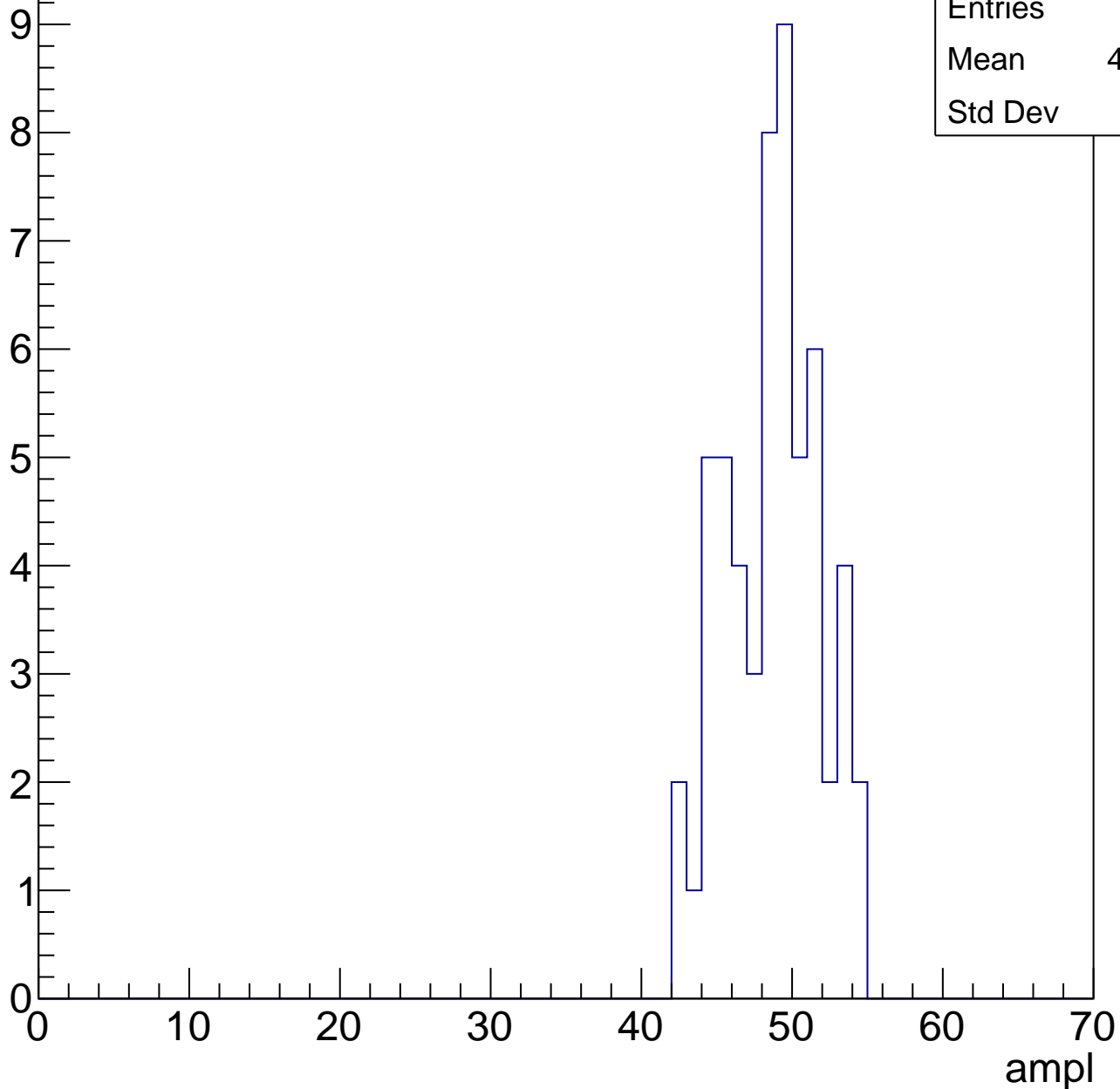
Gaus Width: 4.2467



B1L103S, U24-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



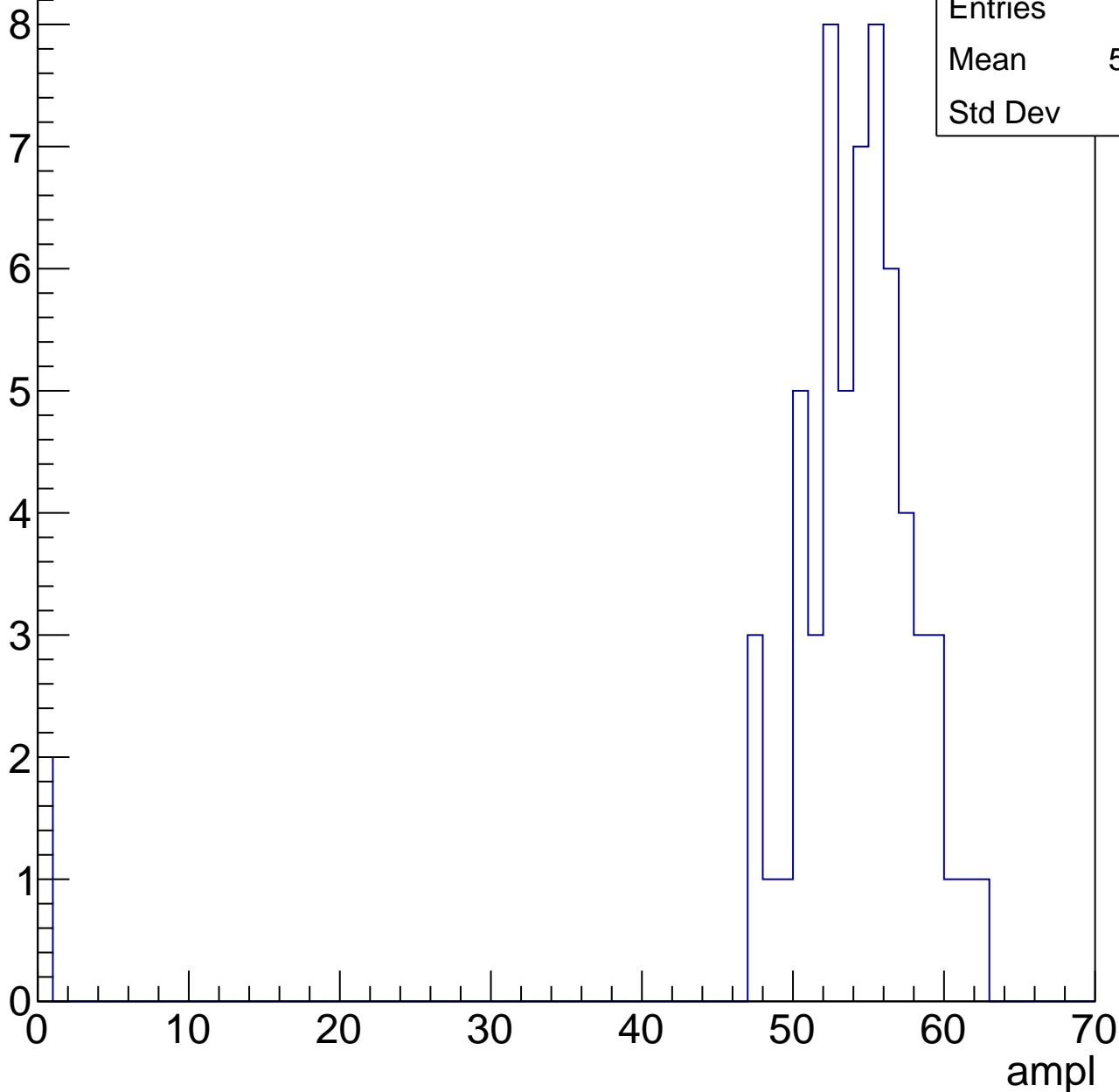
Entries	56
Mean	48.25
Std Dev	3.06

B1L103S, U24-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	52.23
Std Dev	10.1



B1L103S, U24-ch8, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	58.44
Std Dev	7.898

ampl

0

10

20

30

40

50

60

70

B1L103S, U24-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	4
Mean	62
Std Dev	1.225

B1L103S, U24-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

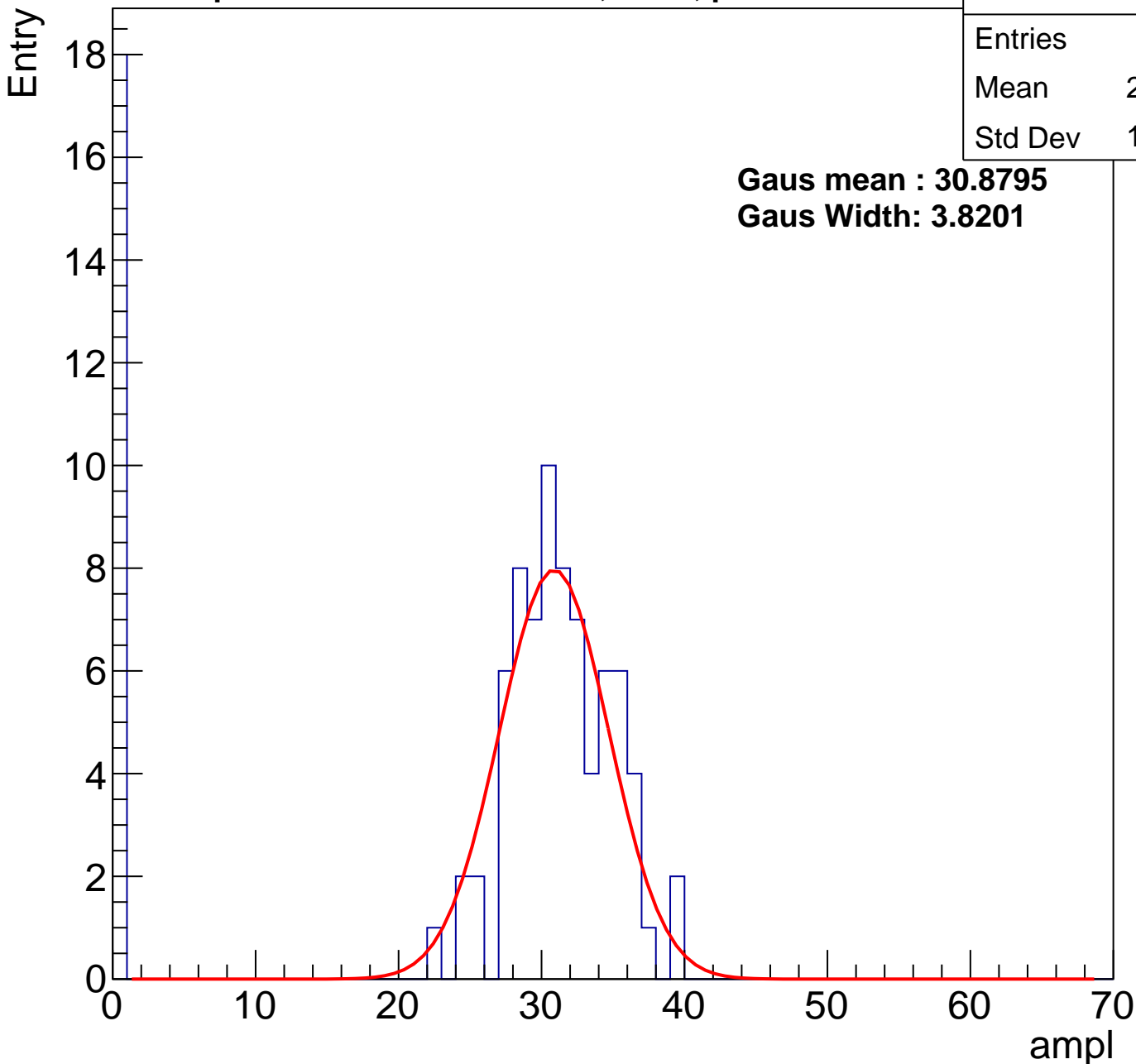
B1L103S, U24-ch9, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	24.85
Std Dev	12.64

Gaus mean : 30.8795

Gaus Width: 3.8201



B1L103S, U24-ch9, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	31.76
Std Dev	14

Gaus mean : 37.9718

Gaus Width: 4.2845

Entry

12

10

8

6

4

2

0

0

10

20

30

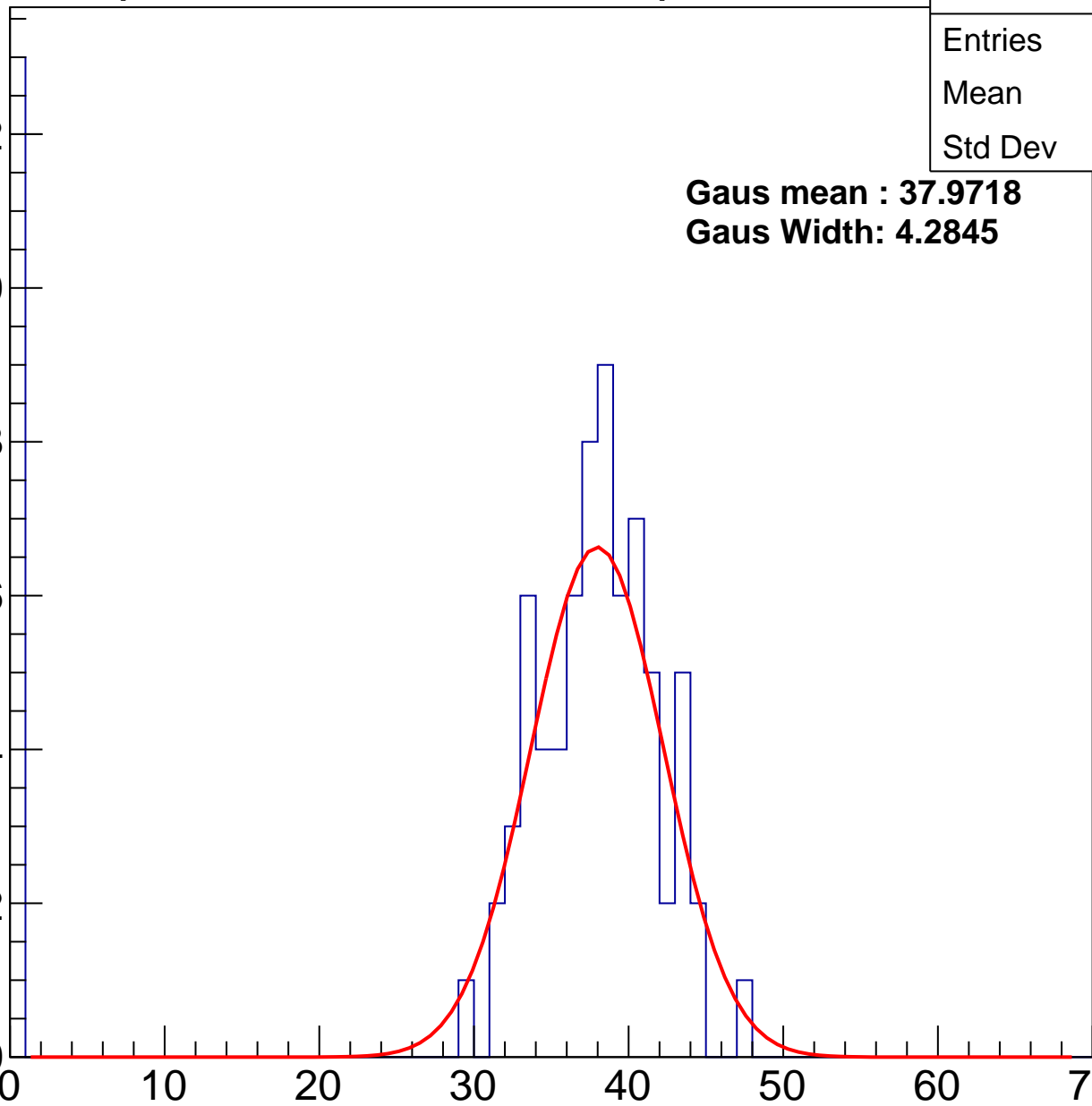
40

50

60

70

ampl



B1L103S, U24-ch9, adc2

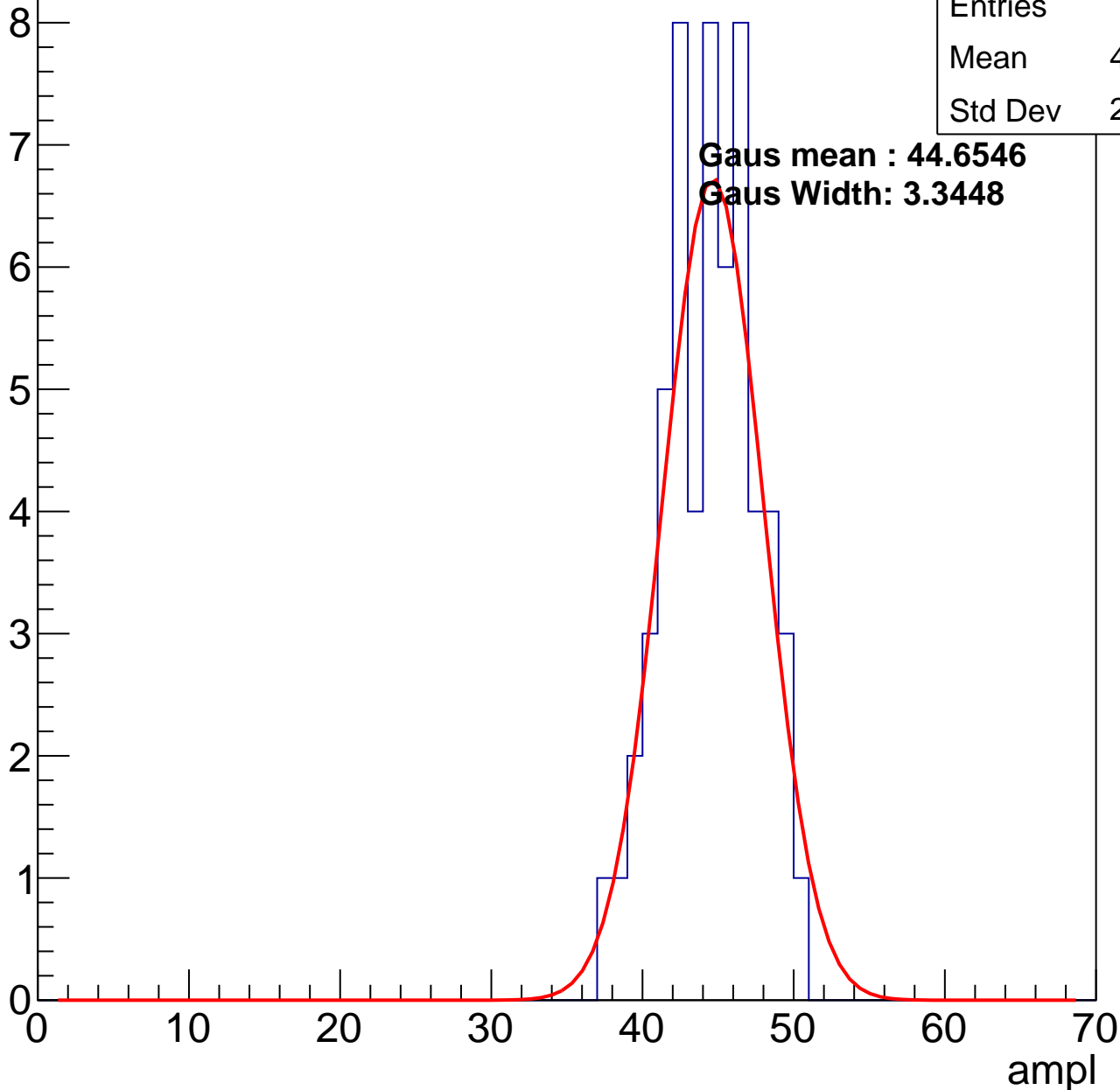
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	44.02
Std Dev	2.974

Gaus mean : 44.6546

Gaus Width: 3.3448

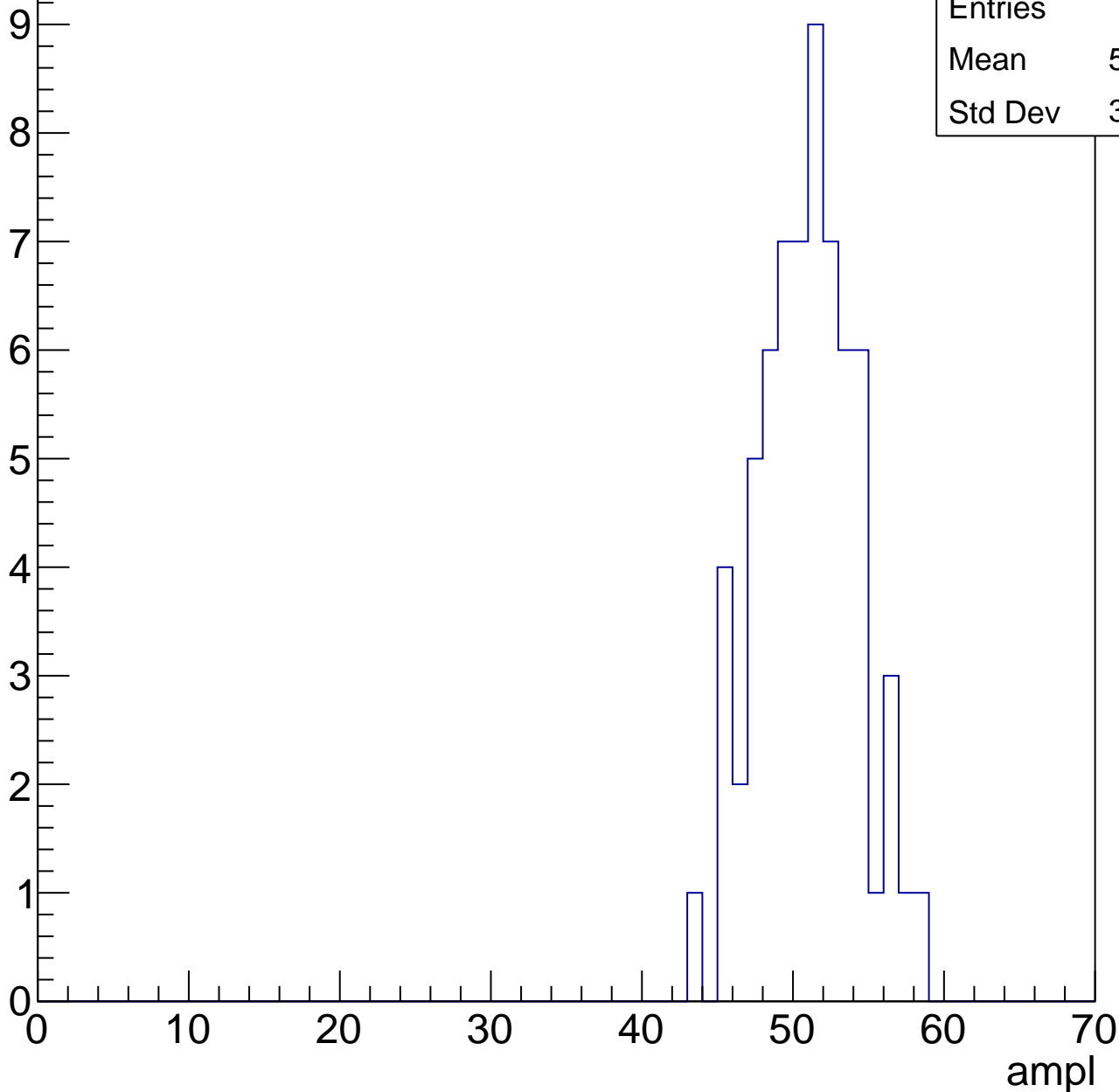


B1L103S, U24-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	50.52
Std Dev	3.192



B1L103S, U24-ch9, adc4

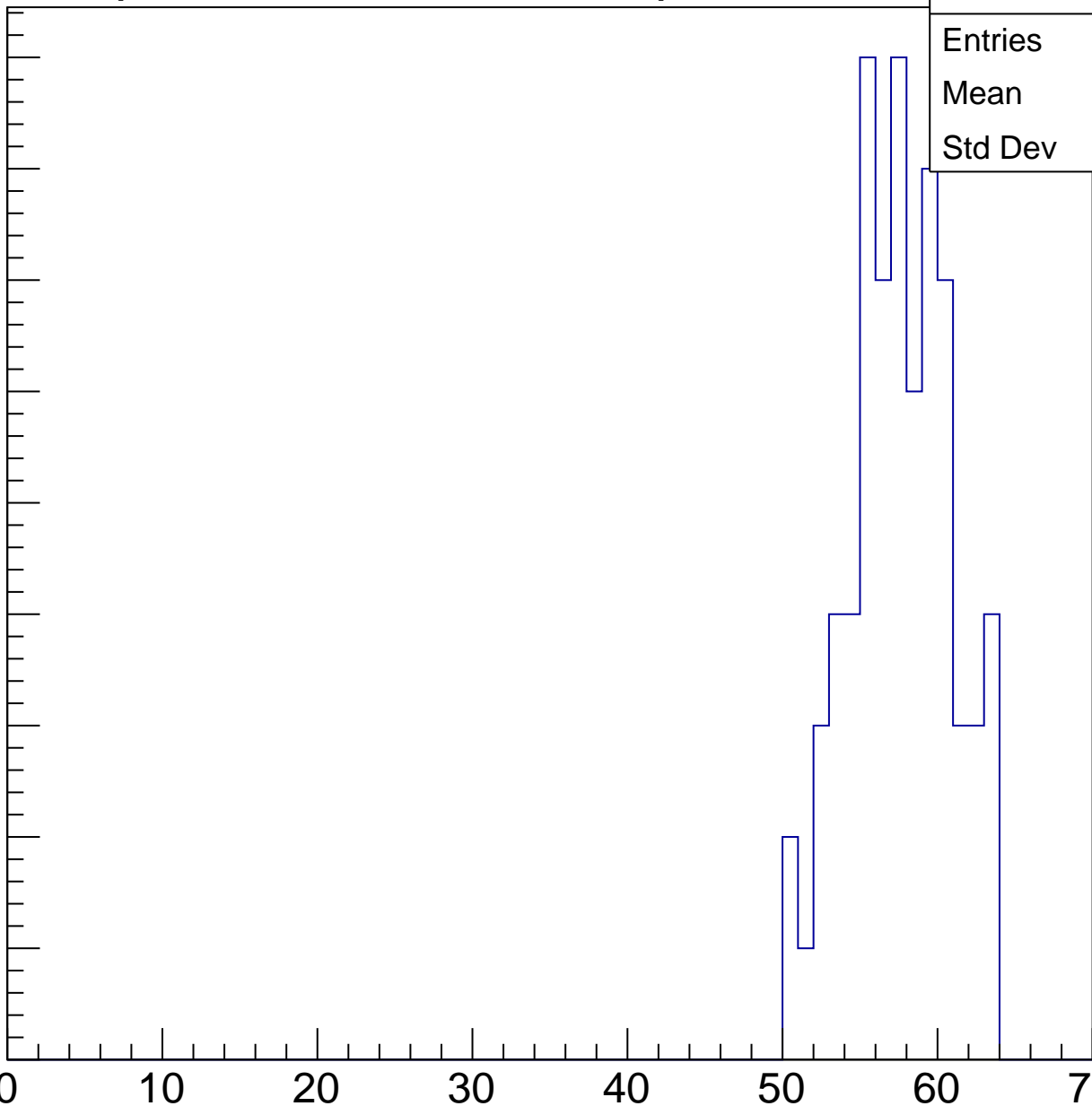
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	70
Mean	57.09
Std Dev	3.206

ampl

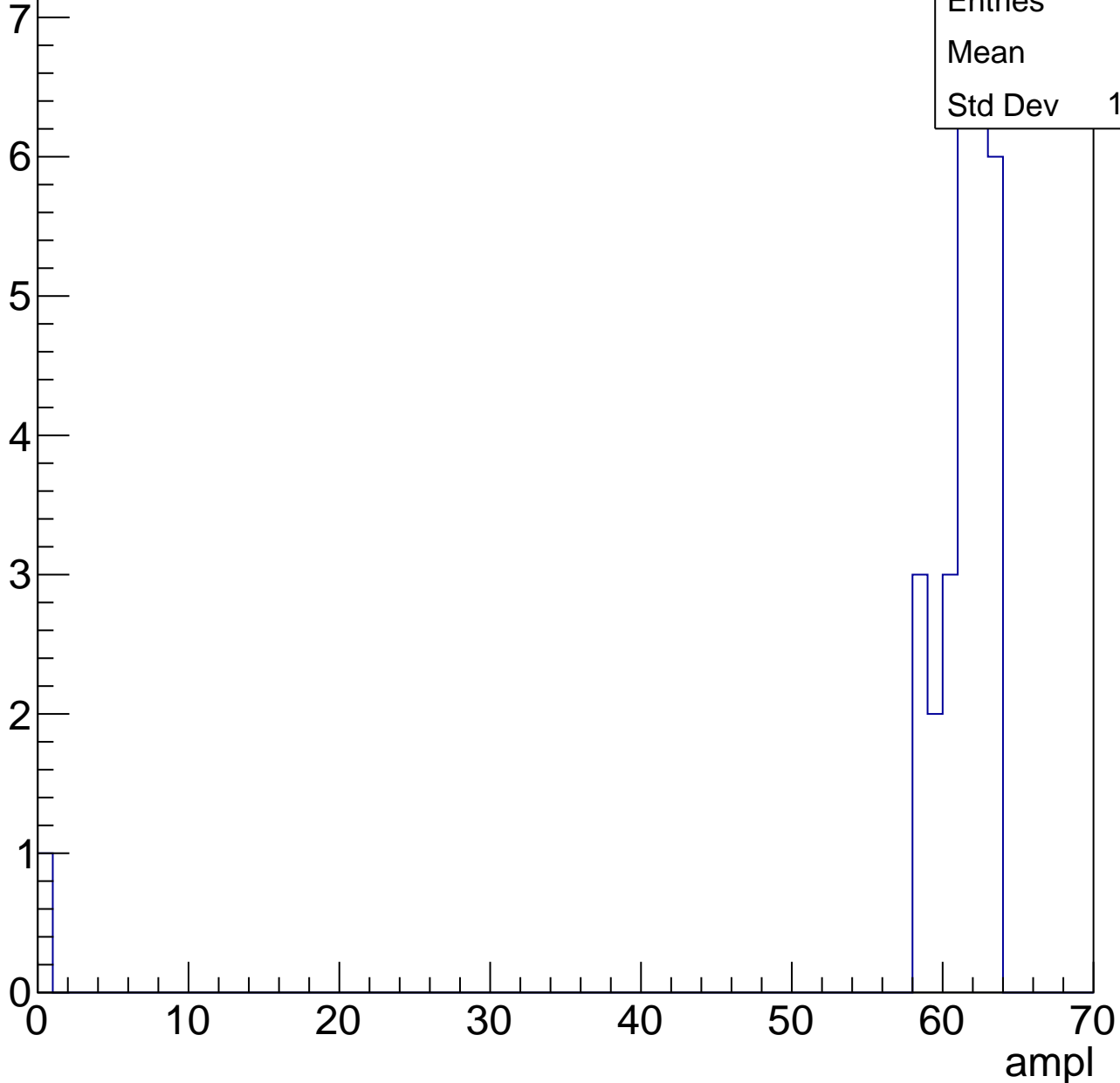


B1L103S, U24-ch9, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	59
Std Dev	11.26



B1L103S, U24-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

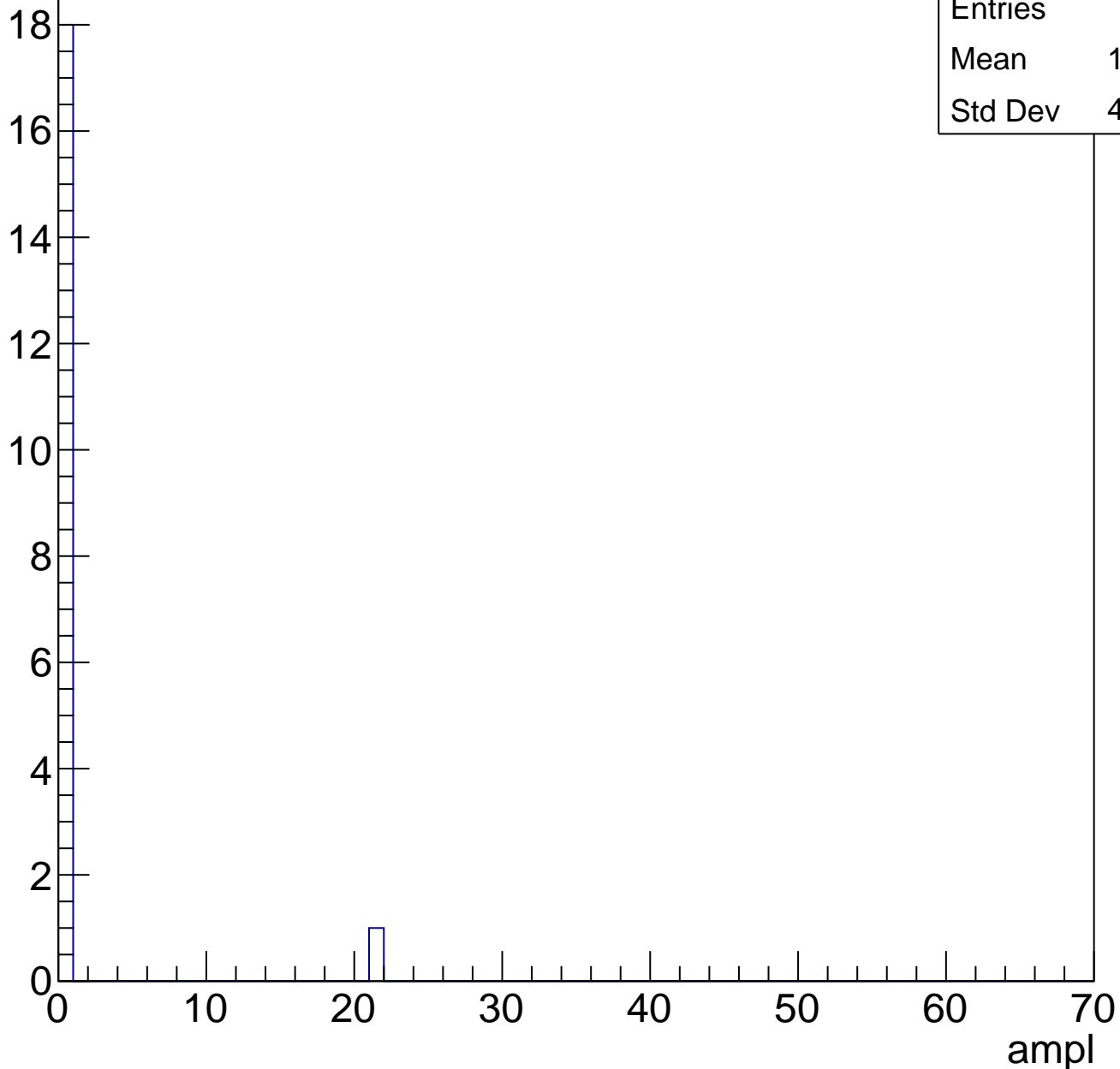


Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	1.105
Std Dev	4.689

B1L103S, U24-ch10, adc0

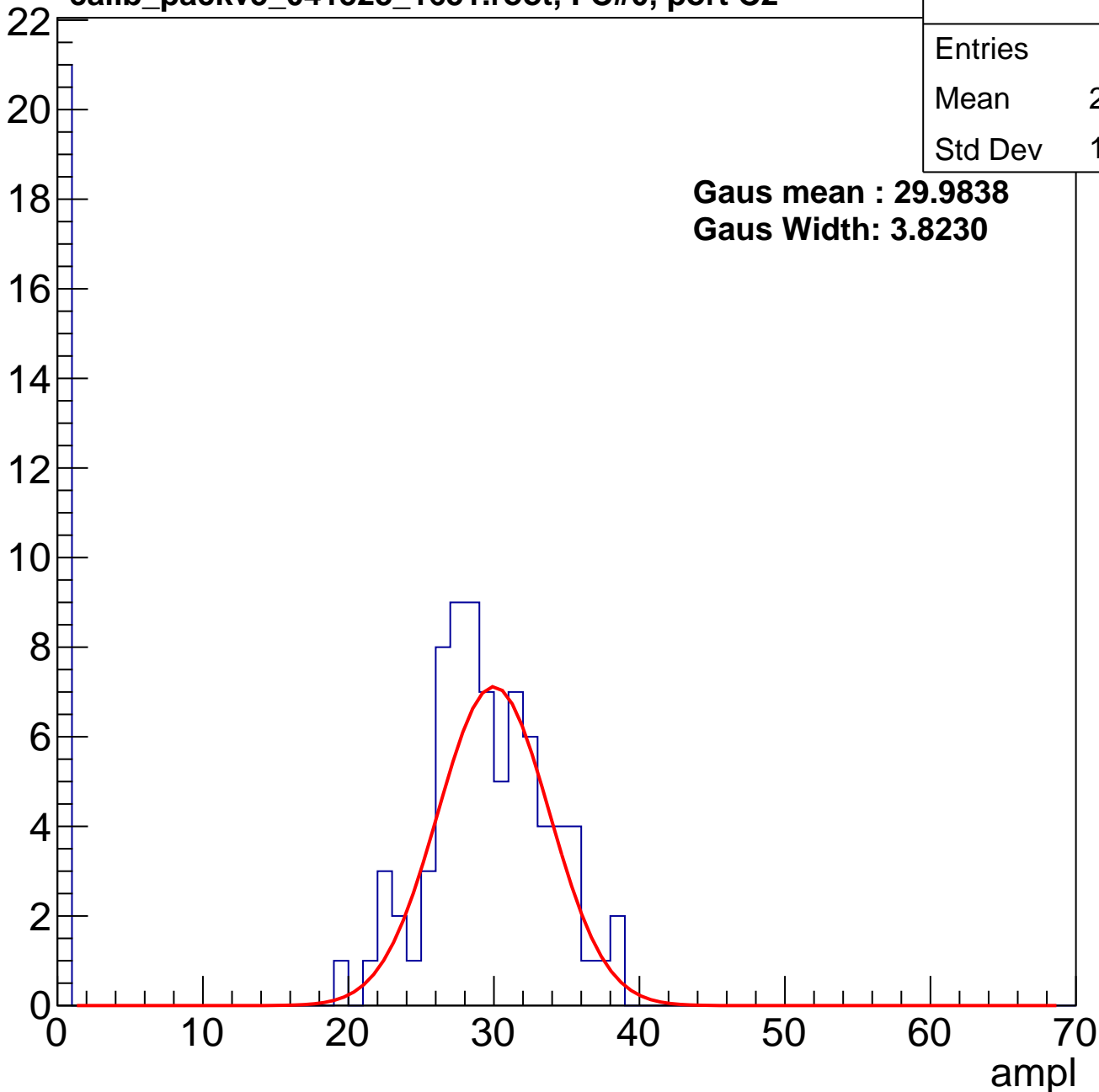
calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	22.96
Std Dev	12.43

Gaus mean : 29.9838

Gaus Width: 3.8230

Entry



B1L103S, U24-ch10, adc1

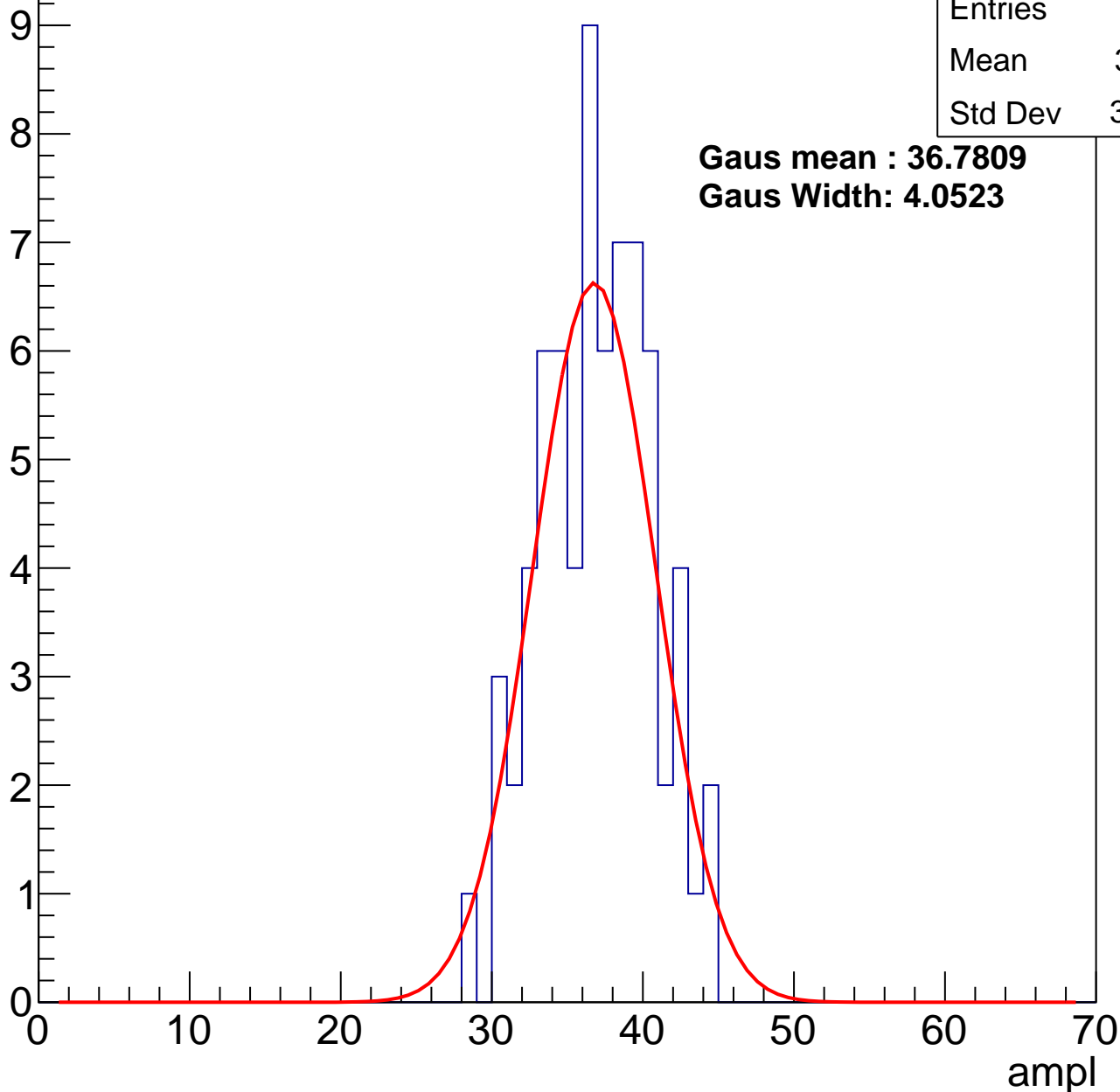
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	36.51
Std Dev	3.612

Gaus mean : 36.7809

Gaus Width: 4.0523



B1L103S, U24-ch10, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	36.59
Std Dev	15.88

Gaus mean : 43.7680

Gaus Width: 3.8959

10

8

6

4

2

0

0

10

20

30

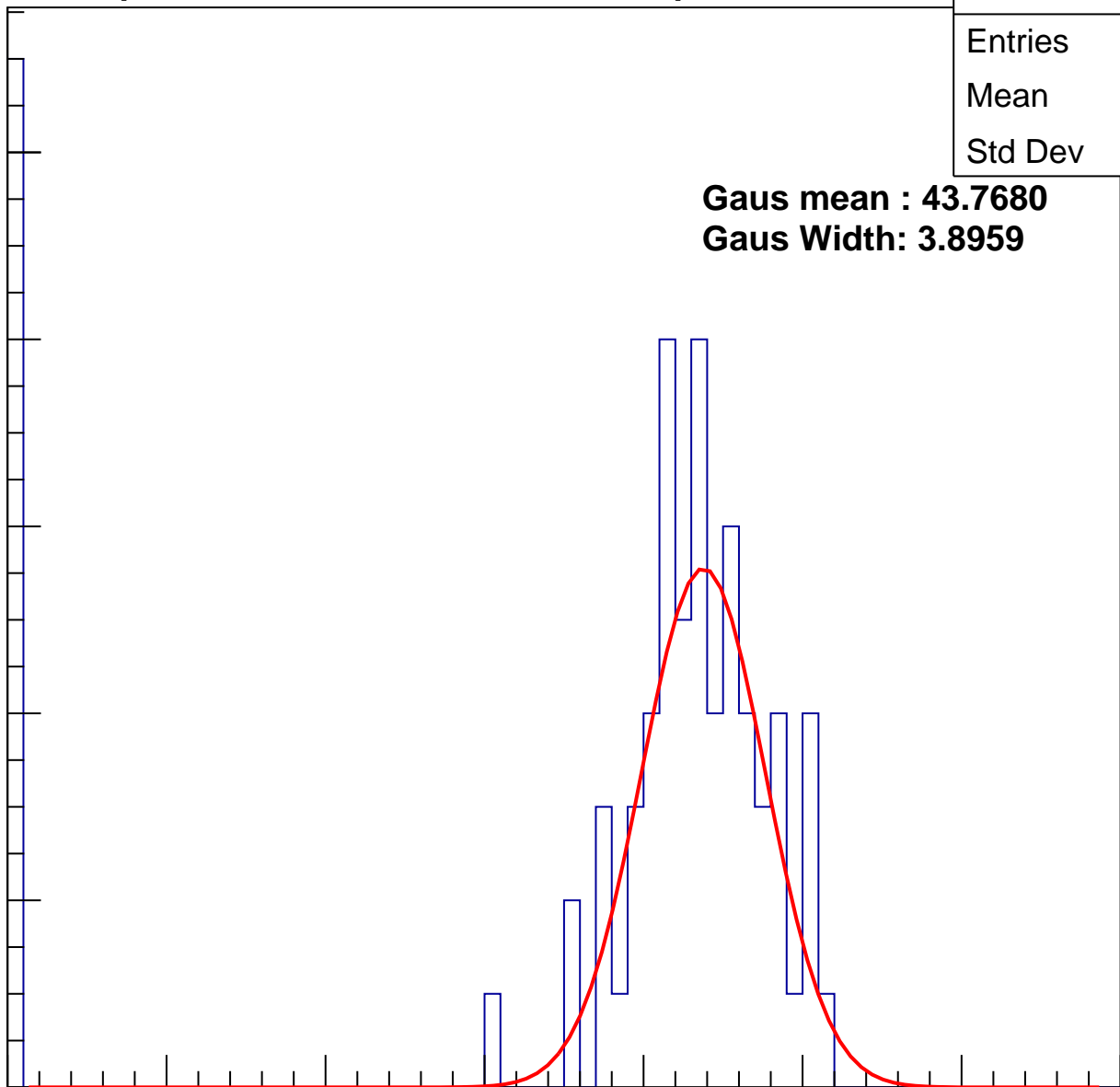
40

50

60

70

ampl

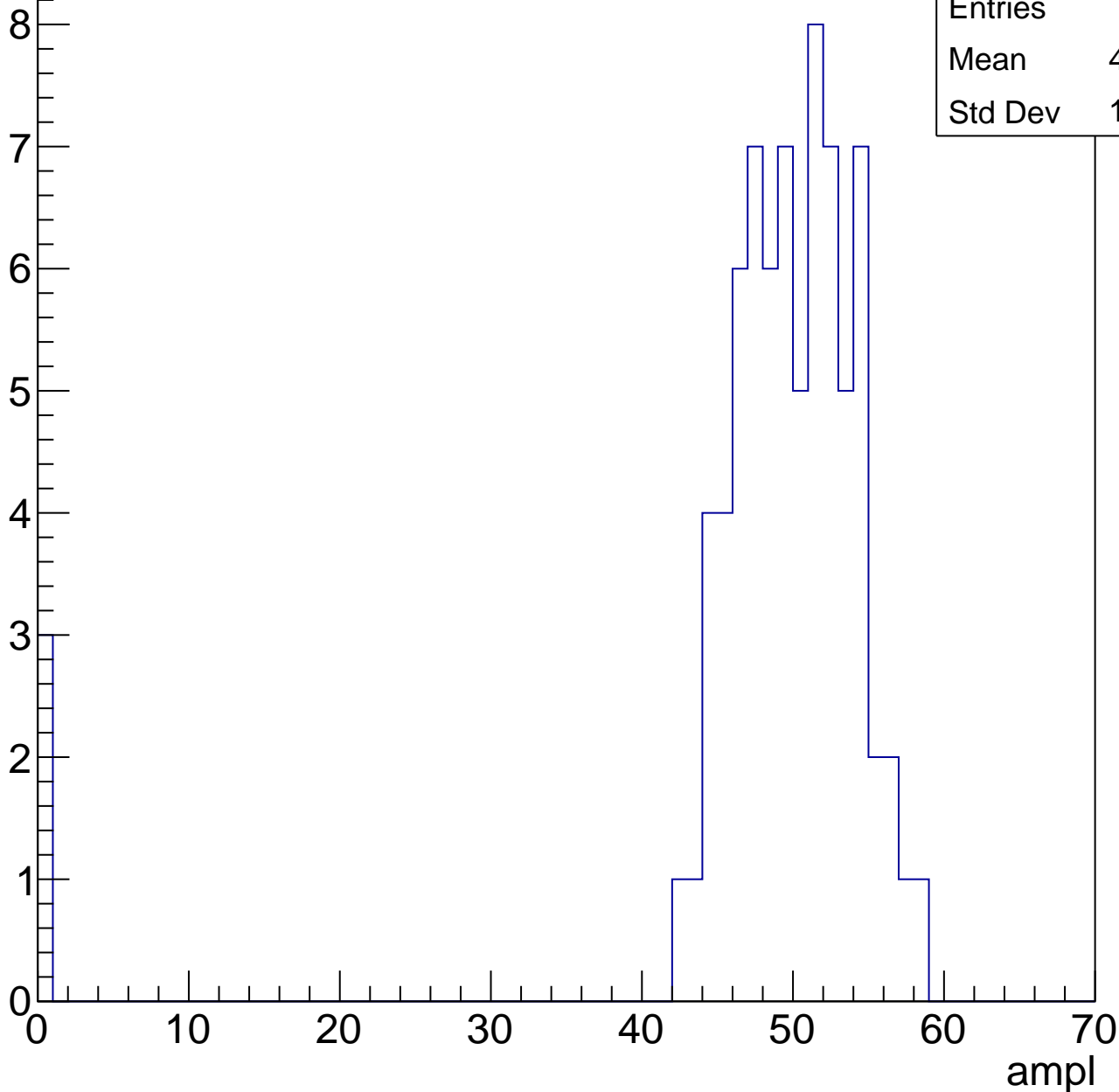


B1L103S, U24-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	47.78
Std Dev	10.25



B1L103S, U24-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

6

5

4

3

2

1

0

Entries 46

Mean 55.98

Std Dev 3.193

0

10

20

30

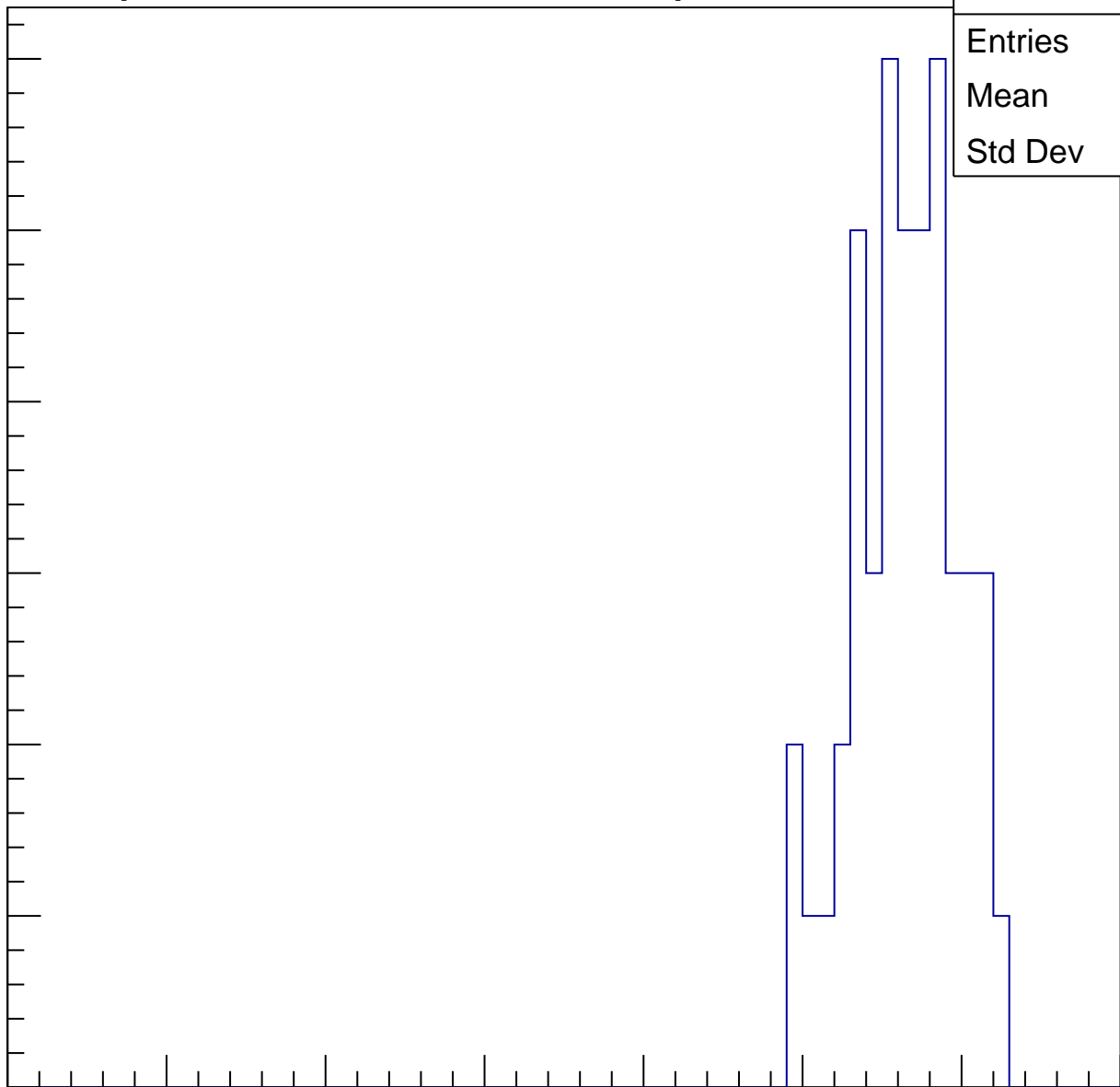
40

50

60

70

ampl

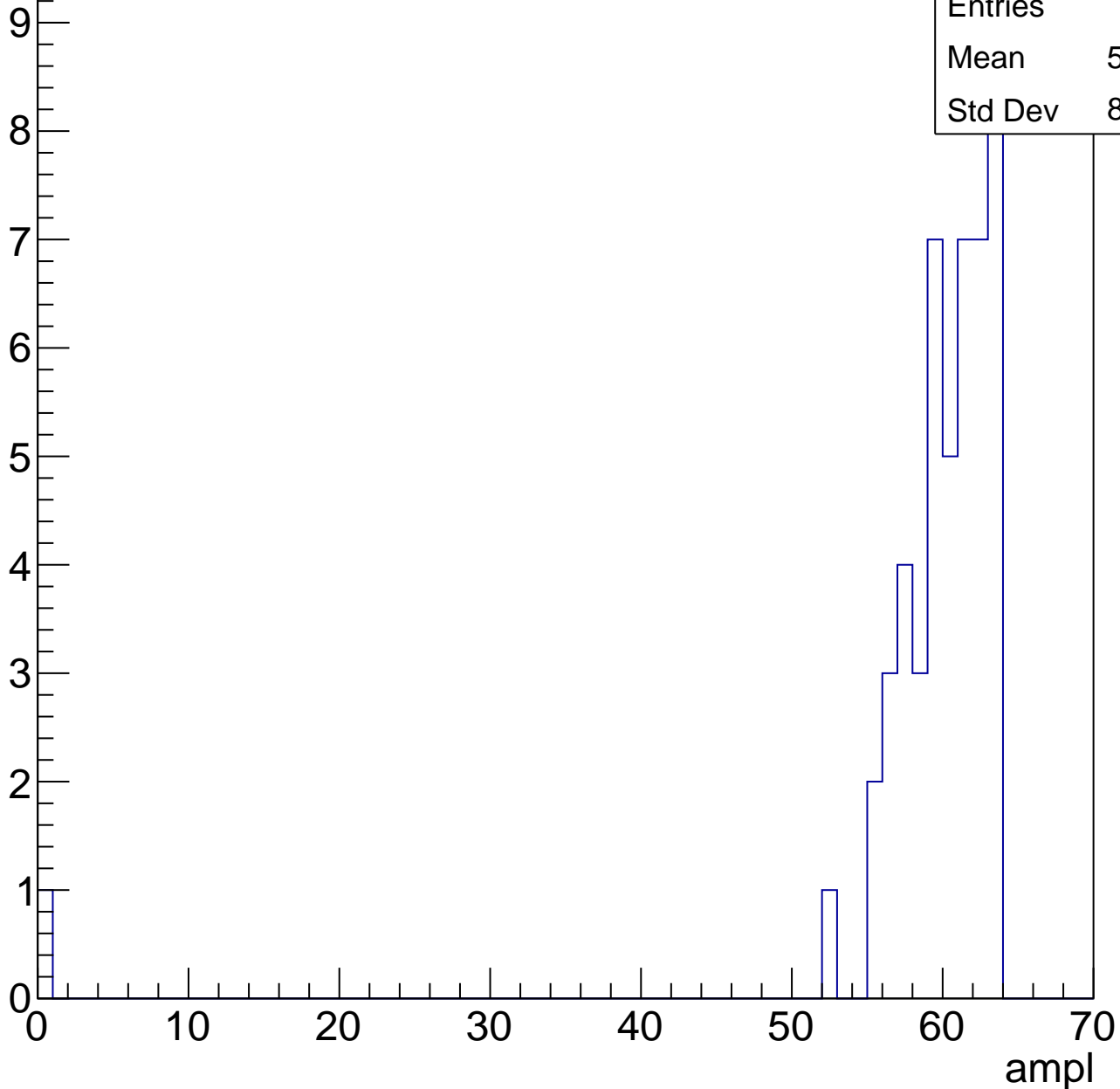


B1L103S, U24-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

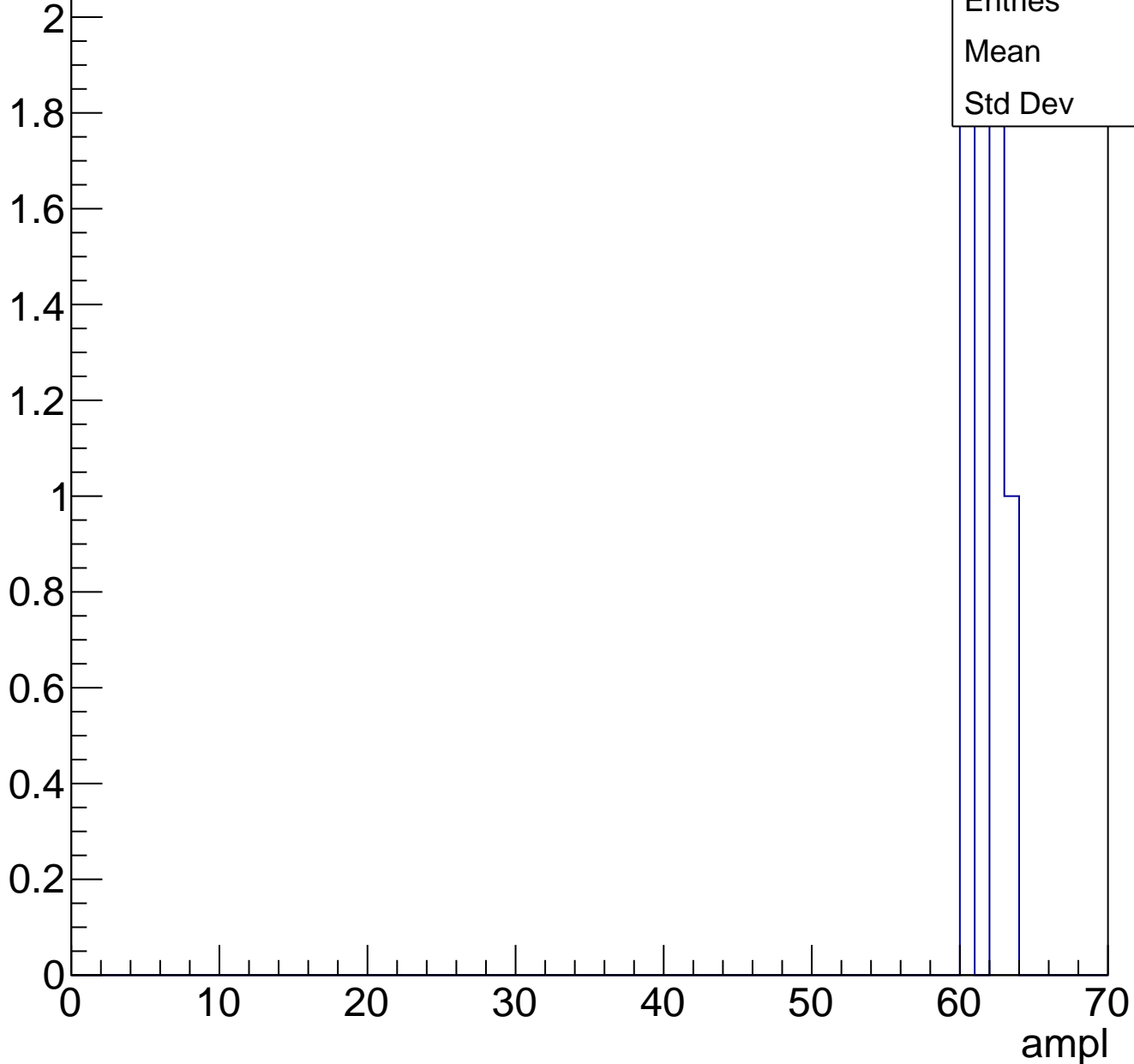
Entries	49
Mean	58.63
Std Dev	8.854



B1L103S, U24-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch11, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	23.09
Std Dev	14.11

Gaus mean : 31.5370

Gaus Width: 4.1226

Entry

25

20

15

10

5

0

0

10

20

30

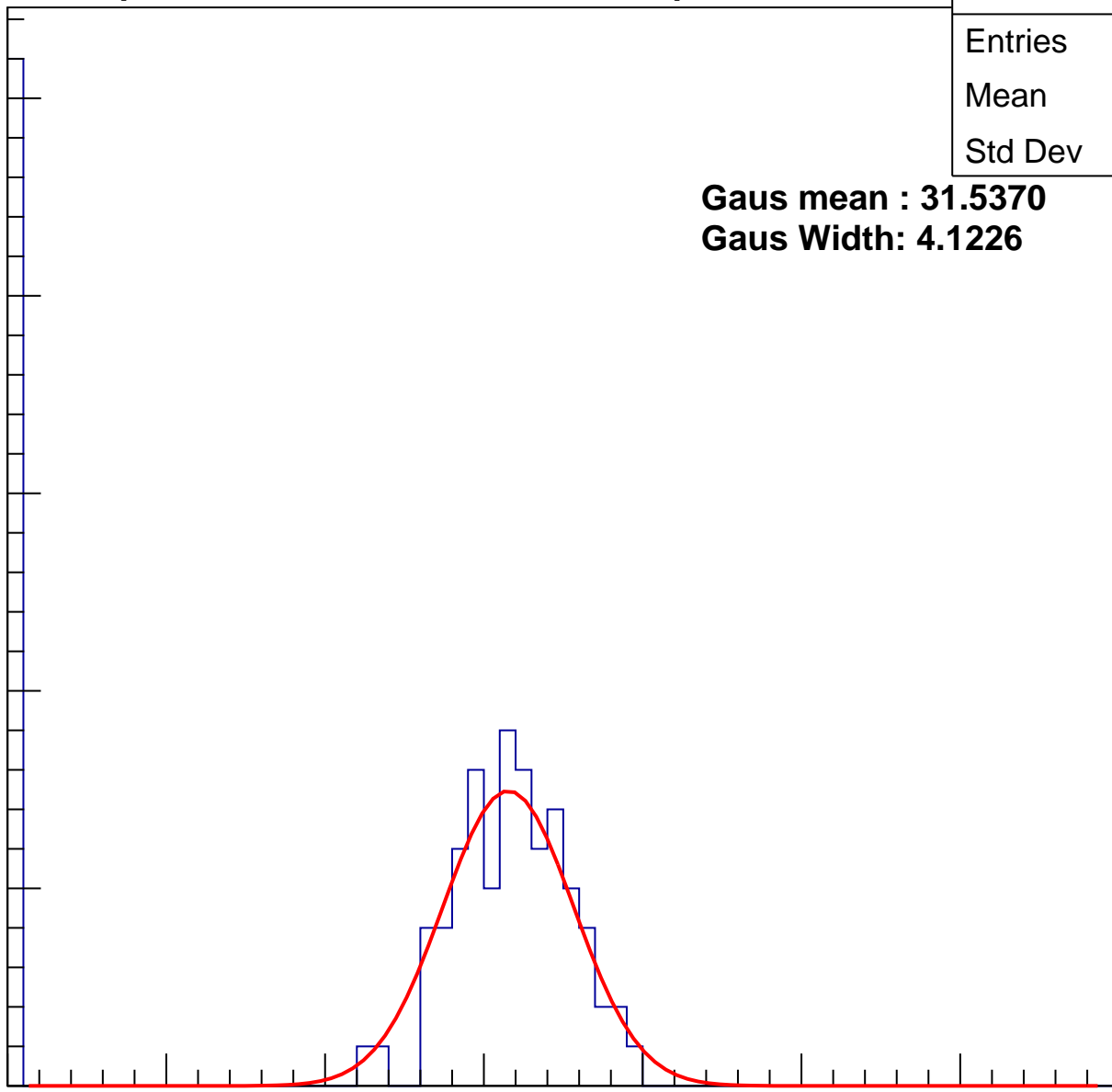
40

50

60

70

ampl



B1L103S, U24-ch11, adc1

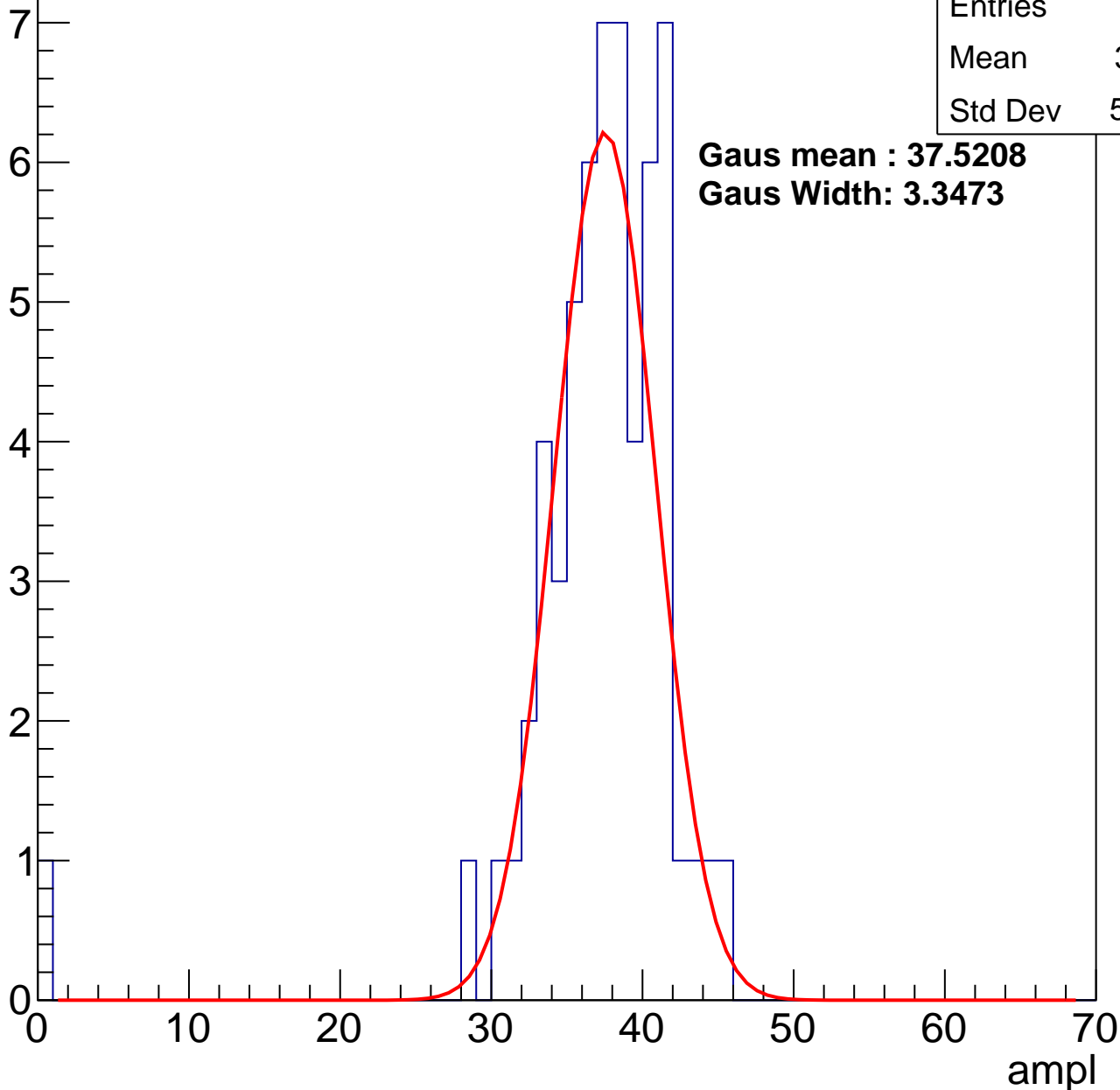
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	36.61
Std Dev	5.903

Gaus mean : 37.5208

Gaus Width: 3.3473



B1L103S, U24-ch11, adc2

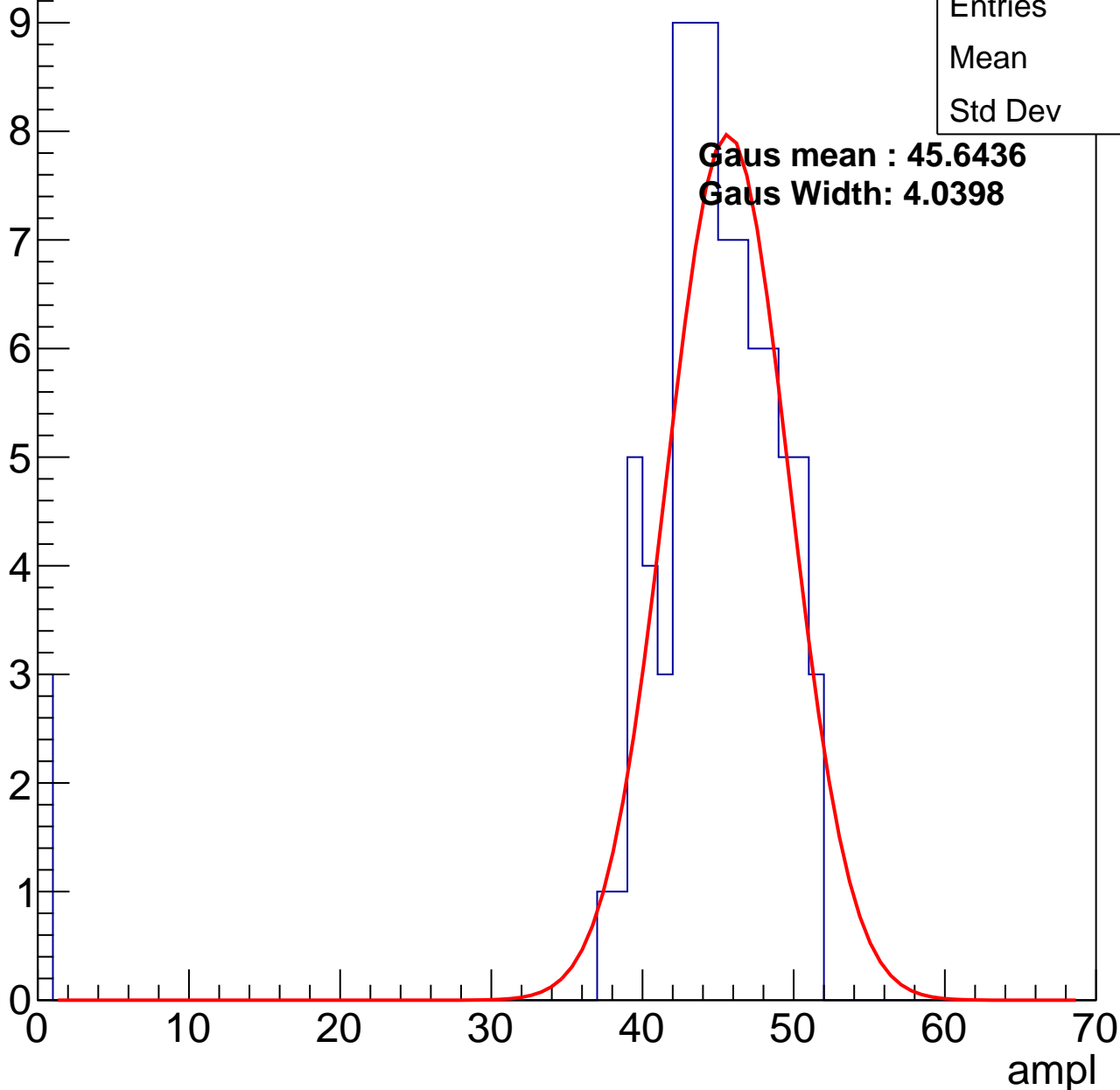
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	43
Std Dev	8.99

Gaus mean : 45.6436

Gaus Width: 4.0398

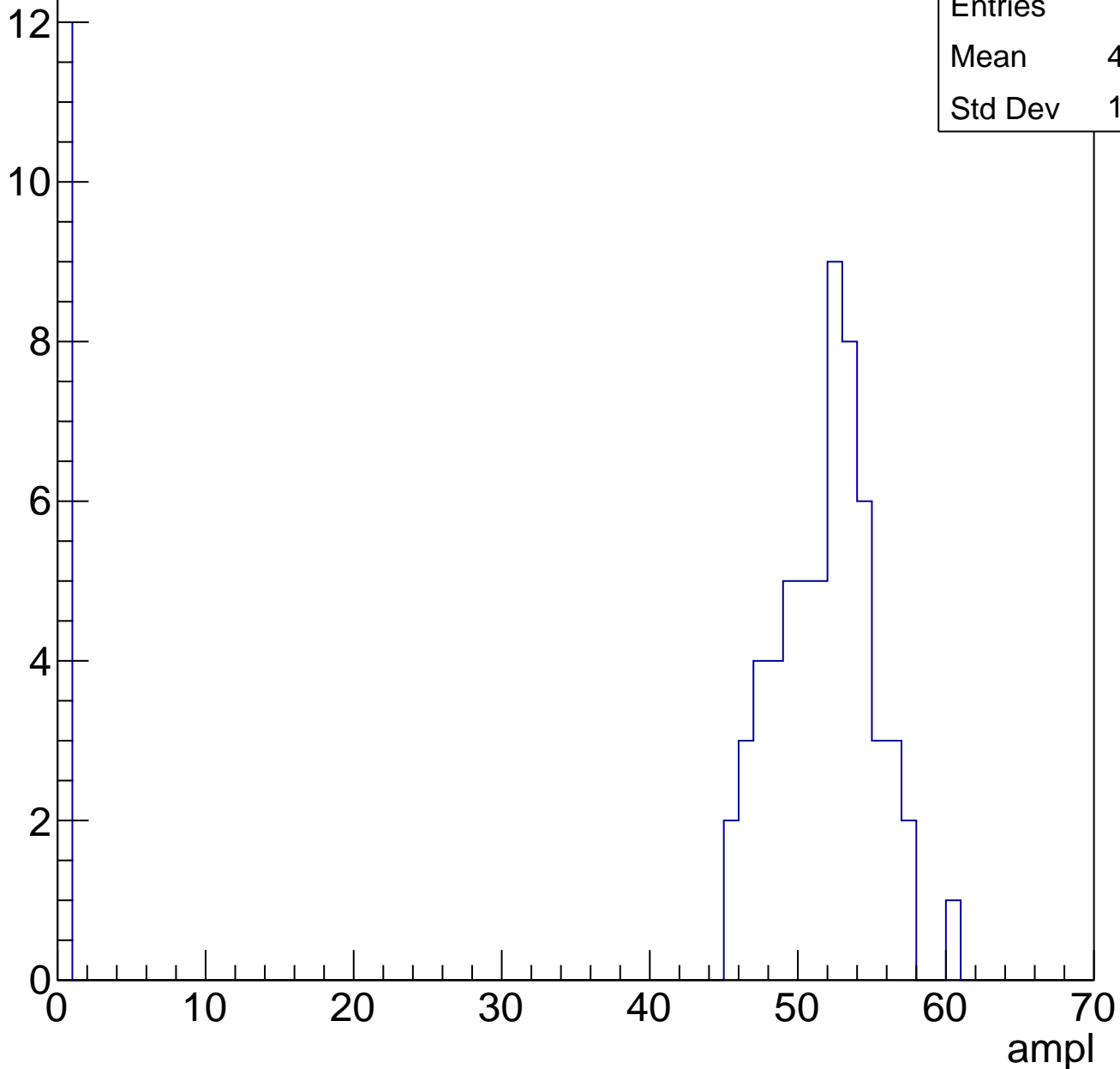


B1L103S, U24-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	42.79
Std Dev	19.37

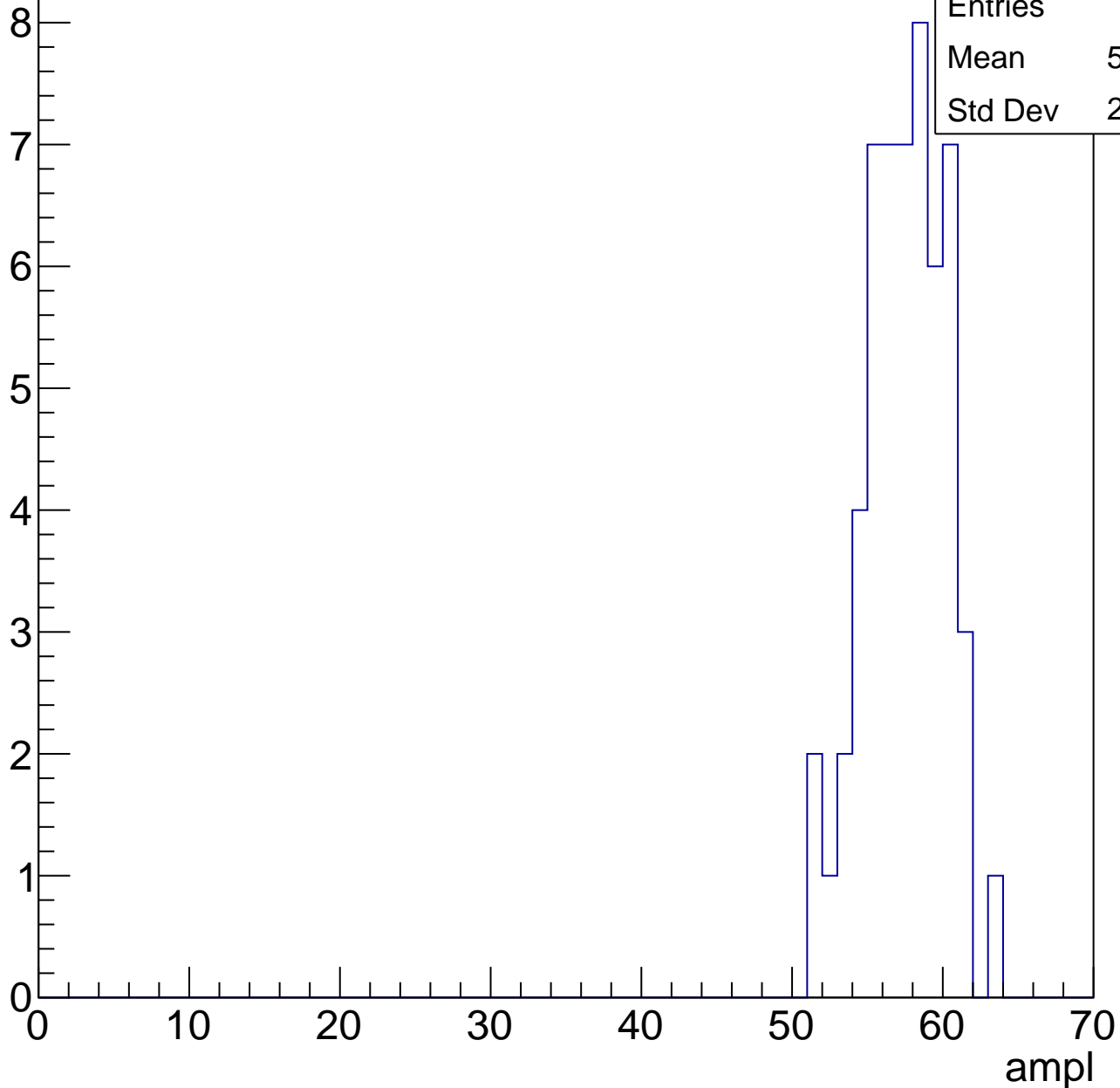
Entry



B1L103S, U24-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

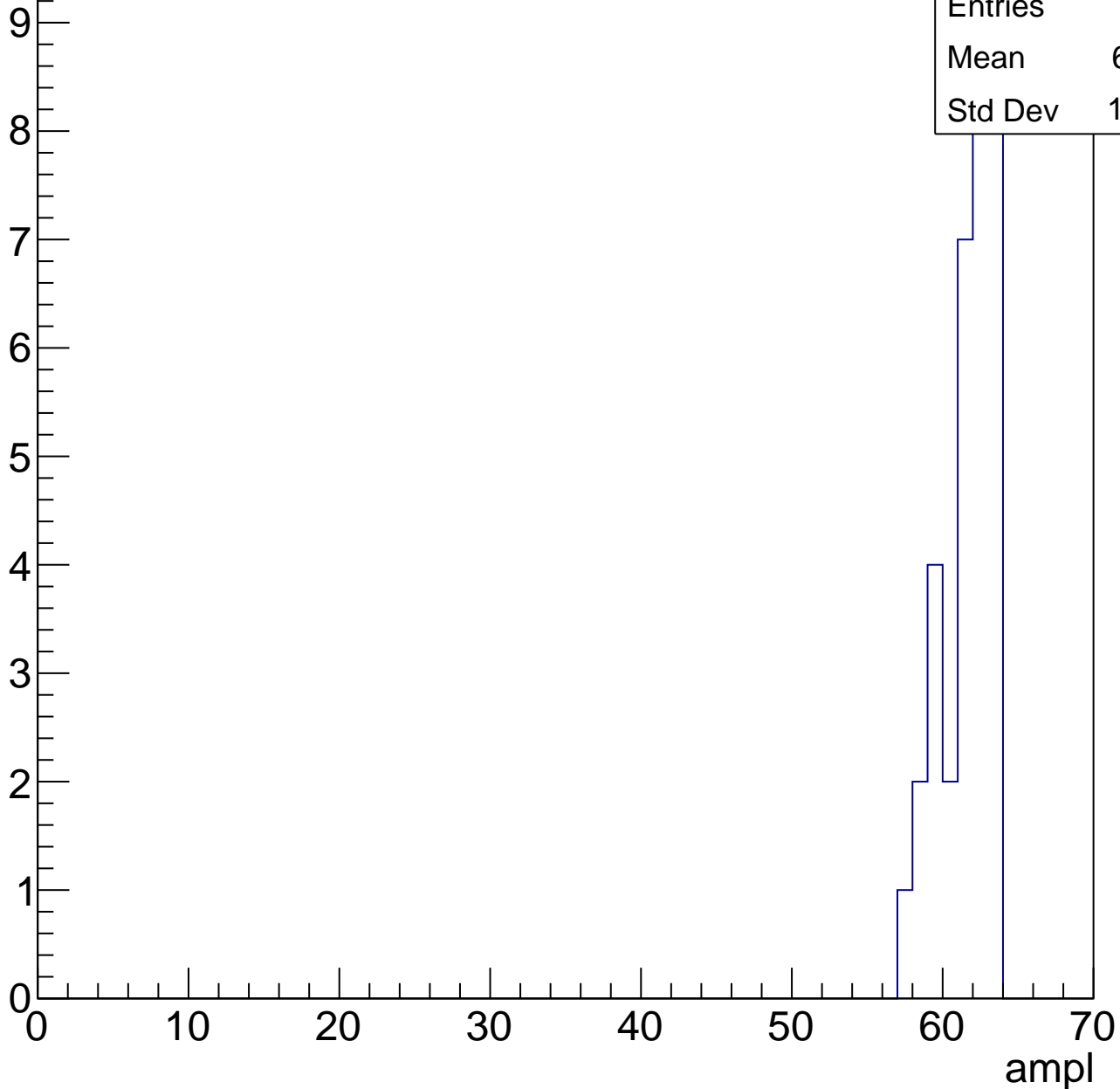


B1L103S, U24-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

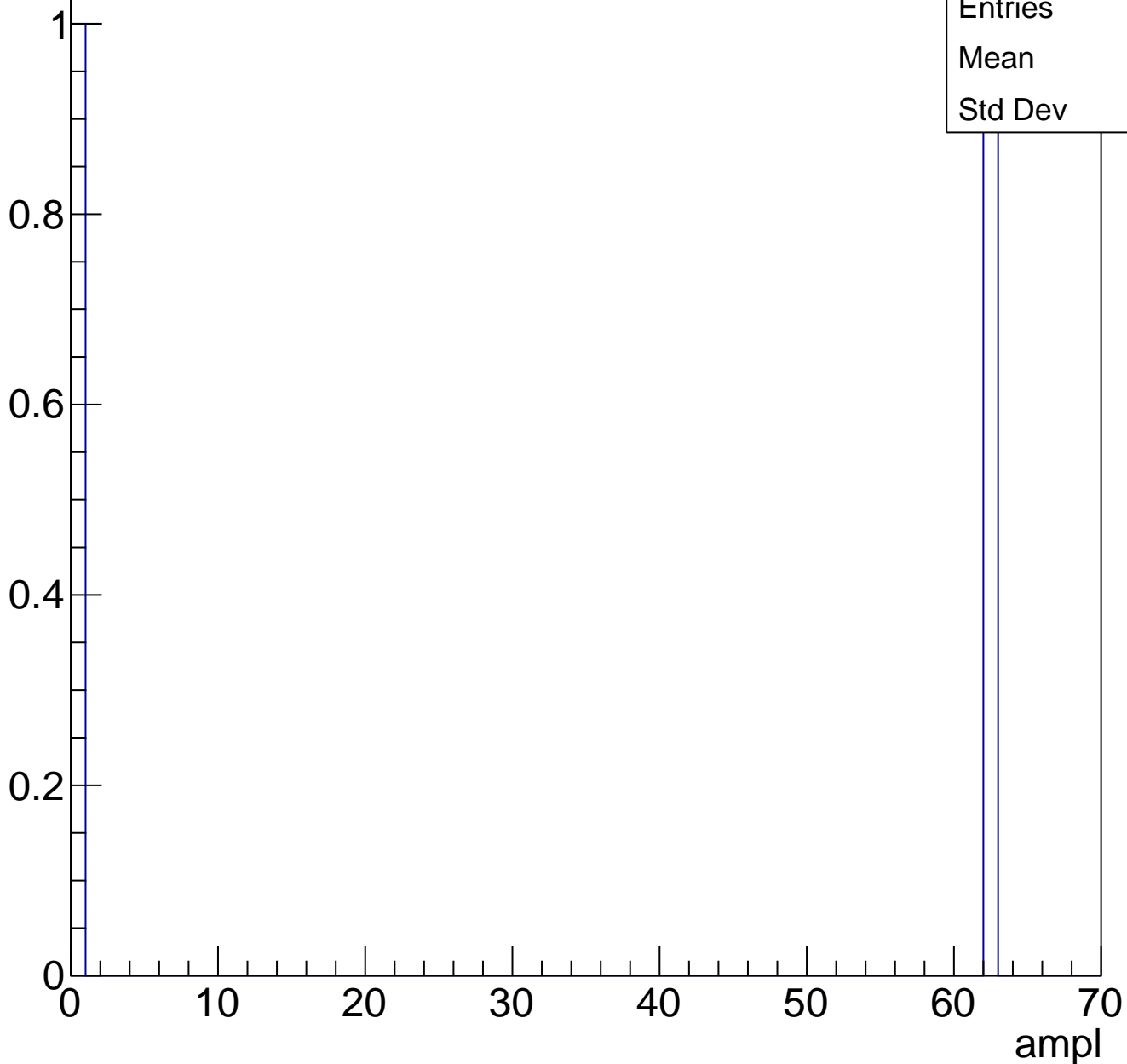
Entries	34
Mean	61.21
Std Dev	1.676



B1L103S, U24-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch12, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	20.26
Std Dev	12.21

Gaus mean : 27.6982

Gaus Width: 4.1968

Entry

25

20

15

10

5

0

0

10

20

30

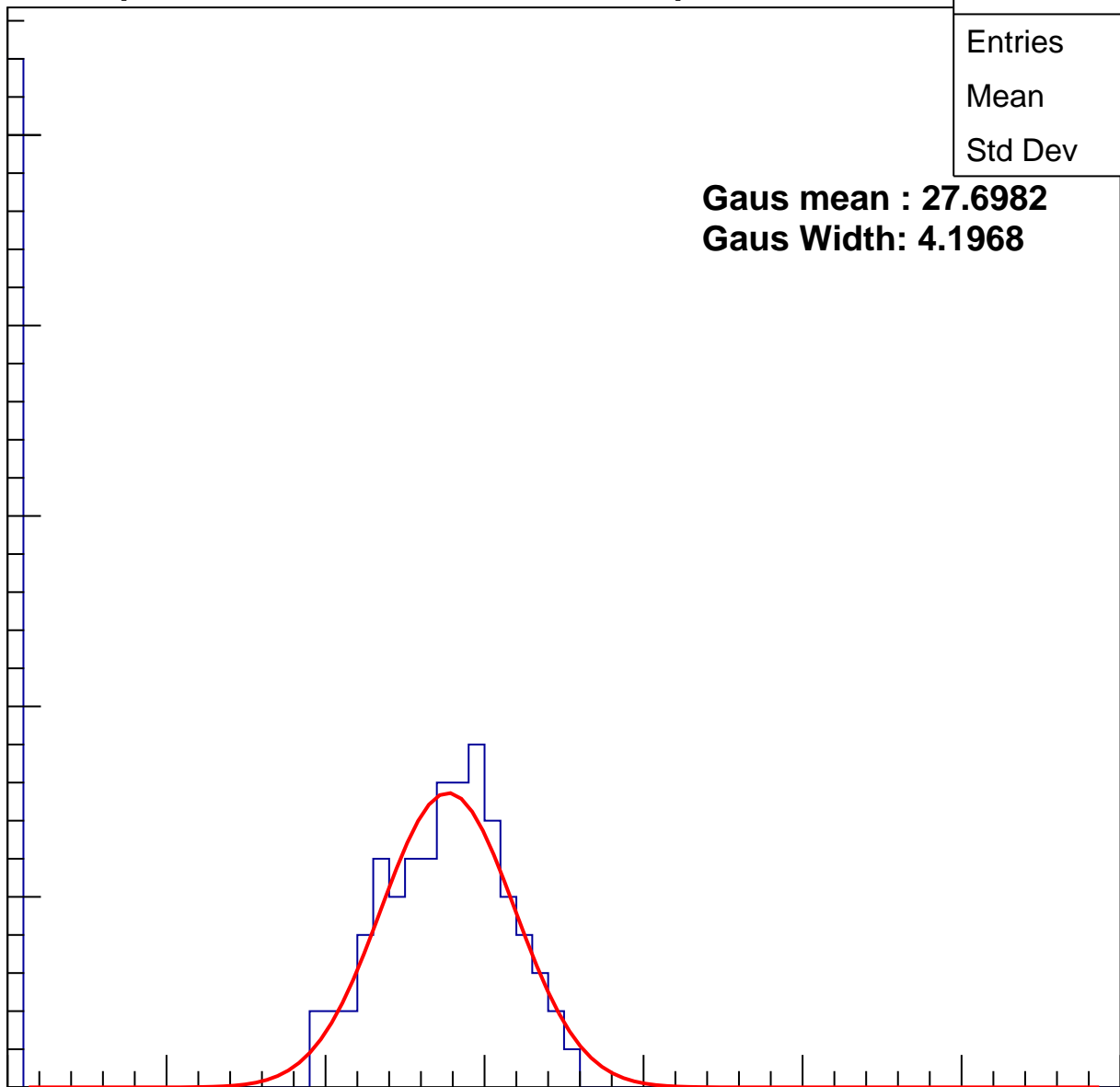
40

50

60

70

ampl



B1L103S, U24-ch12, adc1

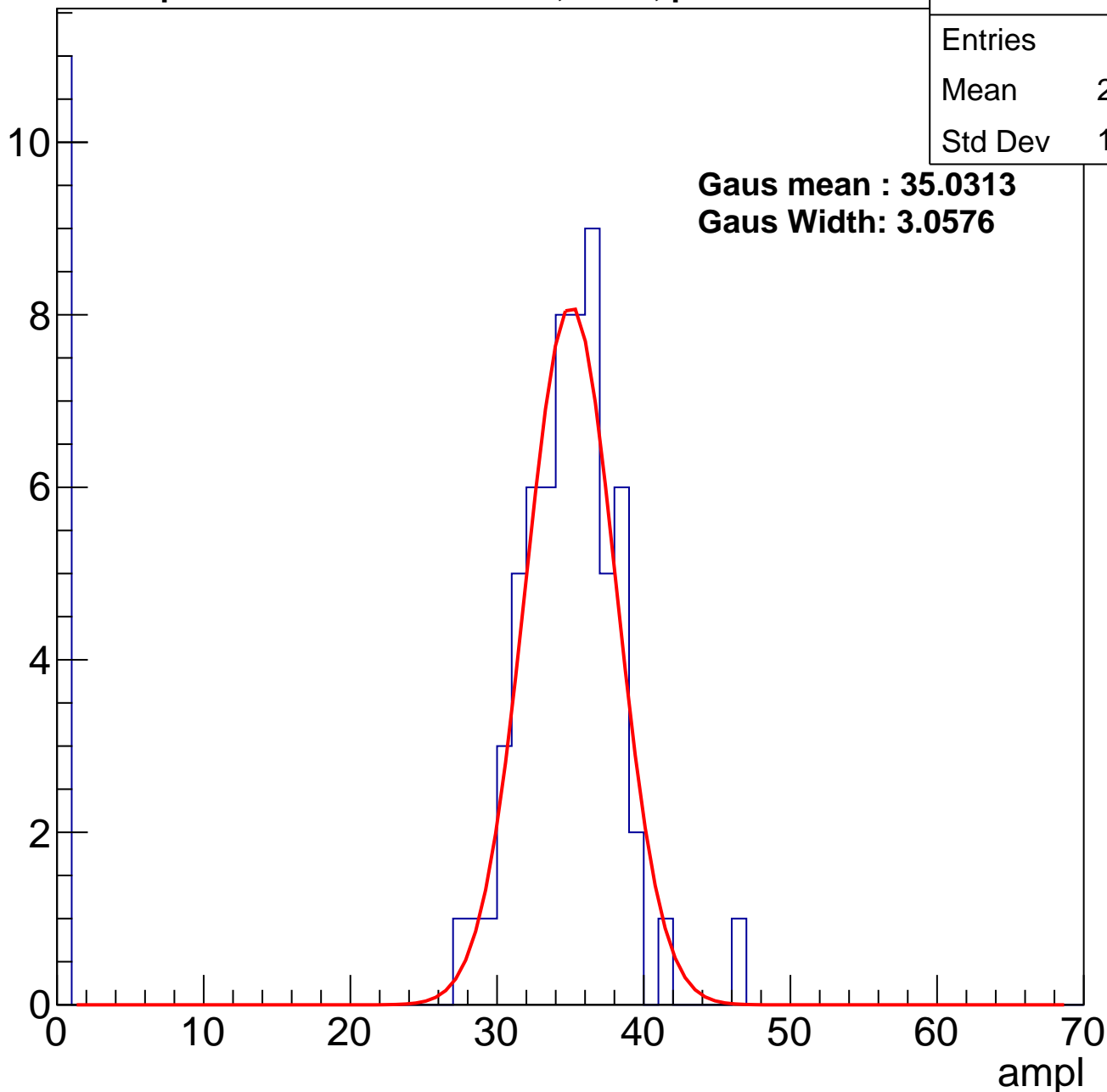
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	29.36
Std Dev	12.62

Gaus mean : 35.0313

Gaus Width: 3.0576

Entry



B1L103S, U24-ch12, adc2

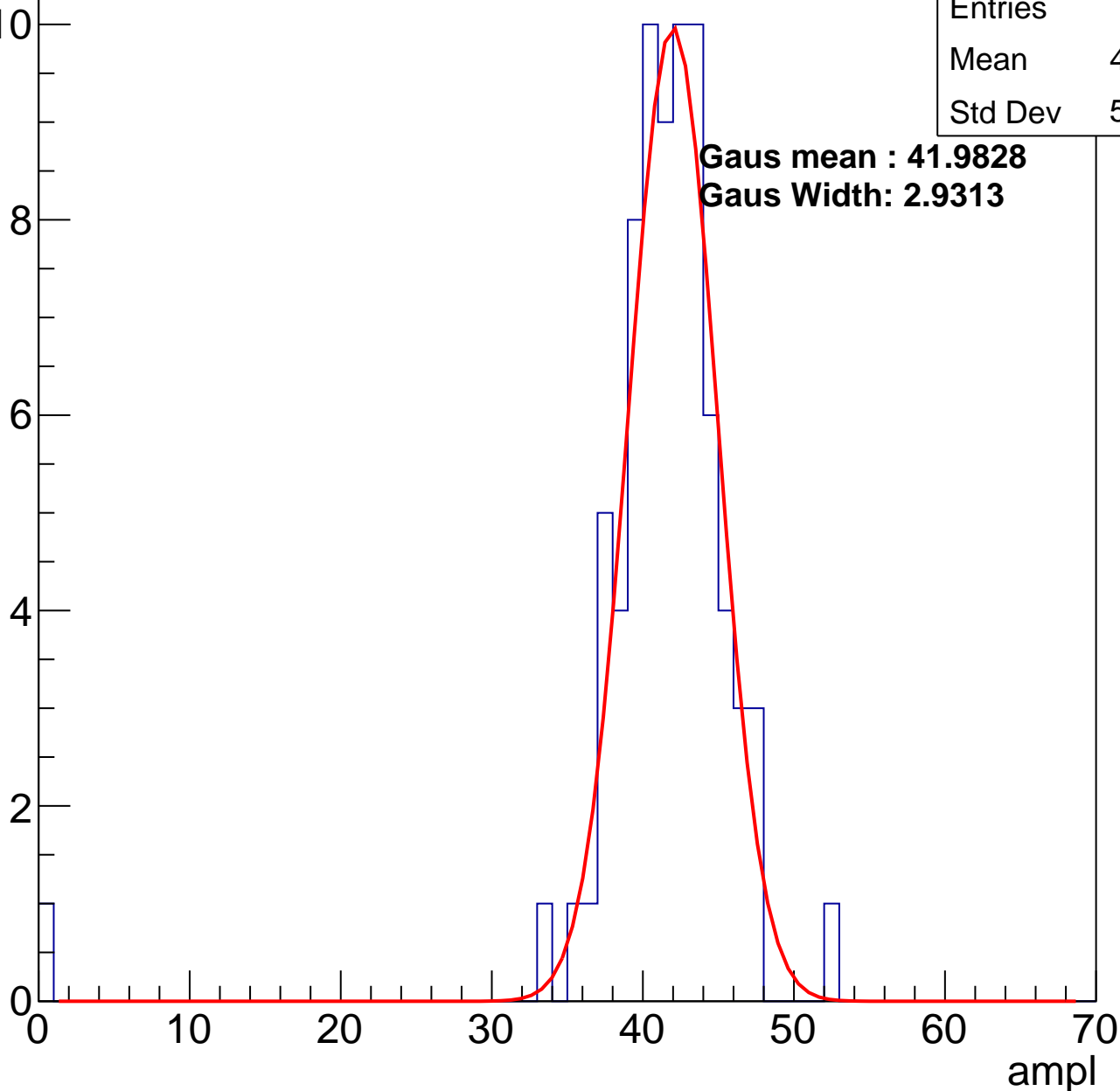
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	40.87
Std Dev	5.623

Gaus mean : 41.9828

Gaus Width: 2.9313

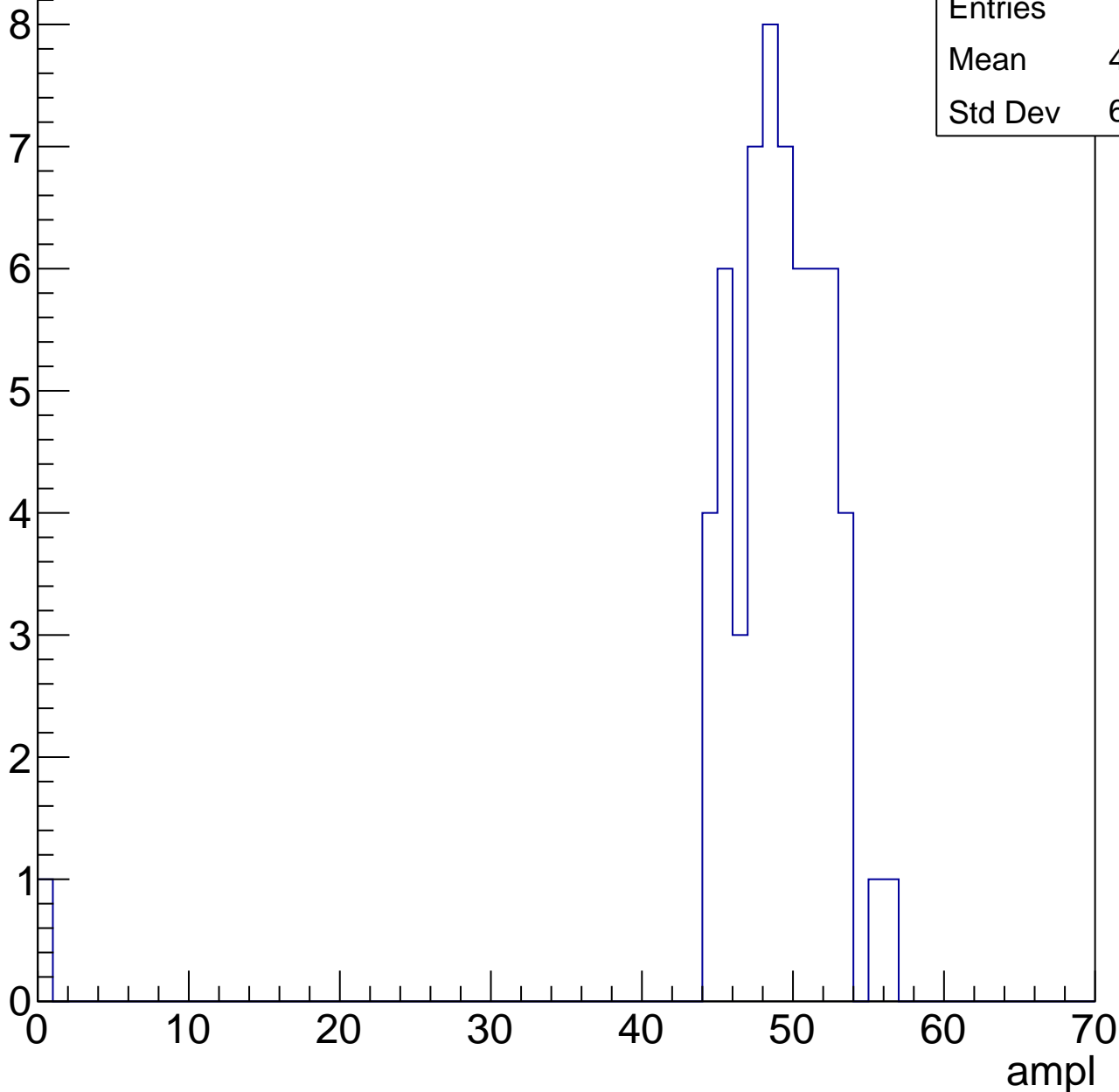


B1L103S, U24-ch12, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	48.02
Std Dev	6.874

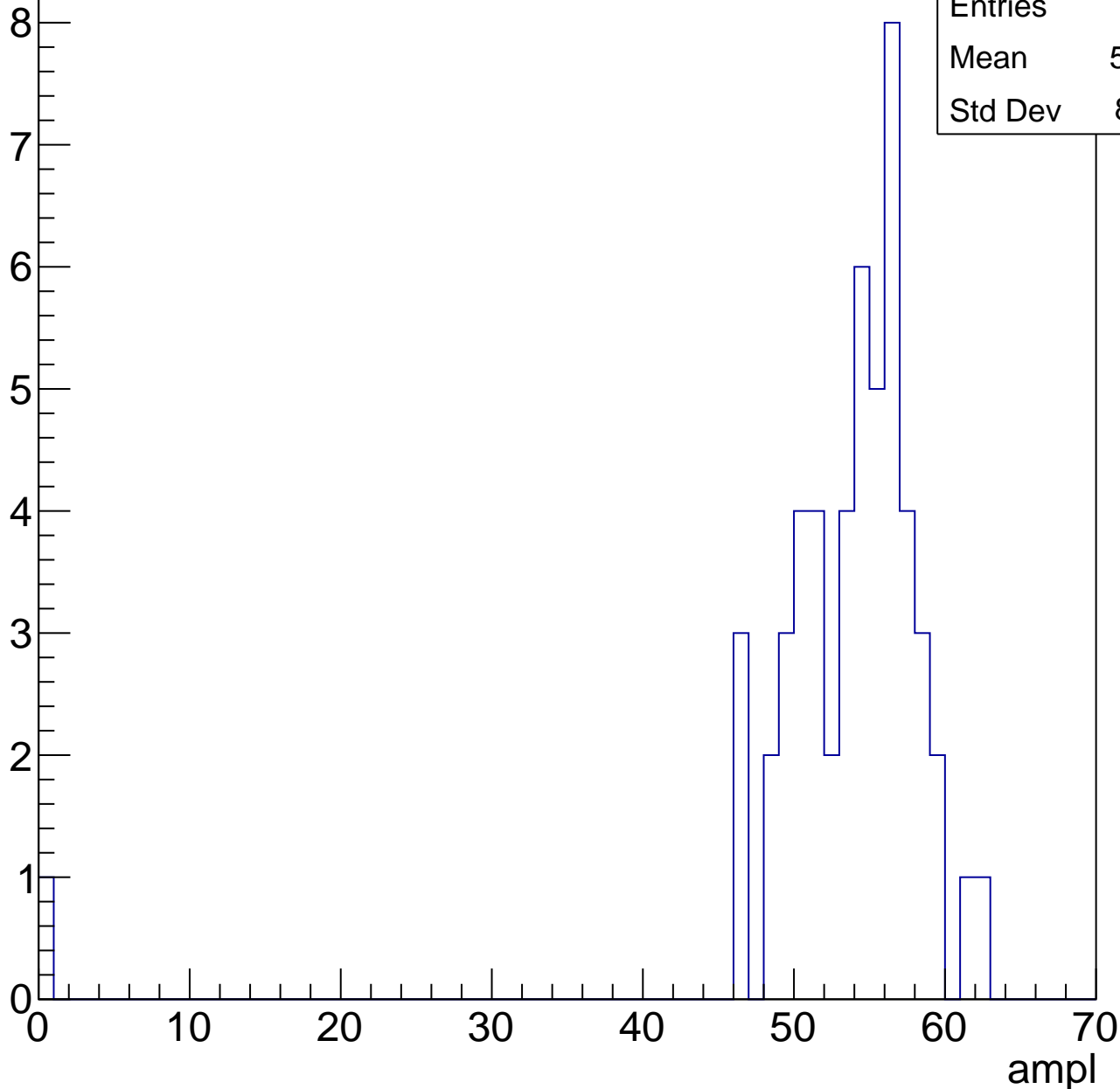


B1L103S, U24-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	52.66
Std Dev	8.191



B1L103S, U24-ch12, adc5

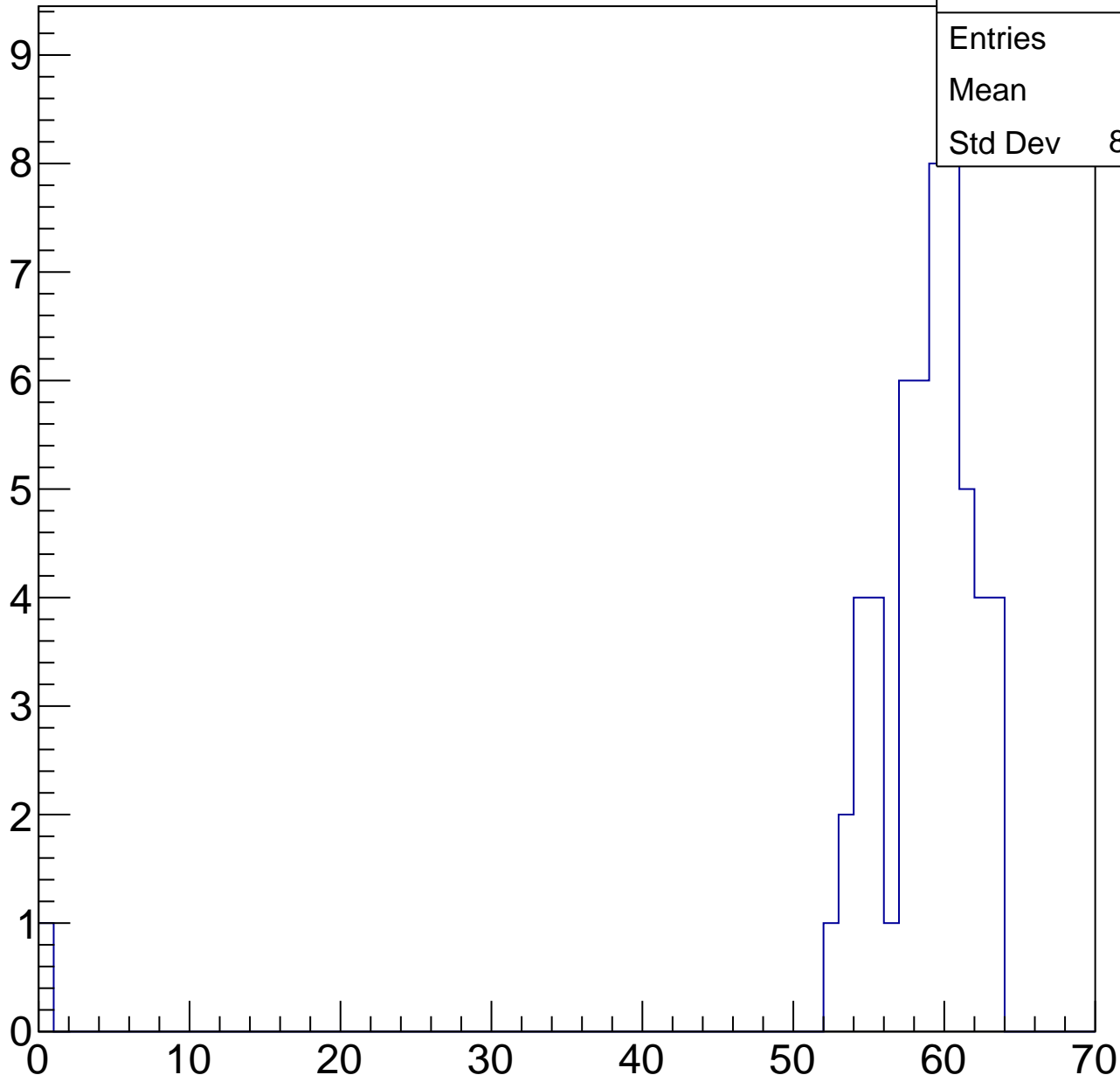
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	57.4
Std Dev	8.305

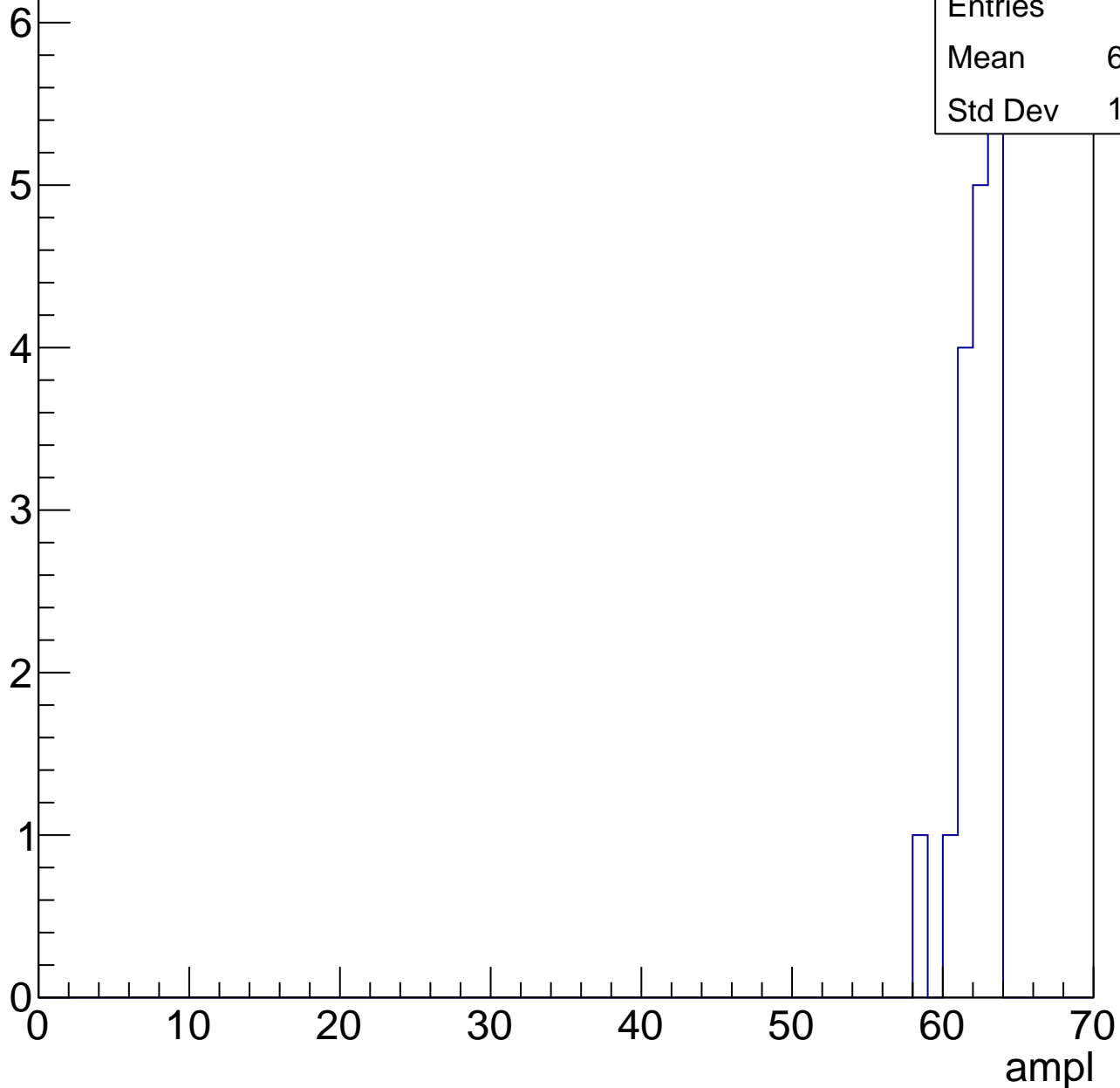
ampl



B1L103S, U24-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch13, adc0

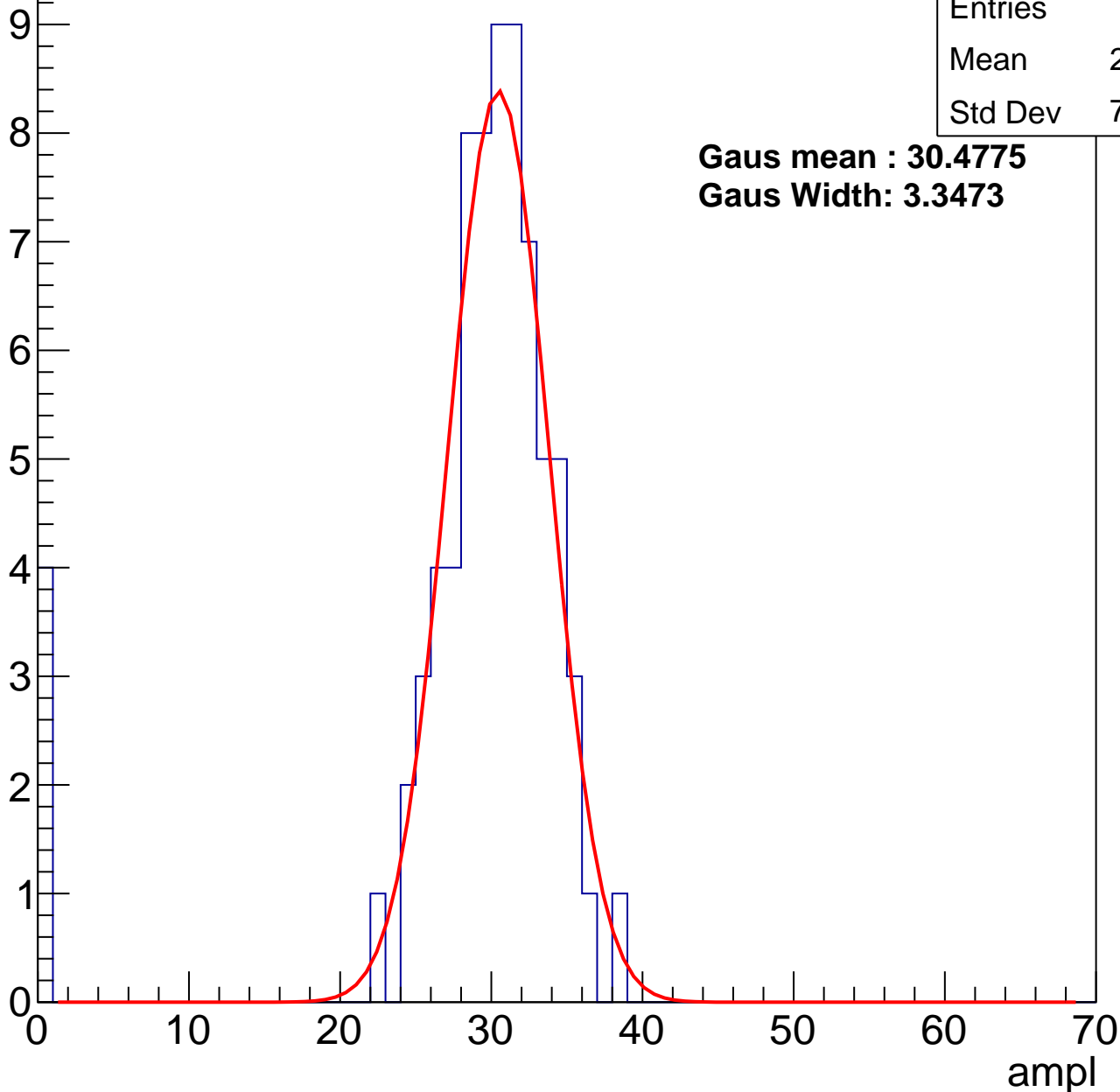
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	28.38
Std Dev	7.437

Gaus mean : 30.4775

Gaus Width: 3.3473



B1L103S, U24-ch13, adc1

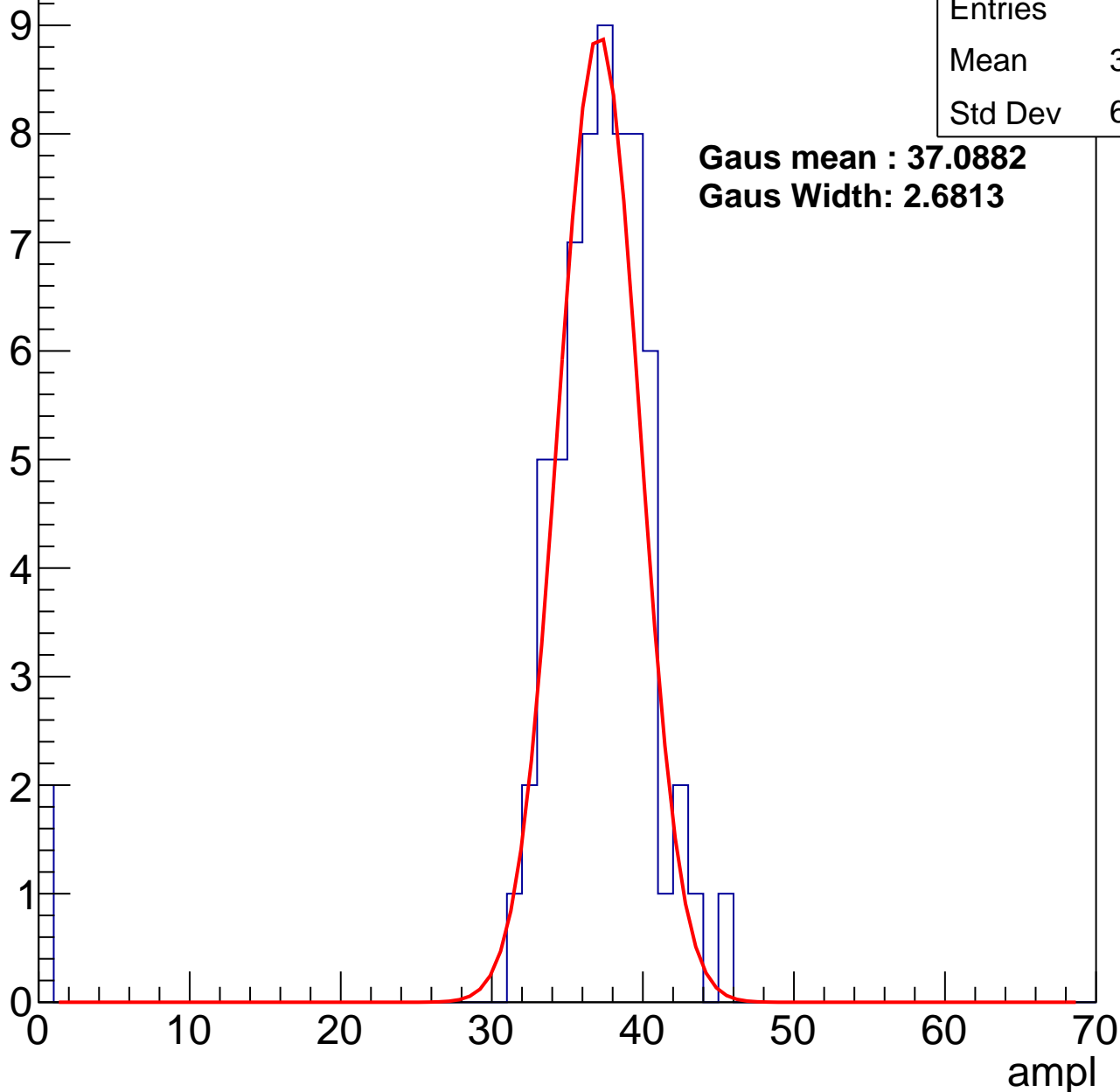
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	35.83
Std Dev	6.914

Gaus mean : 37.0882

Gaus Width: 2.6813



B1L103S, U24-ch13, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	38.63
Std Dev	14.24

Gaus mean : 44.4019

Gaus Width: 3.3544

Entry

10

8

6

4

2

0

0

10

20

30

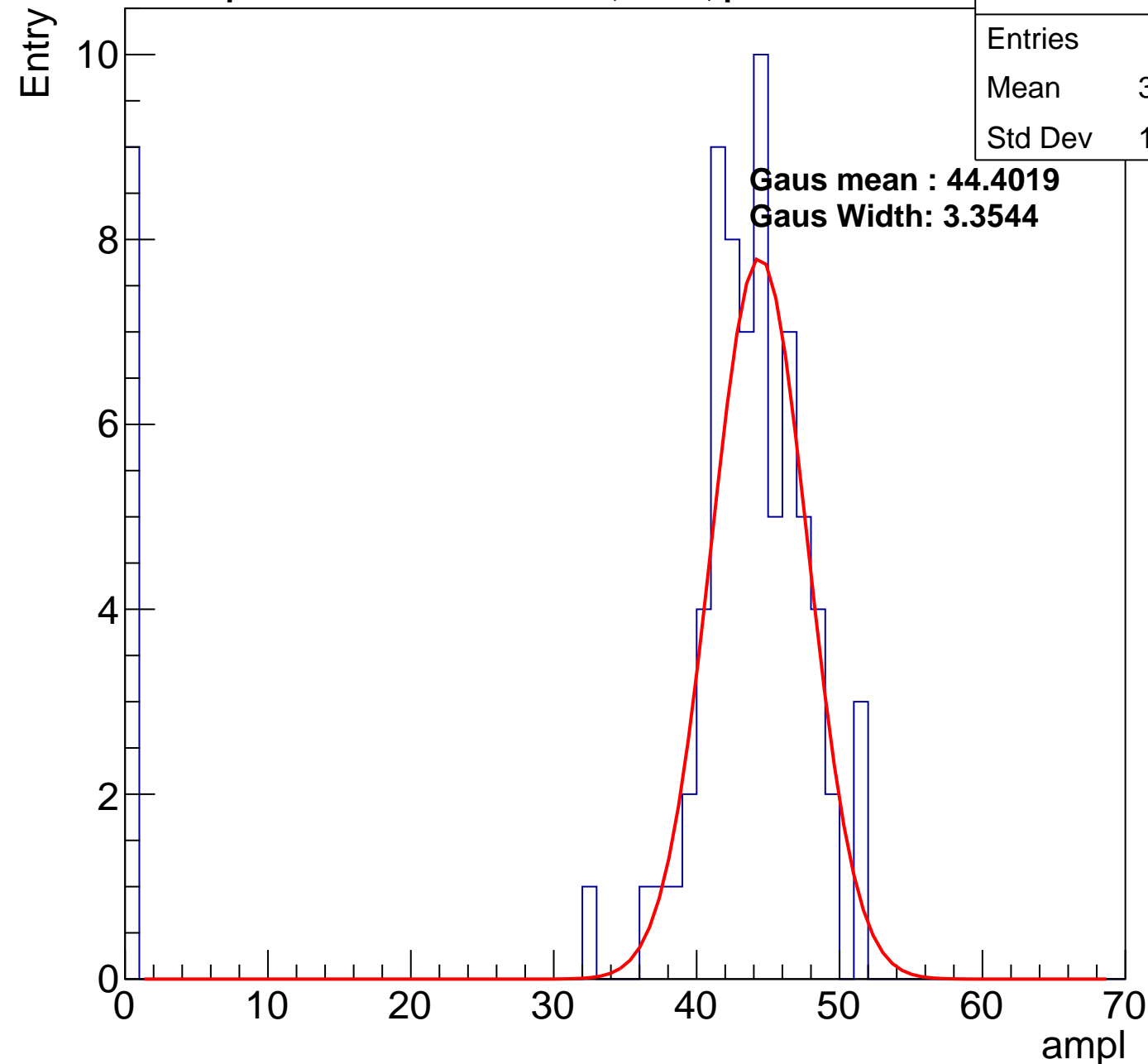
40

50

60

70

ampl

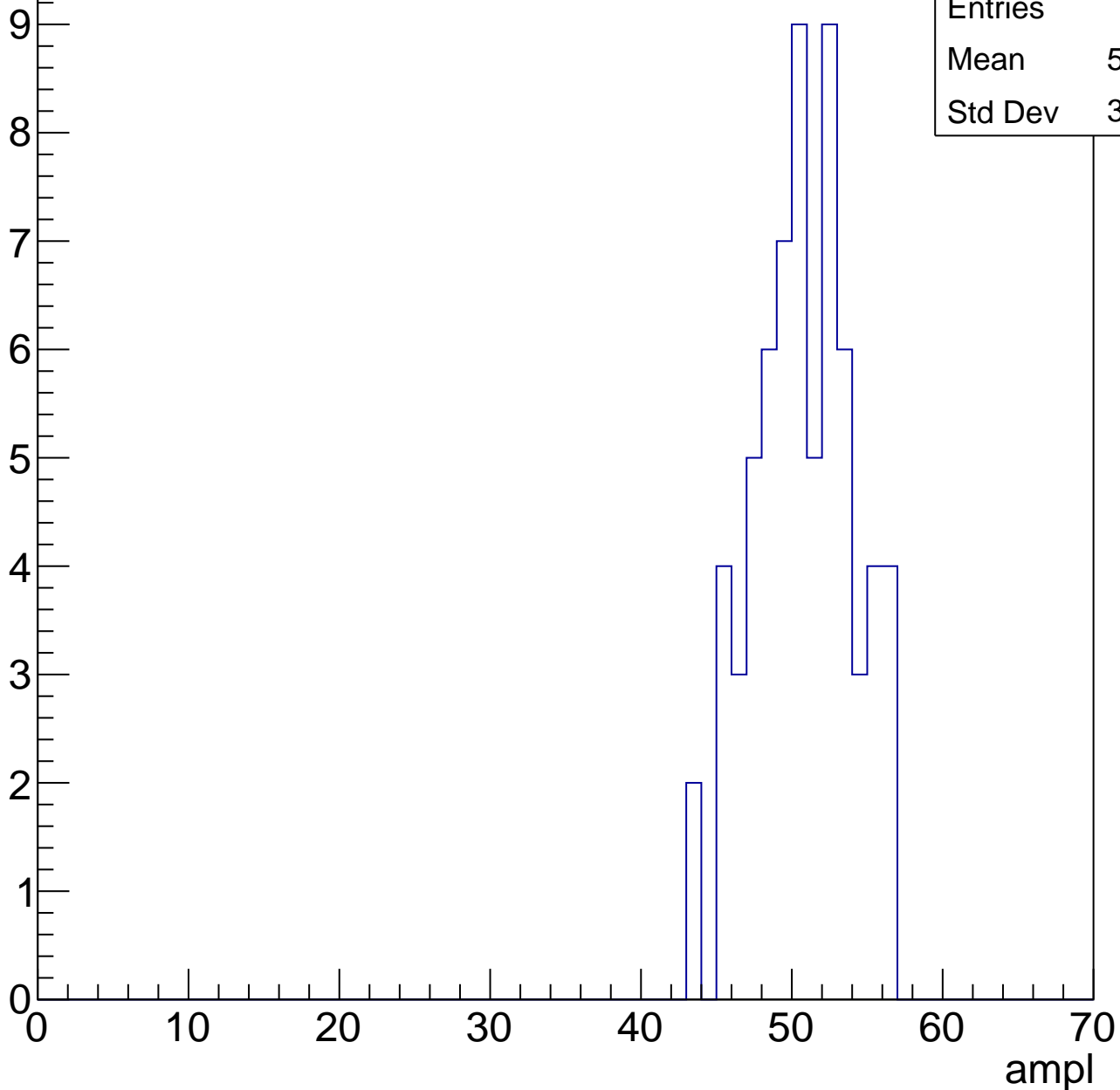


B1L103S, U24-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	50.25
Std Dev	3.243

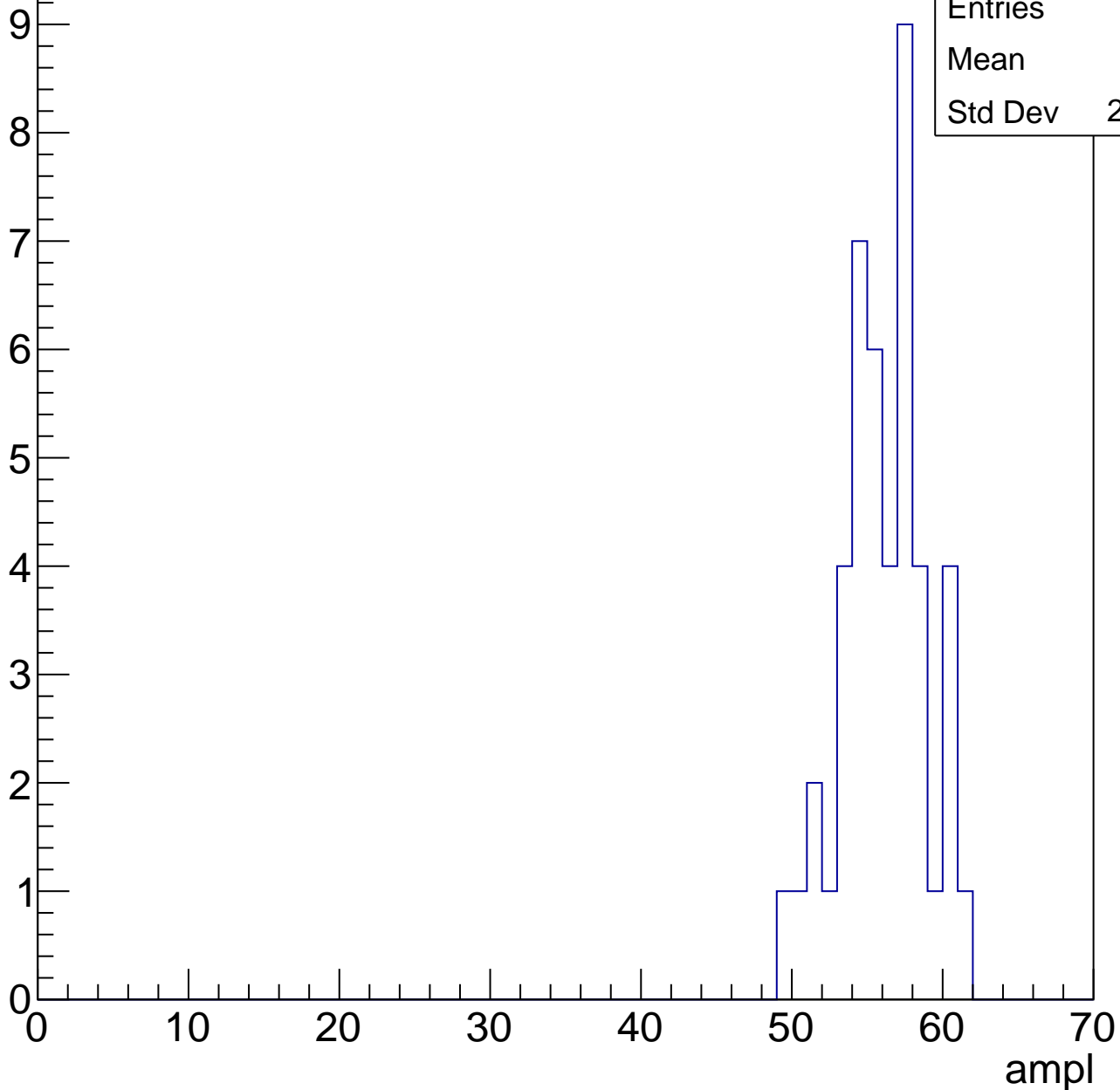


B1L103S, U24-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

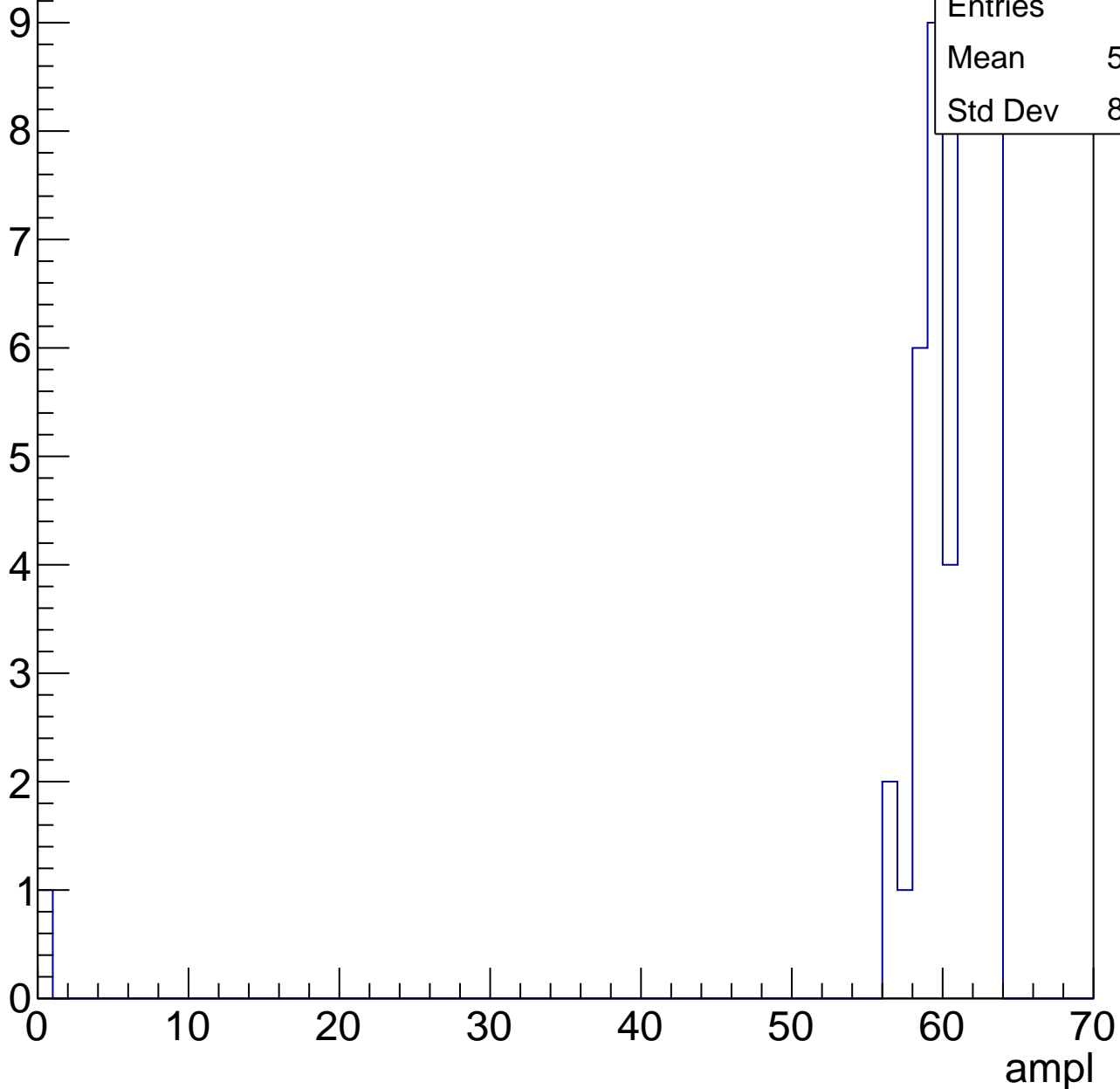
Entries	45
Mean	55.6
Std Dev	2.736



B1L103S, U24-ch13, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.299

ampl

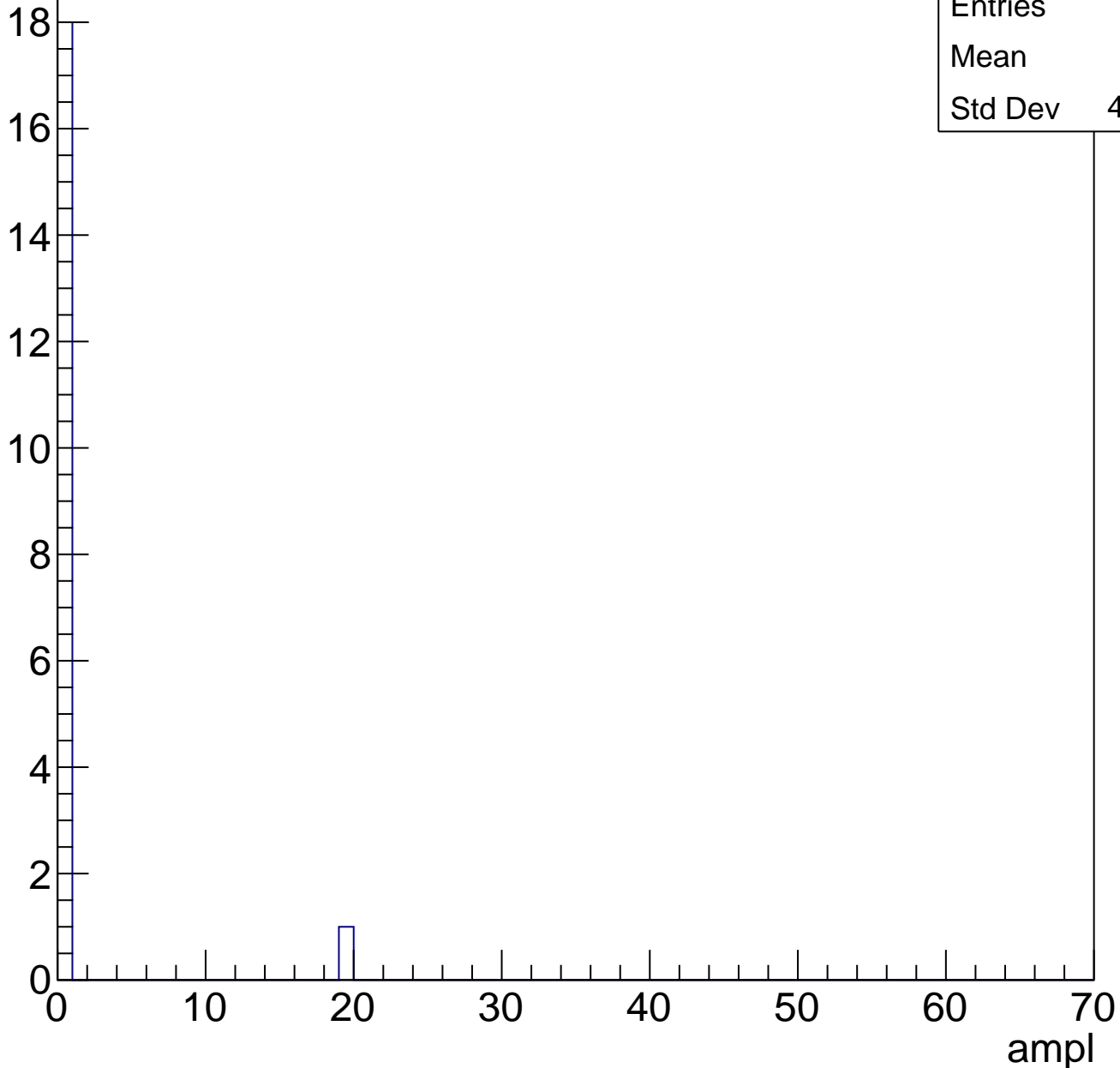
0 10 20 30 40 50 60 70

B1L103S, U24-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1
Std Dev	4.243

Entry



B1L103S, U24-ch14, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	25.71
Std Dev	8.983

Gaus mean : 28.5767

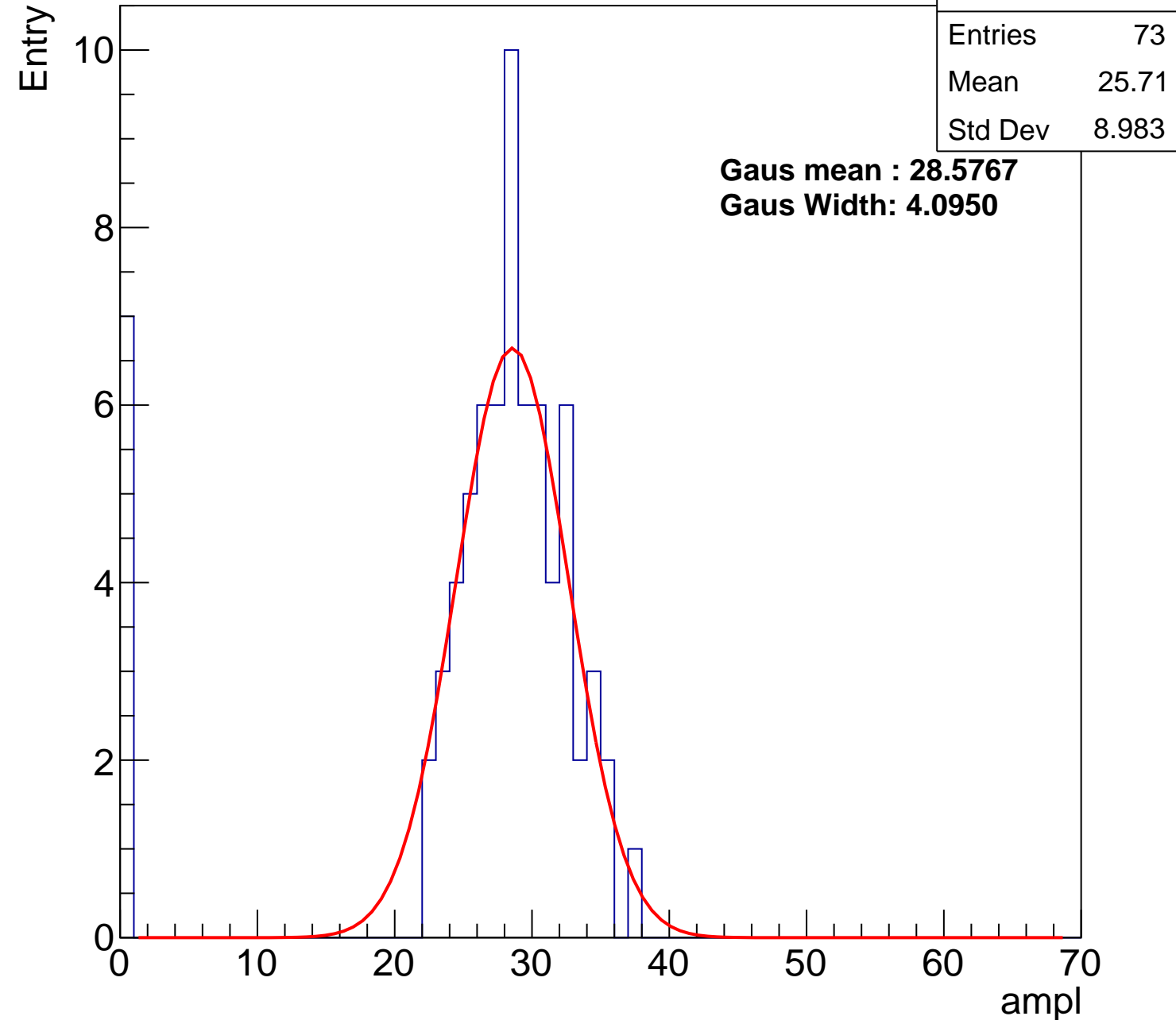
Gaus Width: 4.0950

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch14, adc1

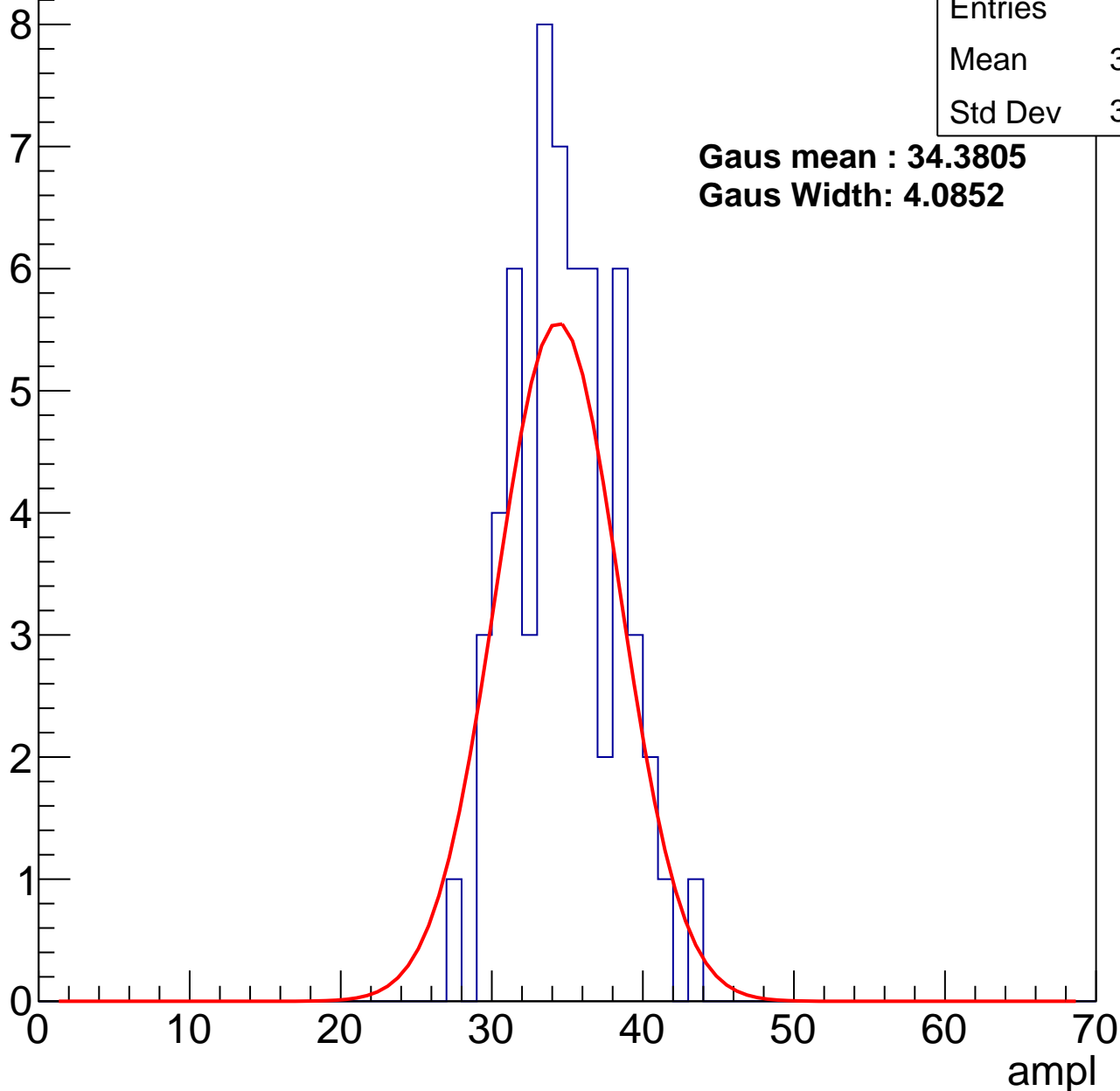
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	34.36
Std Dev	3.384

Gaus mean : 34.3805

Gaus Width: 4.0852



B1L103S, U24-ch14, adc2

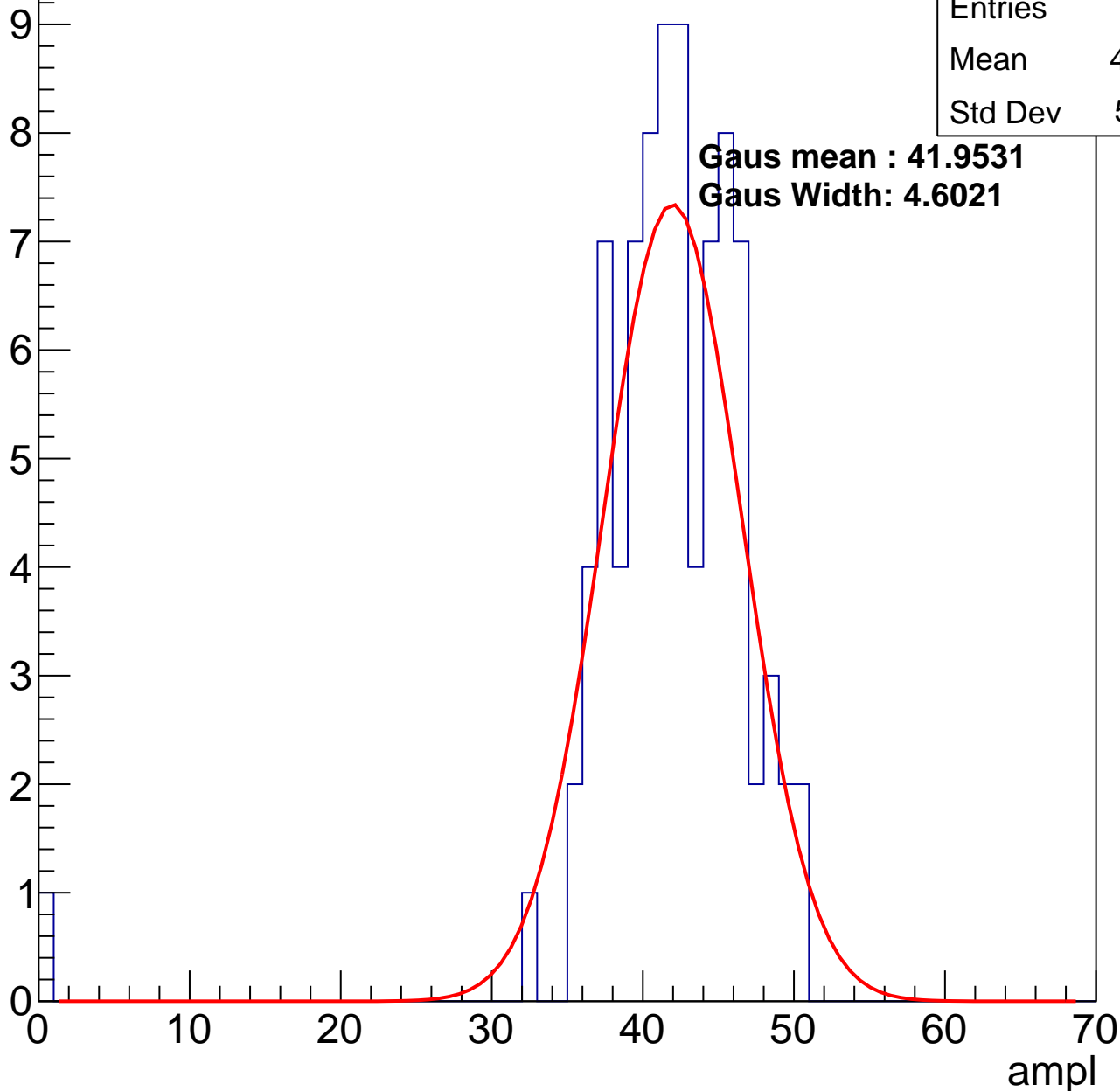
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	41.32
Std Dev	5.881

Gaus mean : 41.9531

Gaus Width: 4.6021

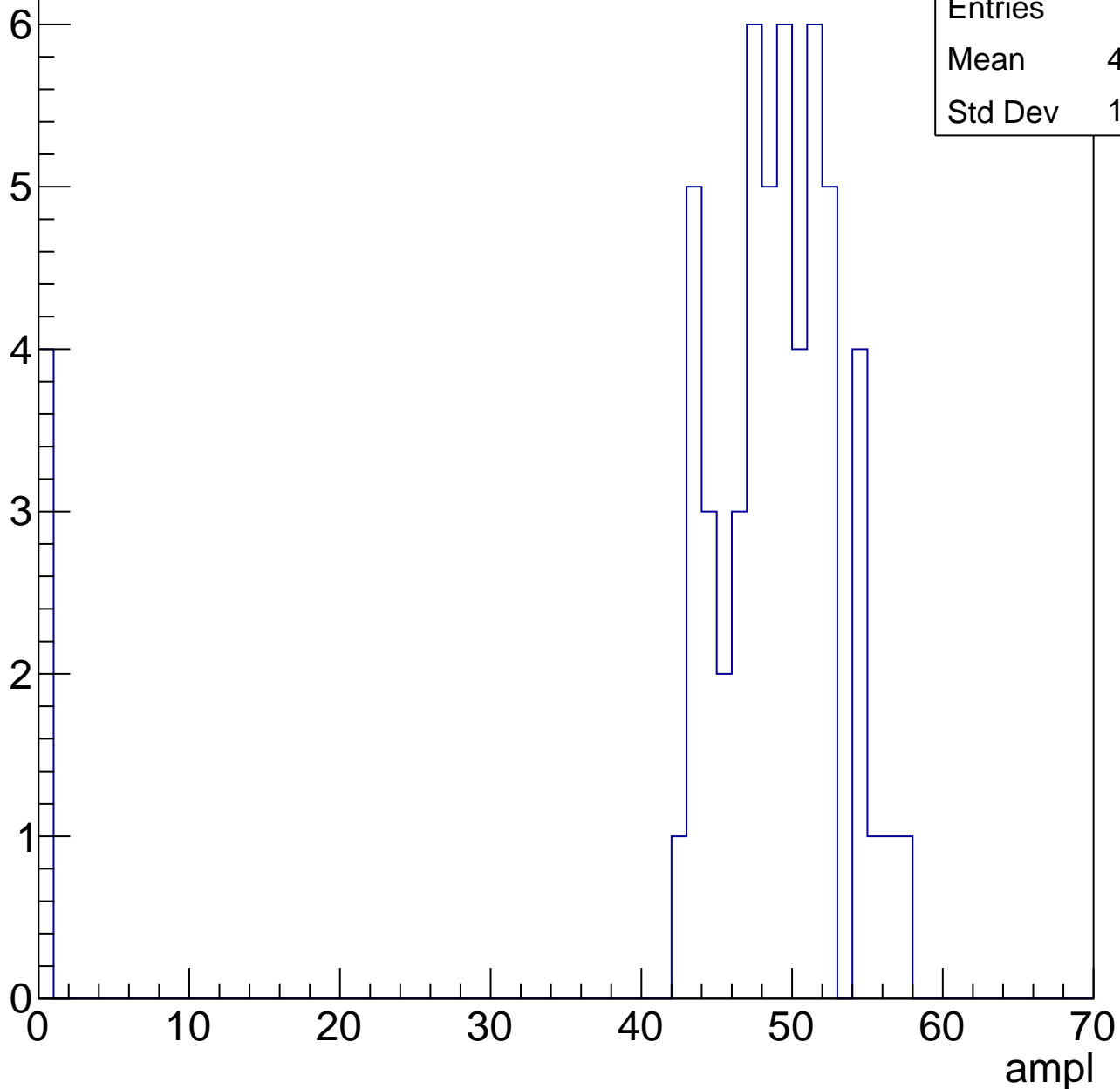


B1L103S, U24-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	45.32
Std Dev	12.94

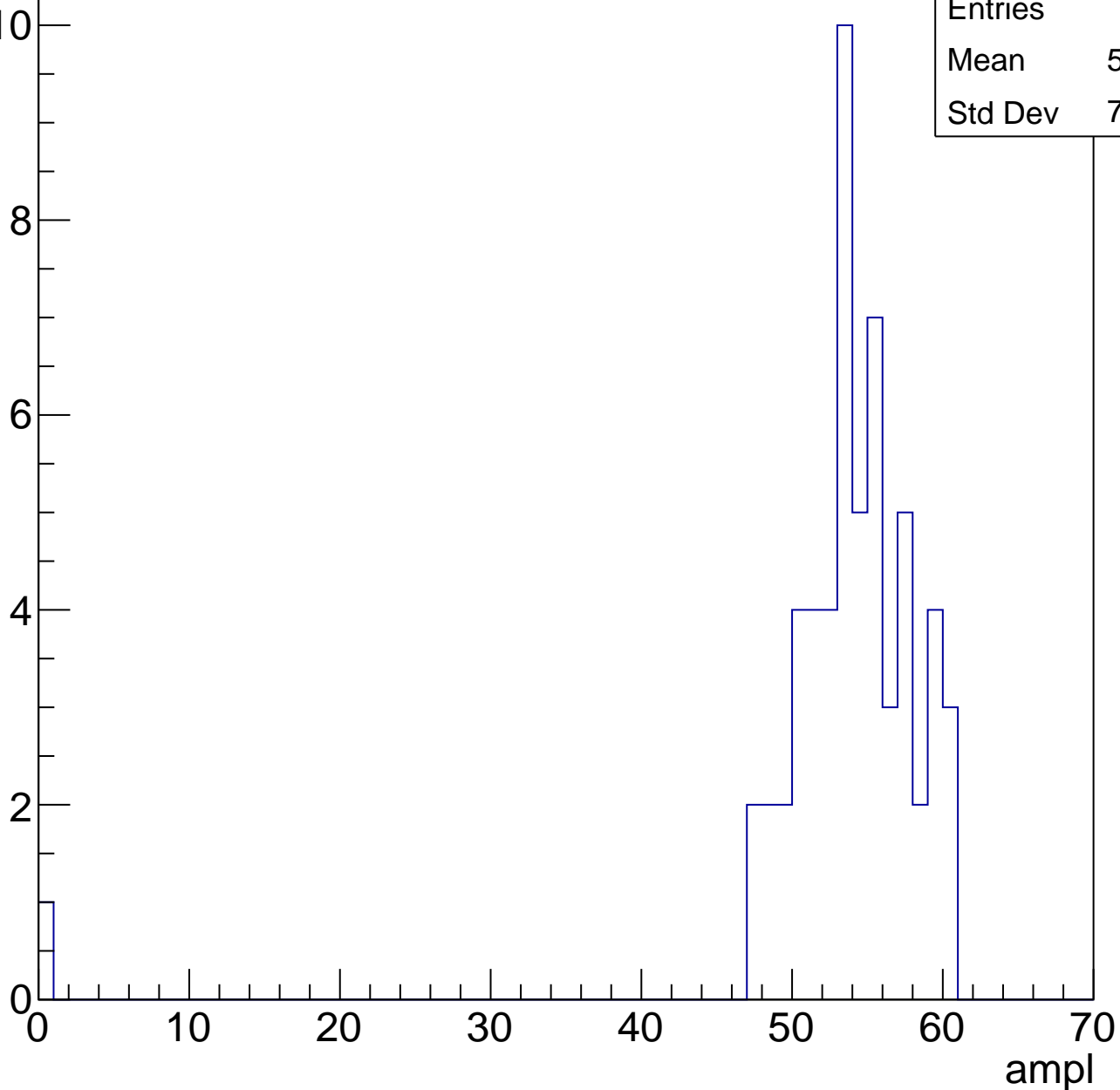


B1L103S, U24-ch14, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	52.93
Std Dev	7.768

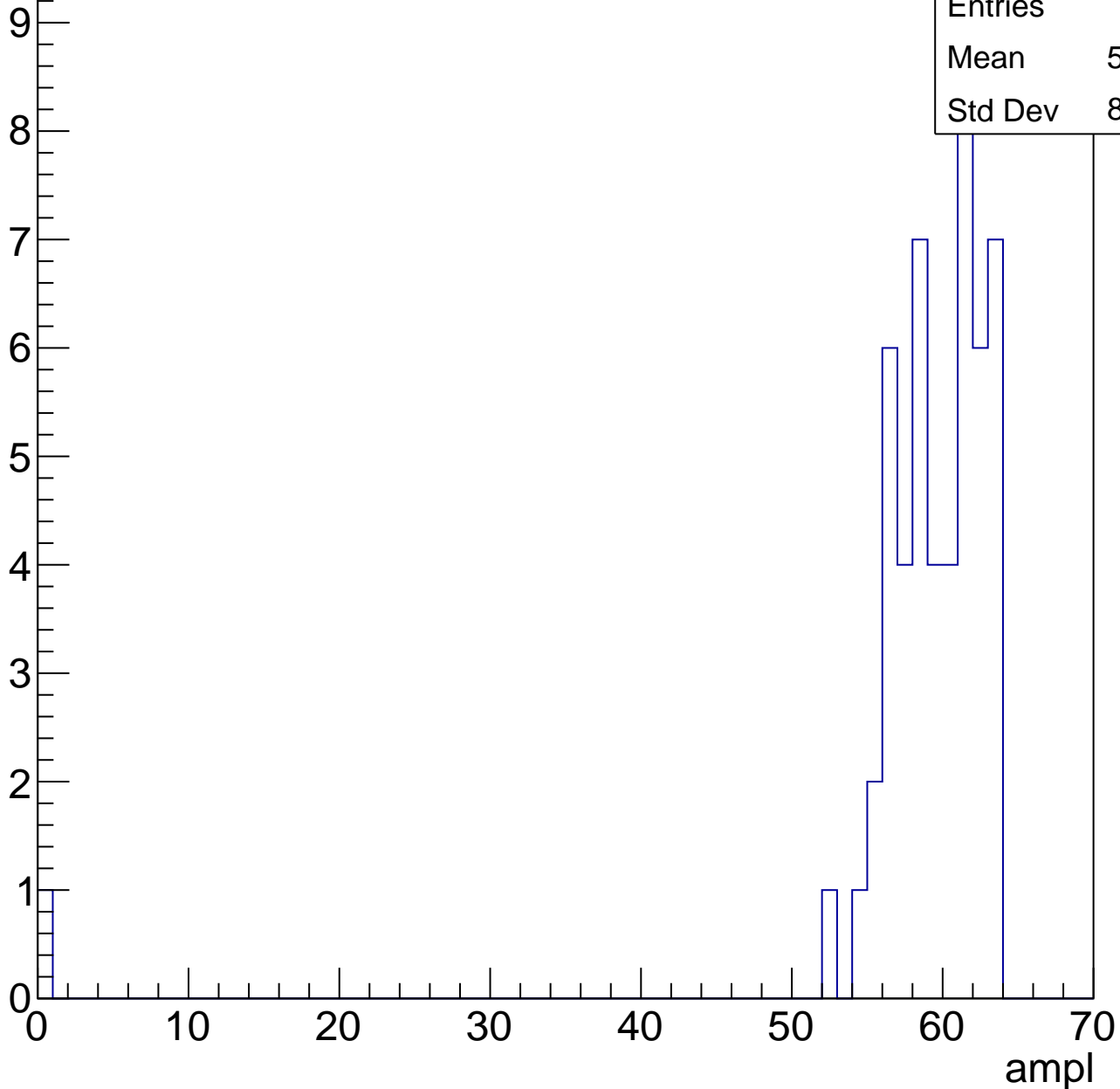


B1L103S, U24-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.15
Std Dev	8.585



B1L103S, U24-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

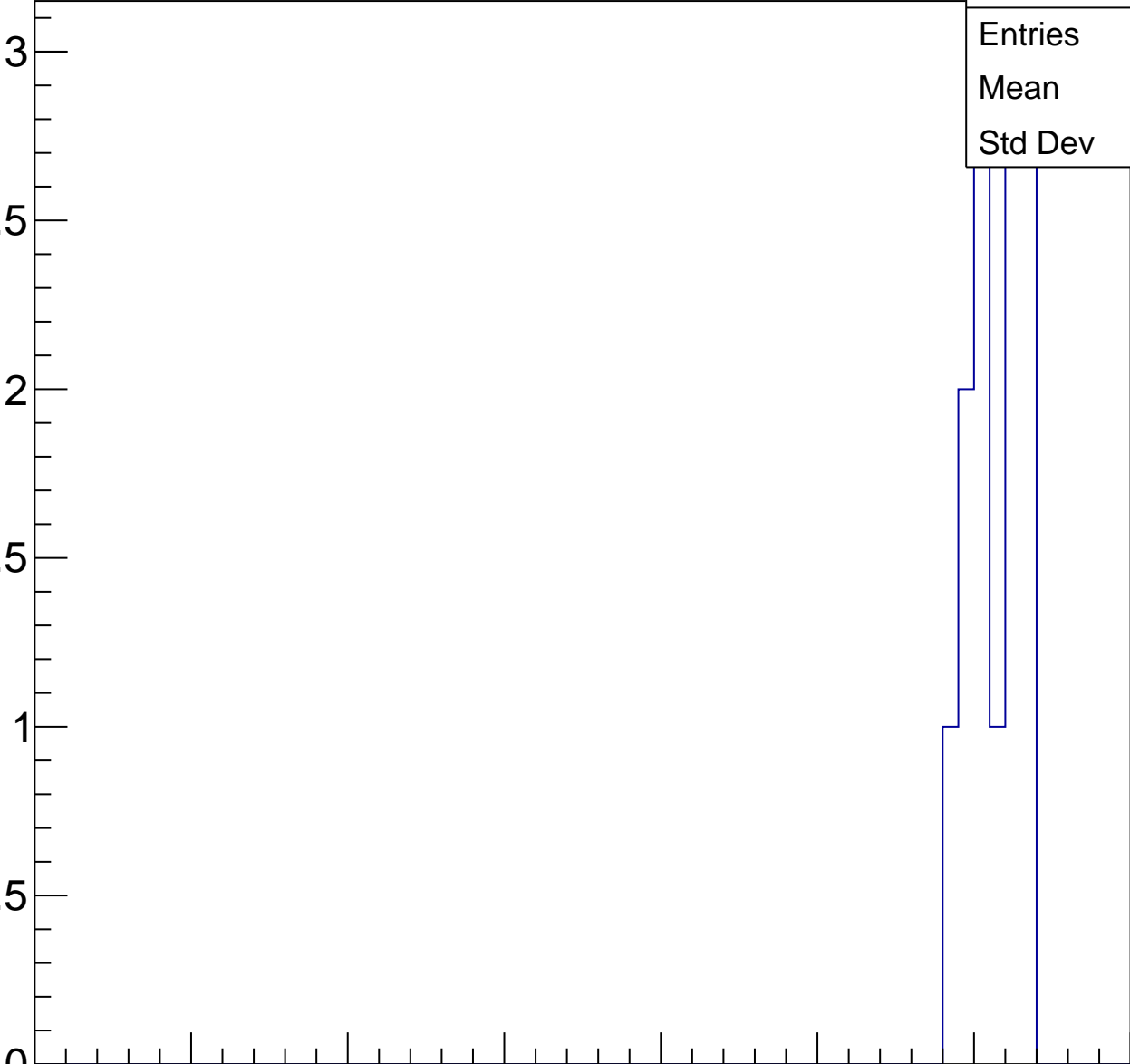
Entry

3
2.5
2
1.5
1
0.5
0

Entries	13
Mean	60.92
Std Dev	1.639

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U24-ch15, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	25.68
Std Dev	11.23

Gaus mean : 30.3183

Gaus Width: 3.0077

Entry

10

8

6

4

2

0

0

10

20

30

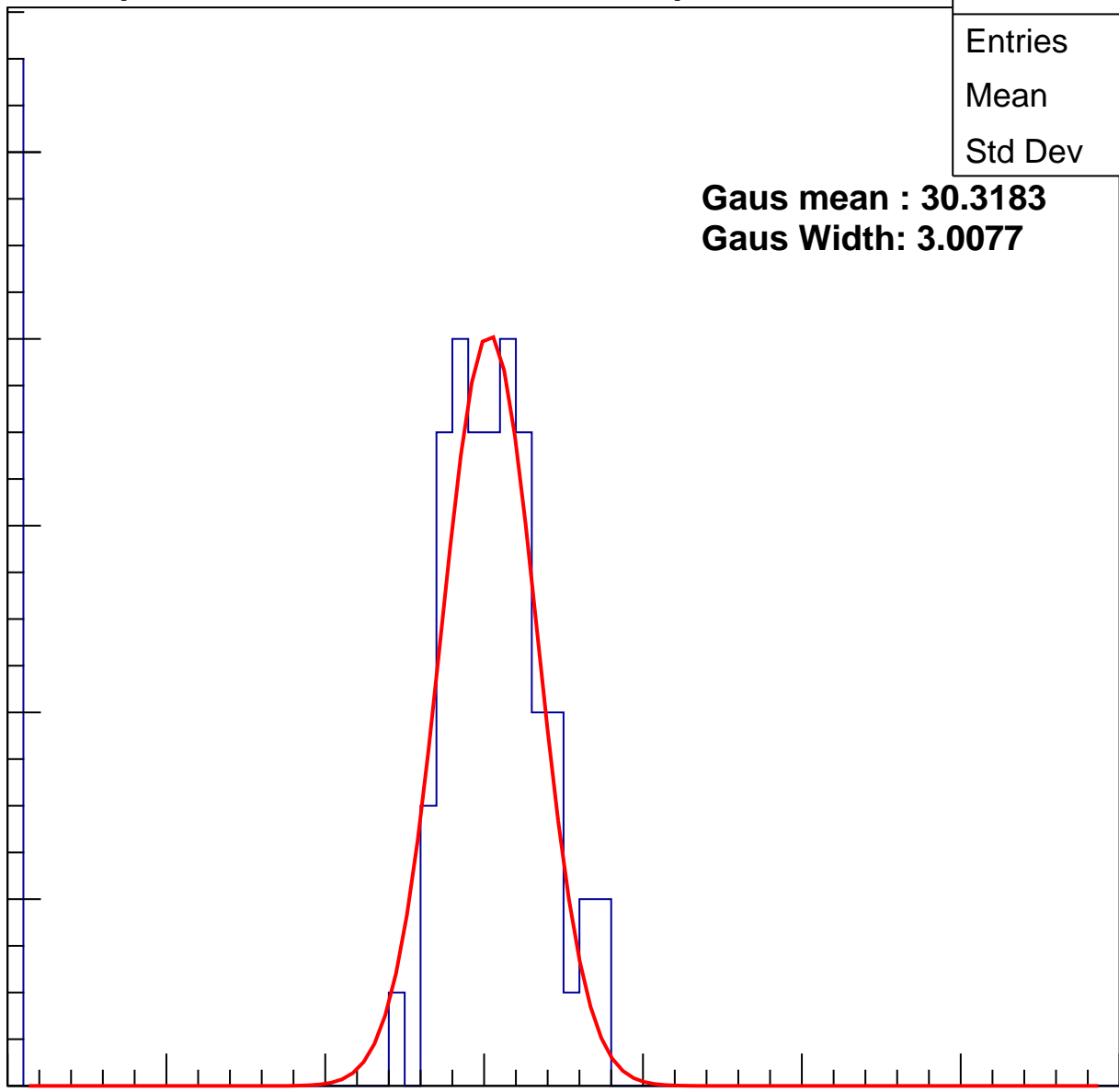
40

50

60

70

ampl



B1L103S, U24-ch15, adc1

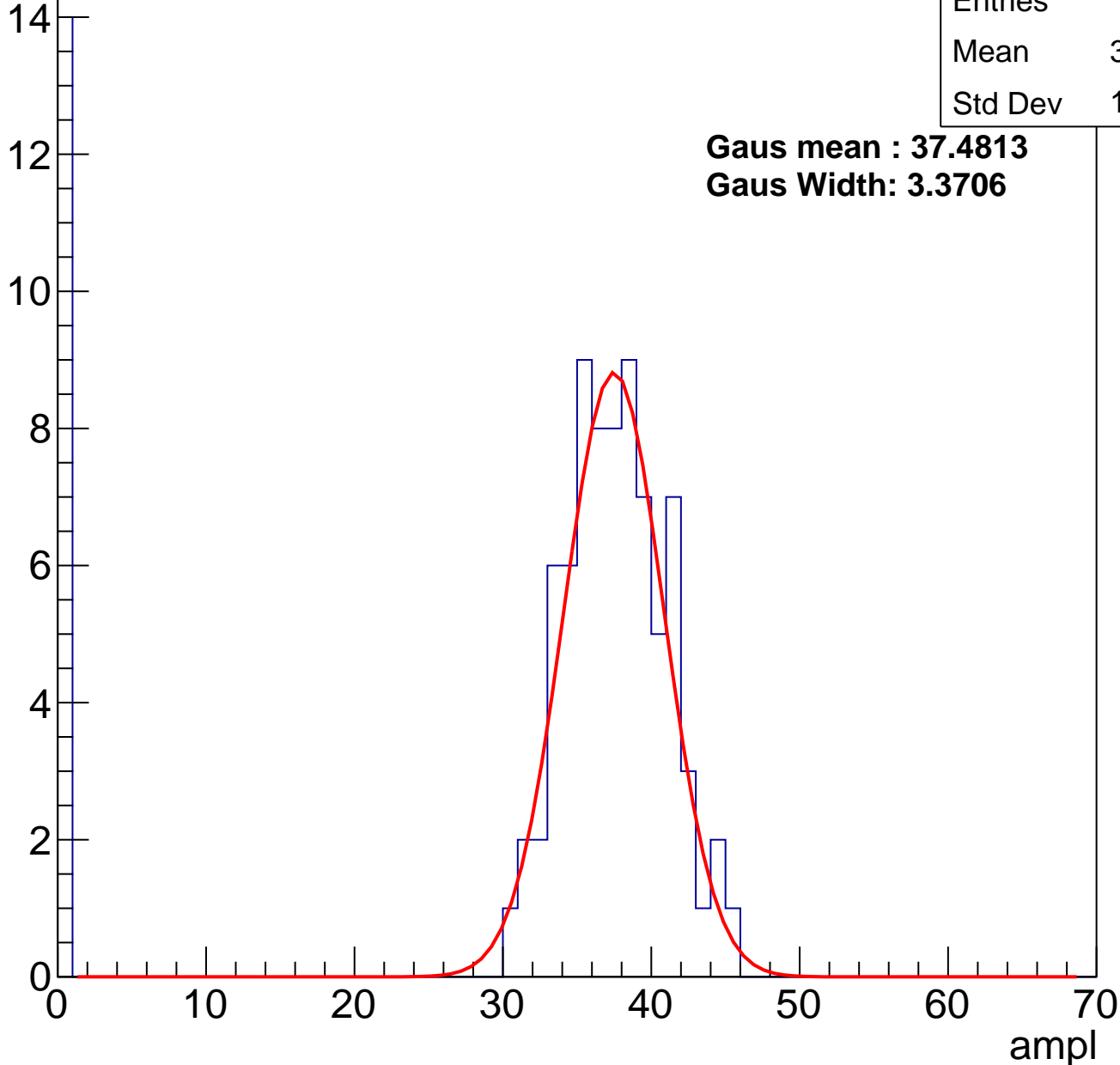
calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	31.44
Std Dev	13.74

Gaus mean : 37.4813

Gaus Width: 3.3706

Entry



B1L103S, U24-ch15, adc2

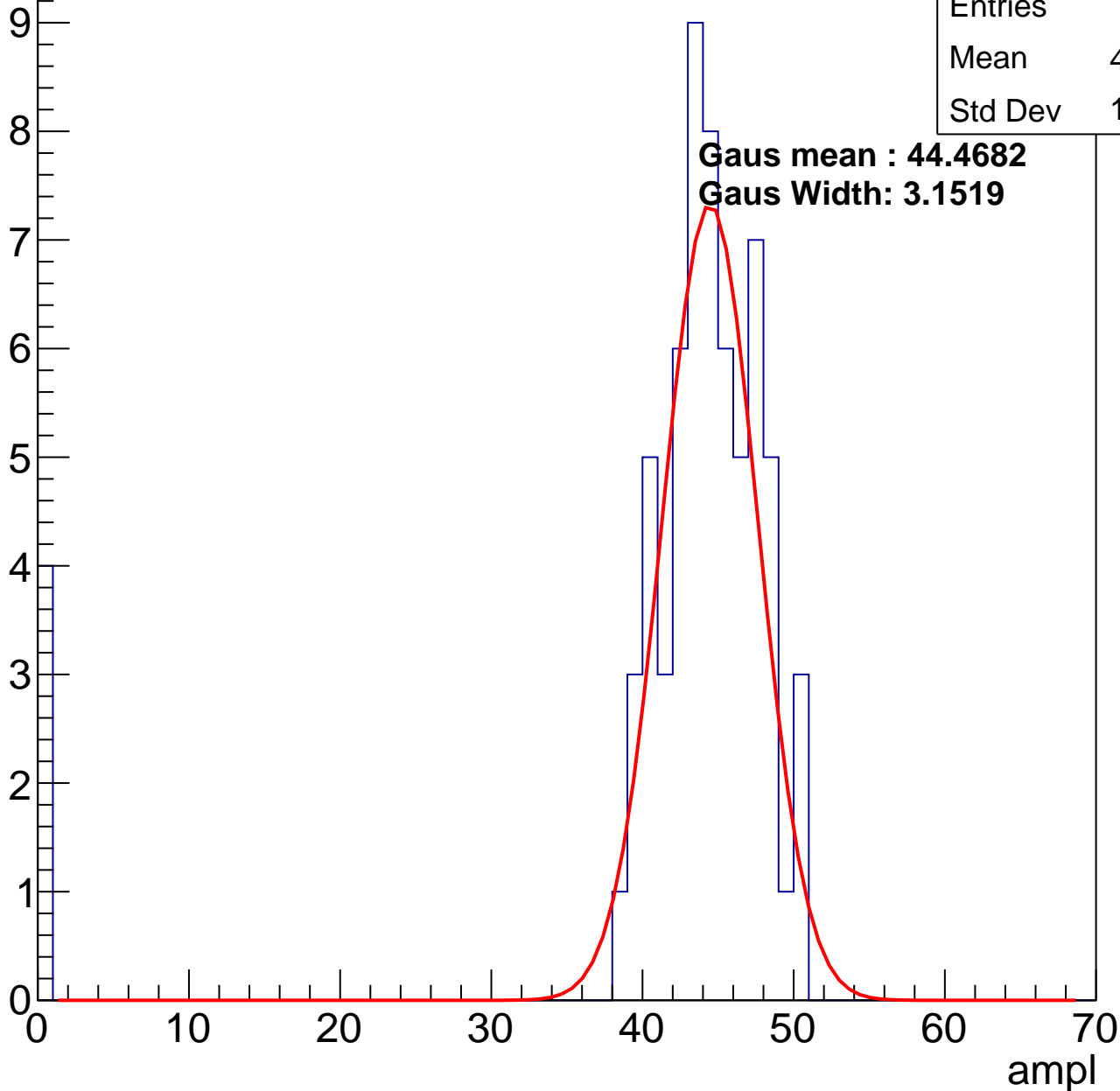
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	41.47
Std Dev	10.92

Gaus mean : 44.4682

Gaus Width: 3.1519

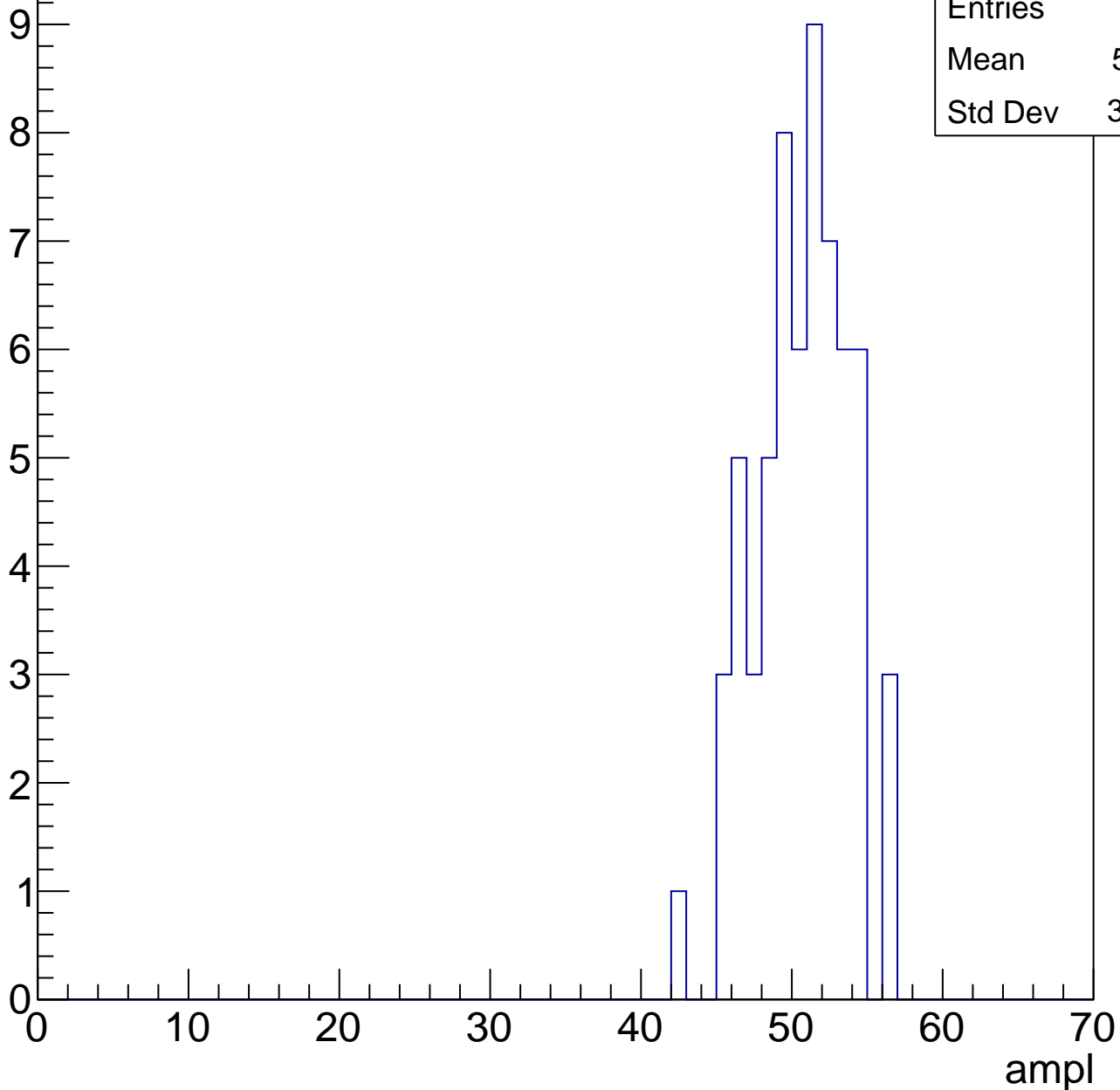


B1L103S, U24-ch15, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	50.21
Std Dev	3.022

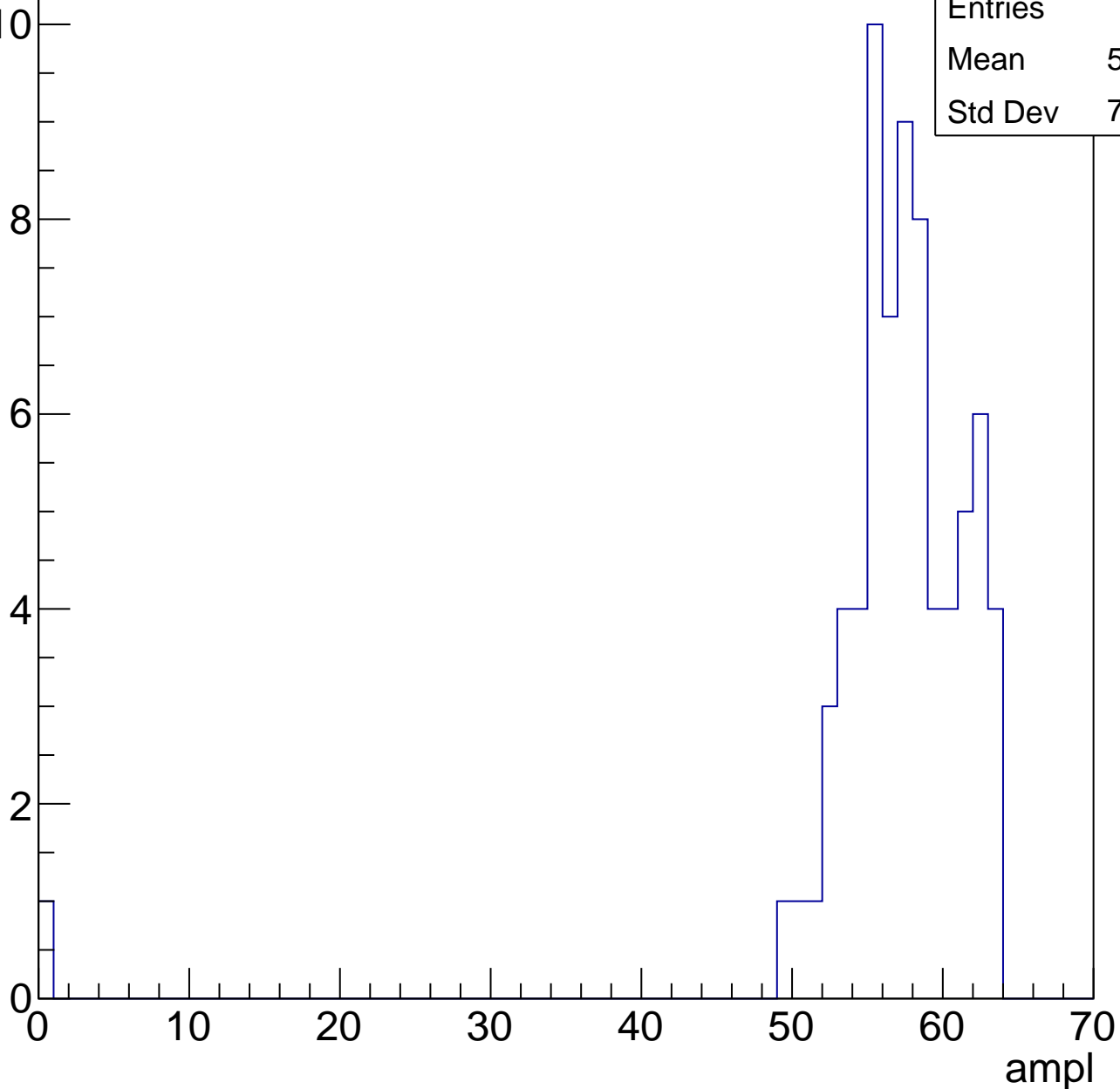


B1L103S, U24-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	56.36
Std Dev	7.476



B1L103S, U24-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries

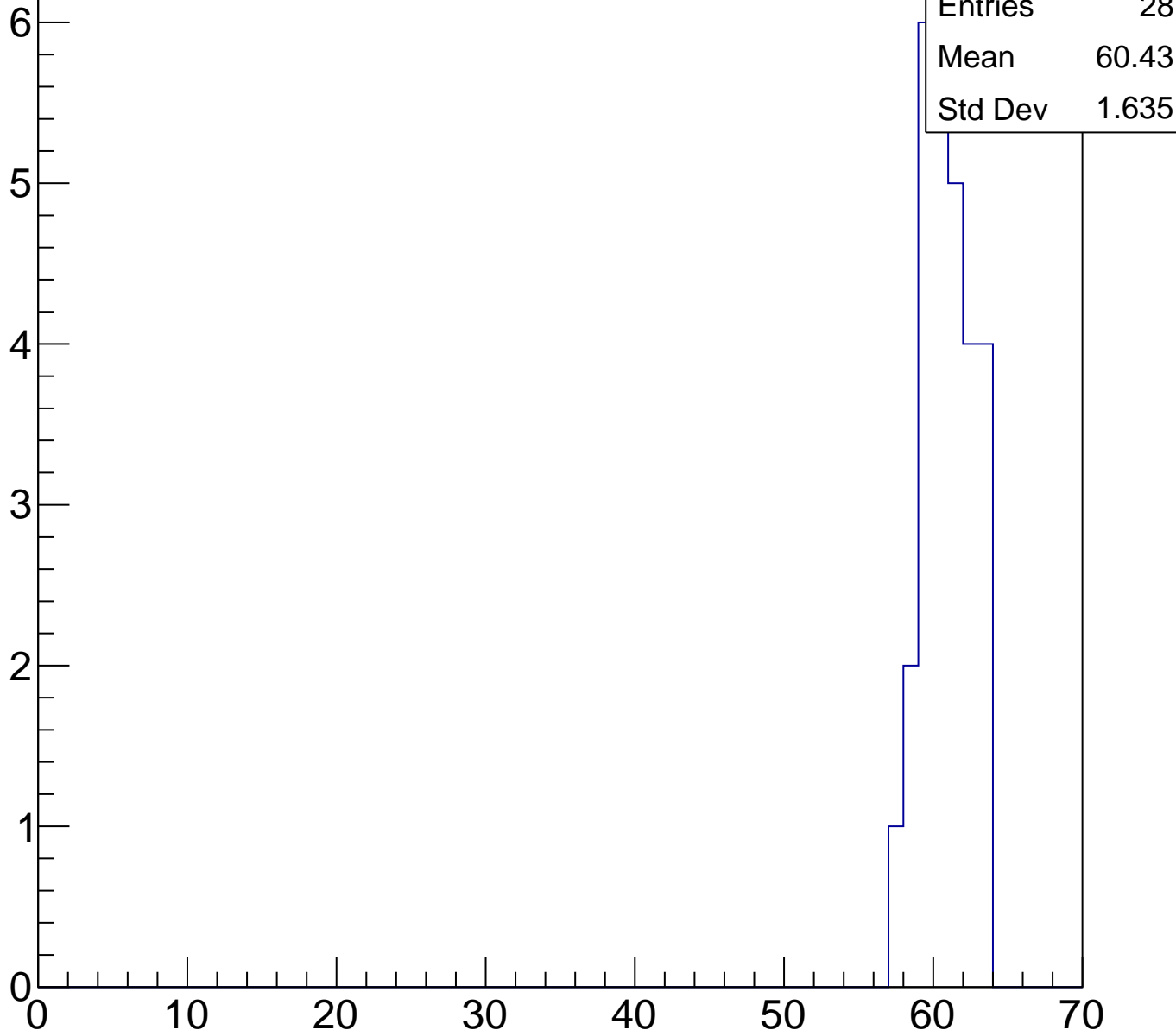
28

Mean

60.43

Std Dev

1.635



B1L103S, U24-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



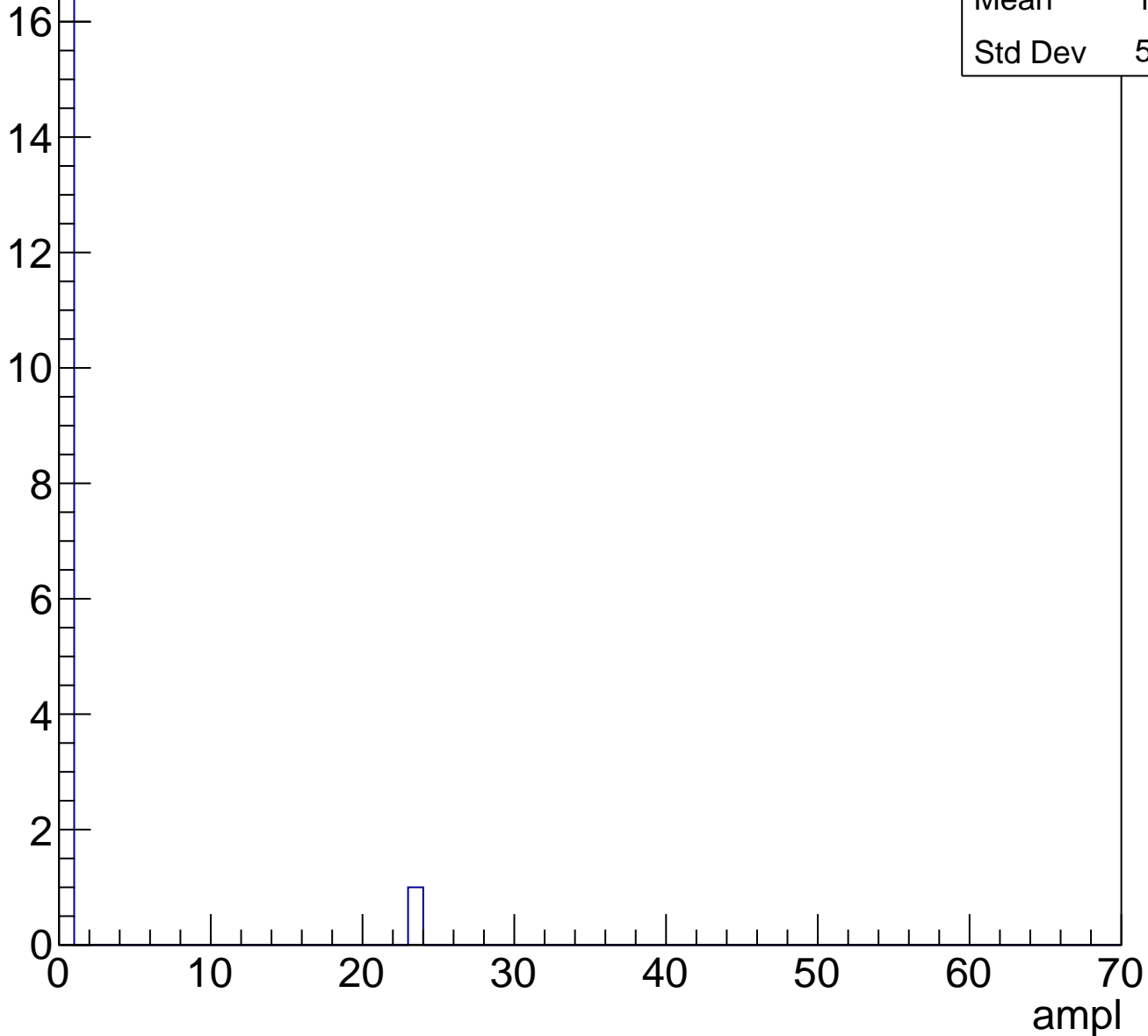
Entries	2
Mean	63
Std Dev	0

B1L103S, U24-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.278
Std Dev	5.268

Entry



B1L103S, U24-ch16, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	24.46
Std Dev	11.94

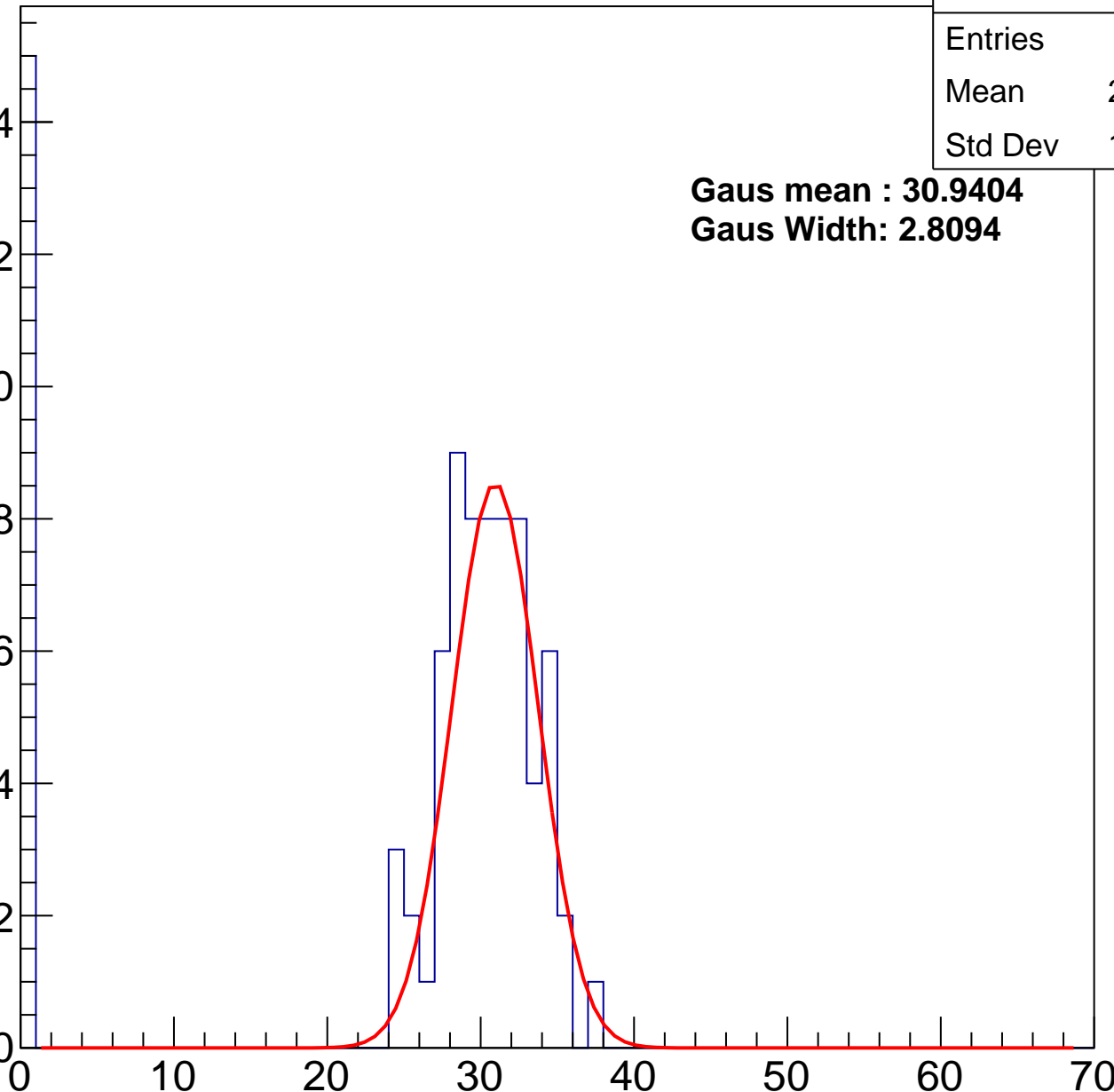
Gaus mean : 30.9404

Gaus Width: 2.8094

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch16, adc1

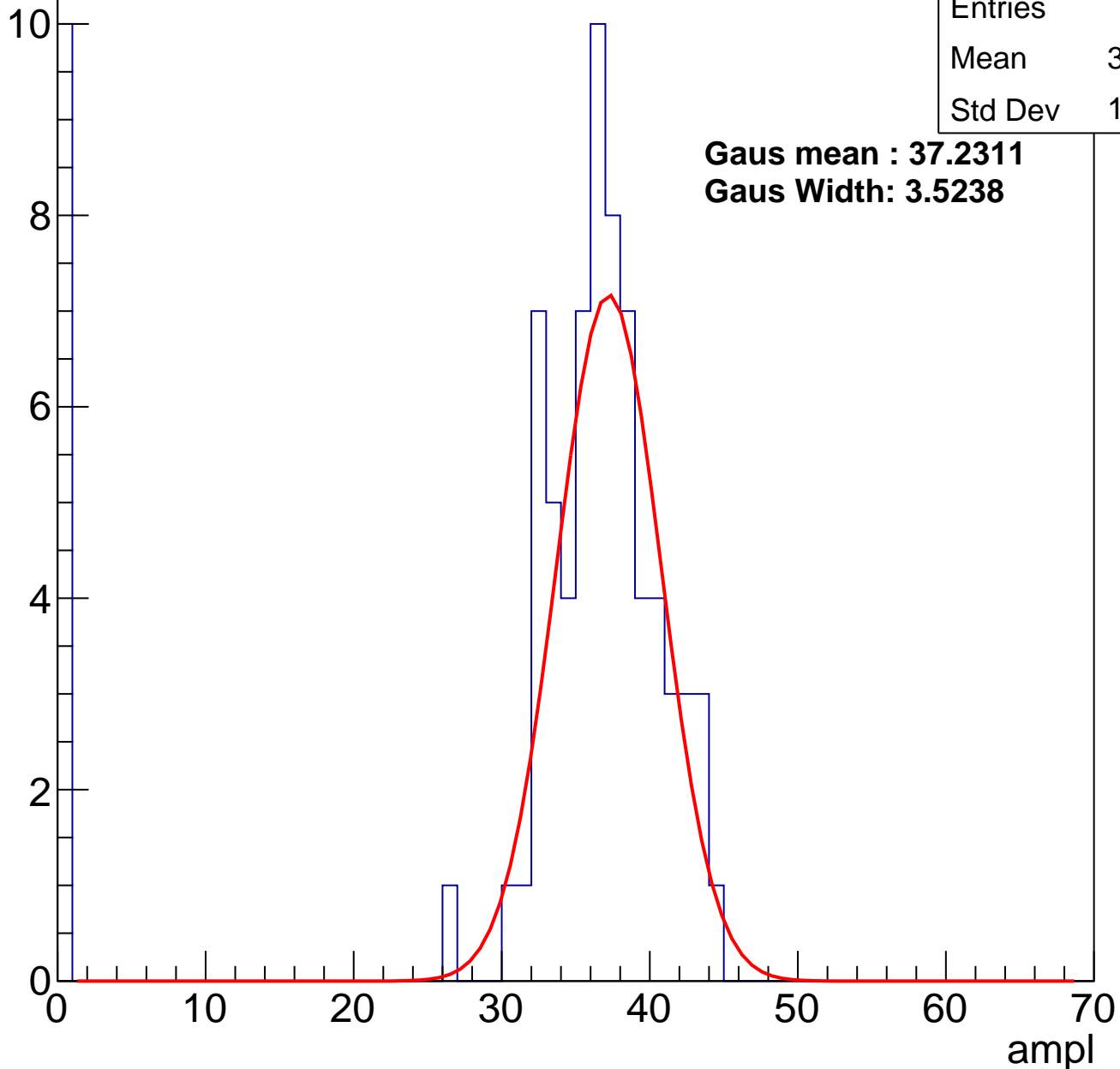
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	31.86
Std Dev	12.57

Gaus mean : 37.2311

Gaus Width: 3.5238

Entry

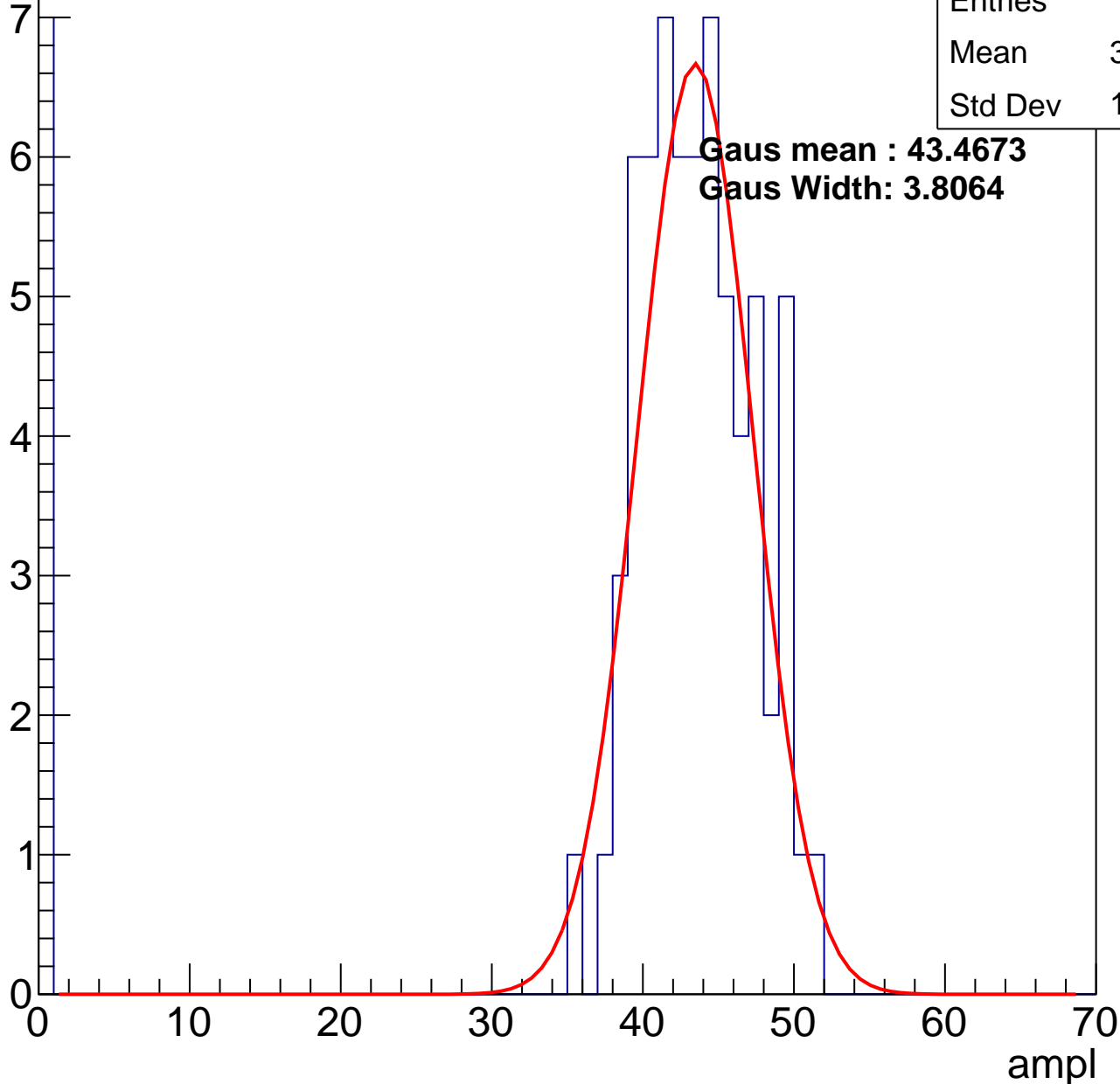


B1L103S, U24-ch16, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	39.05
Std Dev	13.16

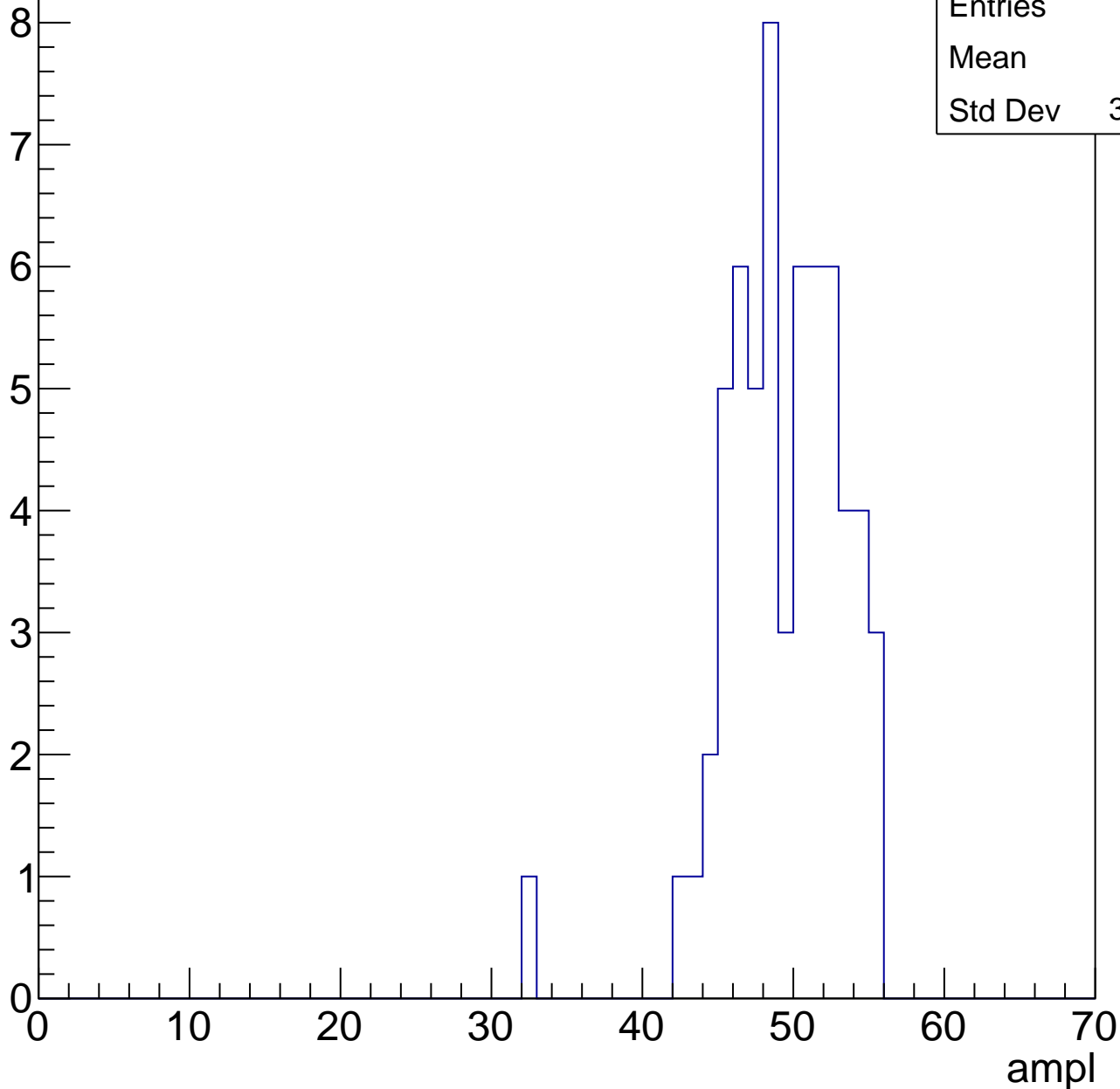


B1L103S, U24-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	48.9
Std Dev	3.928

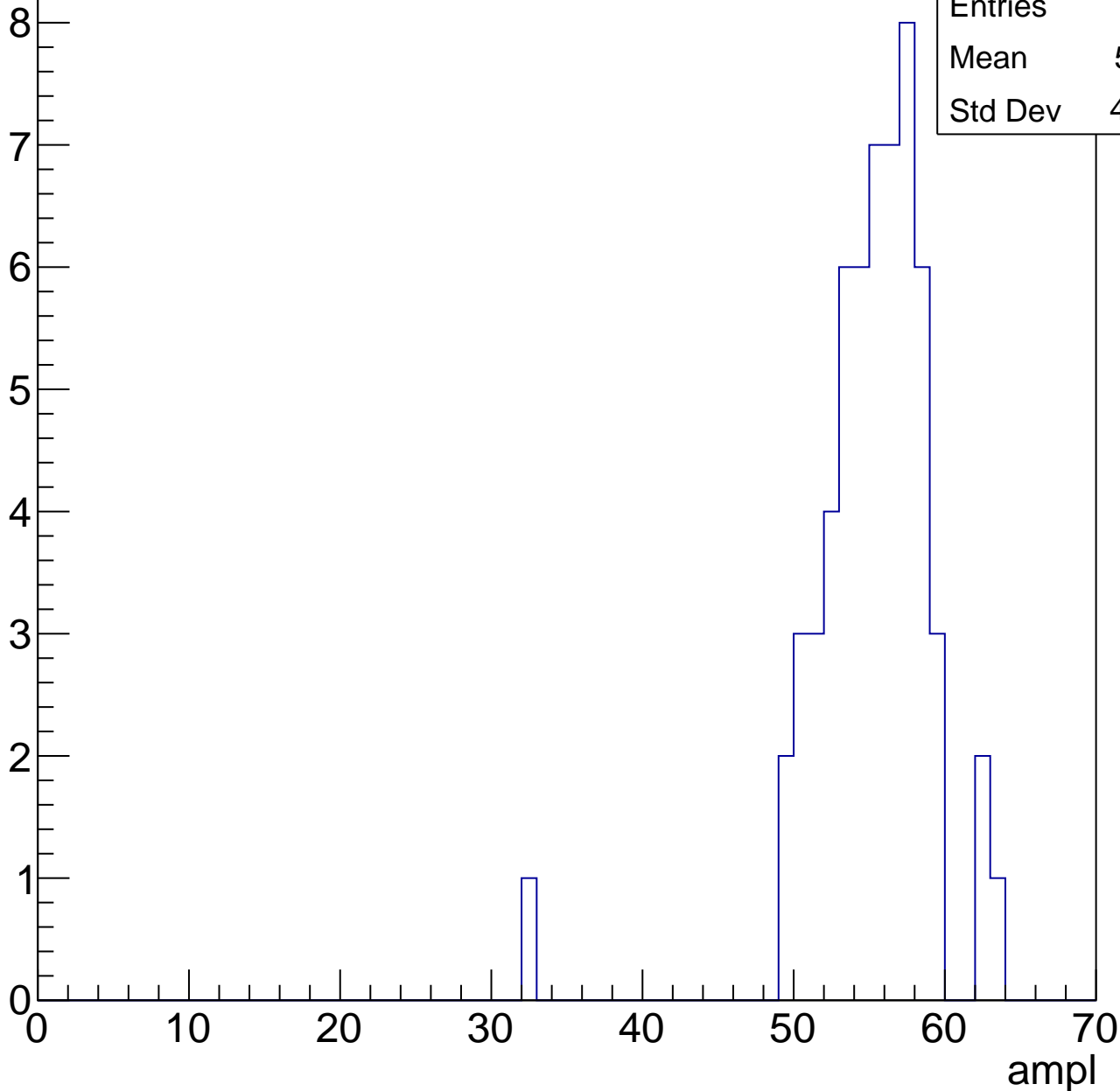


B1L103S, U24-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.71
Std Dev	4.294

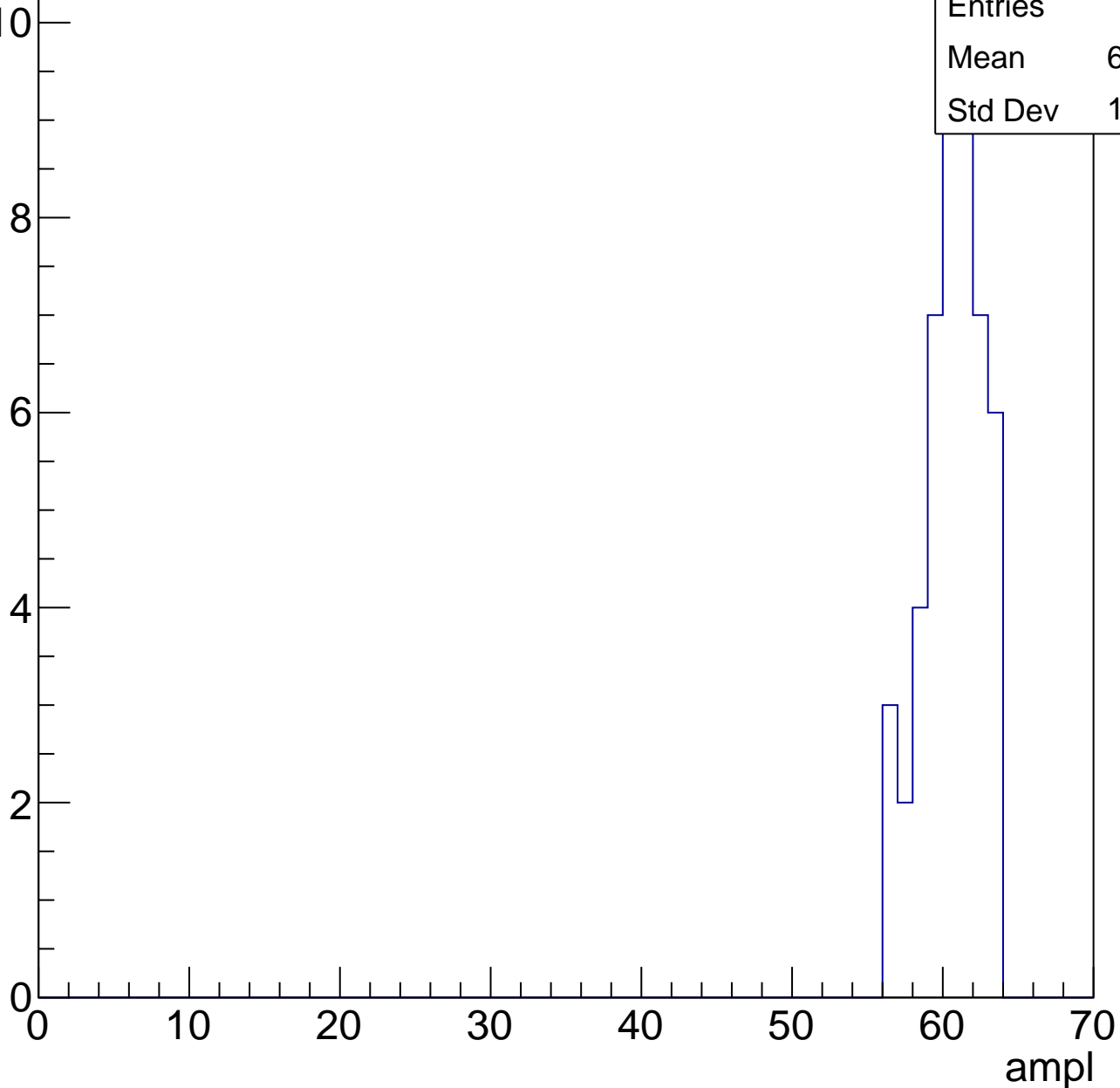


B1L103S, U24-ch16, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

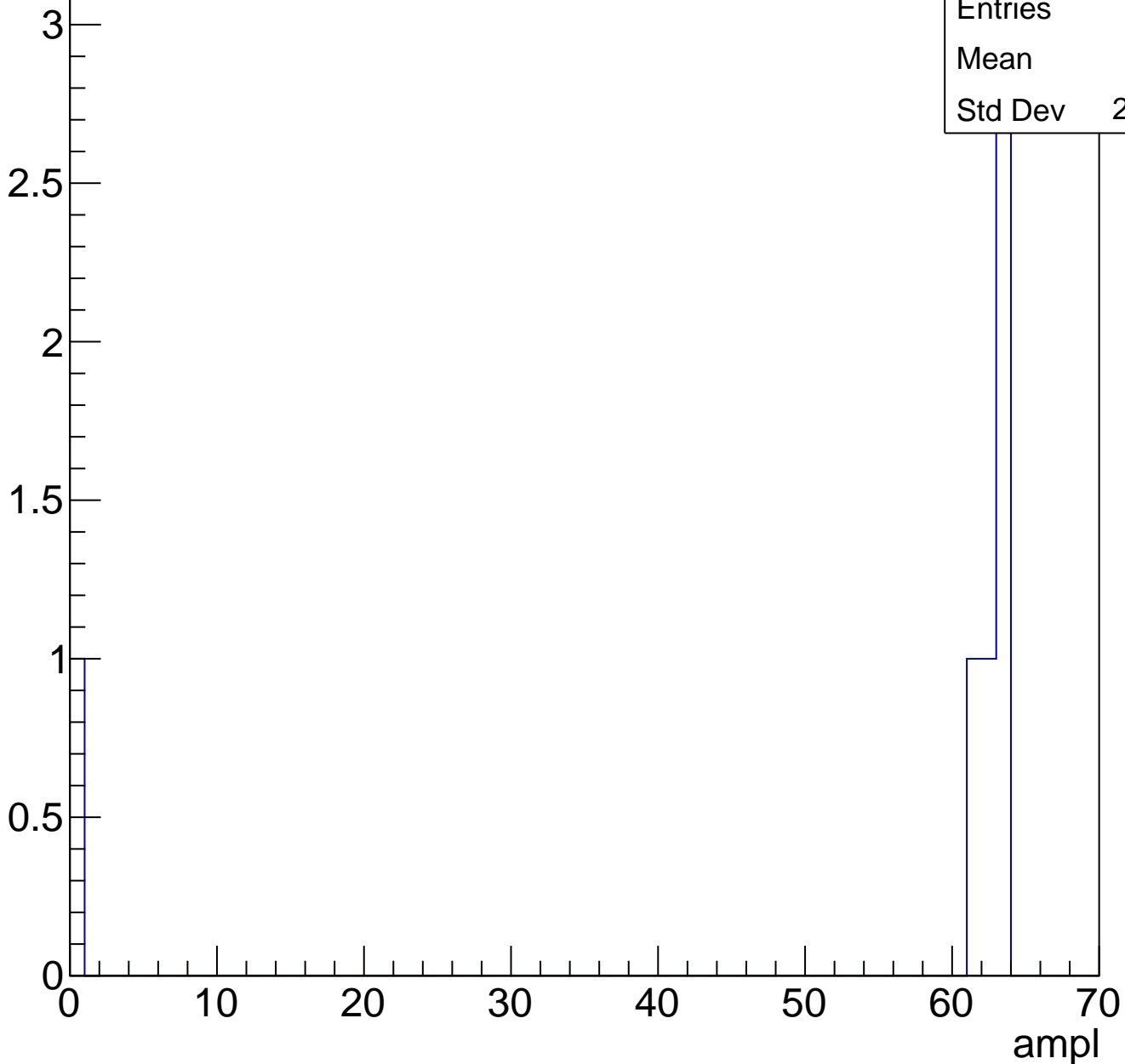
Entries	48
Mean	60.17
Std Dev	1.929



B1L103S, U24-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

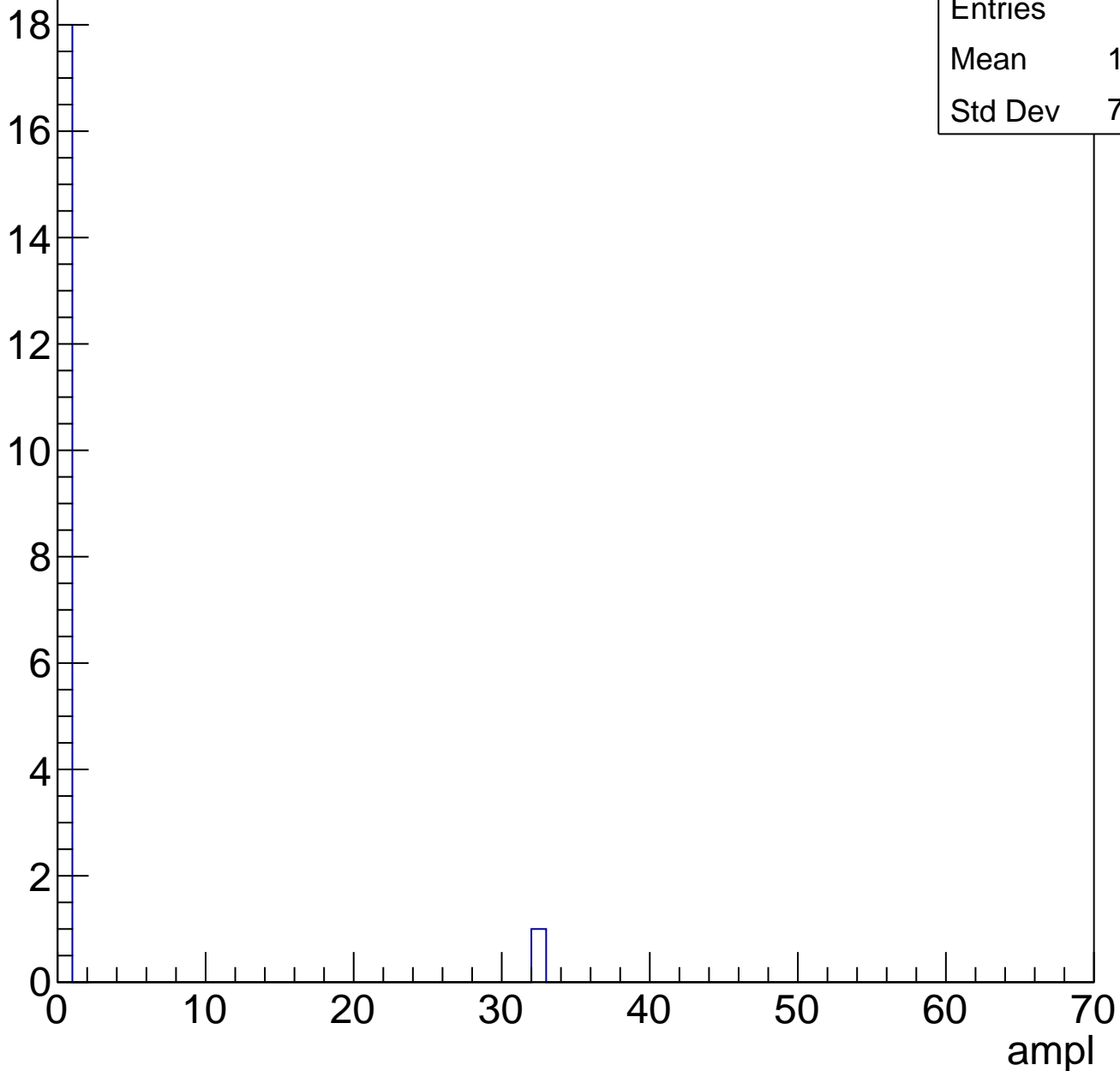


B1L103S, U24-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.684
Std Dev	7.146

Entry



B1L103S, U24-ch17, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	23.99
Std Dev	11

Gaus mean : 29.1413

Gaus Width: 3.4803

Entry

12

10

8

6

4

2

0

0

10

20

30

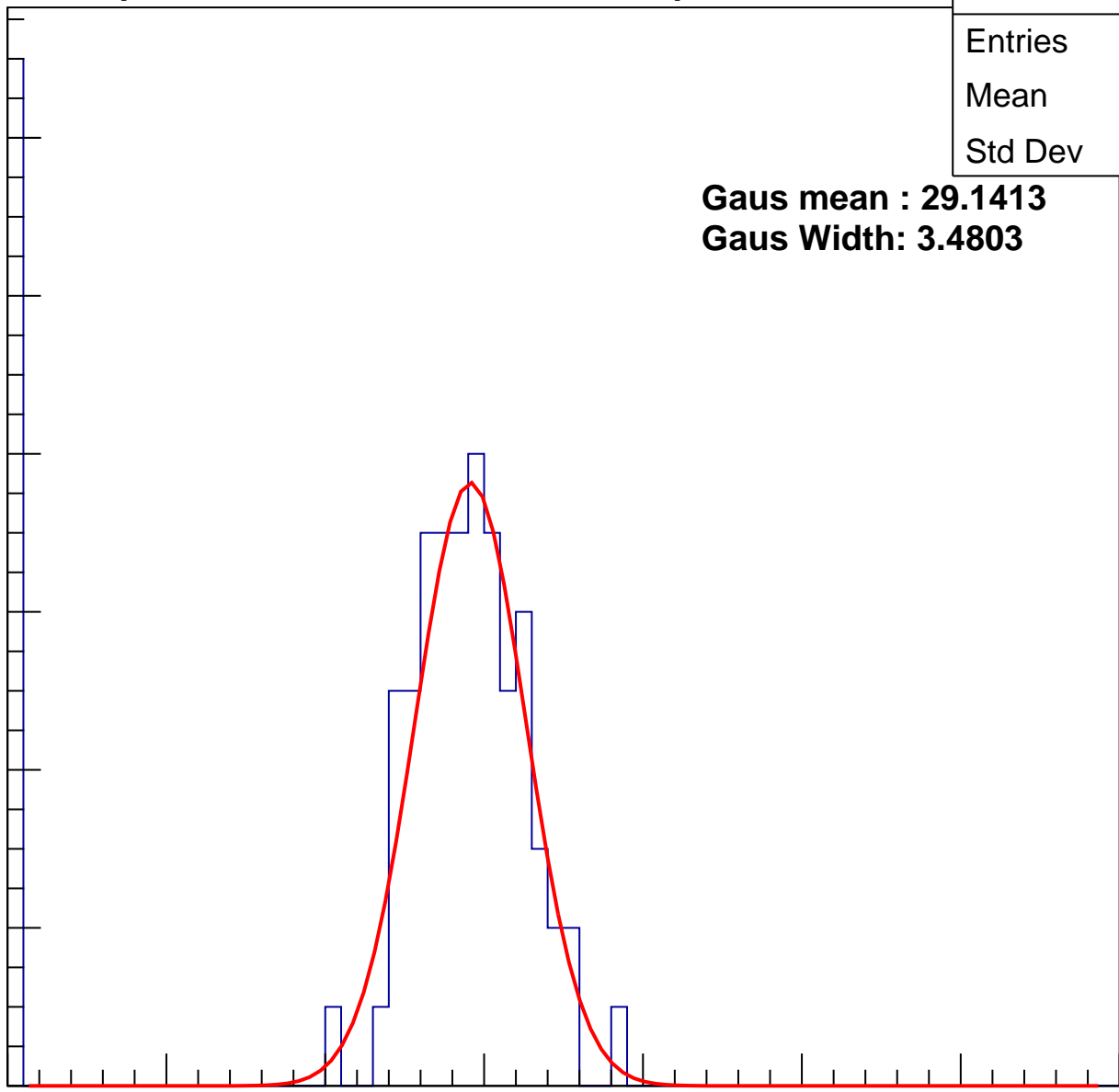
40

50

60

70

ampl



B1L103S, U24-ch17, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	30.32
Std Dev	13.38

Gaus mean : 36.2199

Gaus Width: 3.6520

Entry

12

10

8

6

4

2

0

0

10

20

30

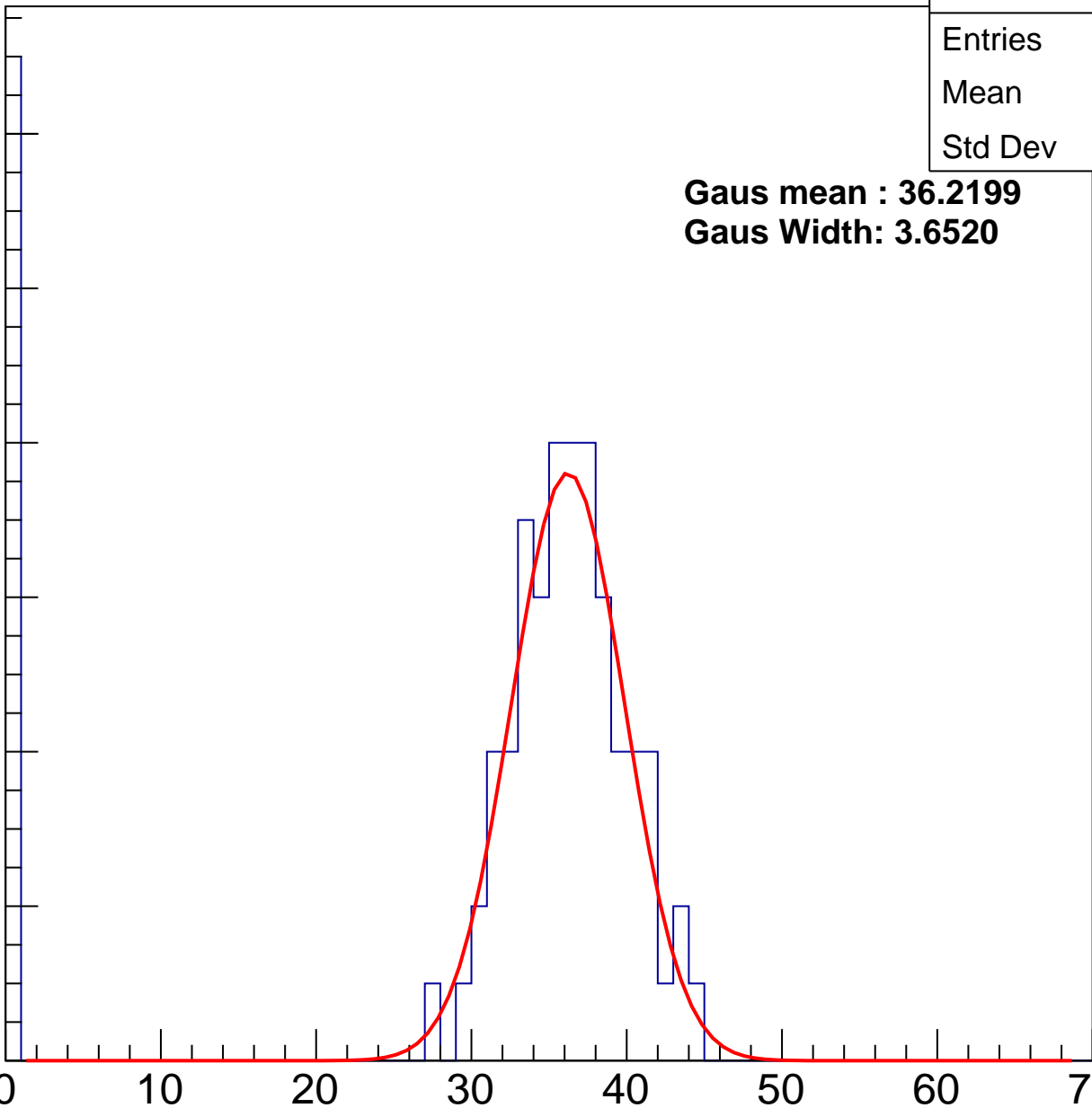
40

50

60

70

ampl



B1L103S, U24-ch17, adc2

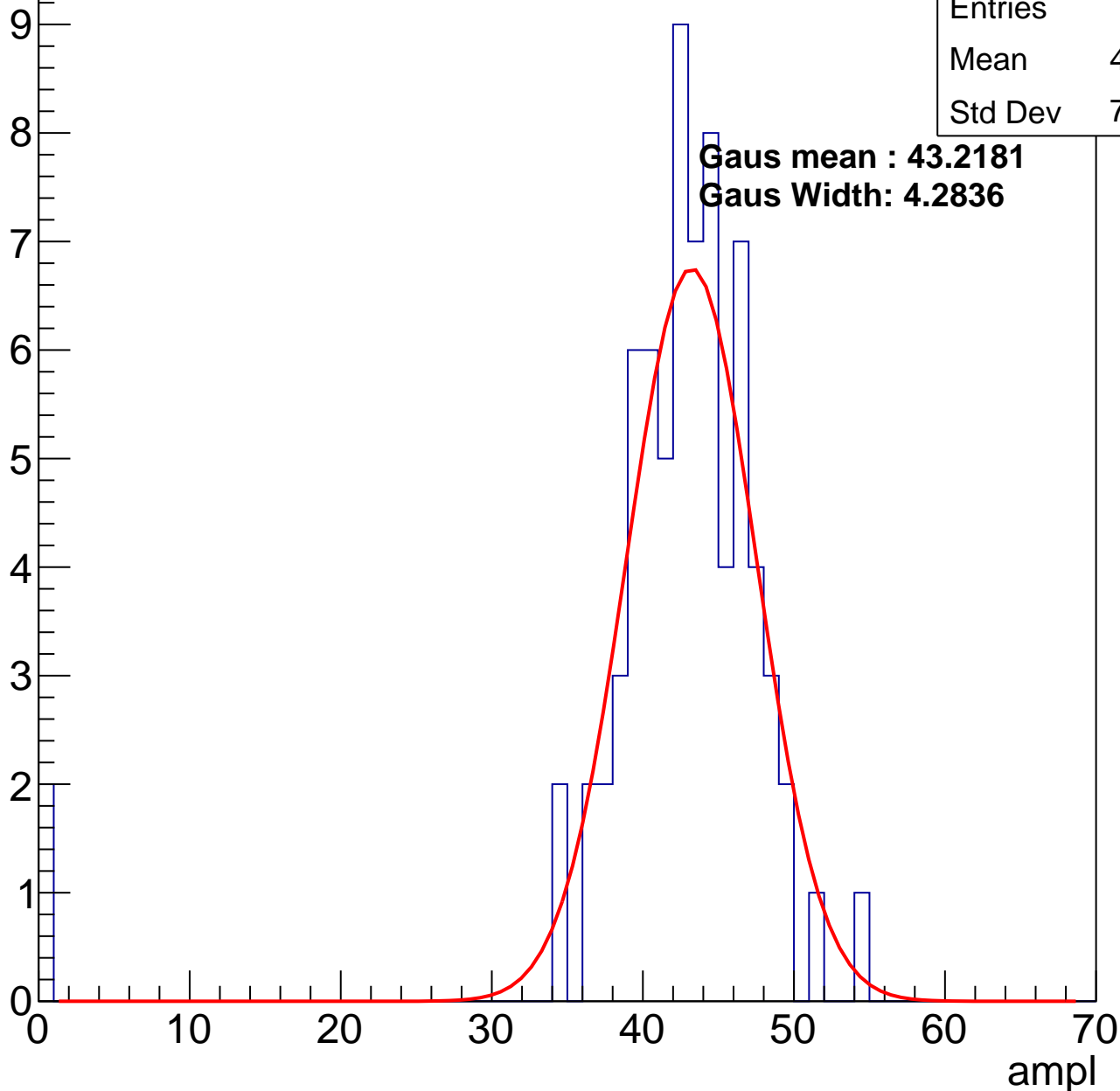
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	41.55
Std Dev	7.895

Gaus mean : 43.2181

Gaus Width: 4.2836

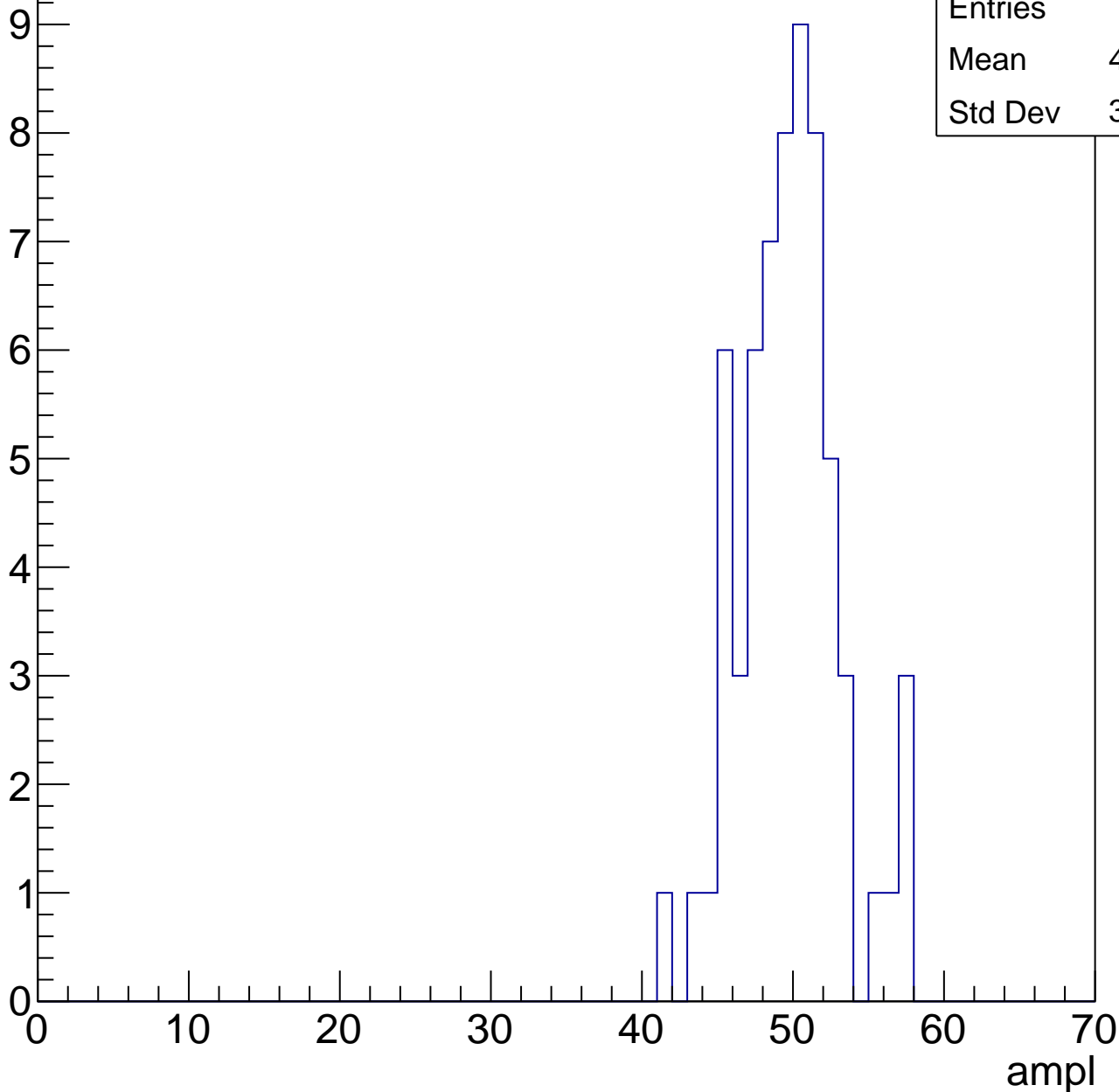


B1L103S, U24-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.29
Std Dev	3.297

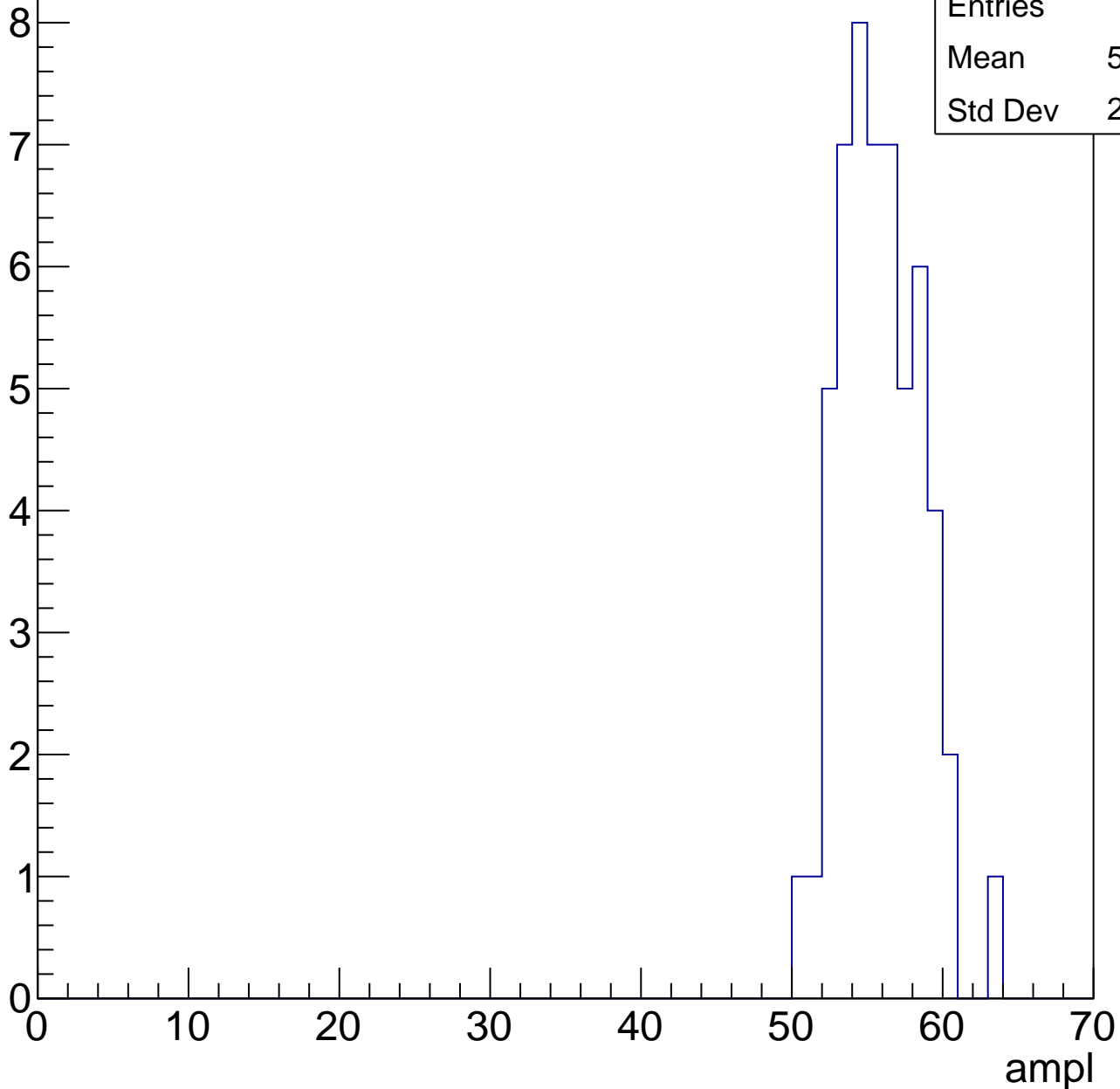


B1L103S, U24-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	55.43
Std Dev	2.622

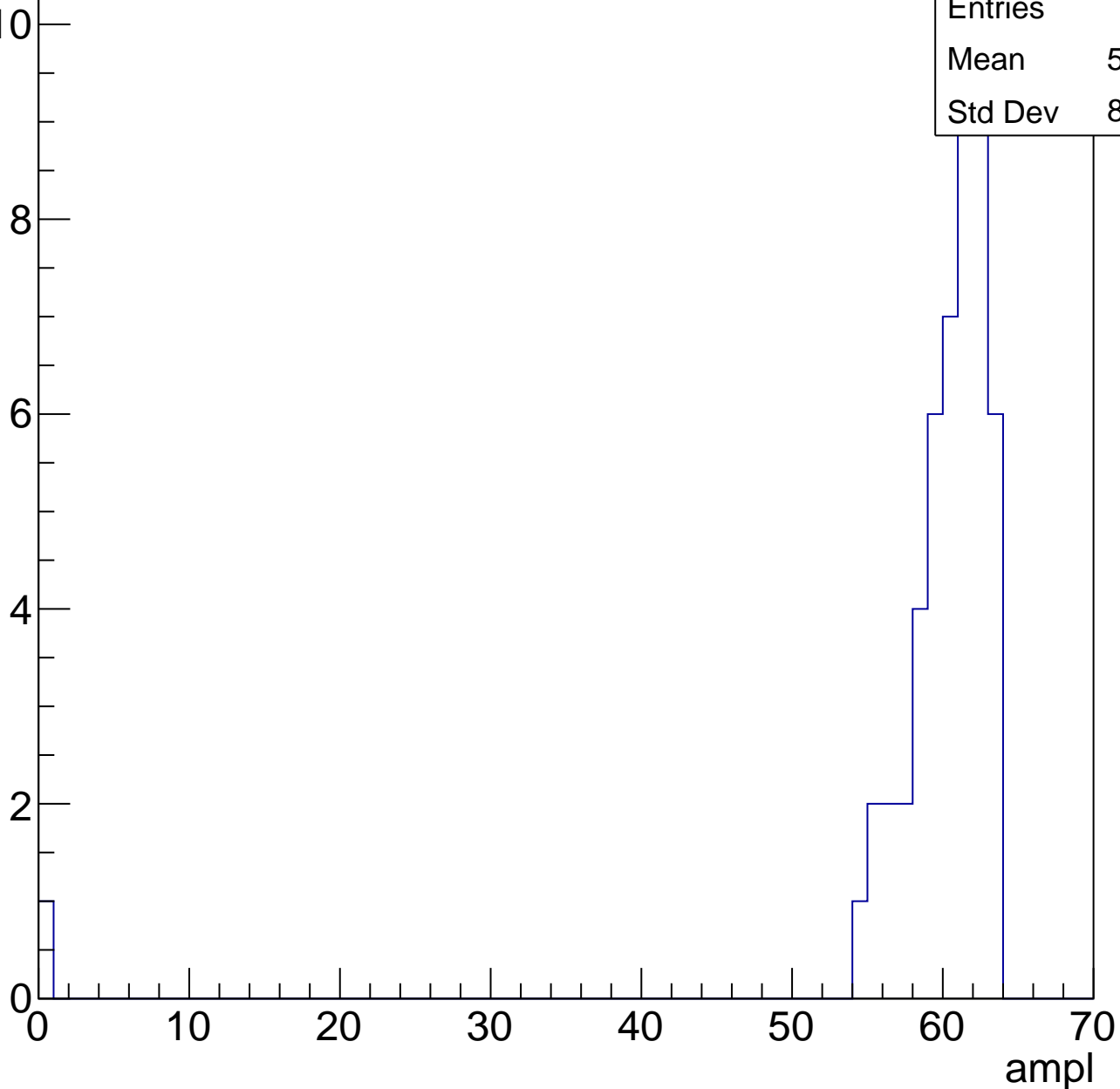


B1L103S, U24-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.86
Std Dev	8.713



B1L103S, U24-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

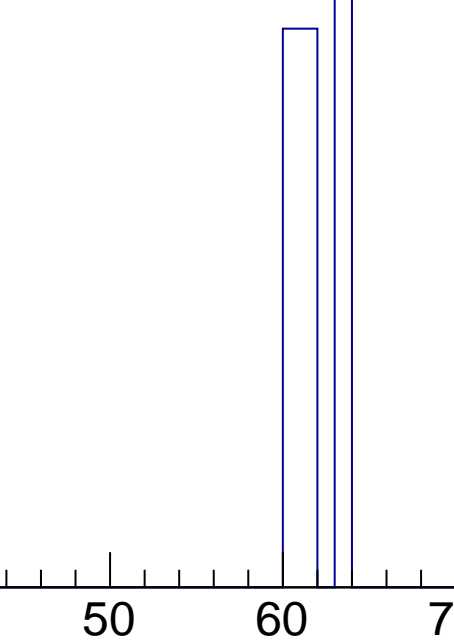
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.299

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U24-ch18, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	25.03
Std Dev	11.09

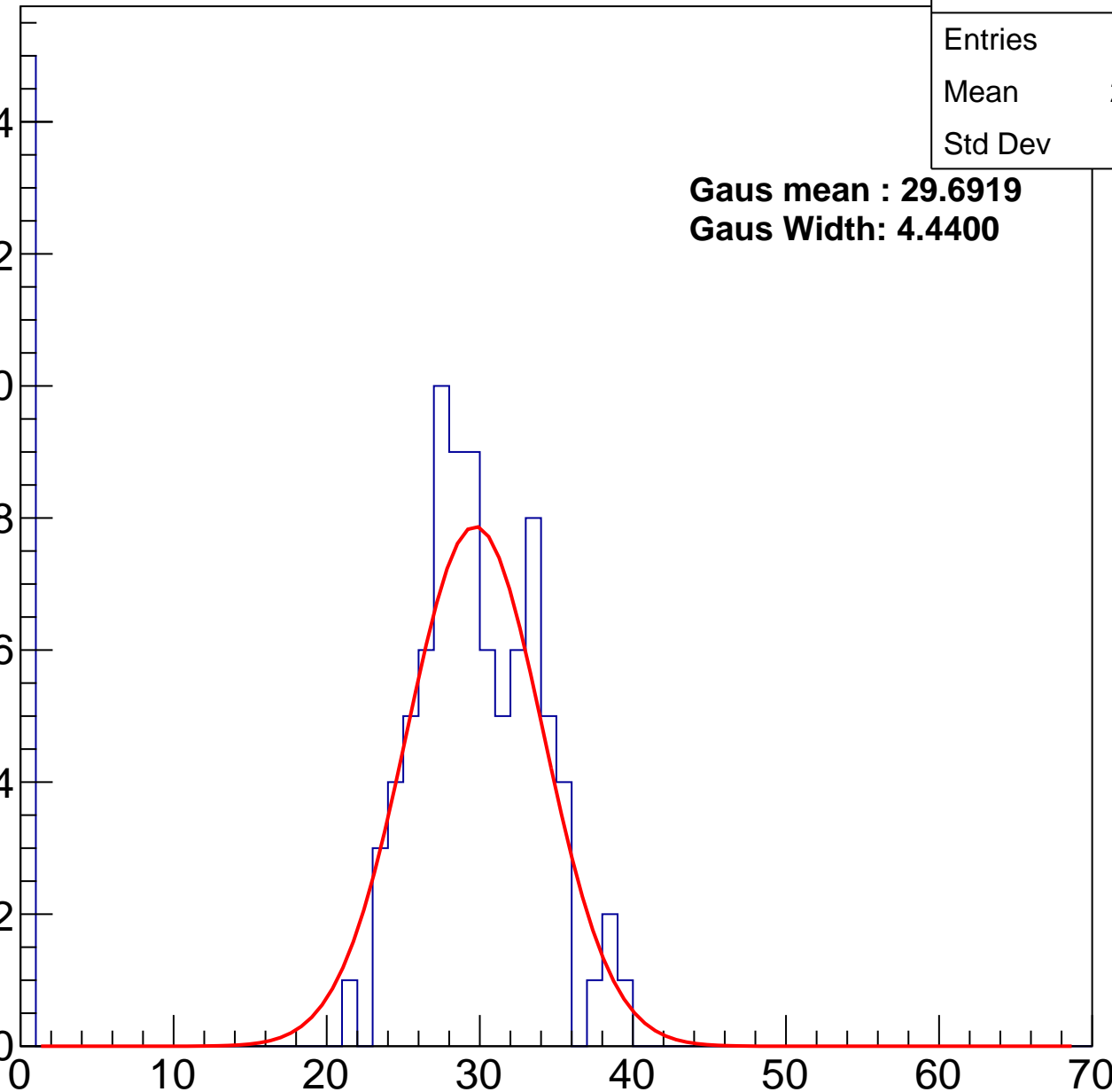
Gaus mean : 29.6919

Gaus Width: 4.4400

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch18, adc1

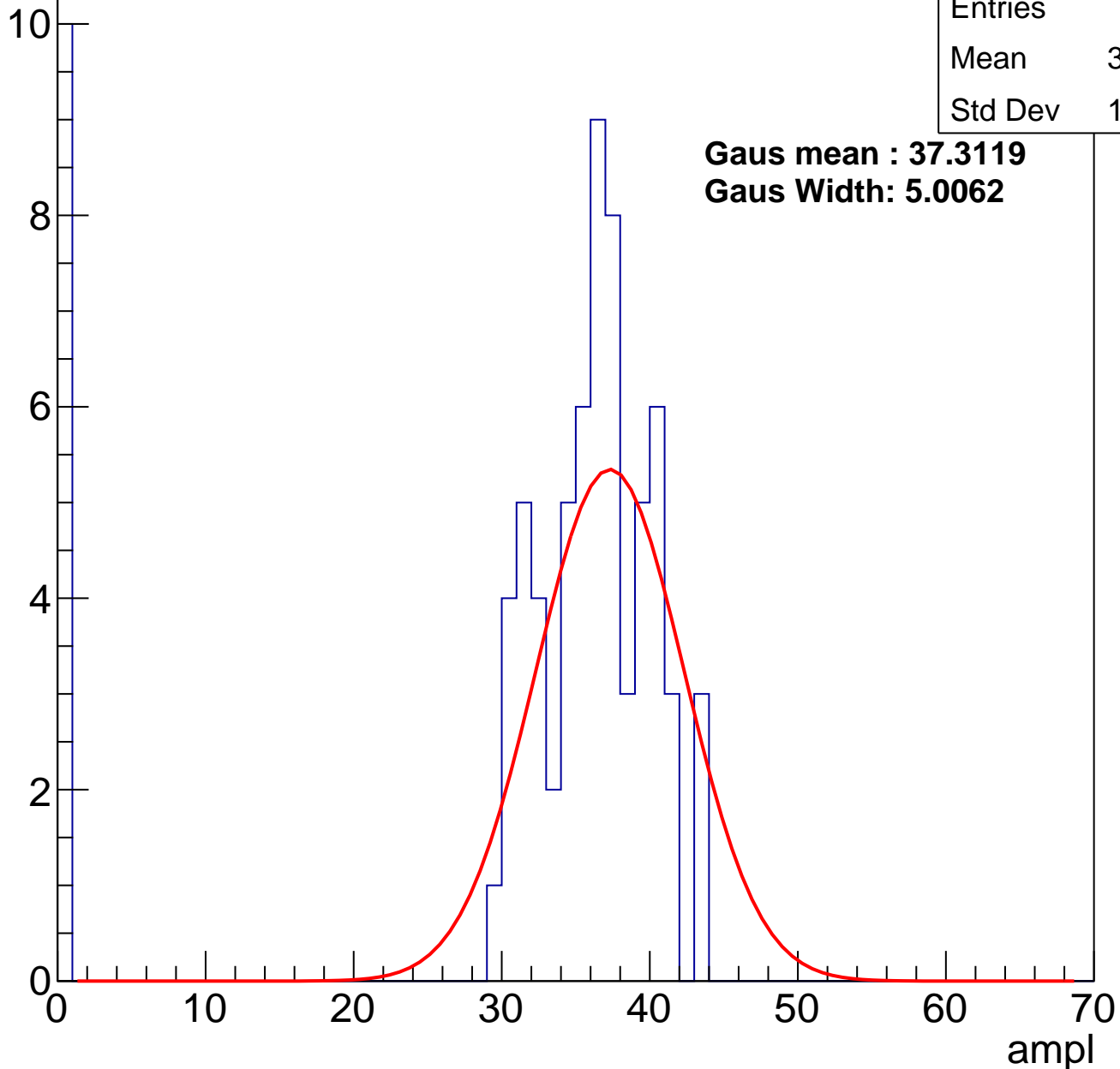
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	31.07
Std Dev	12.72

Gaus mean : 37.3119

Gaus Width: 5.0062

Entry



B1L103S, U24-ch18, adc2

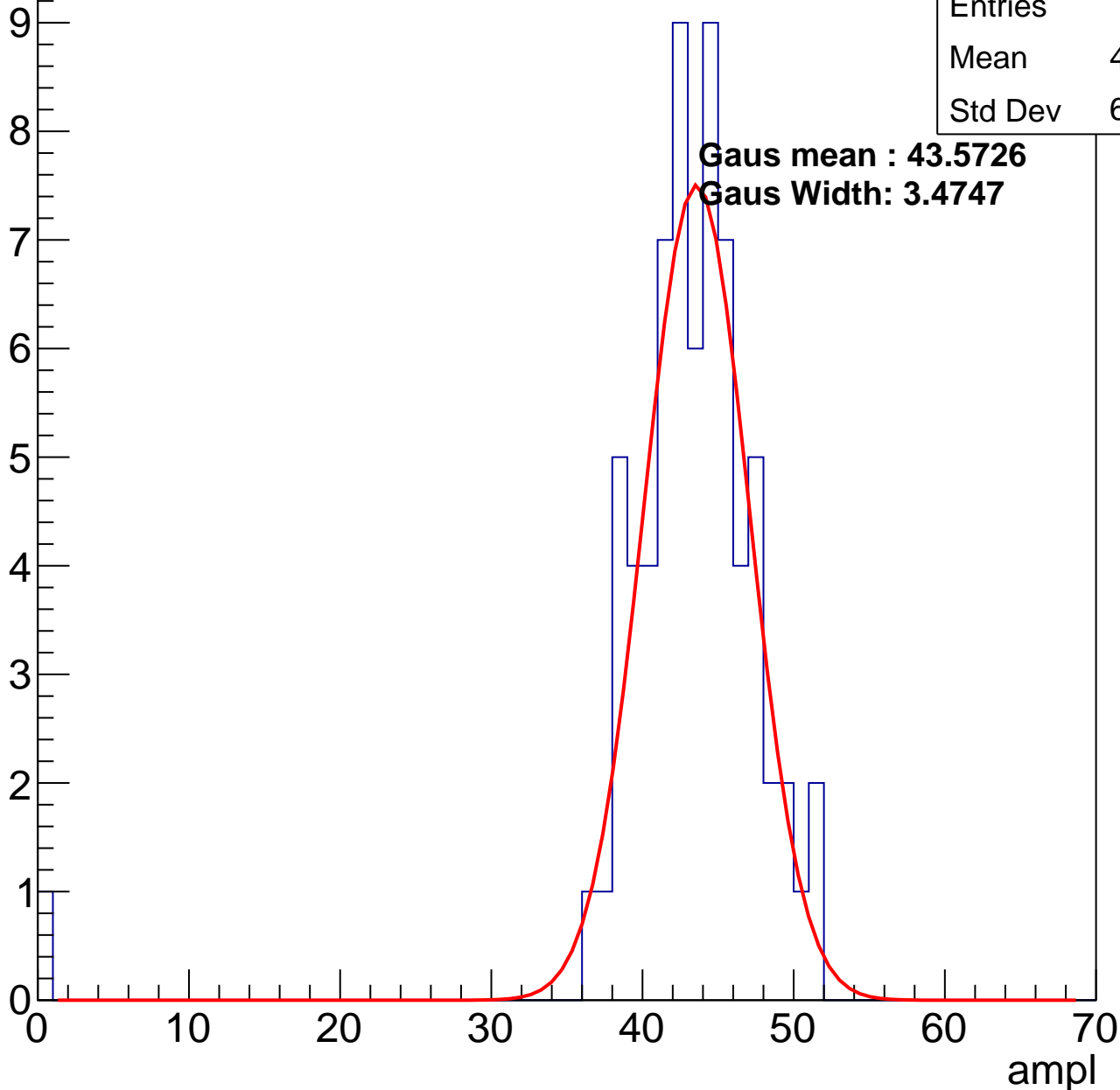
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	42.54
Std Dev	6.133

Gaus mean : 43.5726

Gaus Width: 3.4747

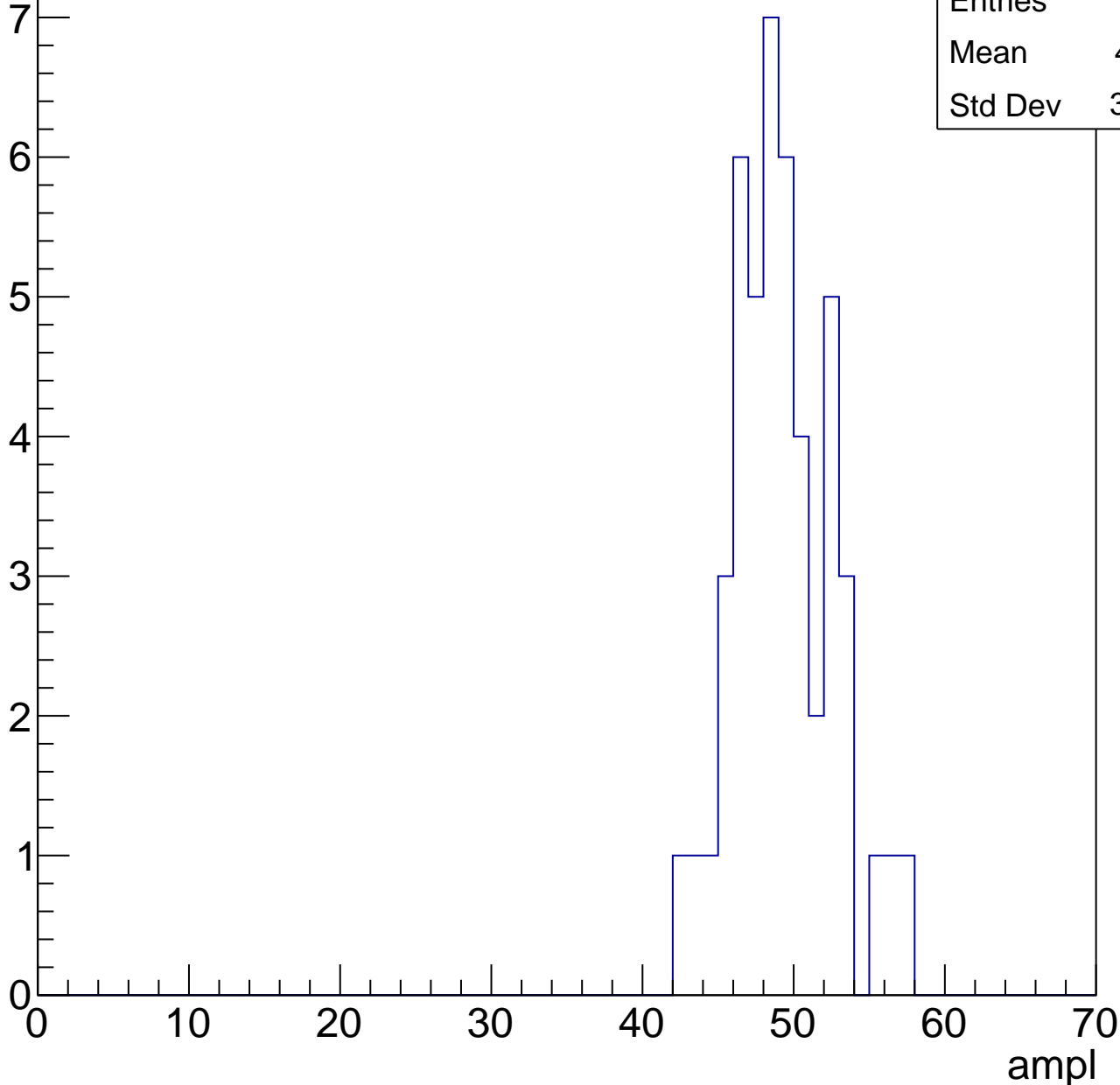


B1L103S, U24-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	48.81
Std Dev	3.233



B1L103S, U24-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	55.05
Std Dev	3.365

Entry

10

8

6

4

2

0

0

10

20

30

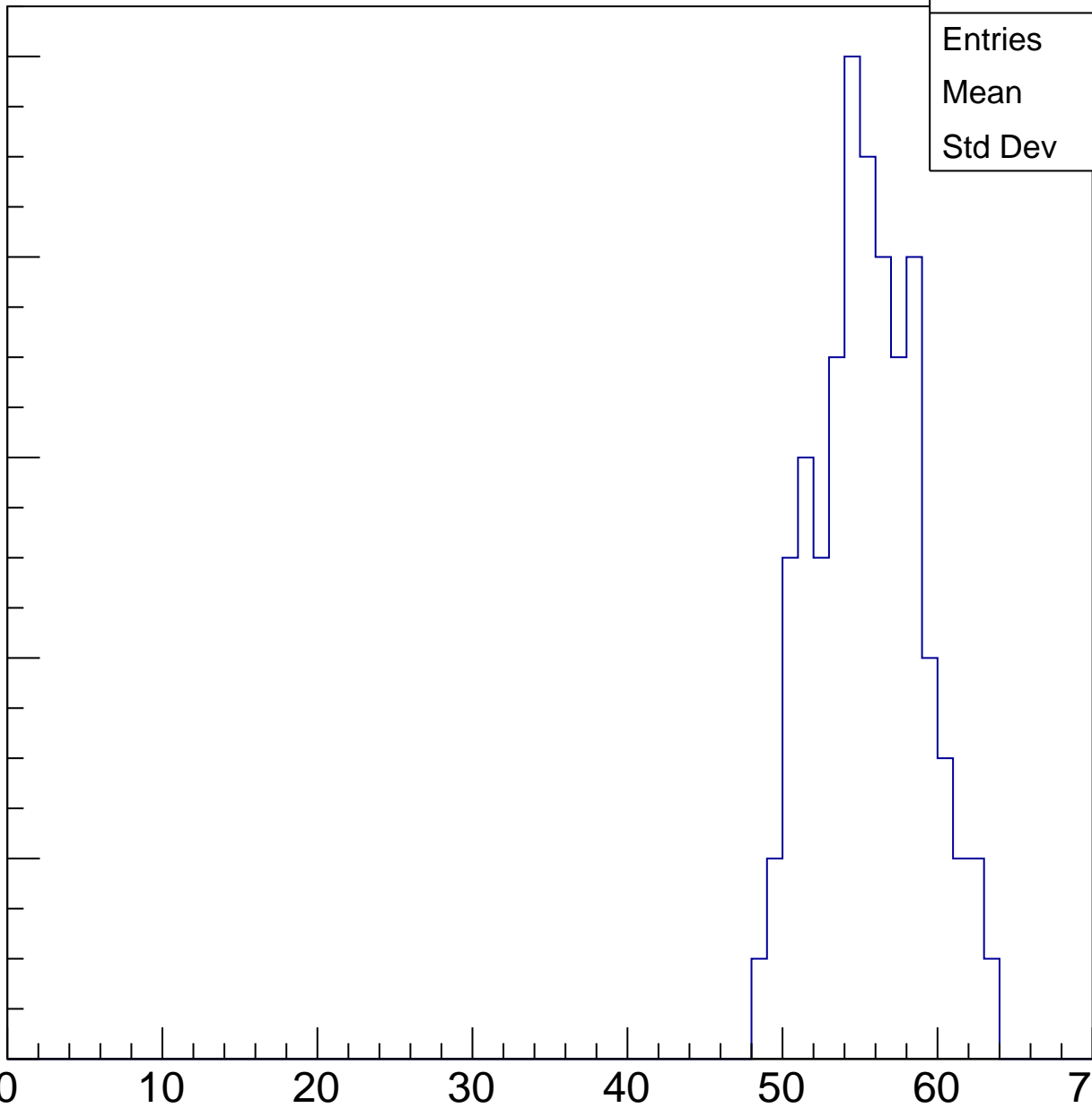
40

50

60

70

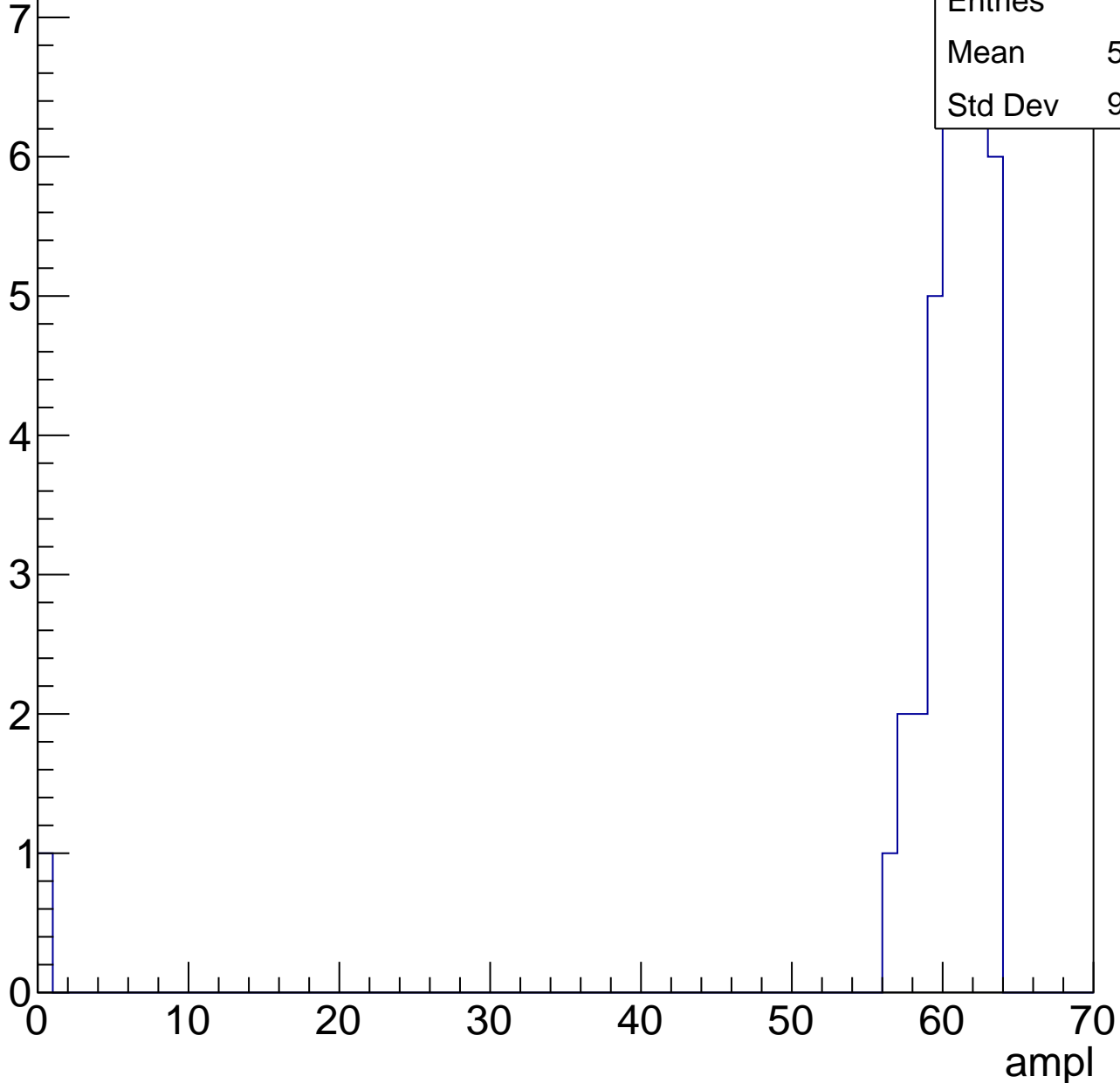
ampl



B1L103S, U24-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	38
Mean	58.95
Std Dev	9.859

B1L103S, U24-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch19, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	24.75
Std Dev	11.01

Gaus mean : 29.6473

Gaus Width: 3.6039

Entry

10

8

6

4

2

0

0

10

20

30

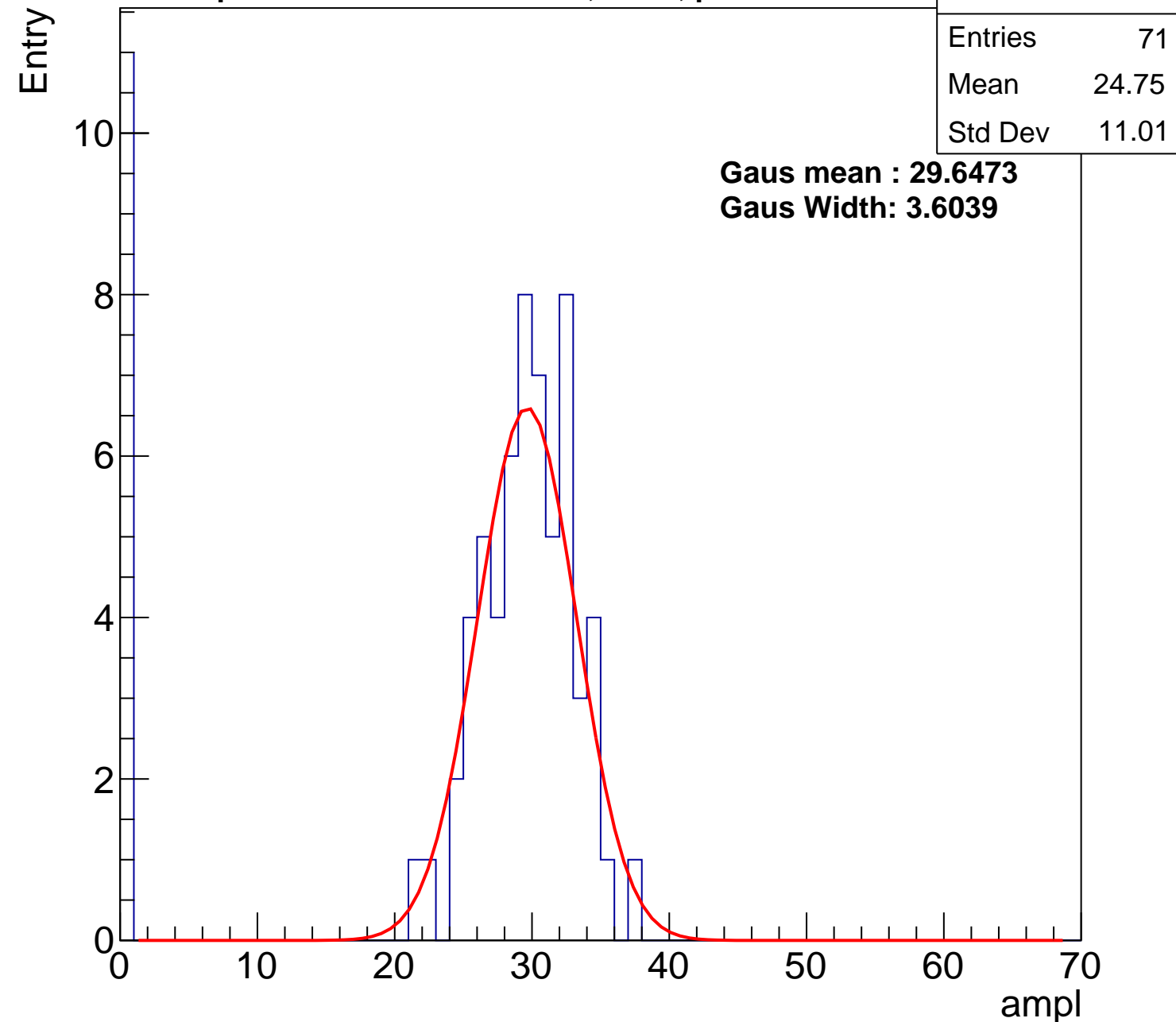
40

50

60

70

ampl



B1L103S, U24-ch19, adc1

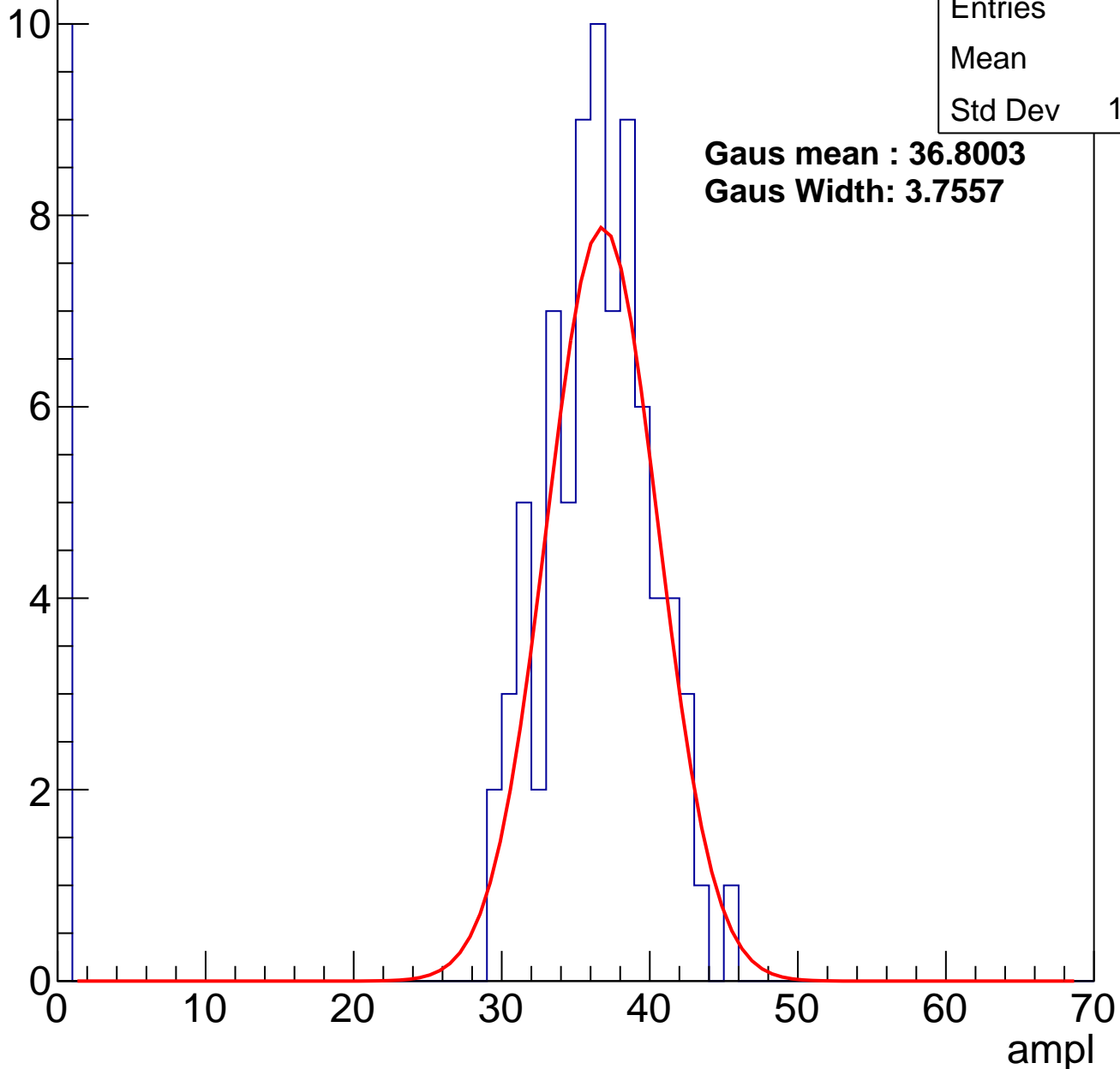
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	32
Std Dev	11.92

Gaus mean : 36.8003

Gaus Width: 3.7557

Entry



B1L103S, U24-ch19, adc2

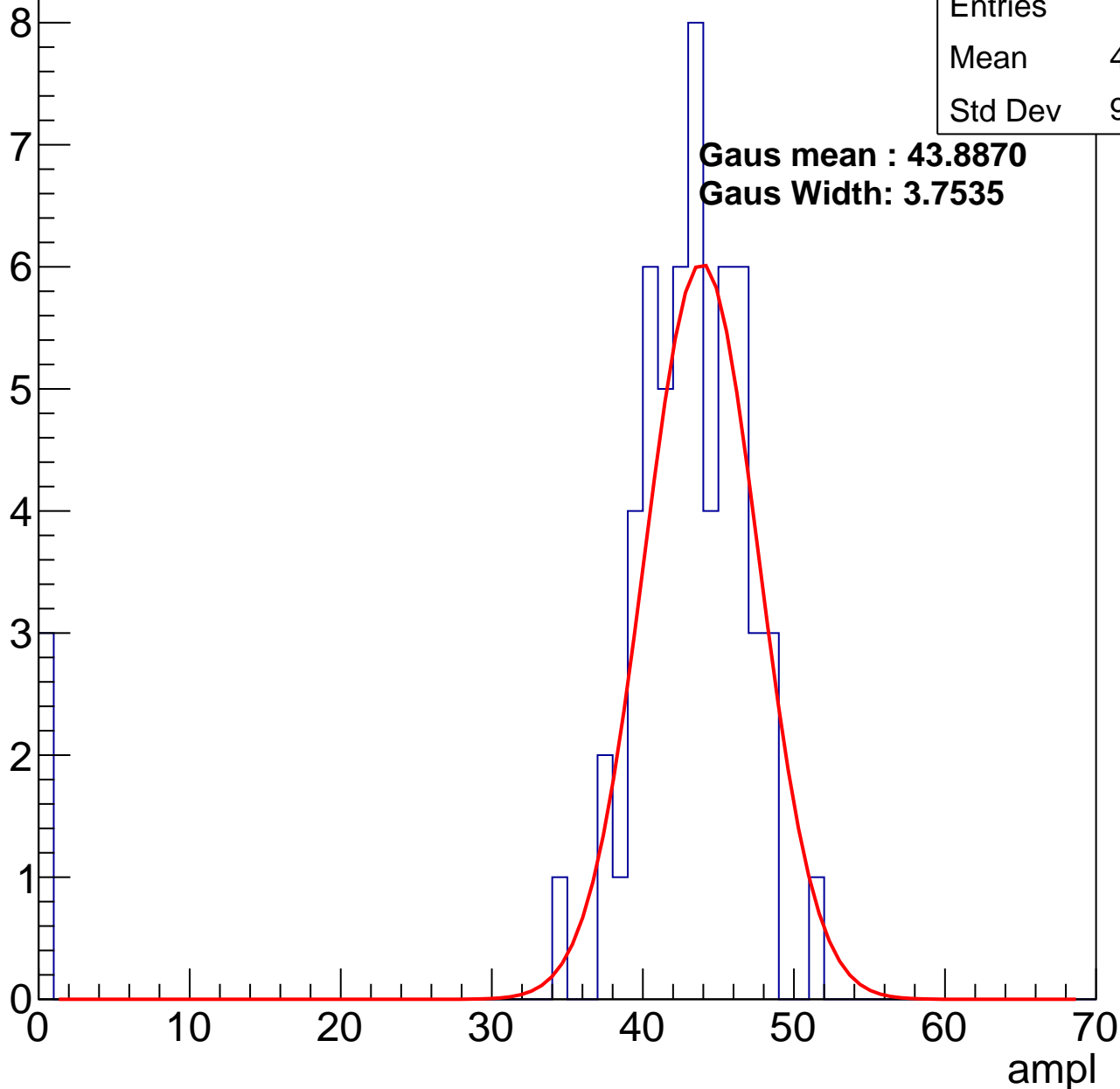
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	40.69
Std Dev	9.936

Gaus mean : 43.8870

Gaus Width: 3.7535

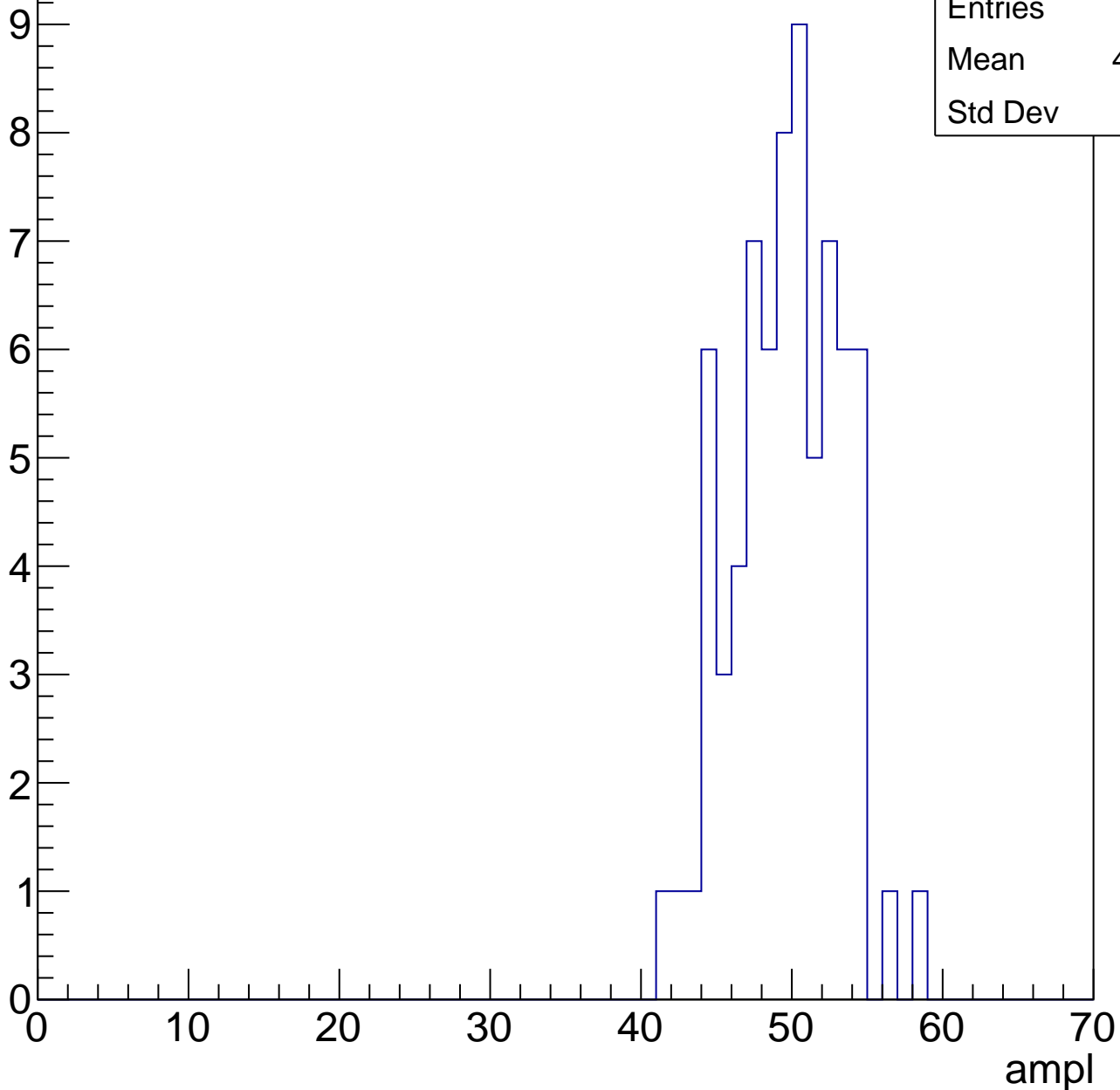


B1L103S, U24-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	49.21
Std Dev	3.5

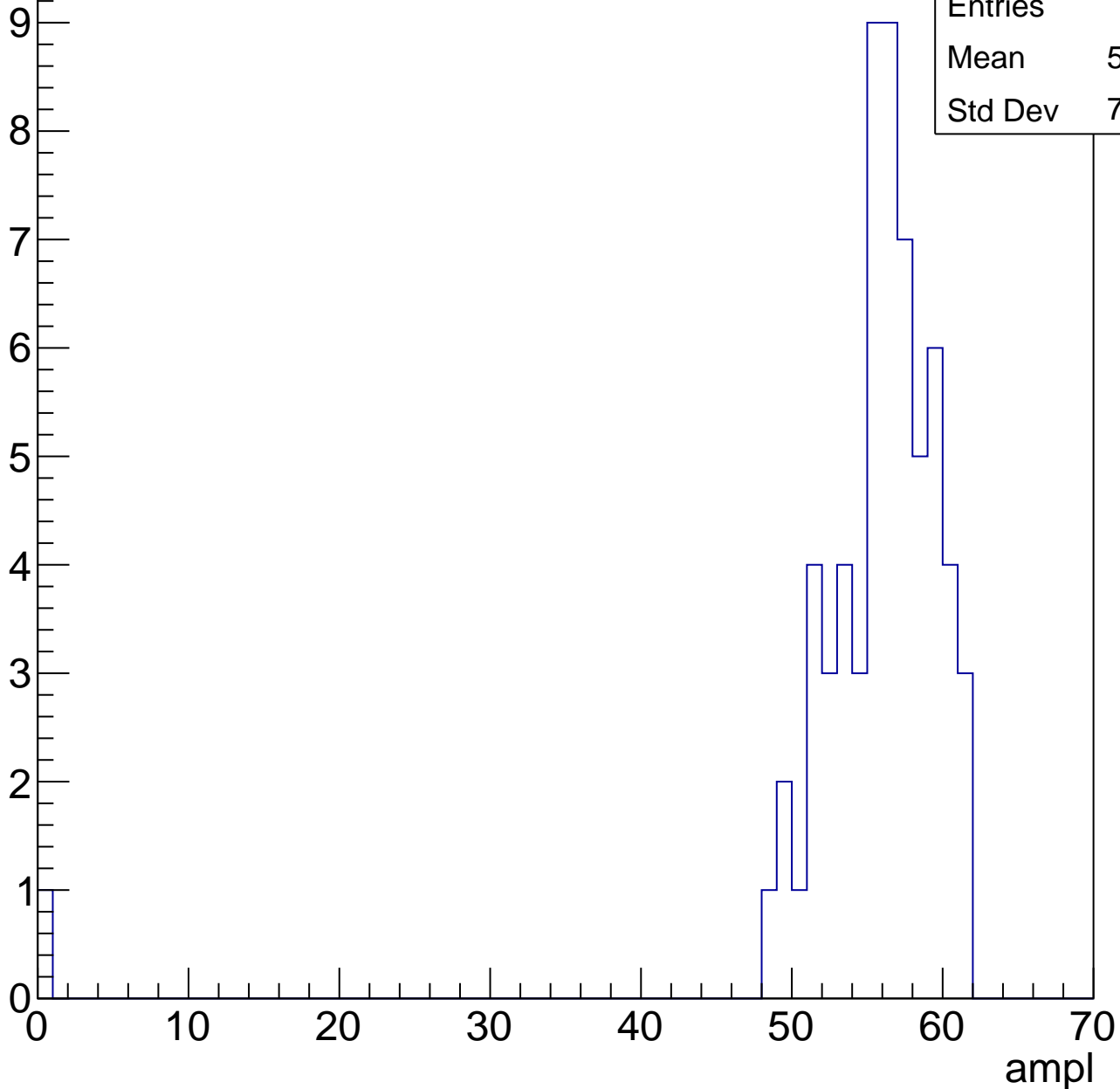


B1L103S, U24-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	54.76
Std Dev	7.687

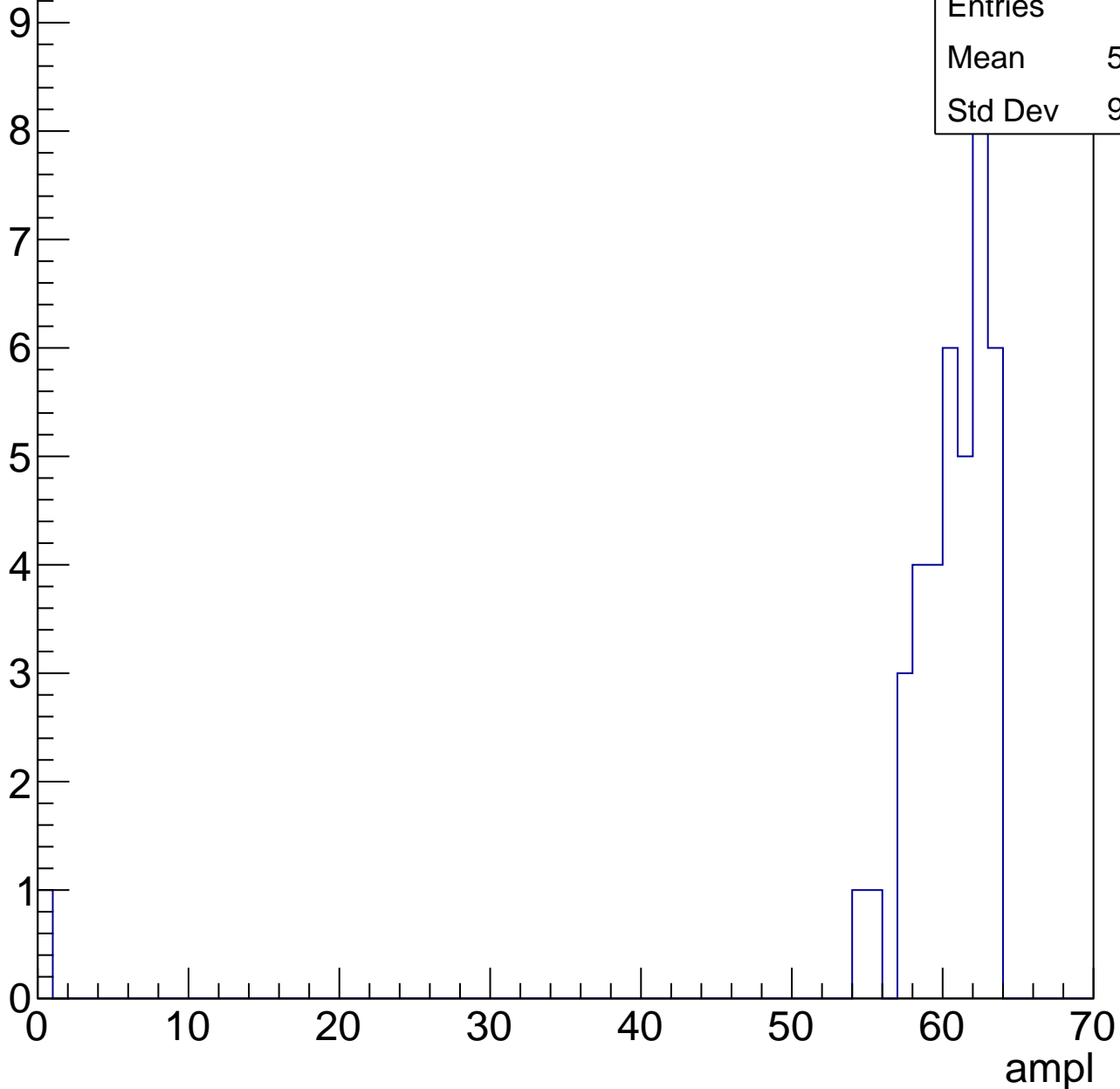


B1L103S, U24-ch19, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

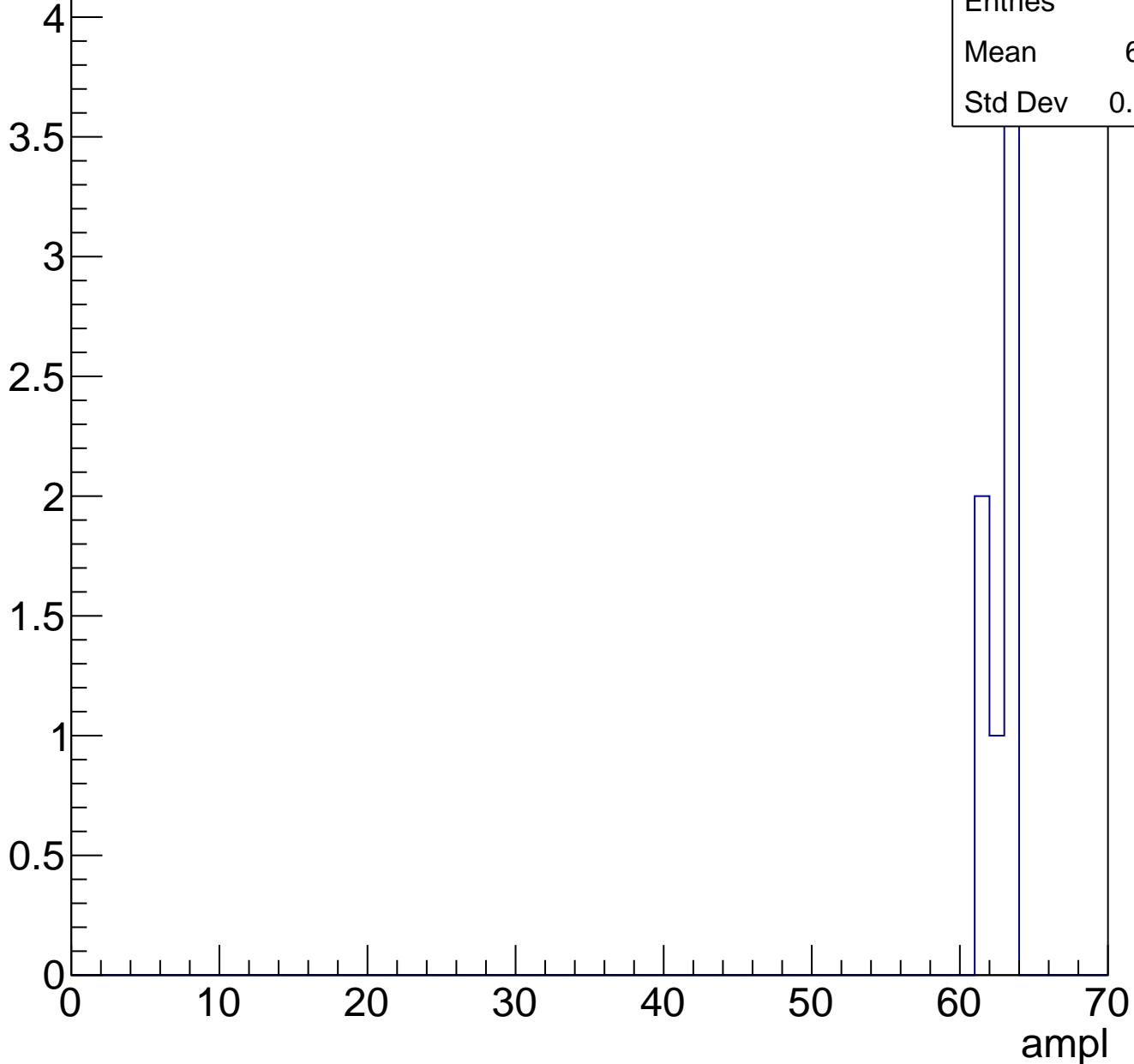
Entries	40
Mean	58.73
Std Dev	9.667



B1L103S, U24-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch20, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	24.37
Std Dev	10.2

Gaus mean : 28.8013

Gaus Width: 3.8423

Entry

10

8

6

4

2

0

0

10

20

30

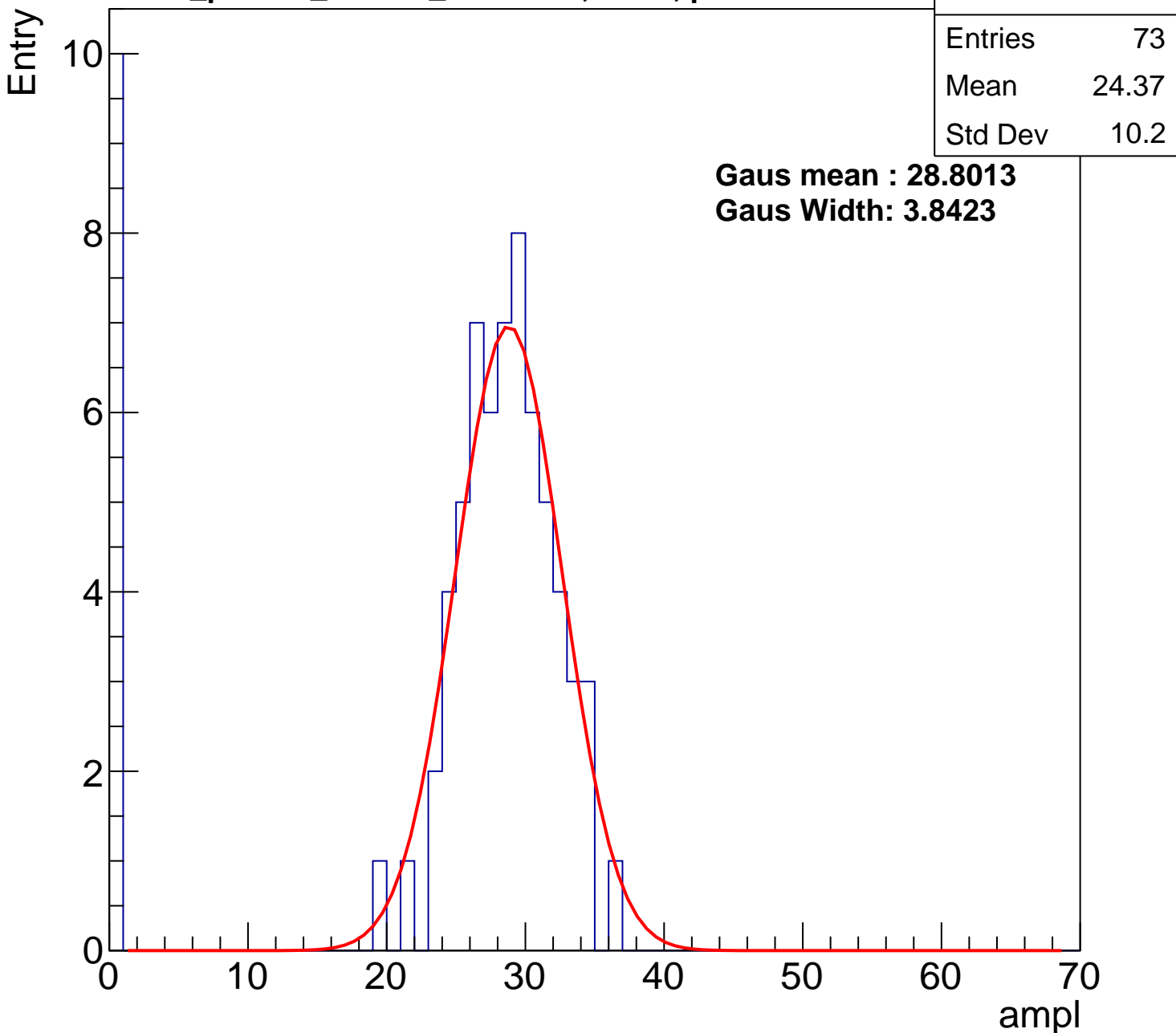
40

50

60

70

ampl



B1L103S, U24-ch20, adc1

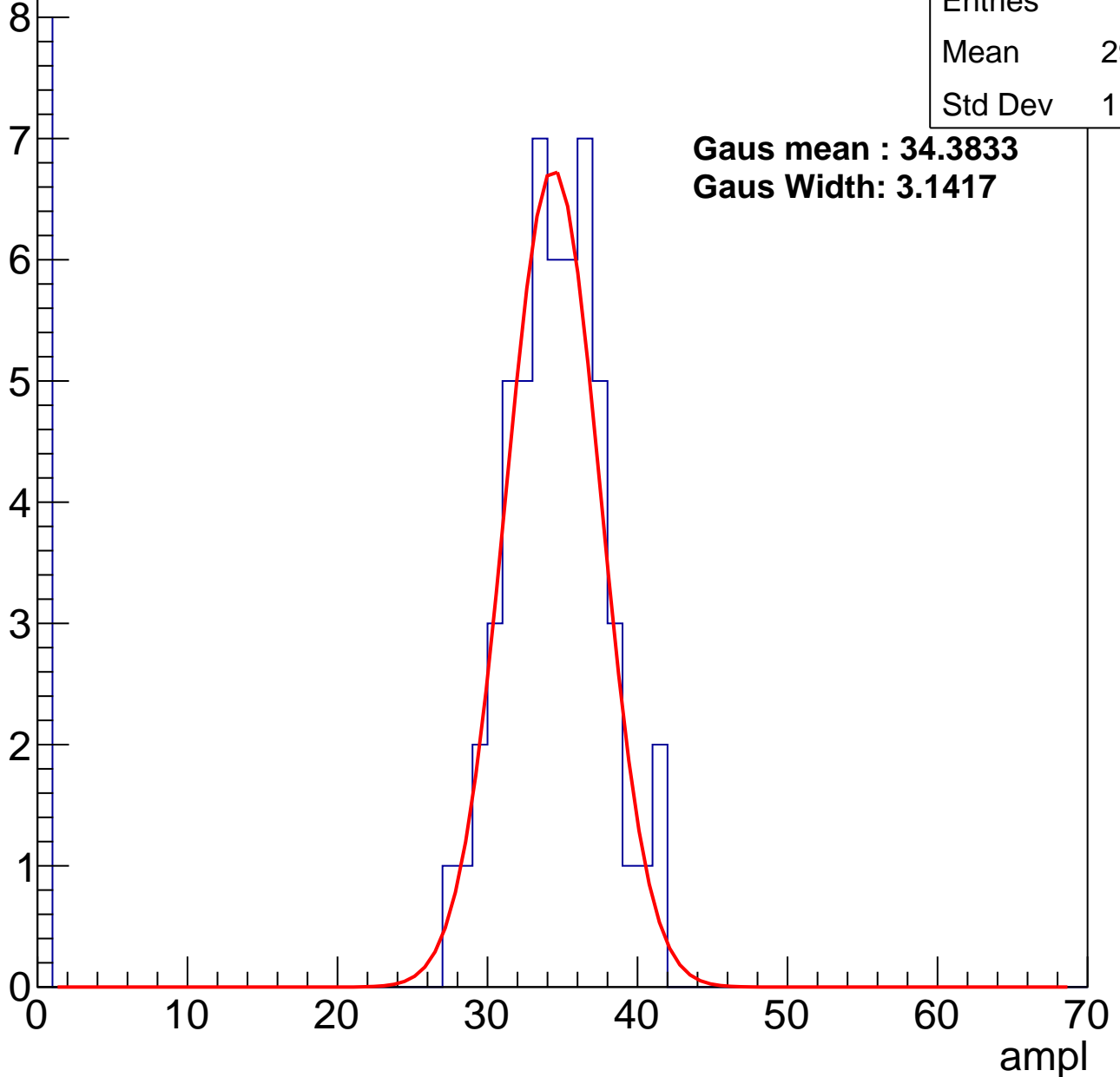
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	29.76
Std Dev	11.72

Gaus mean : 34.3833

Gaus Width: 3.1417



B1L103S, U24-ch20, adc2

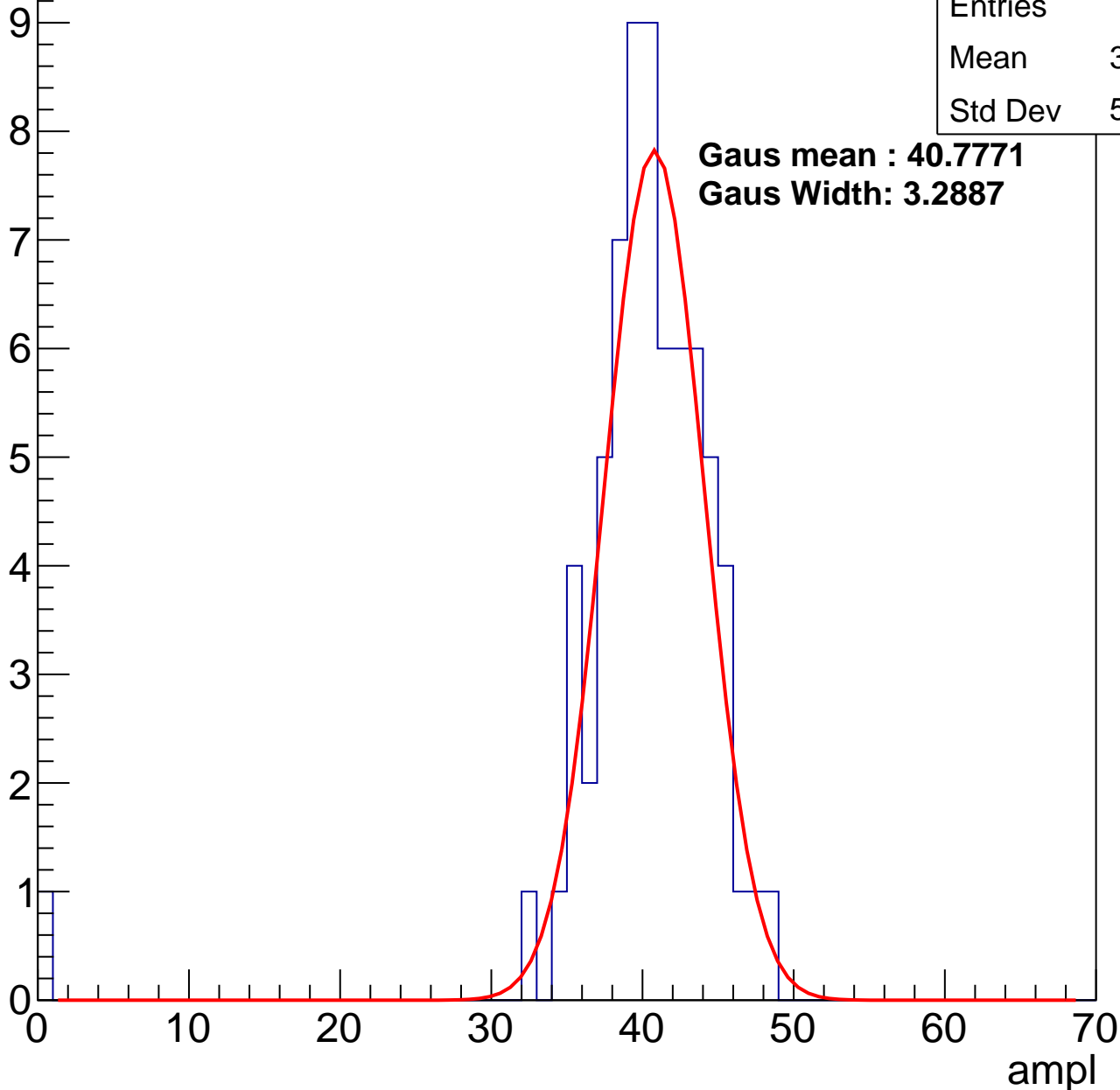
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	39.67
Std Dev	5.798

Gaus mean : 40.7771

Gaus Width: 3.2887

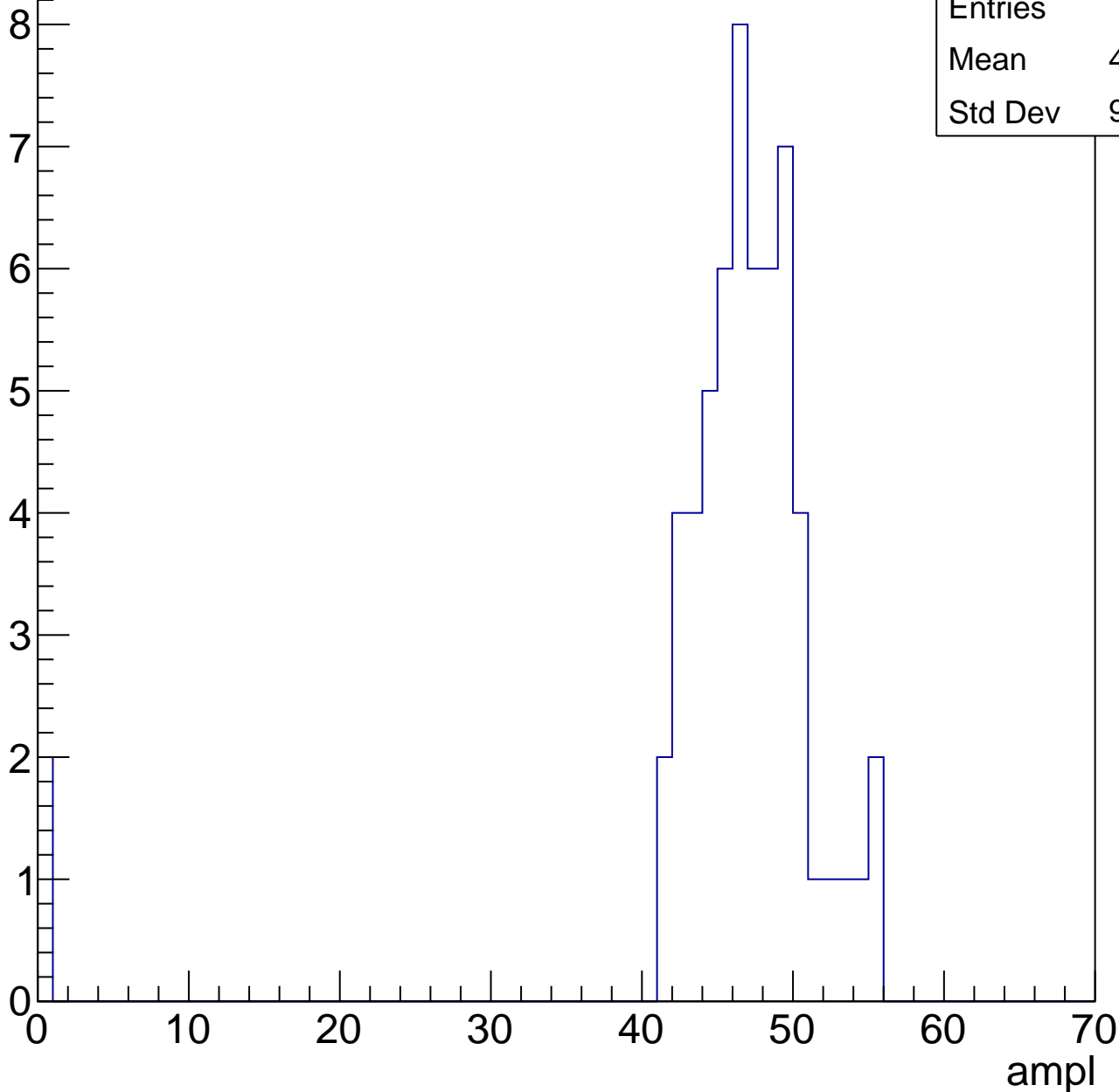


B1L103S, U24-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	45.22
Std Dev	9.004

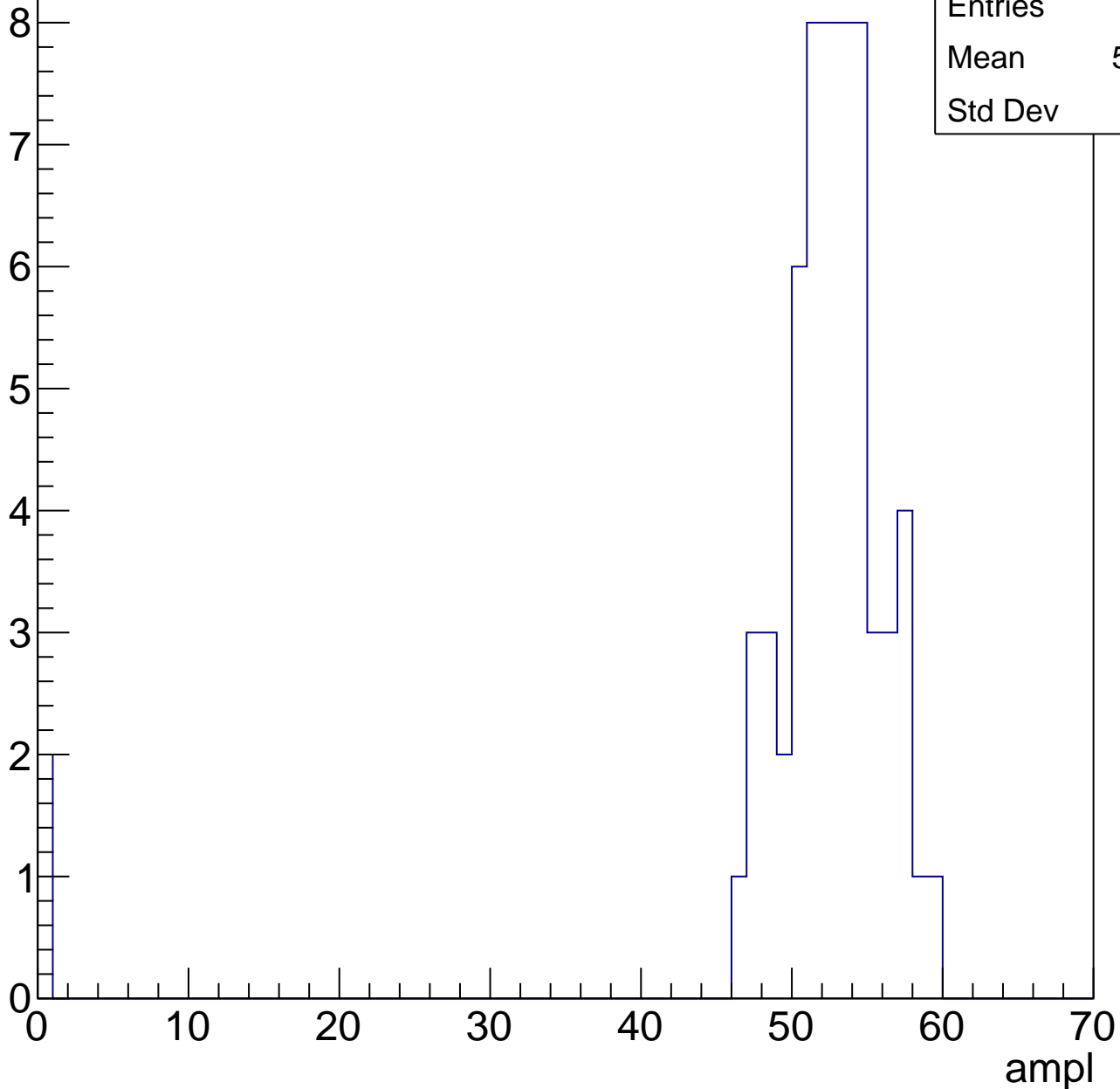


B1L103S, U24-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	50.61
Std Dev	9.75



B1L103S, U24-ch20, adc5

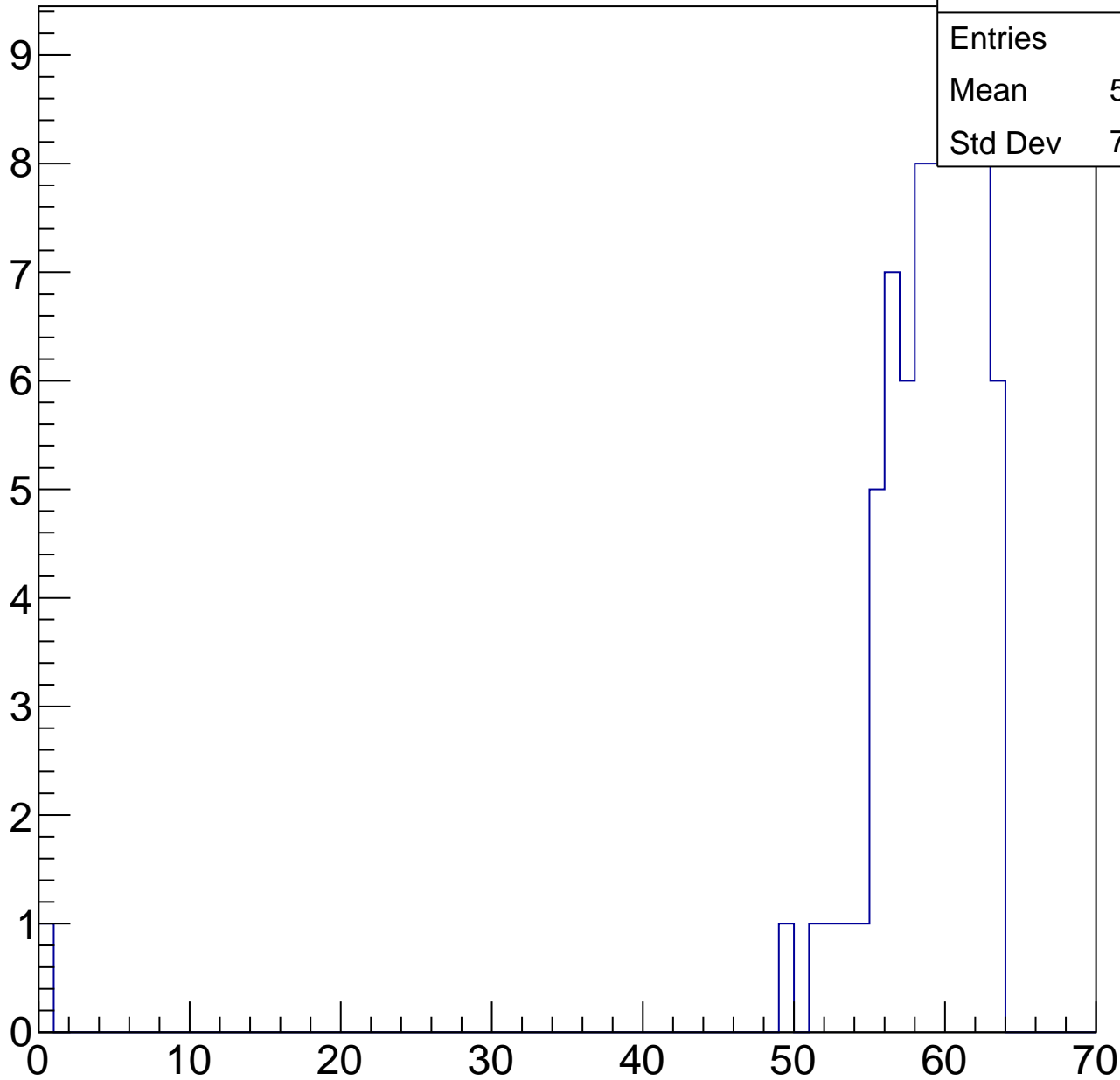
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	71
Mean	57.83
Std Dev	7.545

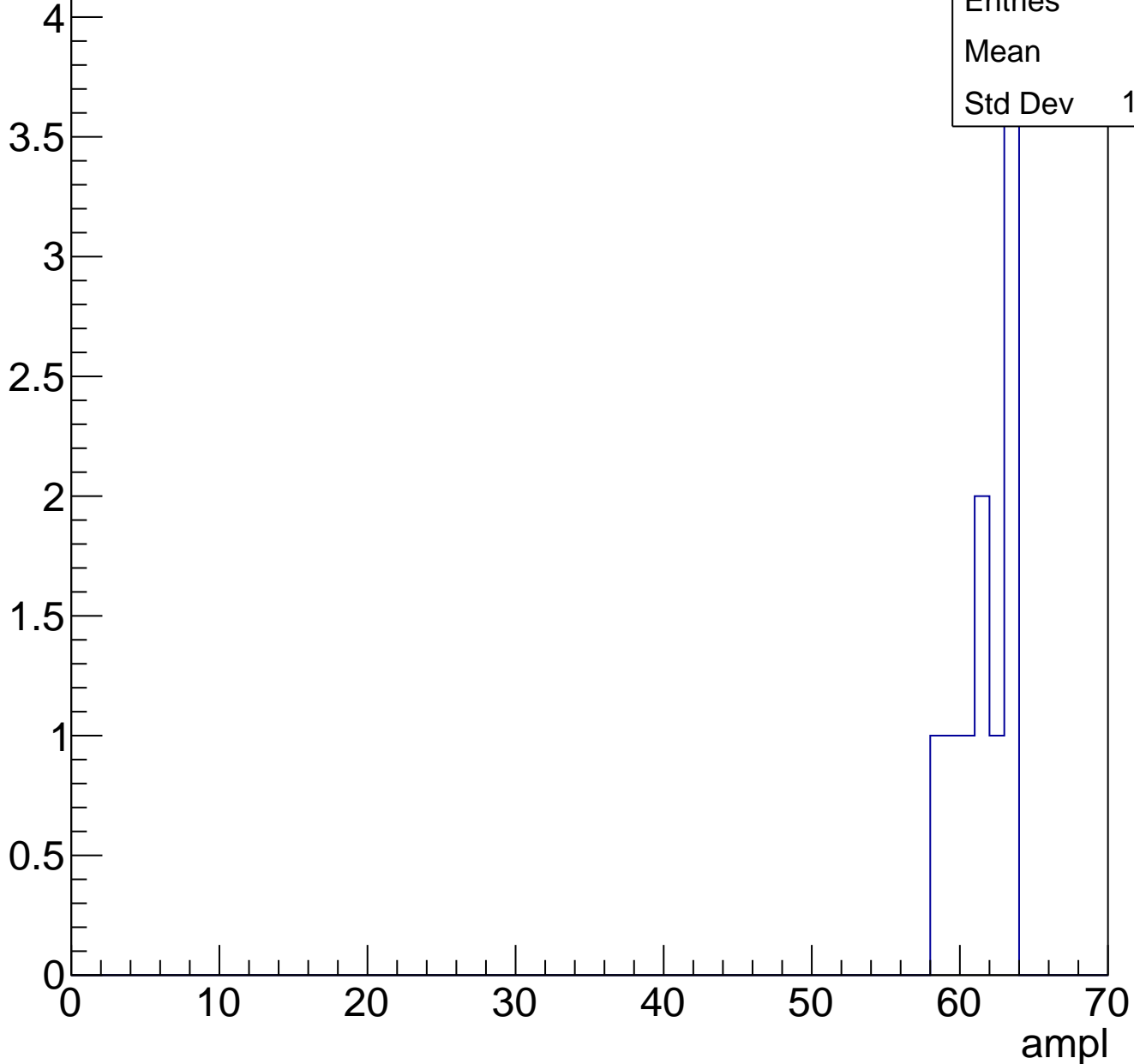
ampl



B1L103S, U24-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	10
Mean	61.3
Std Dev	1.735

B1L103S, U24-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U24-ch21, adc0

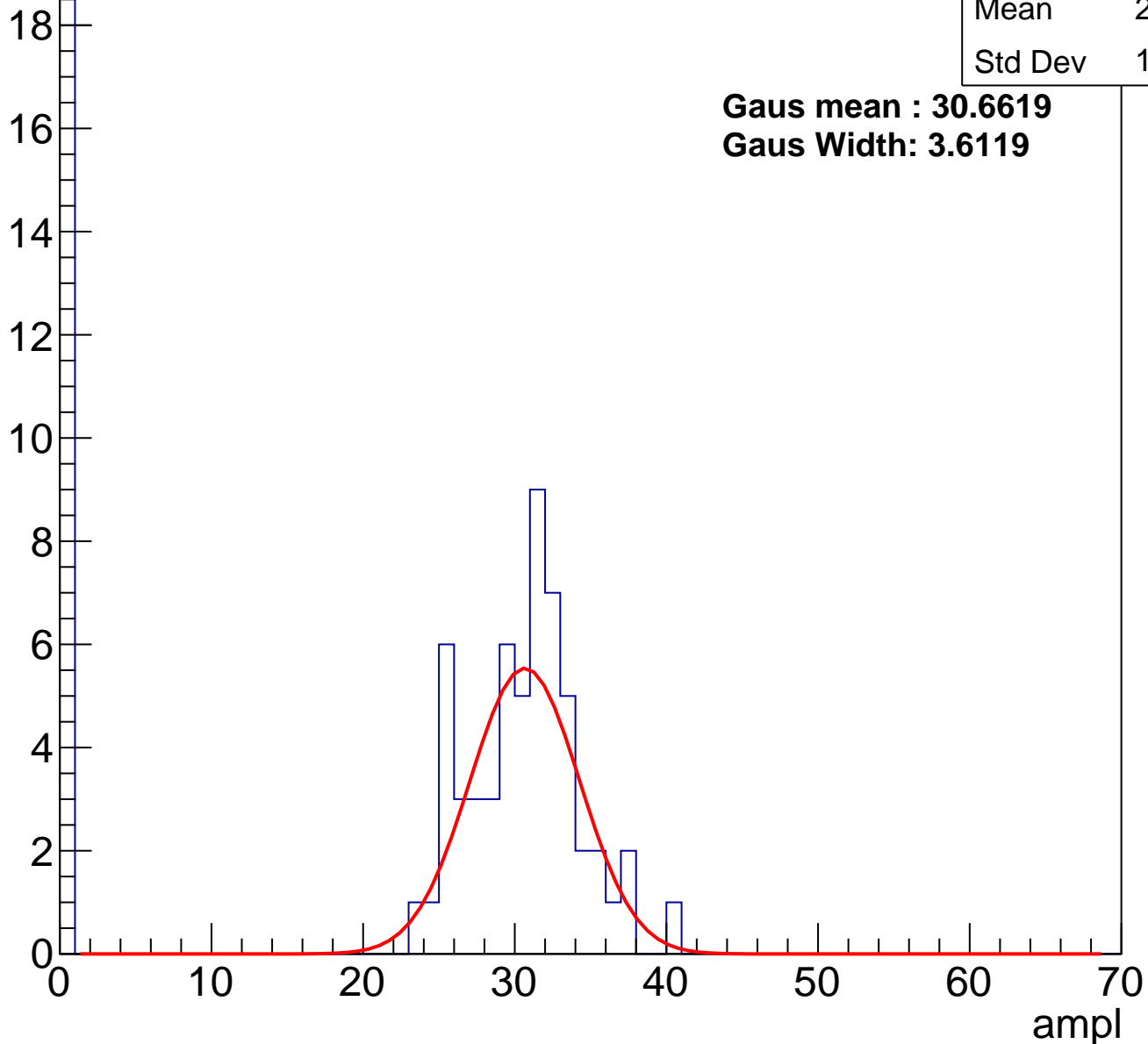
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	22.63
Std Dev	13.43

Gaus mean : 30.6619

Gaus Width: 3.6119

Entry



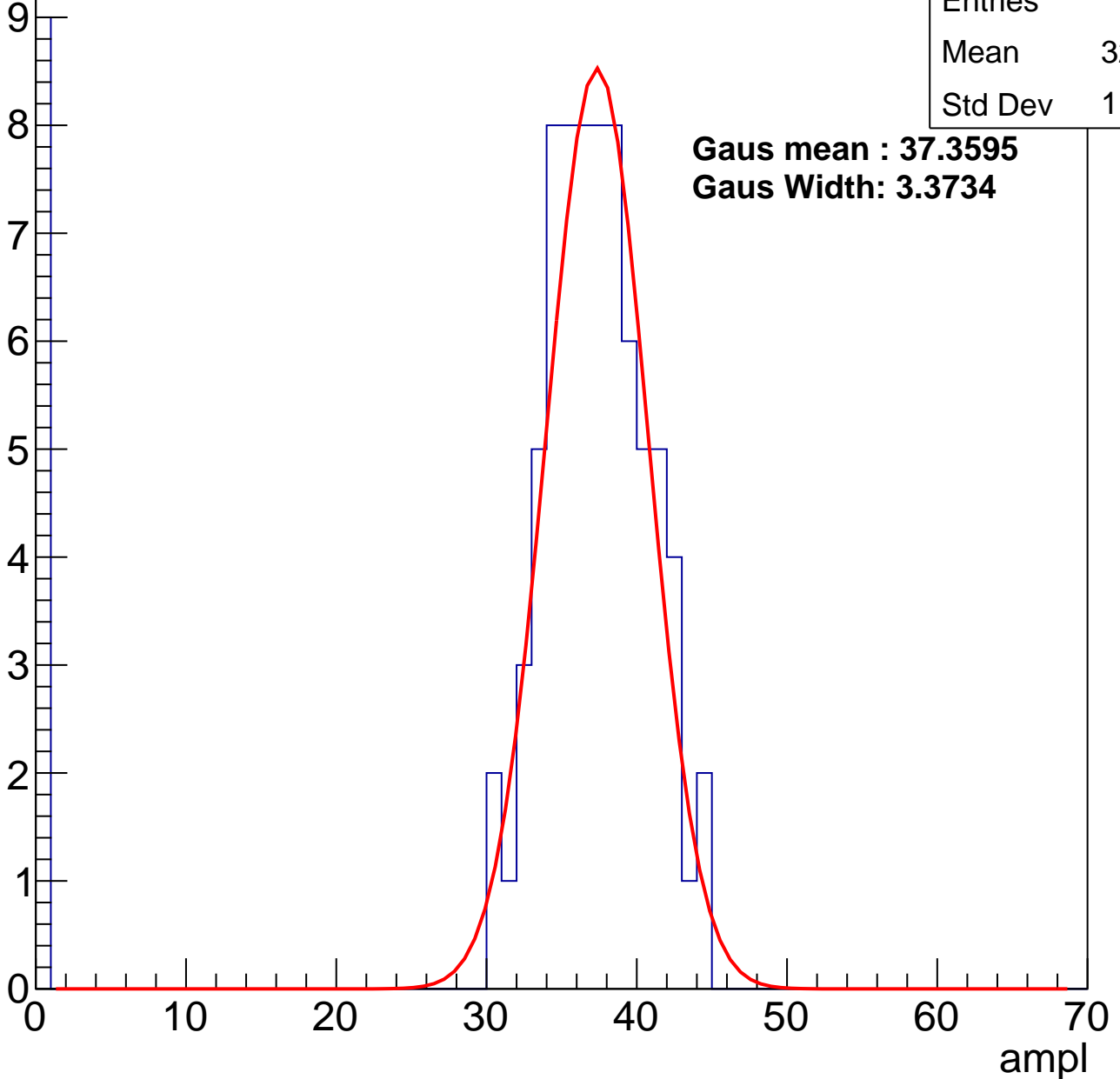
B1L103S, U24-ch21, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	32.89
Std Dev	11.88

Gaus mean : 37.3595
Gaus Width: 3.3734



B1L103S, U24-ch21, adc2

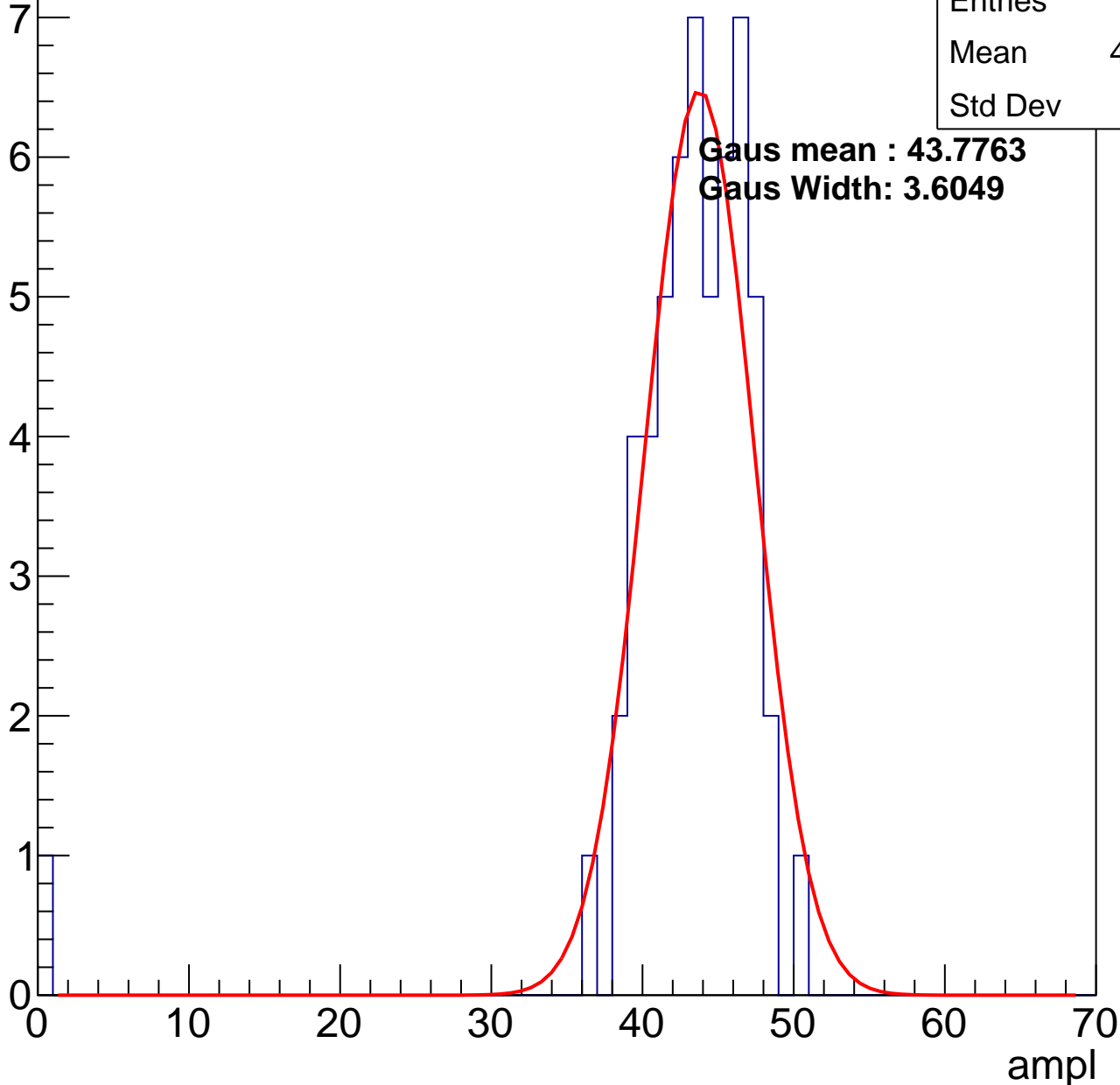
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	42.48
Std Dev	6.45

Gaus mean : 43.7763

Gaus Width: 3.6049

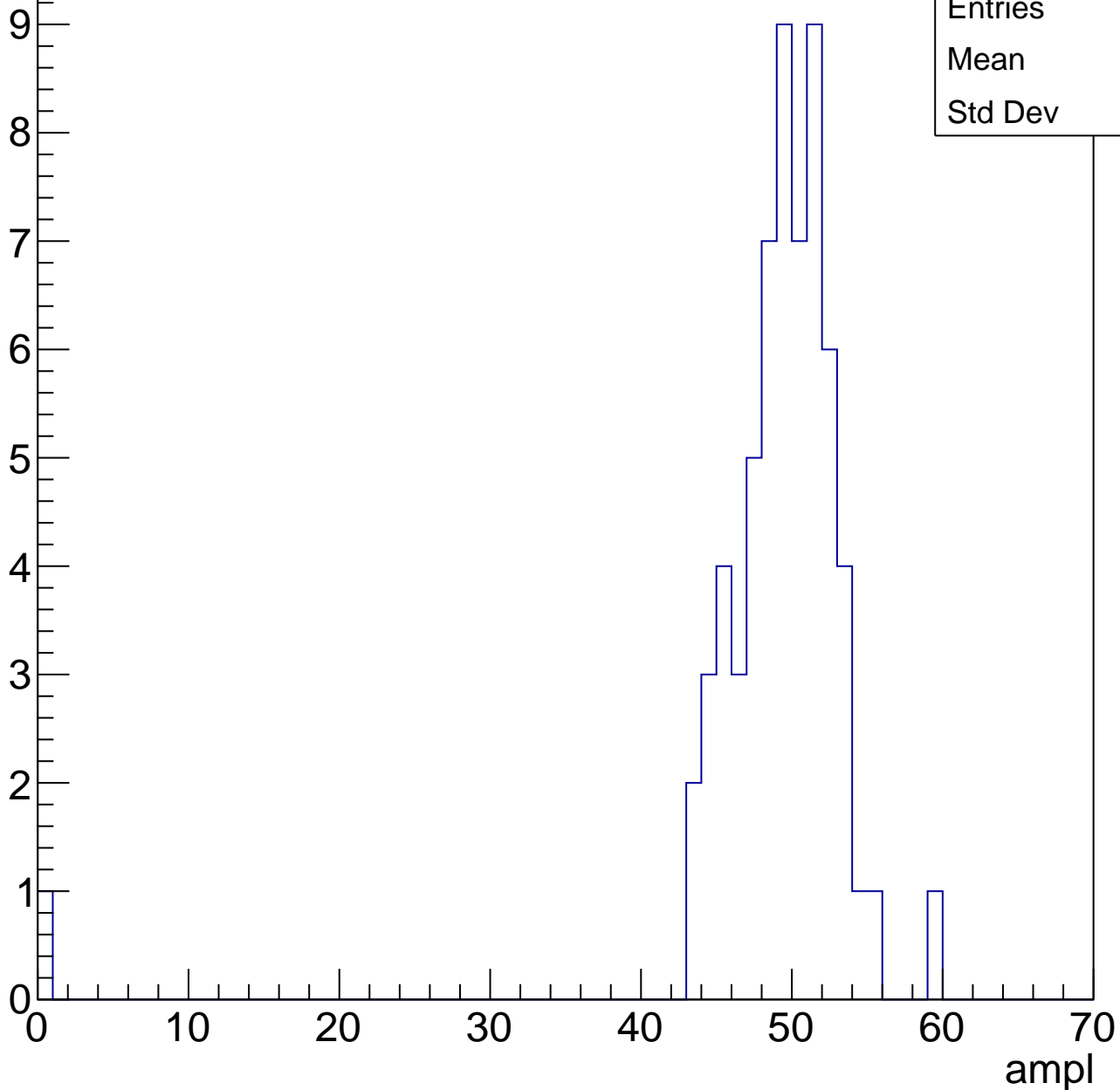


B1L103S, U24-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	48.4
Std Dev	6.86

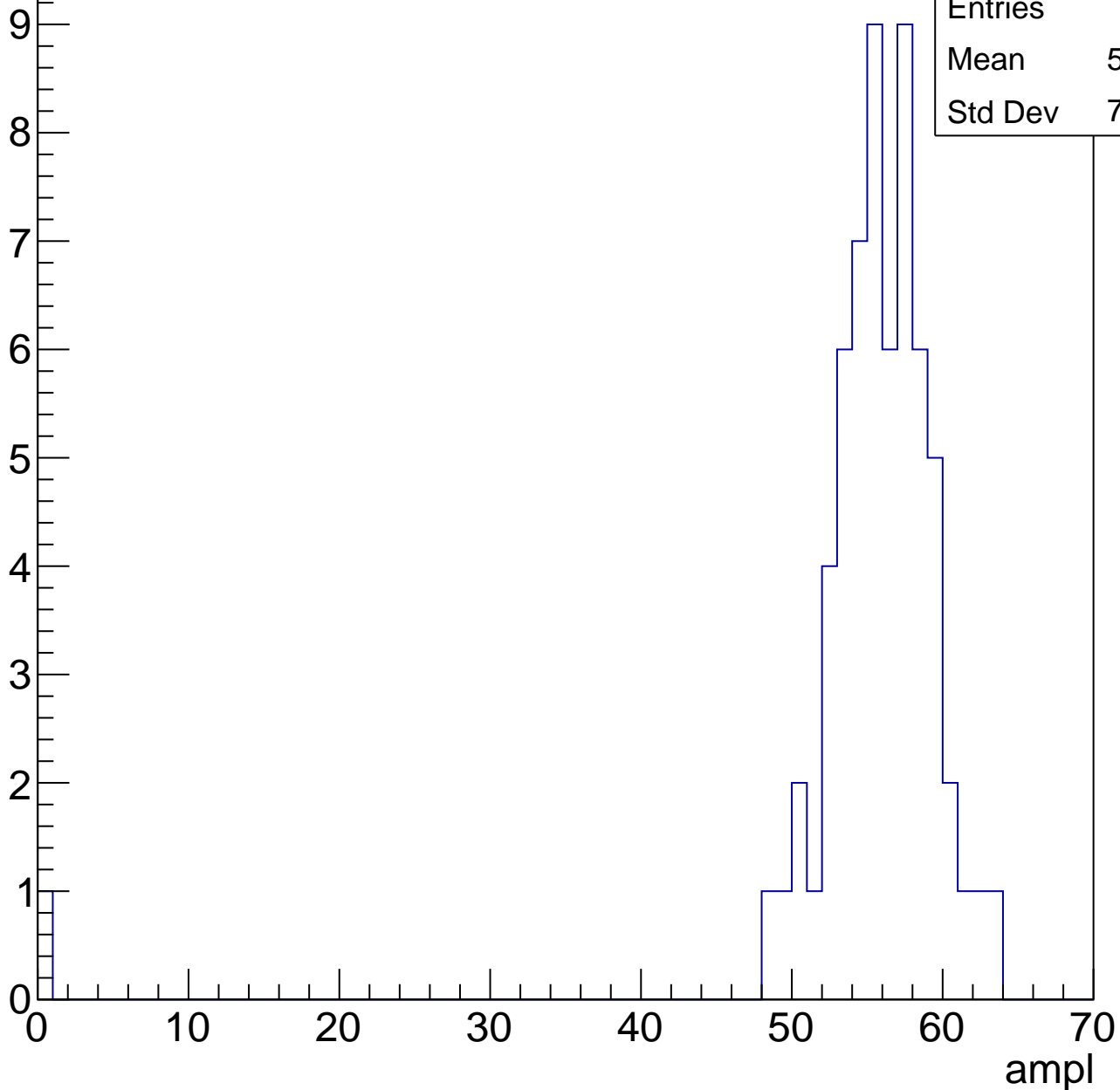


B1L103S, U24-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	54.68
Std Dev	7.572



B1L103S, U24-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

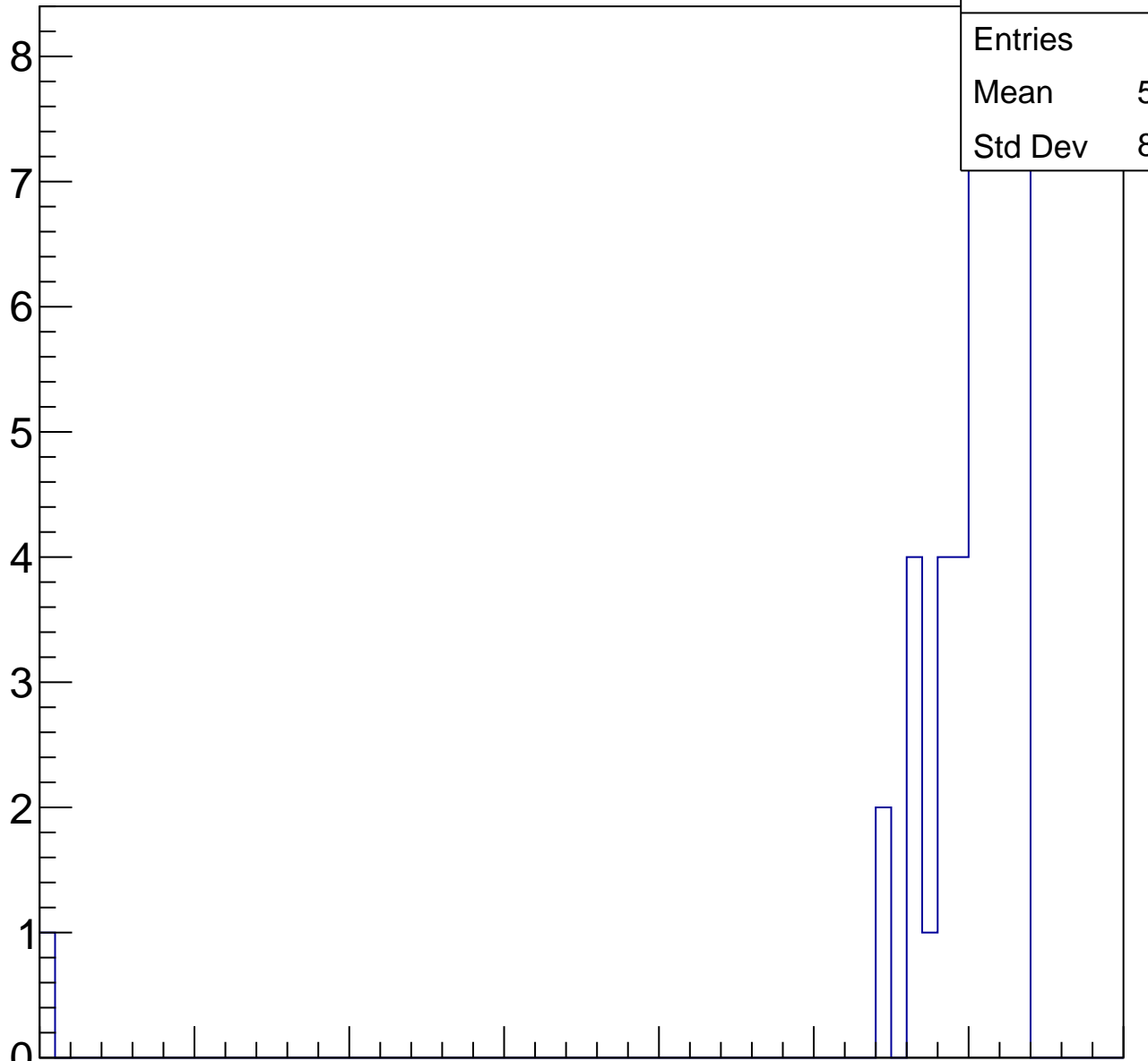
Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.85
Std Dev	8.914

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch22, adc0

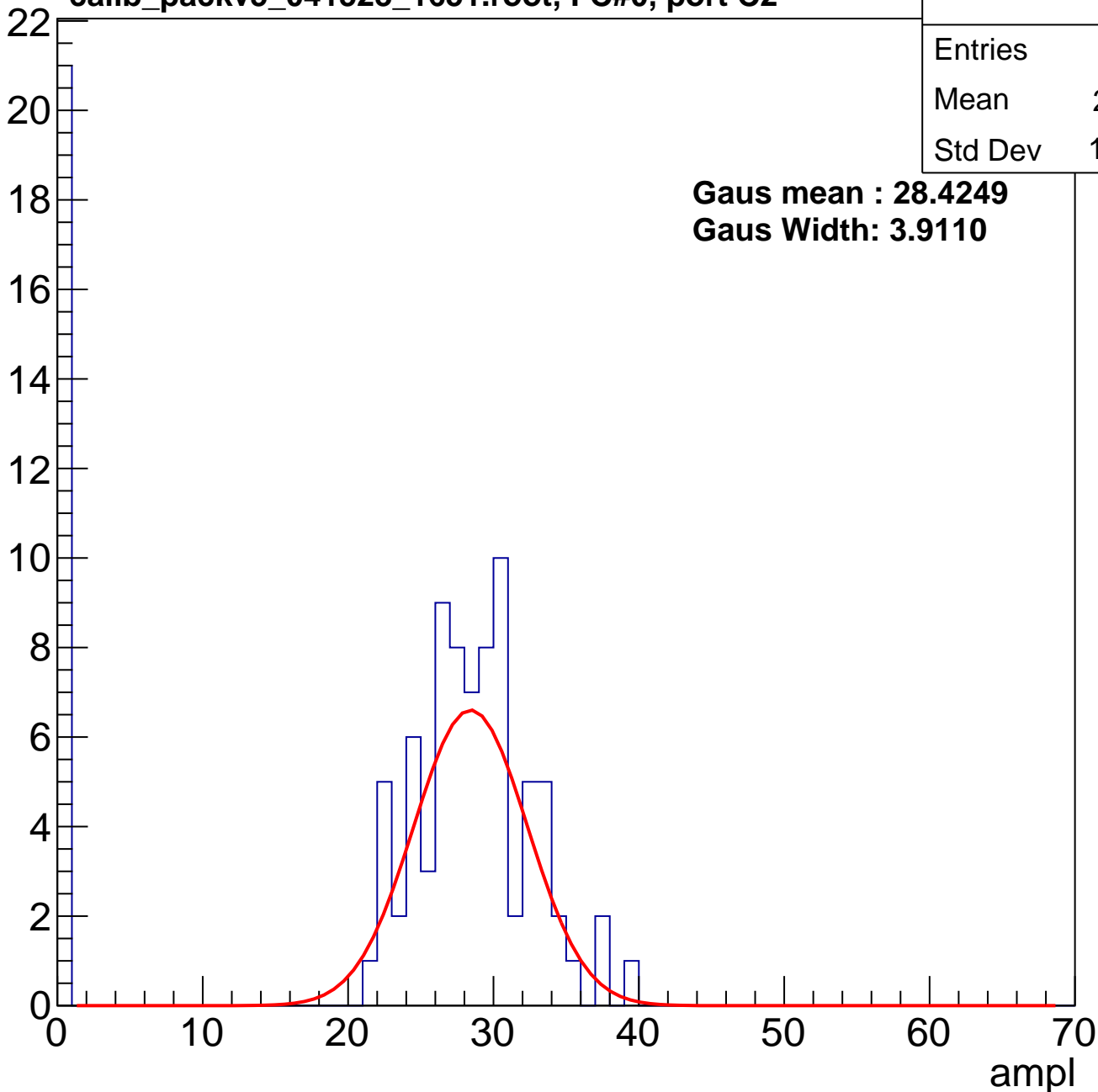
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	22.21
Std Dev	12.08

Gaus mean : 28.4249

Gaus Width: 3.9110

Entry



B1L103S, U24-ch22, adc1

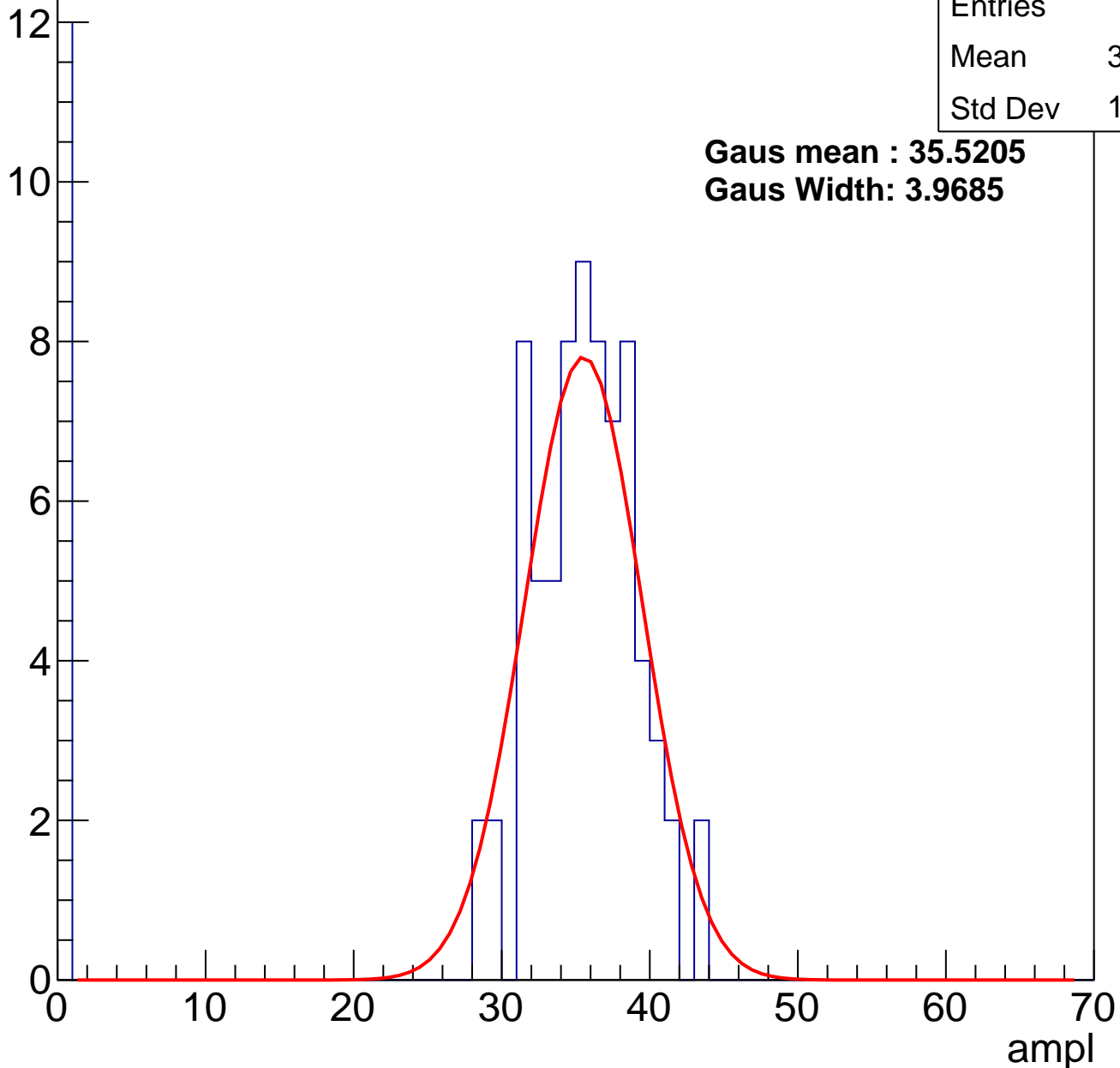
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	30.22
Std Dev	12.64

Gaus mean : 35.5205

Gaus Width: 3.9685

Entry



B1L103S, U24-ch22, adc2

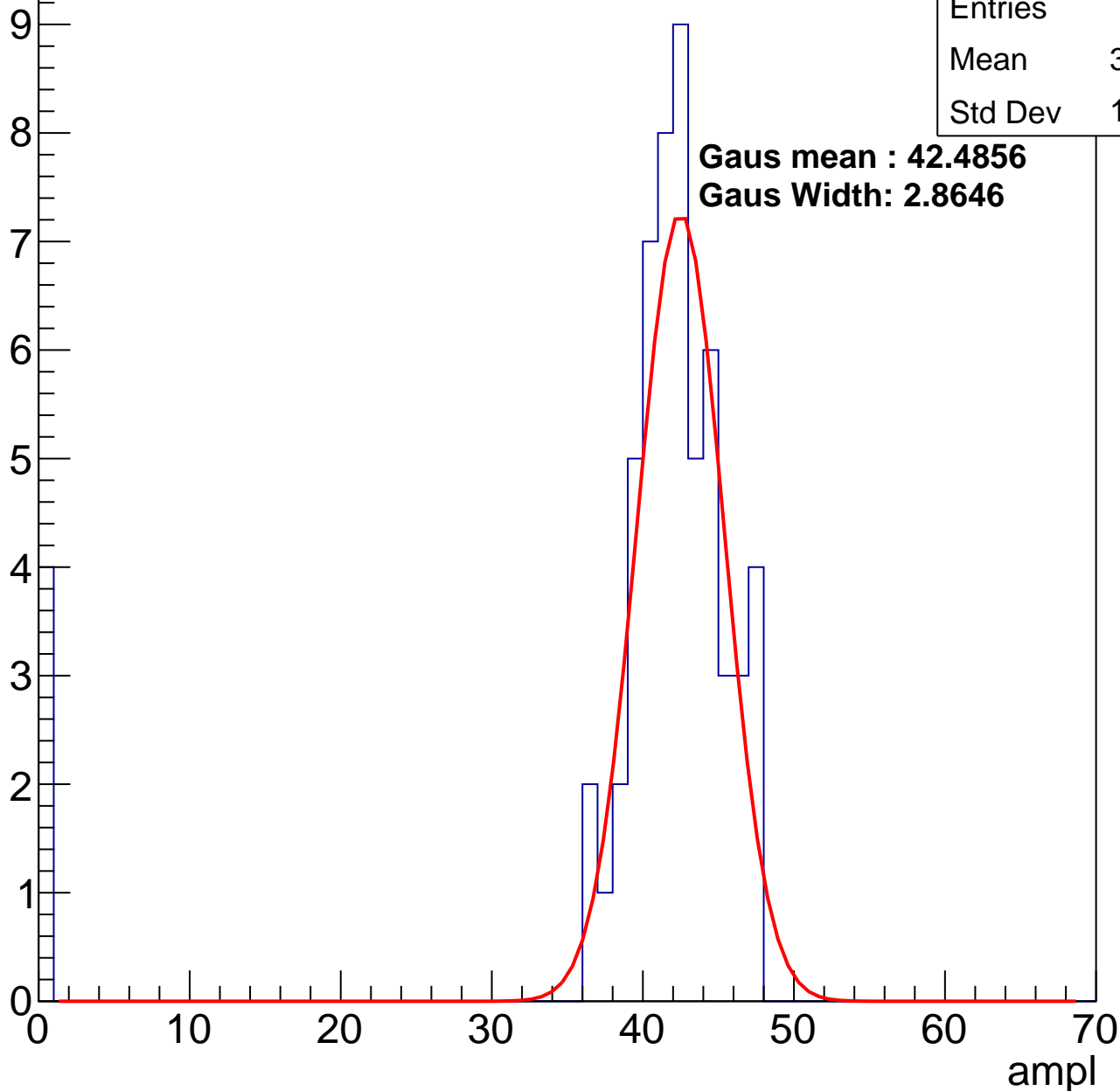
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	39.08
Std Dev	10.87

Gaus mean : 42.4856

Gaus Width: 2.8646

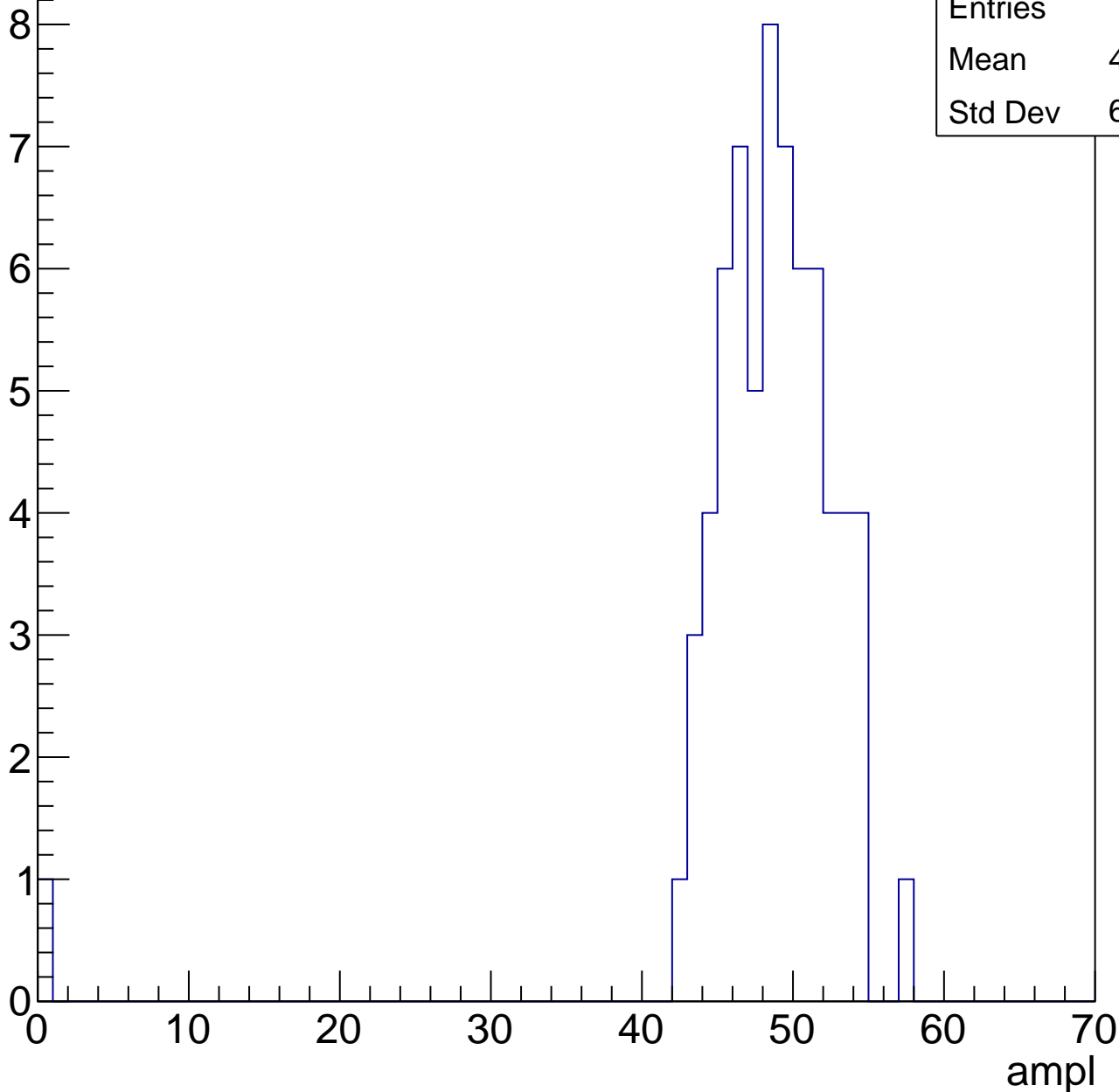


B1L103S, U24-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	47.76
Std Dev	6.732

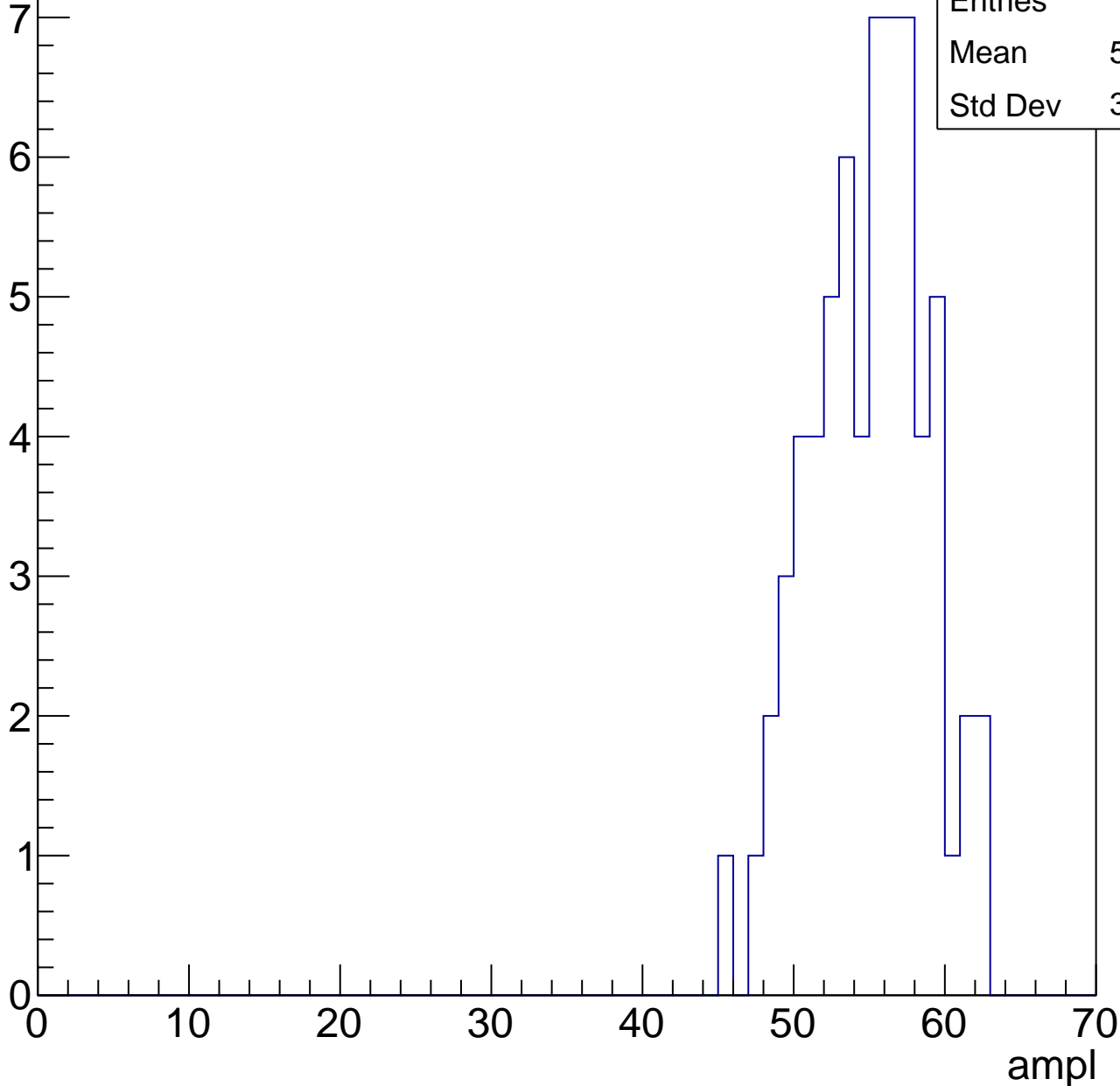


B1L103S, U24-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	54.49
Std Dev	3.795

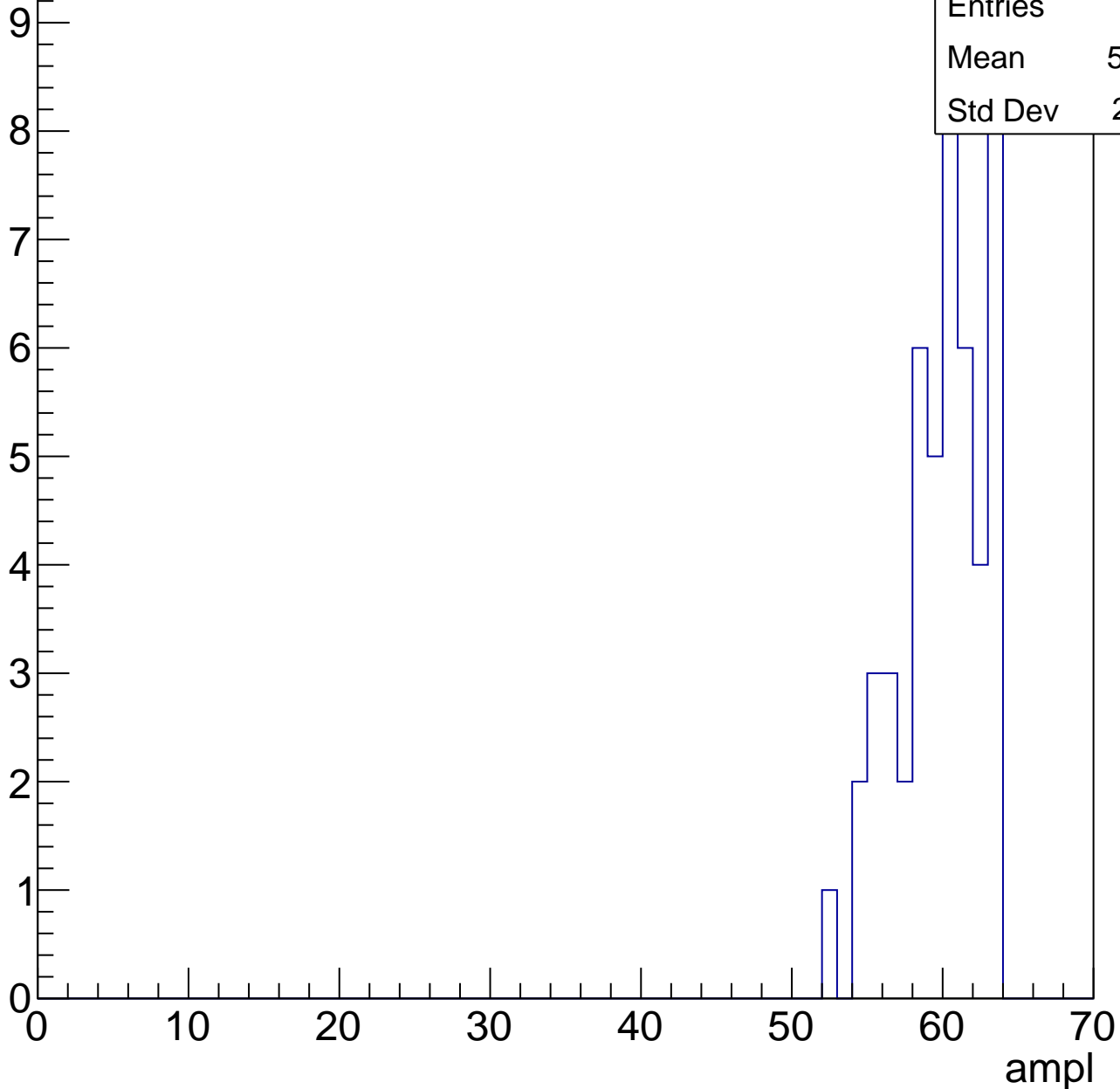


B1L103S, U24-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

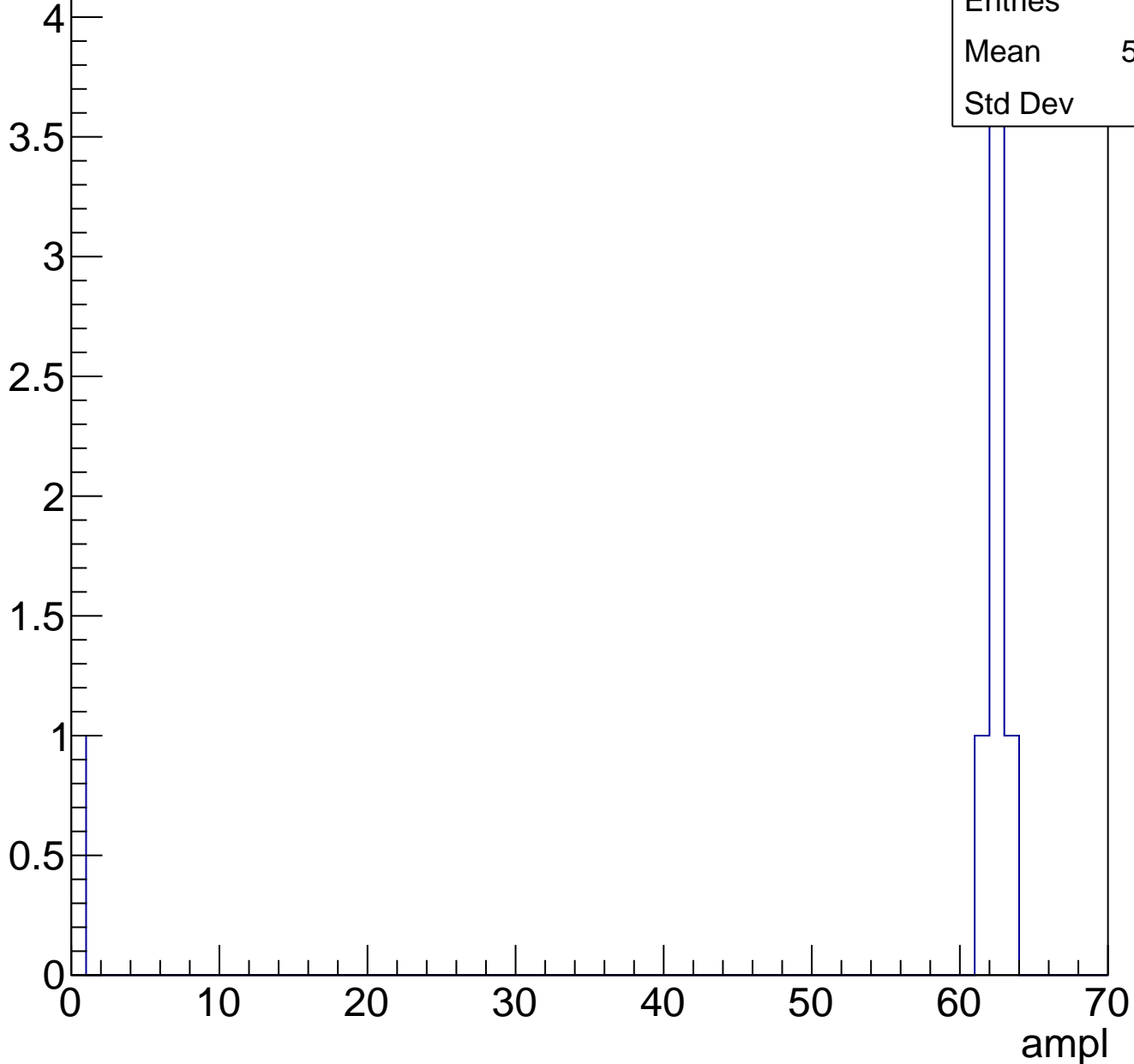
Entries	50
Mean	59.42
Std Dev	2.801



B1L103S, U24-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

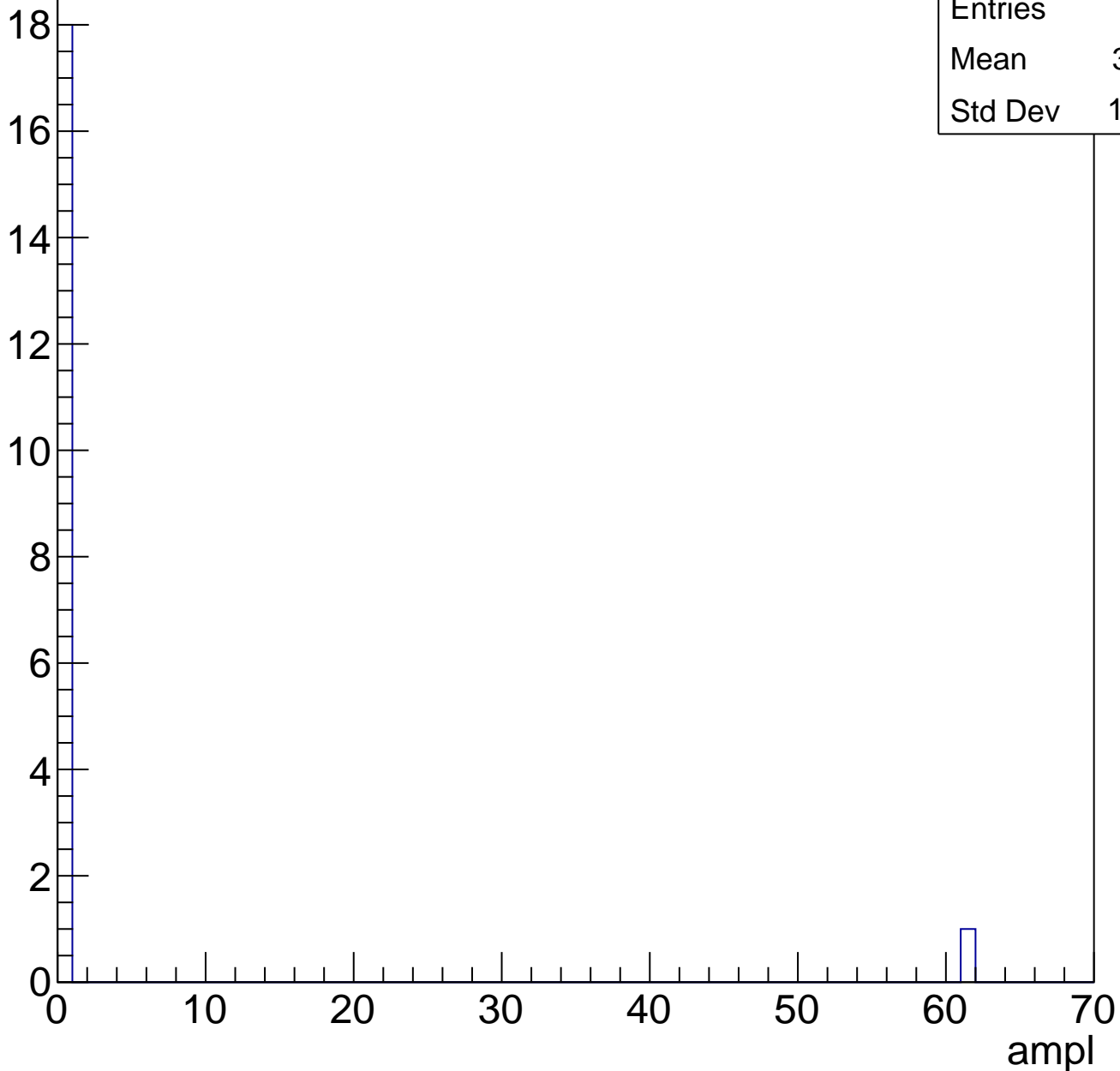


B1L103S, U24-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.211
Std Dev	13.62

Entry



B1L103S, U24-ch23, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	25.24
Std Dev	10.4

Gaus mean : 29.5052

Gaus Width: 3.3501

Entry

10

8

6

4

2

0

0

10

20

30

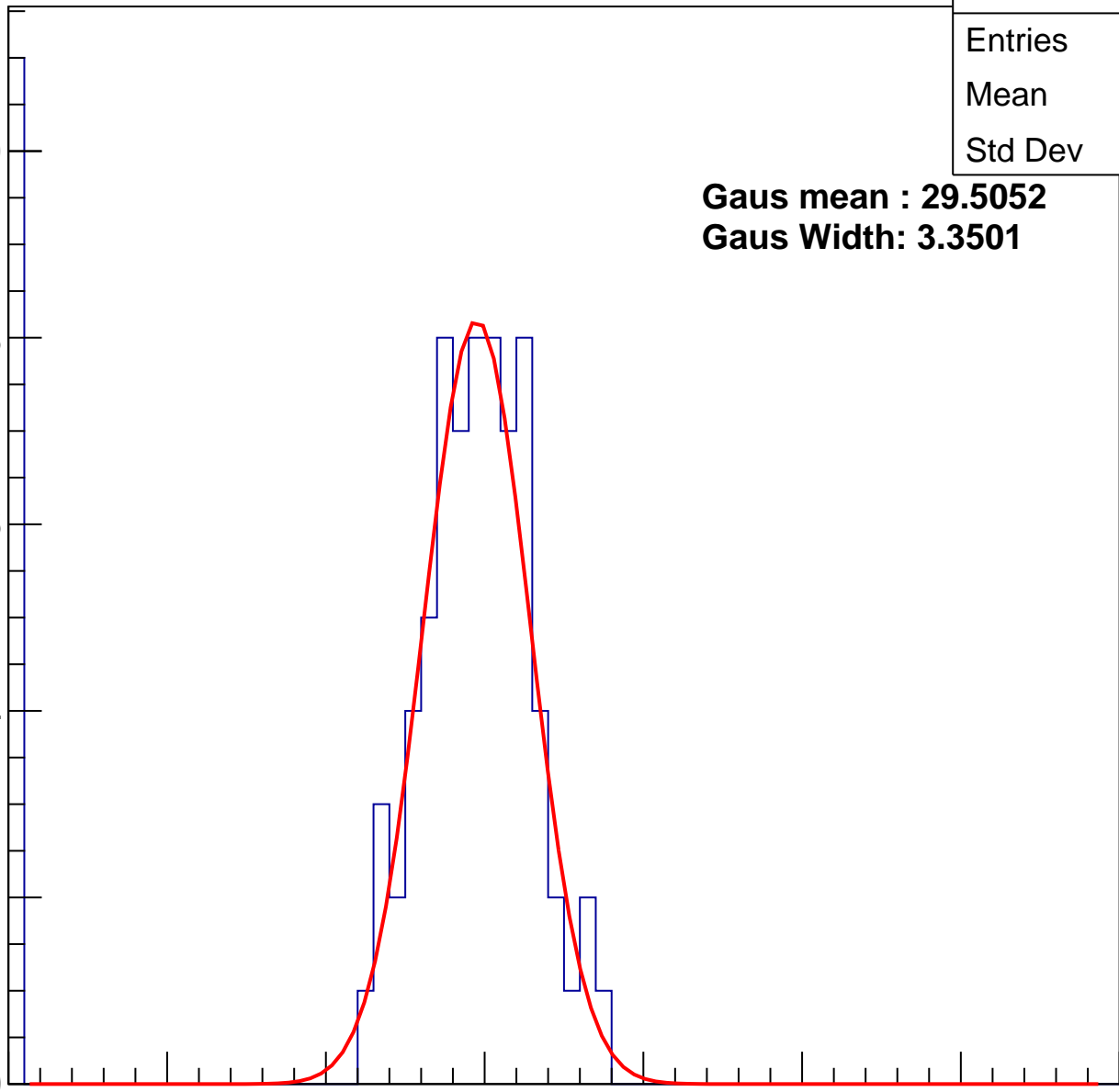
40

50

60

70

ampl



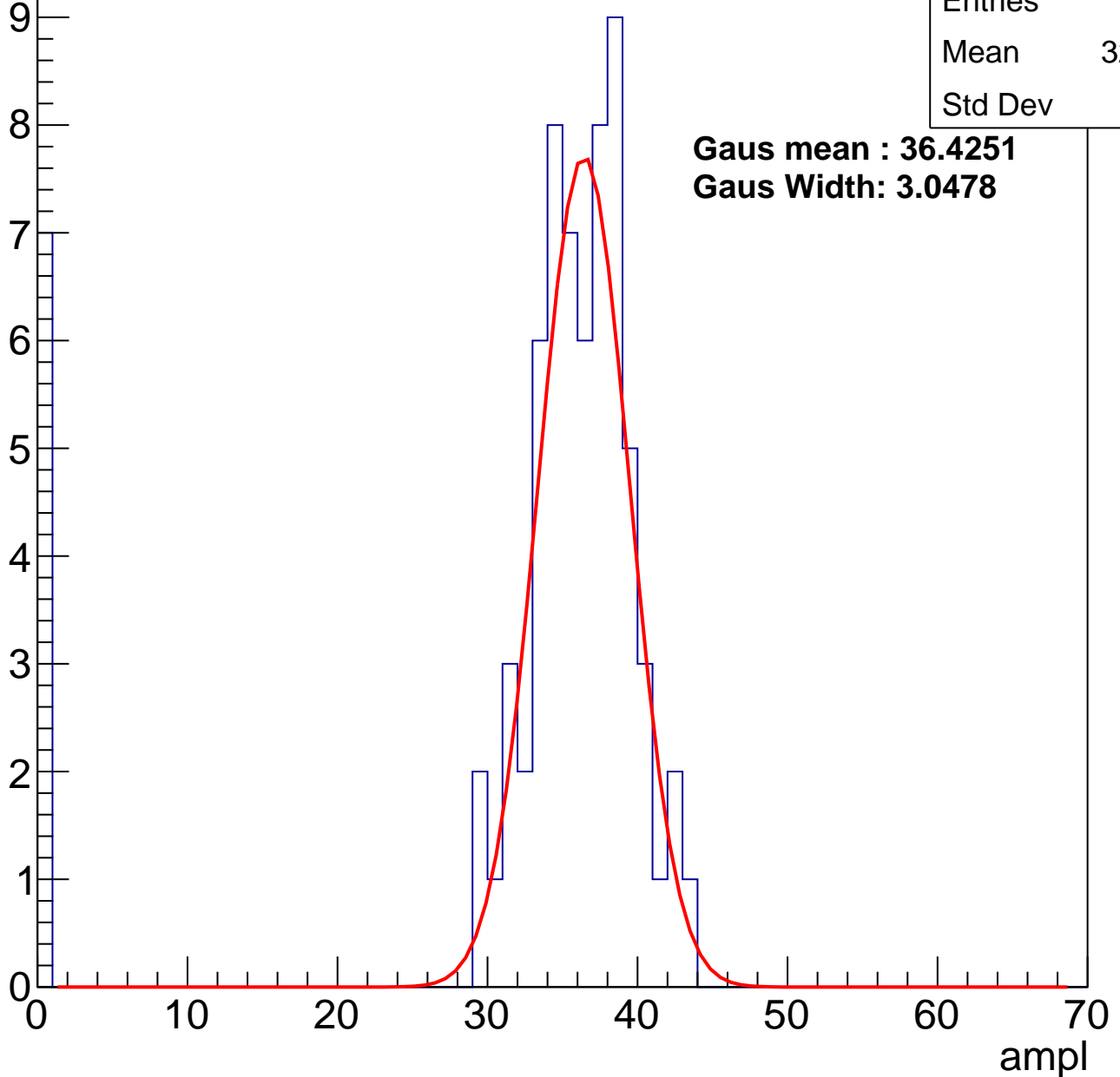
B1L103S, U24-ch23, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	32.35
Std Dev	11.1

Gaus mean : 36.4251
Gaus Width: 3.0478



B1L103S, U24-ch23, adc2

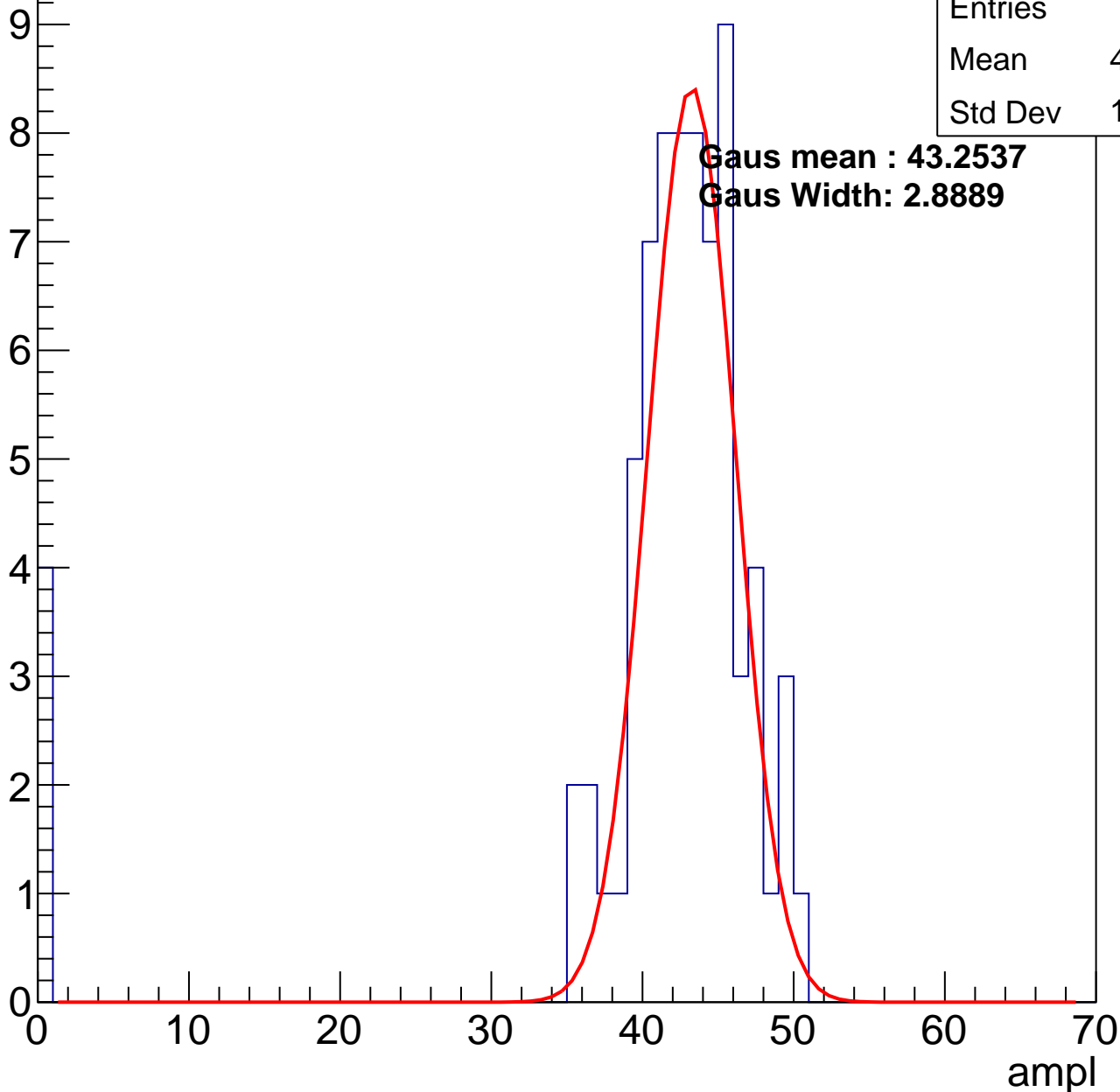
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	40.32
Std Dev	10.17

Gaus mean : 43.2537

Gaus Width: 2.8889

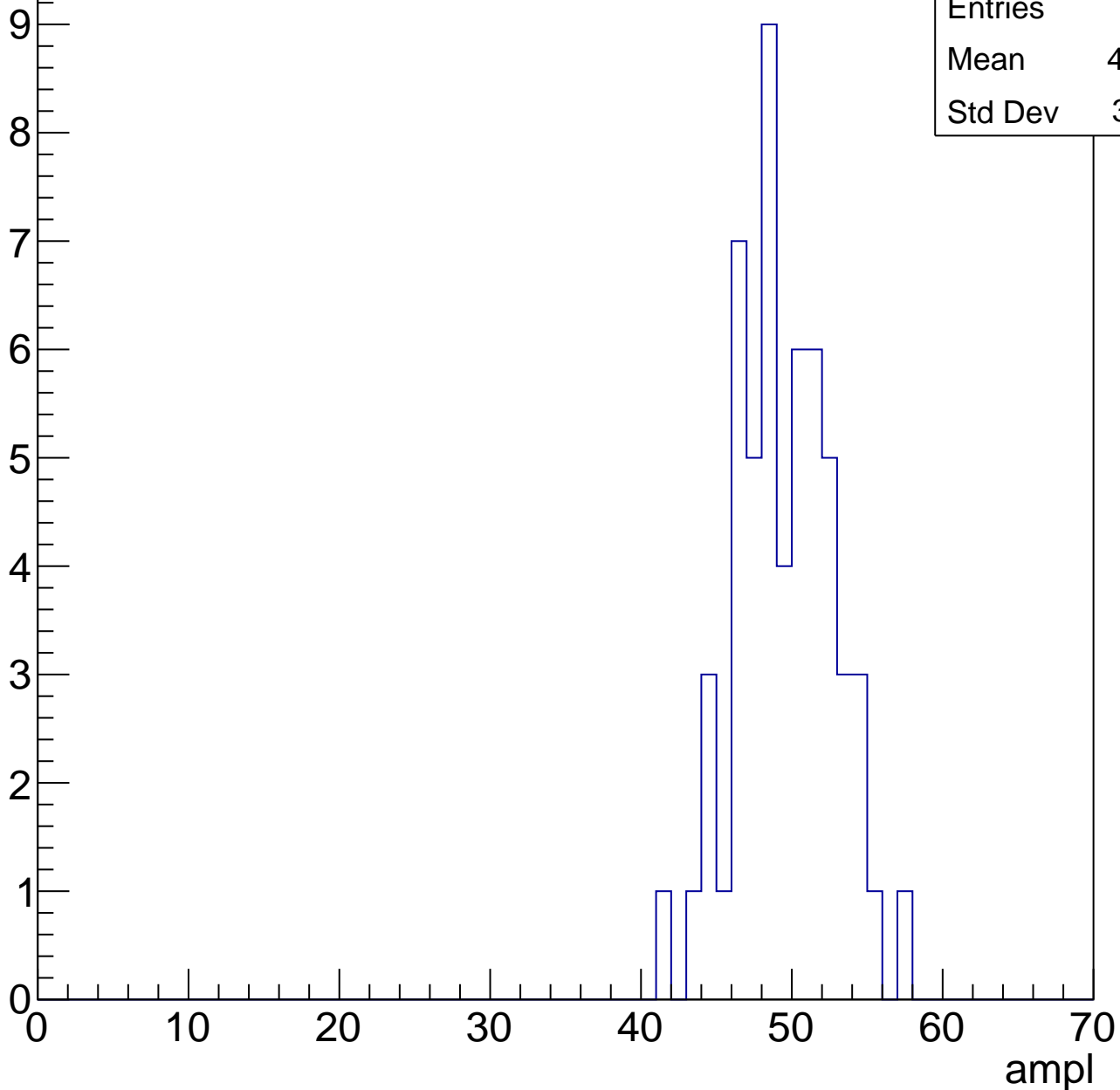


B1L103S, U24-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	49.02
Std Dev	3.221

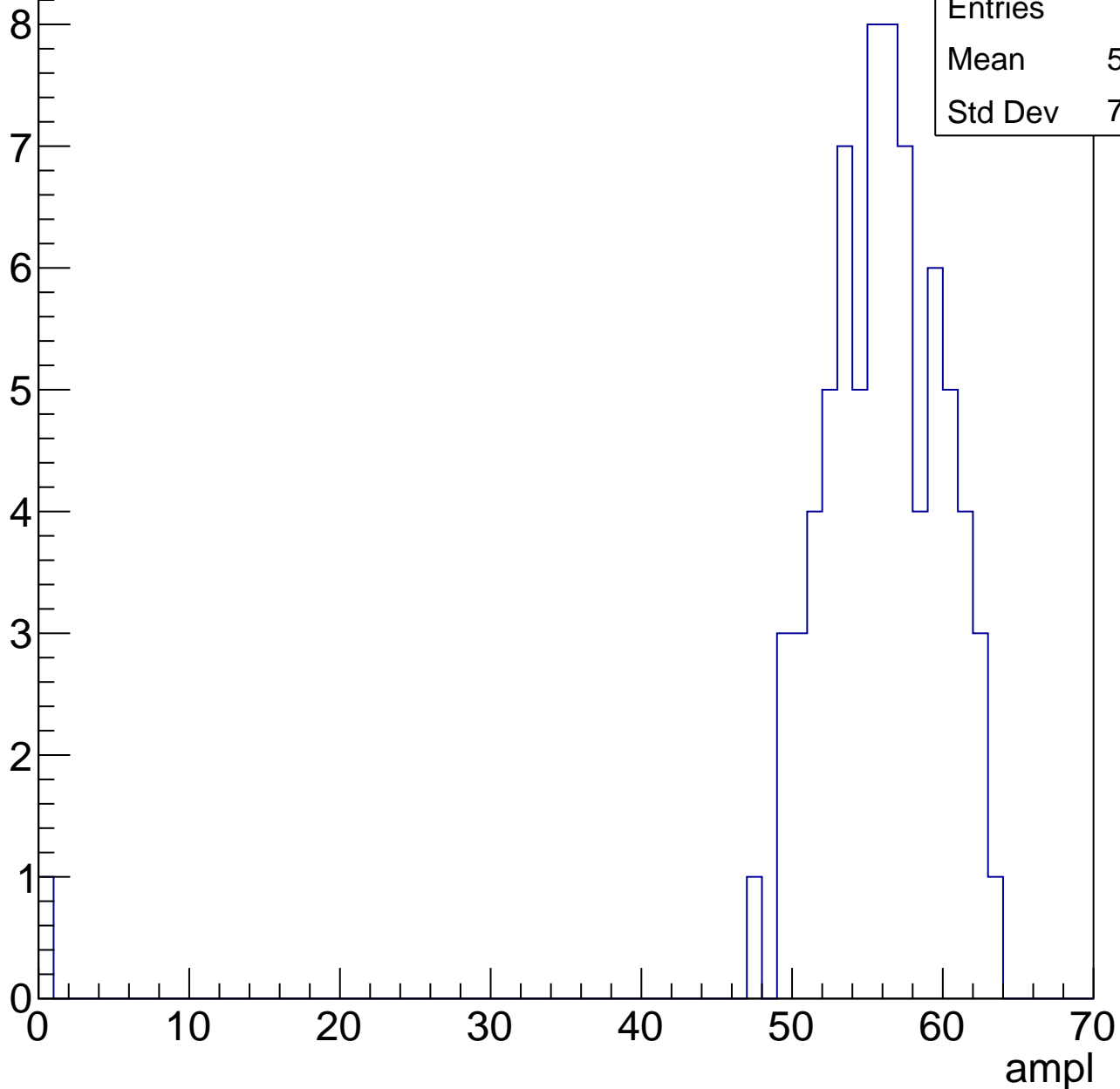


B1L103S, U24-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	54.87
Std Dev	7.358

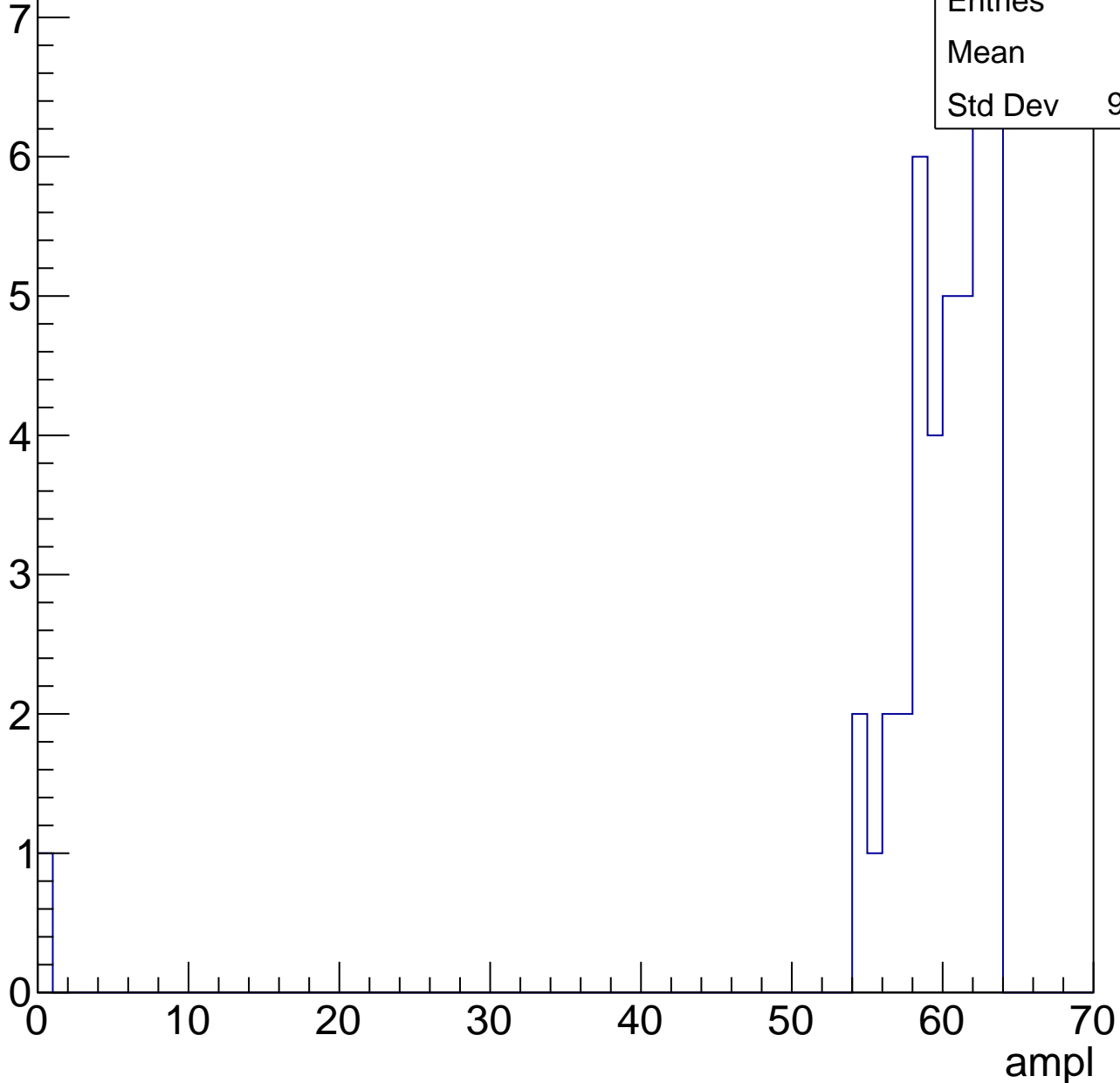


B1L103S, U24-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	58.4
Std Dev	9.467



B1L103S, U24-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl

1

B1L103S, U24-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch24, adc0

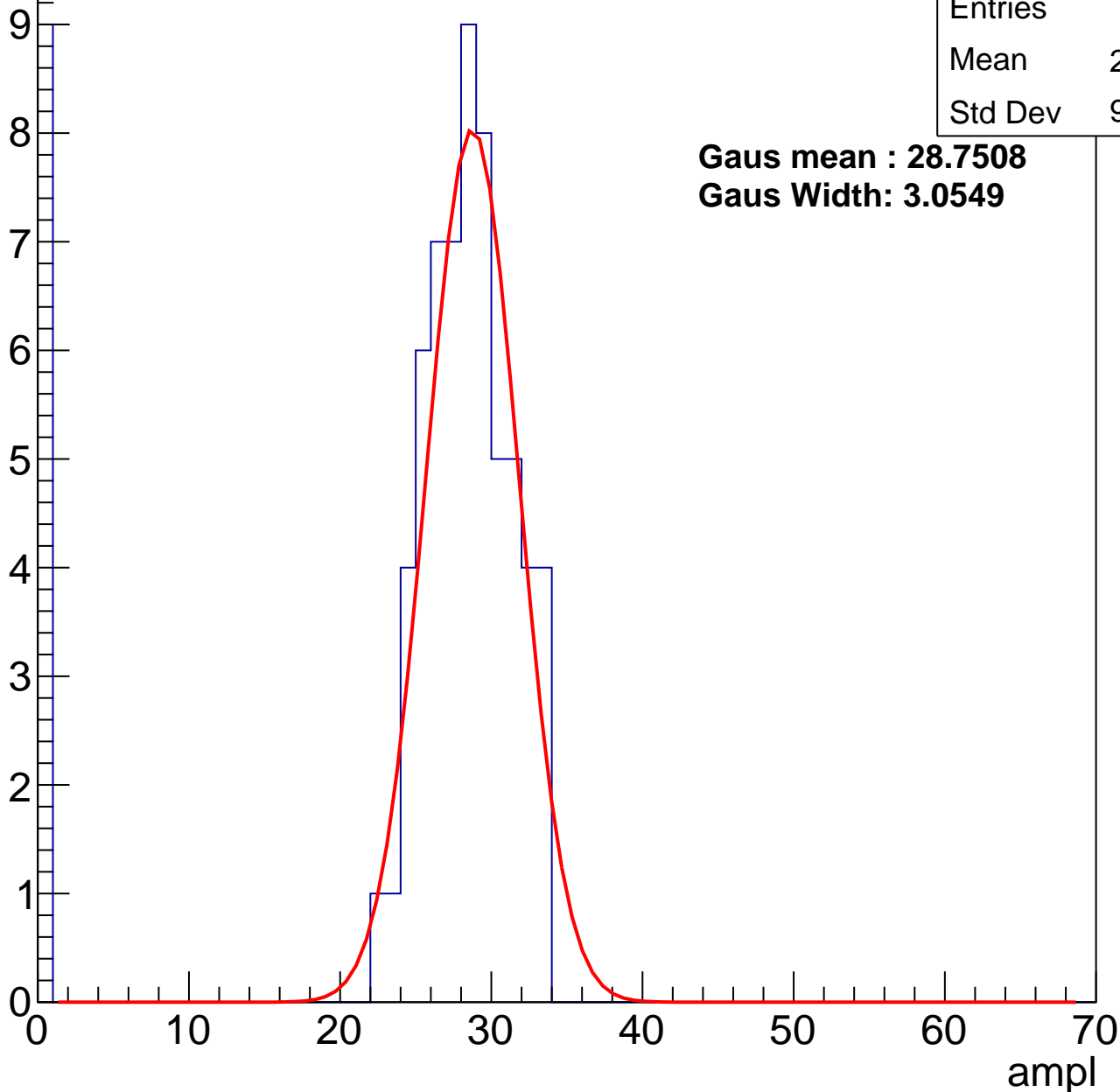
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	24.44
Std Dev	9.726

Gaus mean : 28.7508

Gaus Width: 3.0549



B1L103S, U24-ch24, adc1

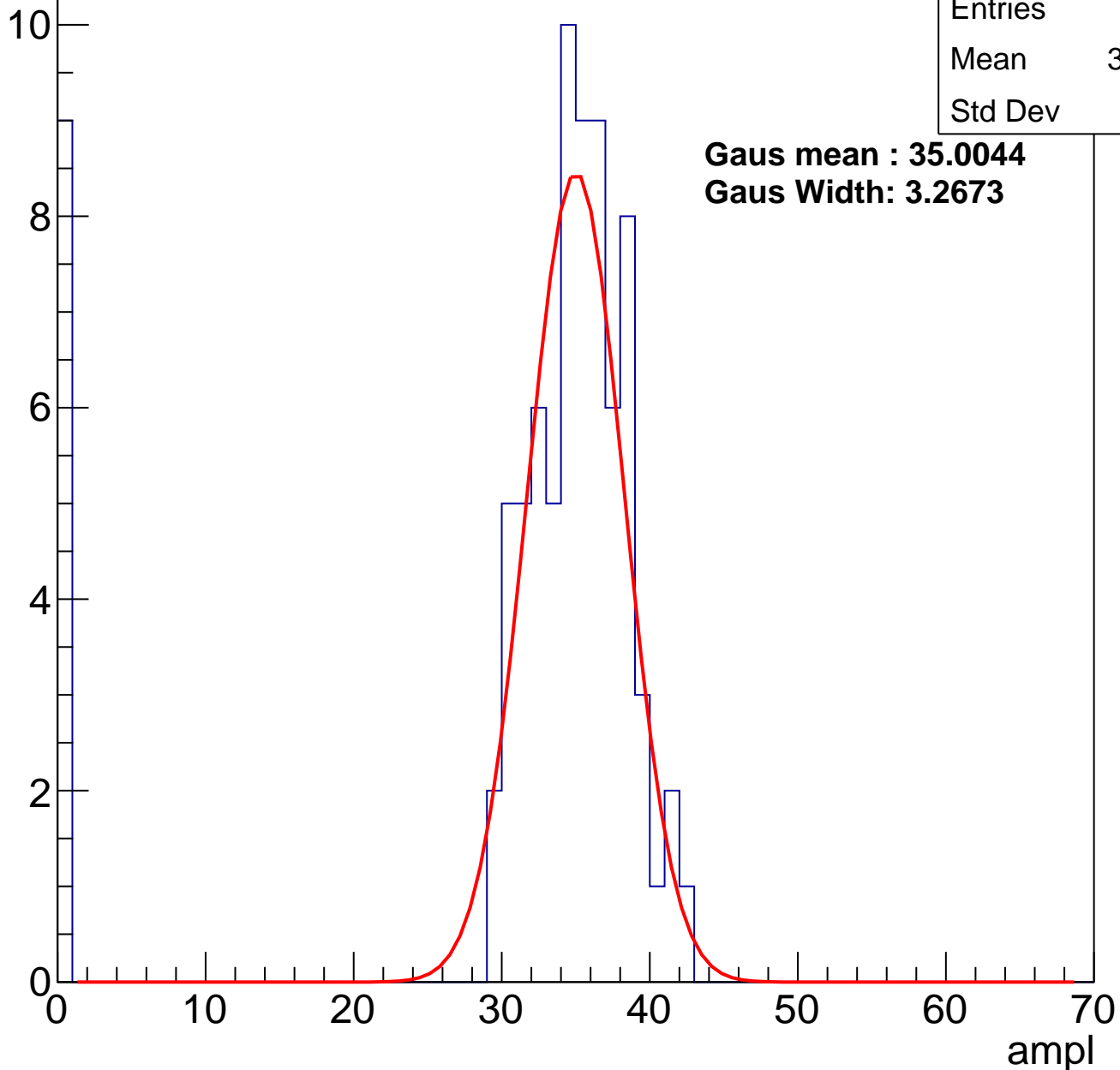
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	30.94
Std Dev	11.3

Gaus mean : 35.0044

Gaus Width: 3.2673

Entry



B1L103S, U24-ch24, adc2

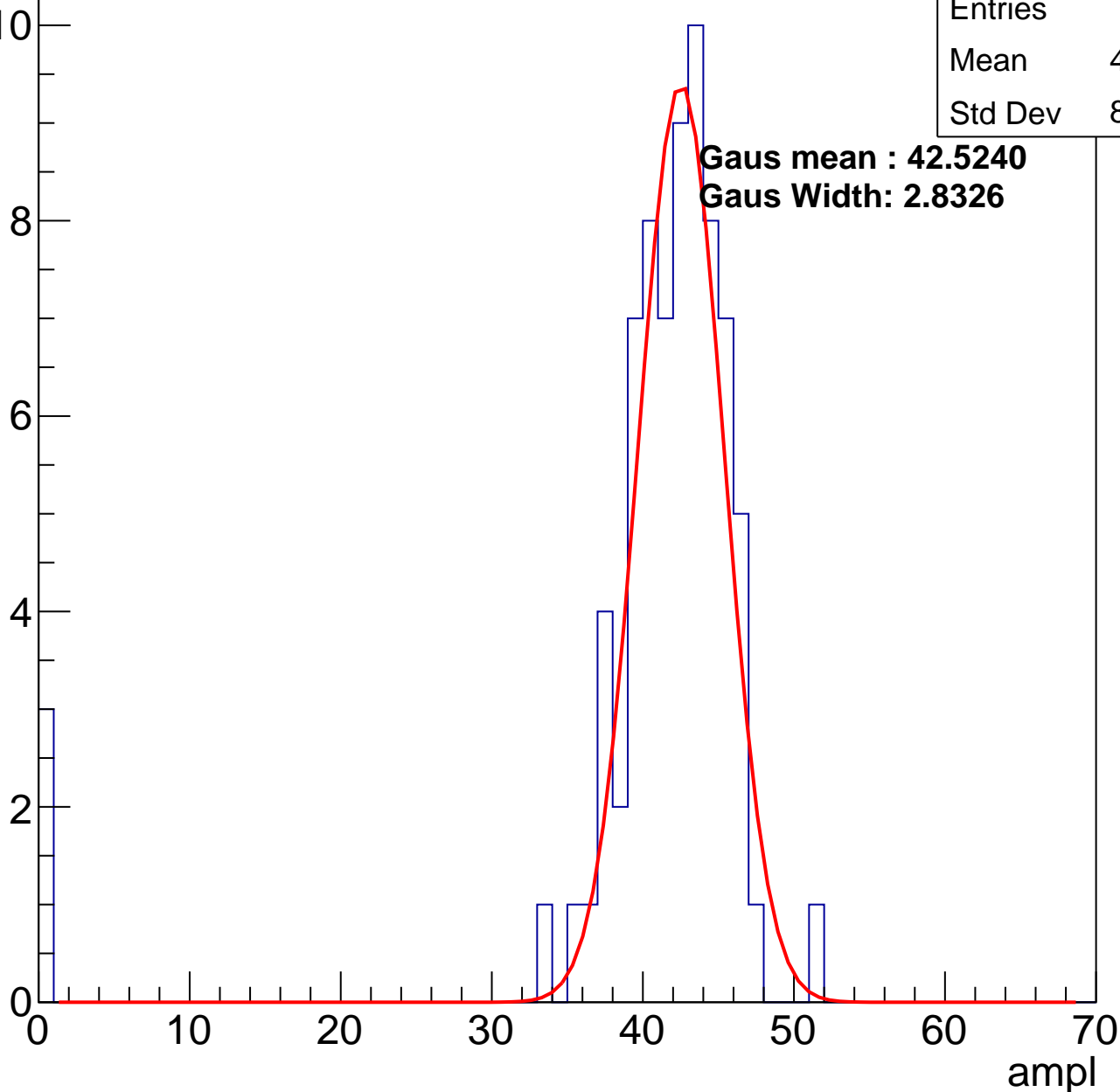
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	40.15
Std Dev	8.737

Gaus mean : 42.5240

Gaus Width: 2.8326

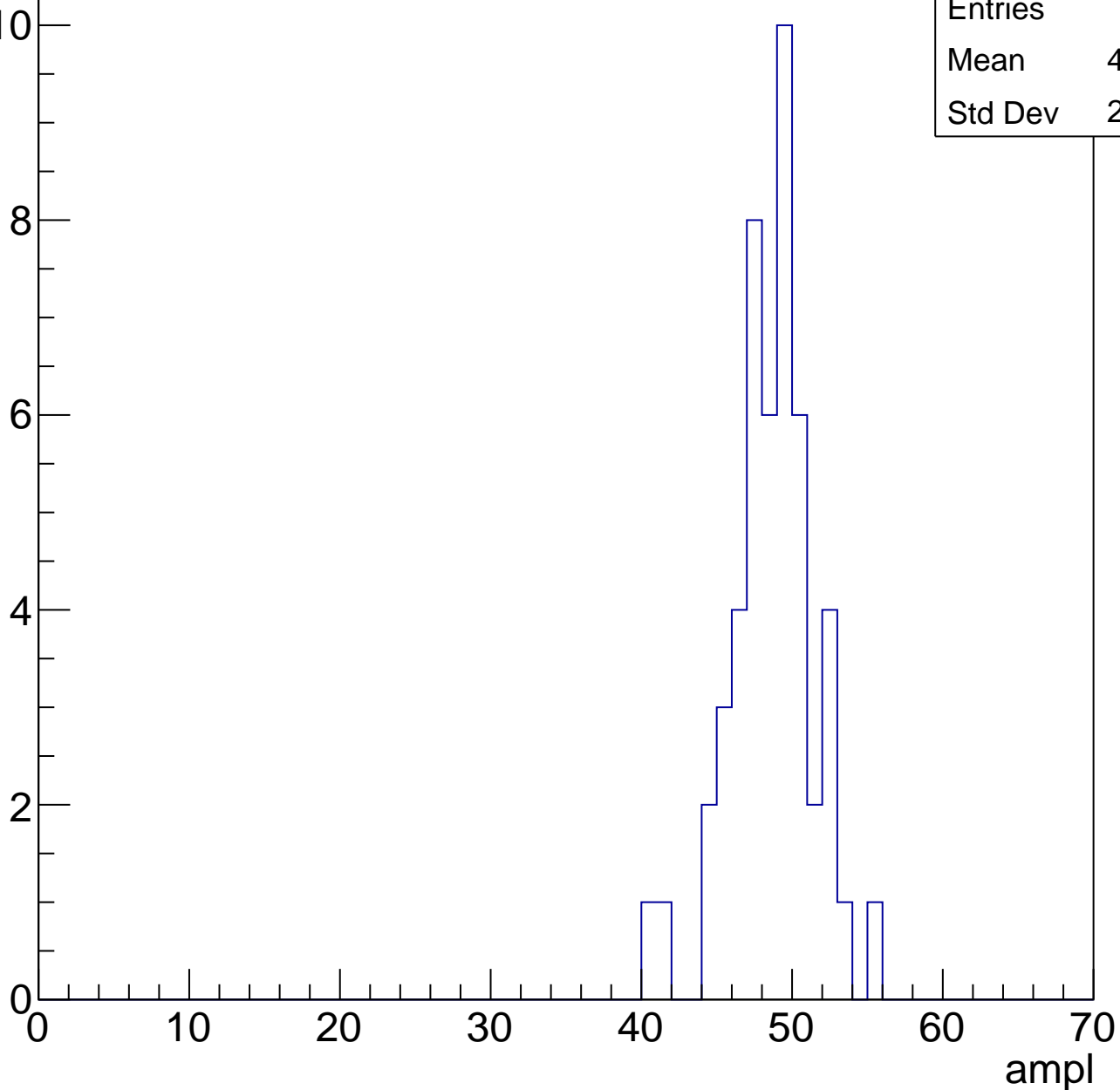


B1L103S, U24-ch24, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	48.16
Std Dev	2.802

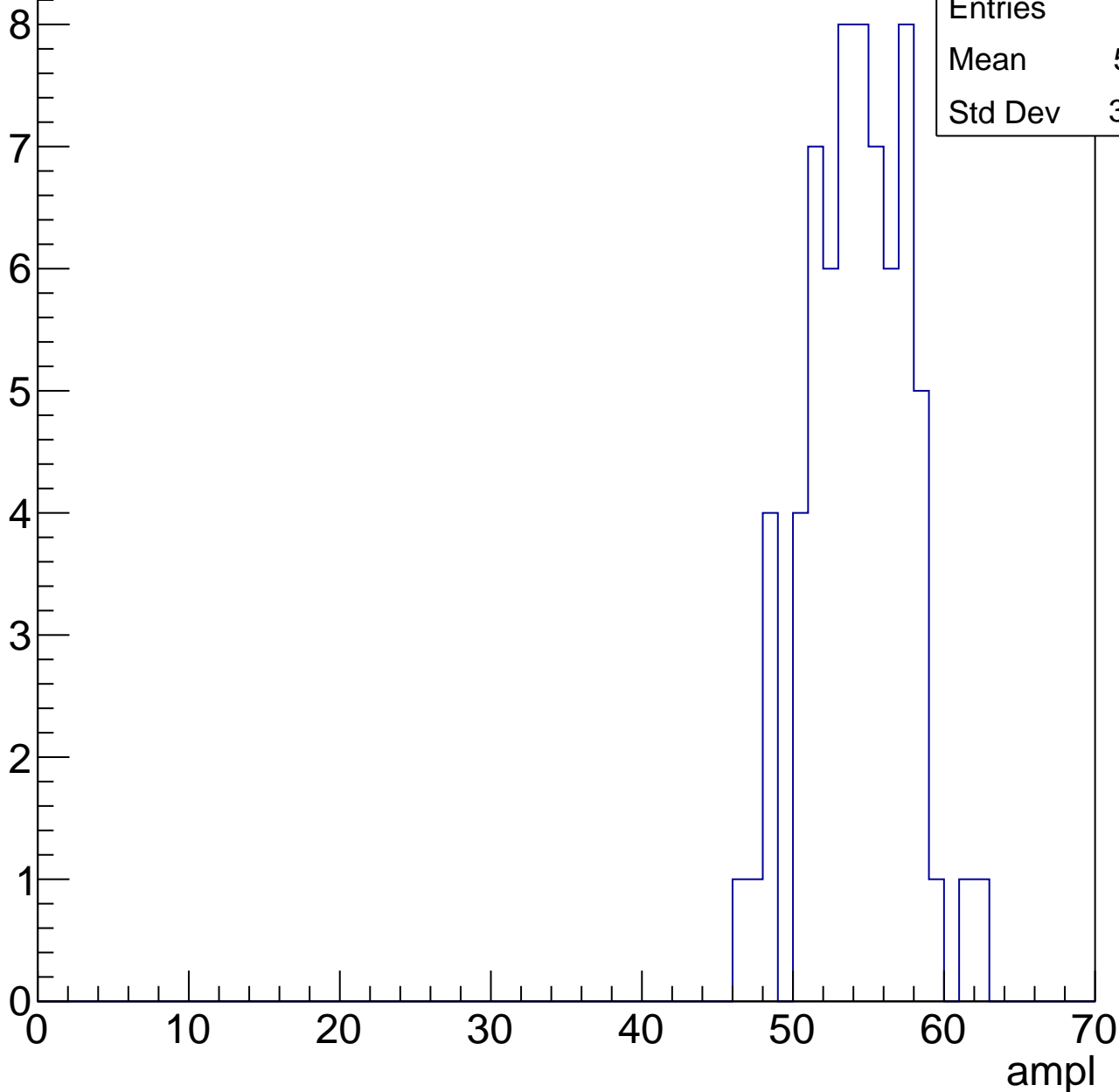


B1L103S, U24-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	53.81
Std Dev	3.287

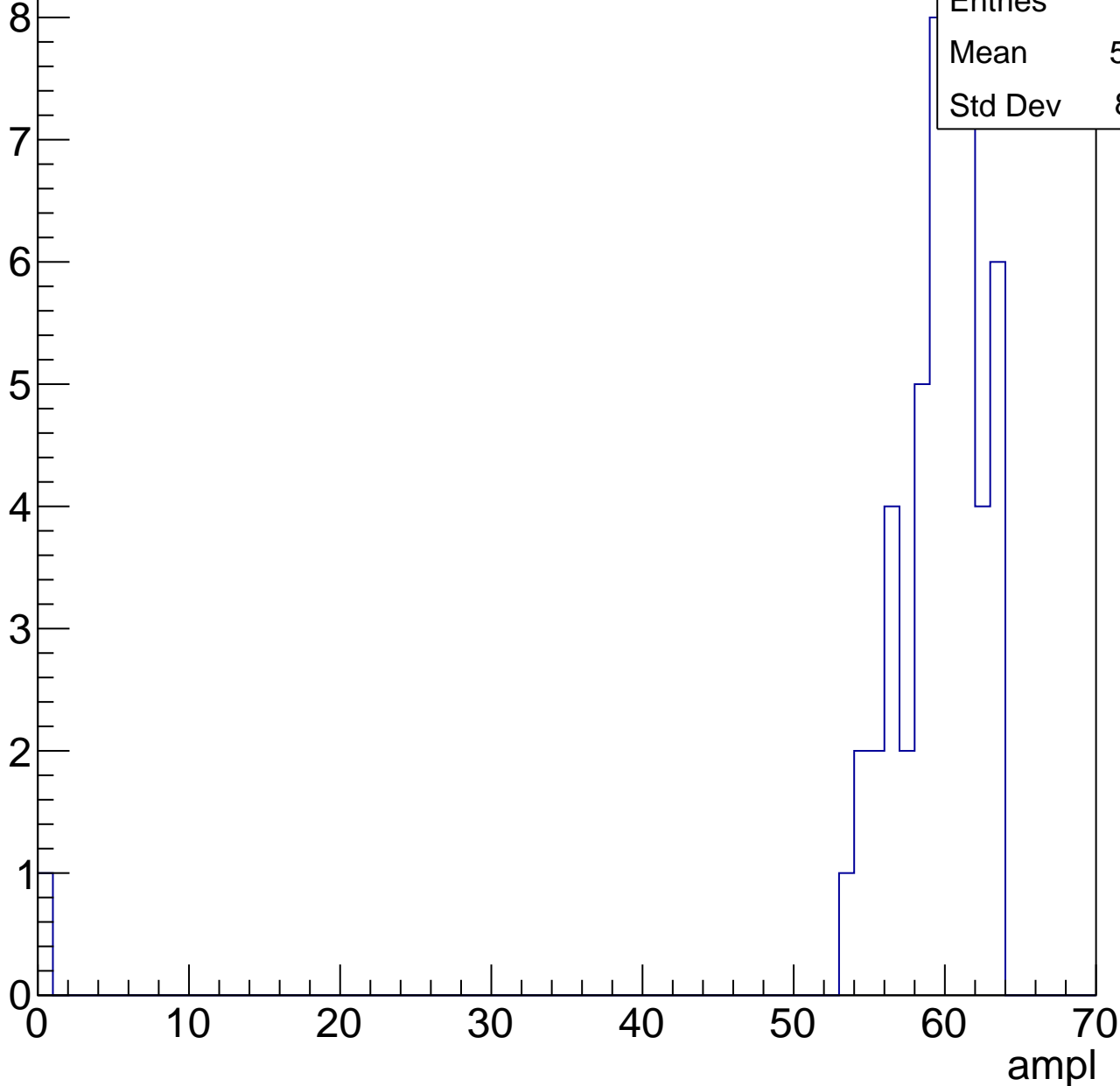


B1L103S, U24-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.14
Std Dev	8.611

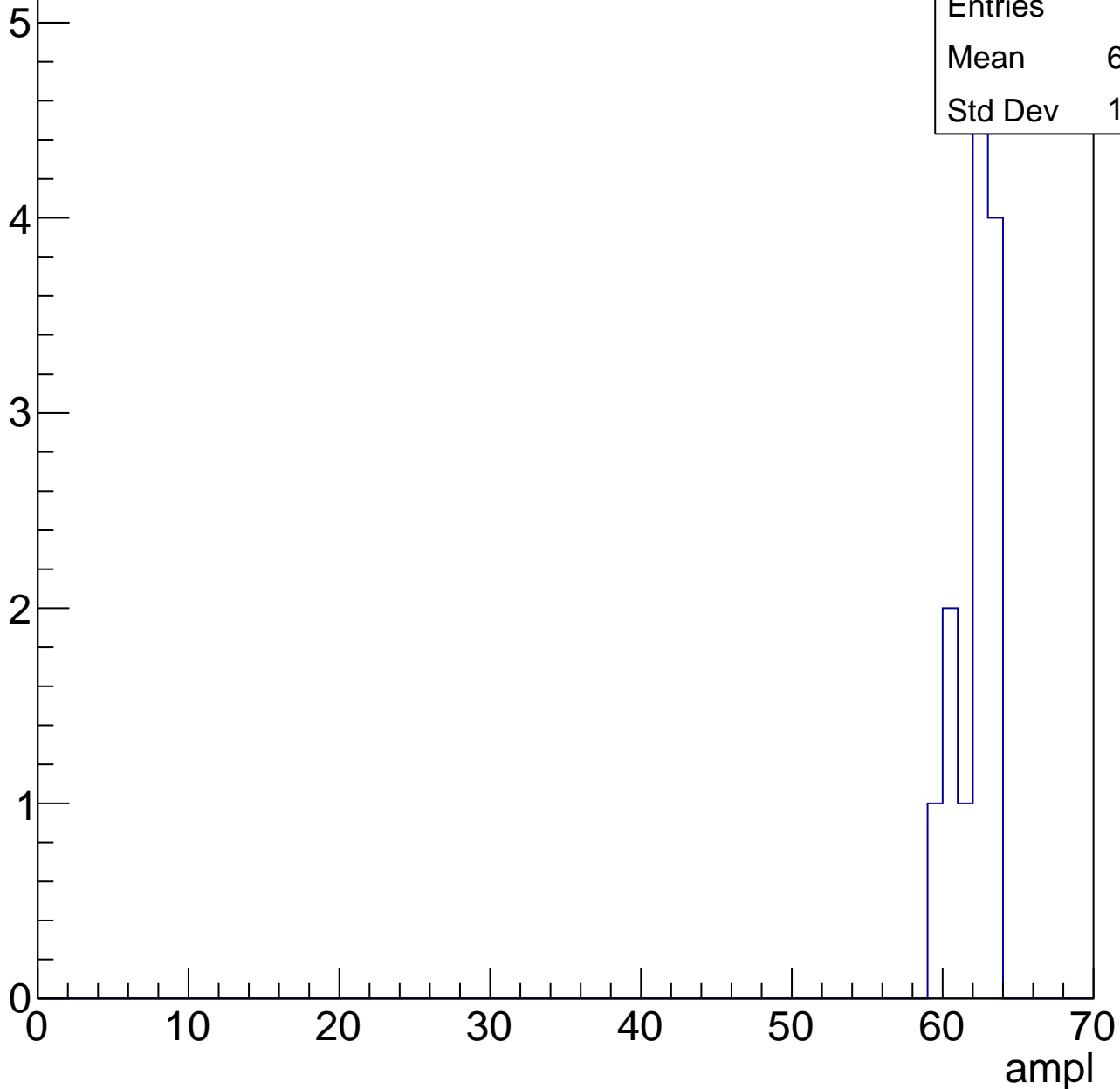


B1L103S, U24-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.69
Std Dev	1.264



B1L103S, U24-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch25, adc0

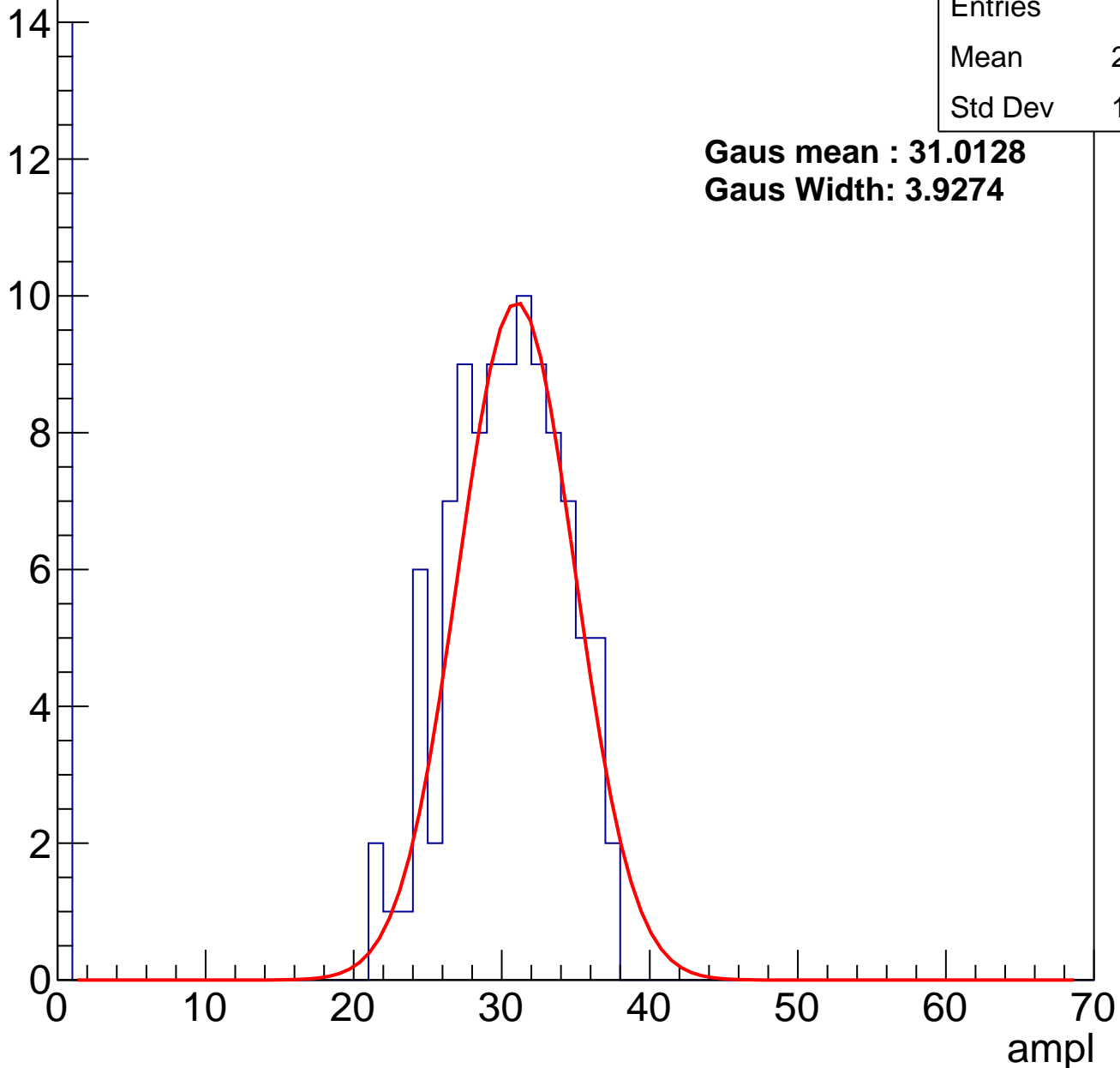
calib_packv5_041523_1651.root, FC#0, port C2

Entries	114
Mean	26.23
Std Dev	10.42

Gaus mean : 31.0128

Gaus Width: 3.9274

Entry



B1L103S, U24-ch25, adc1

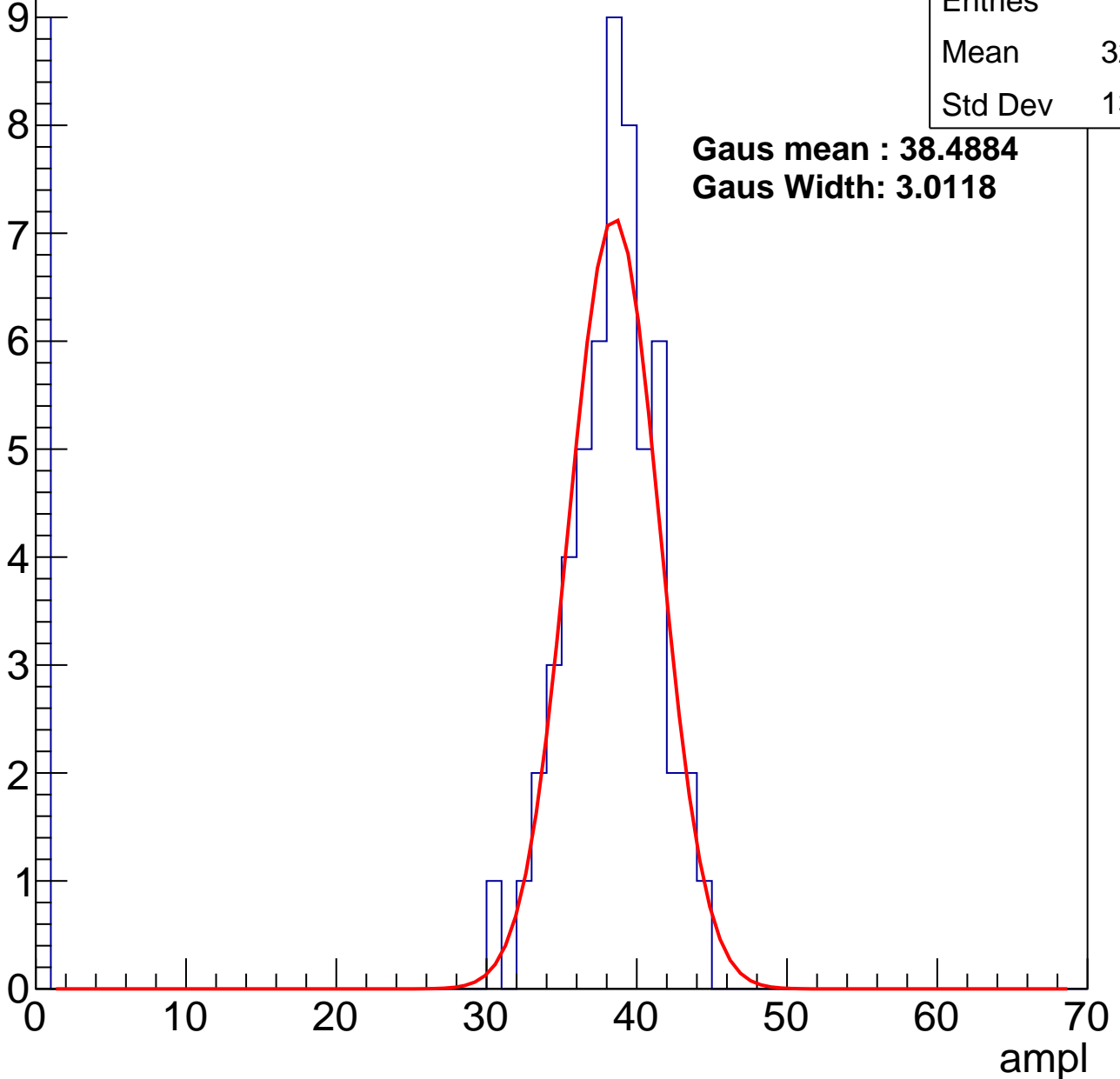
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	32.59
Std Dev	13.45

Gaus mean : 38.4884

Gaus Width: 3.0118



B1L103S, U24-ch25, adc2

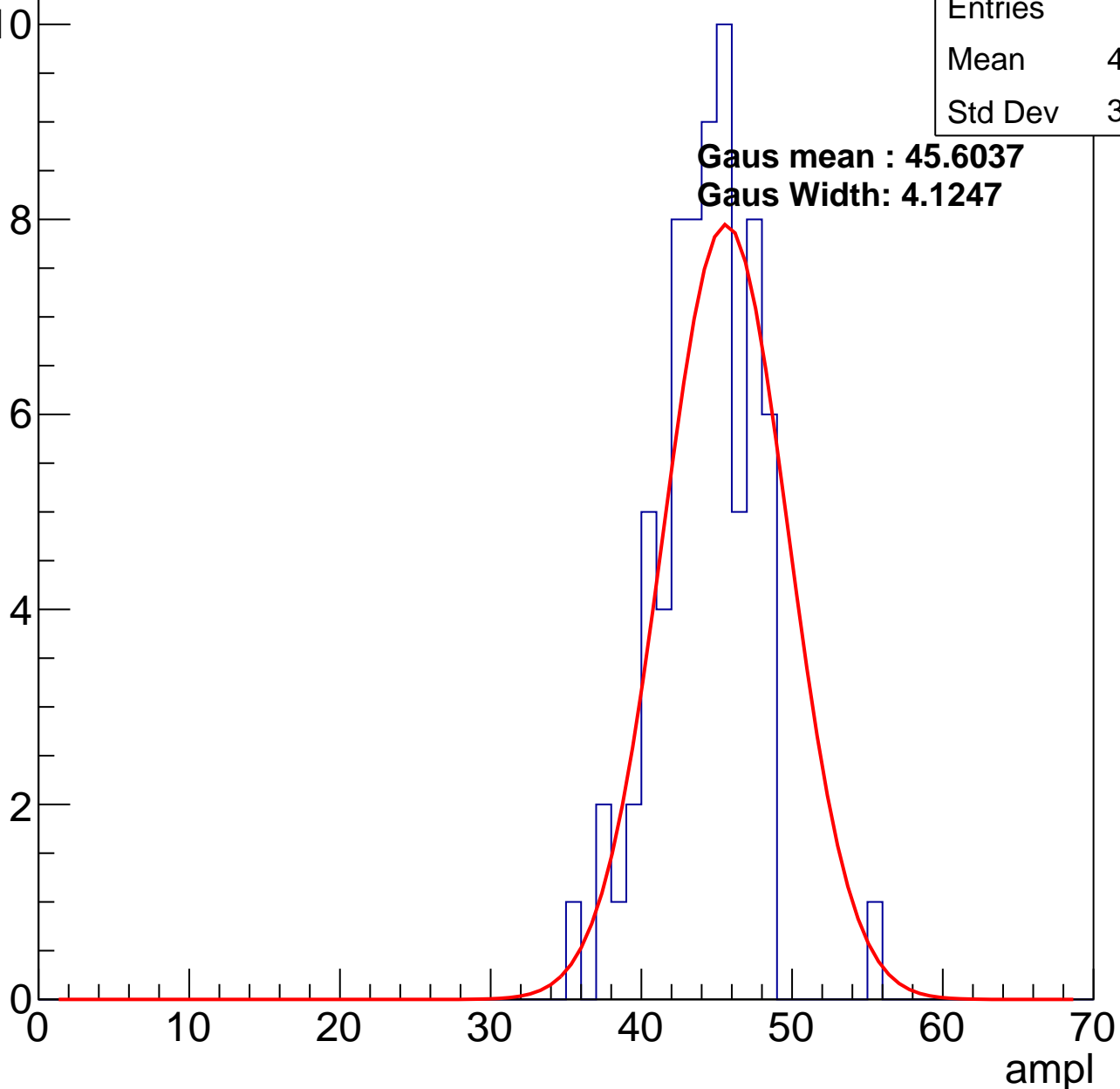
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	43.77
Std Dev	3.243

Gaus mean : 45.6037

Gaus Width: 4.1247

Entry

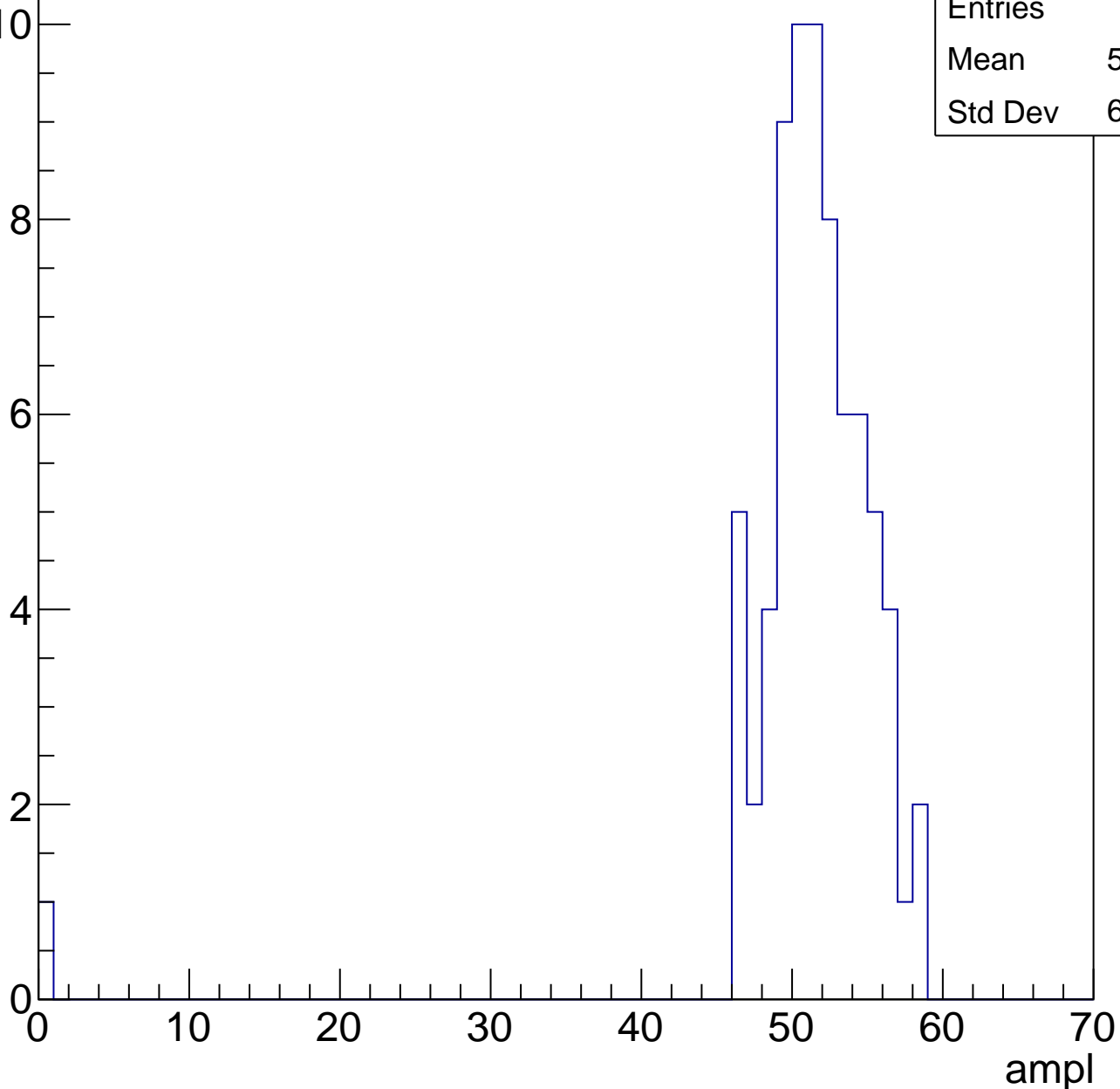


B1L103S, U24-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	50.64
Std Dev	6.653

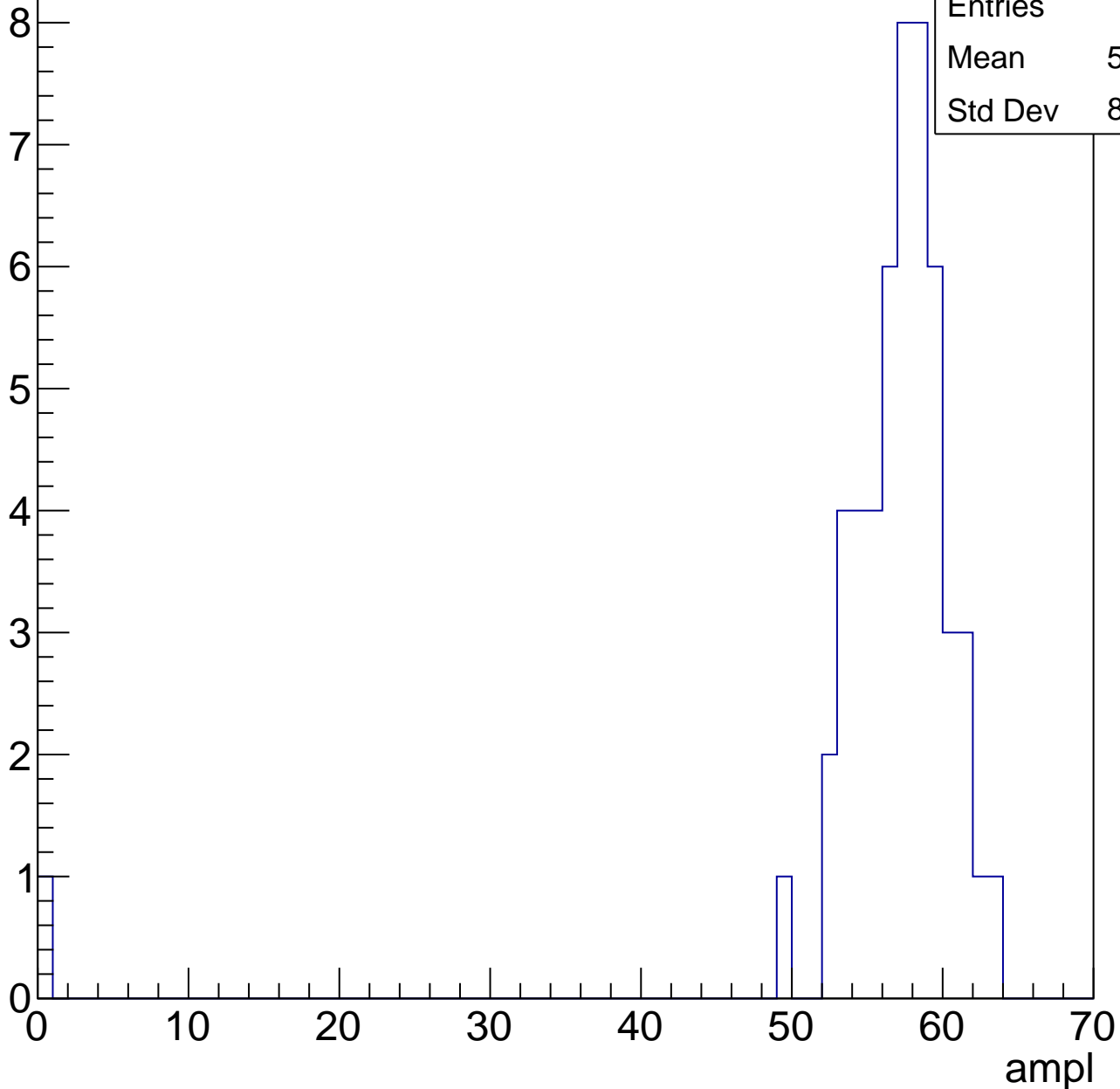


B1L103S, U24-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	55.75
Std Dev	8.288

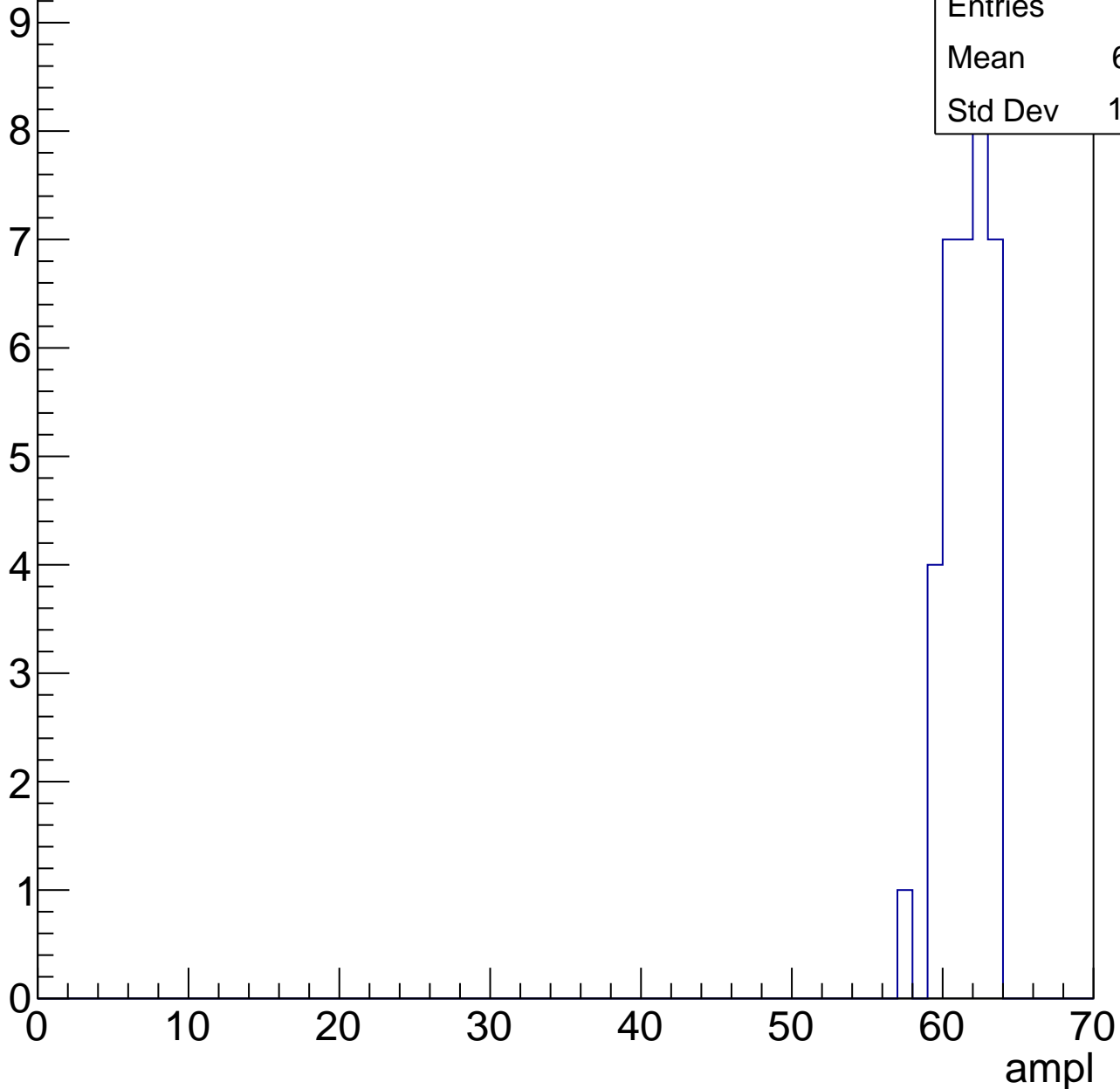


B1L103S, U24-ch25, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	61.11
Std Dev	1.469



B1L103S, U24-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L103S, U24-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch26, adc0

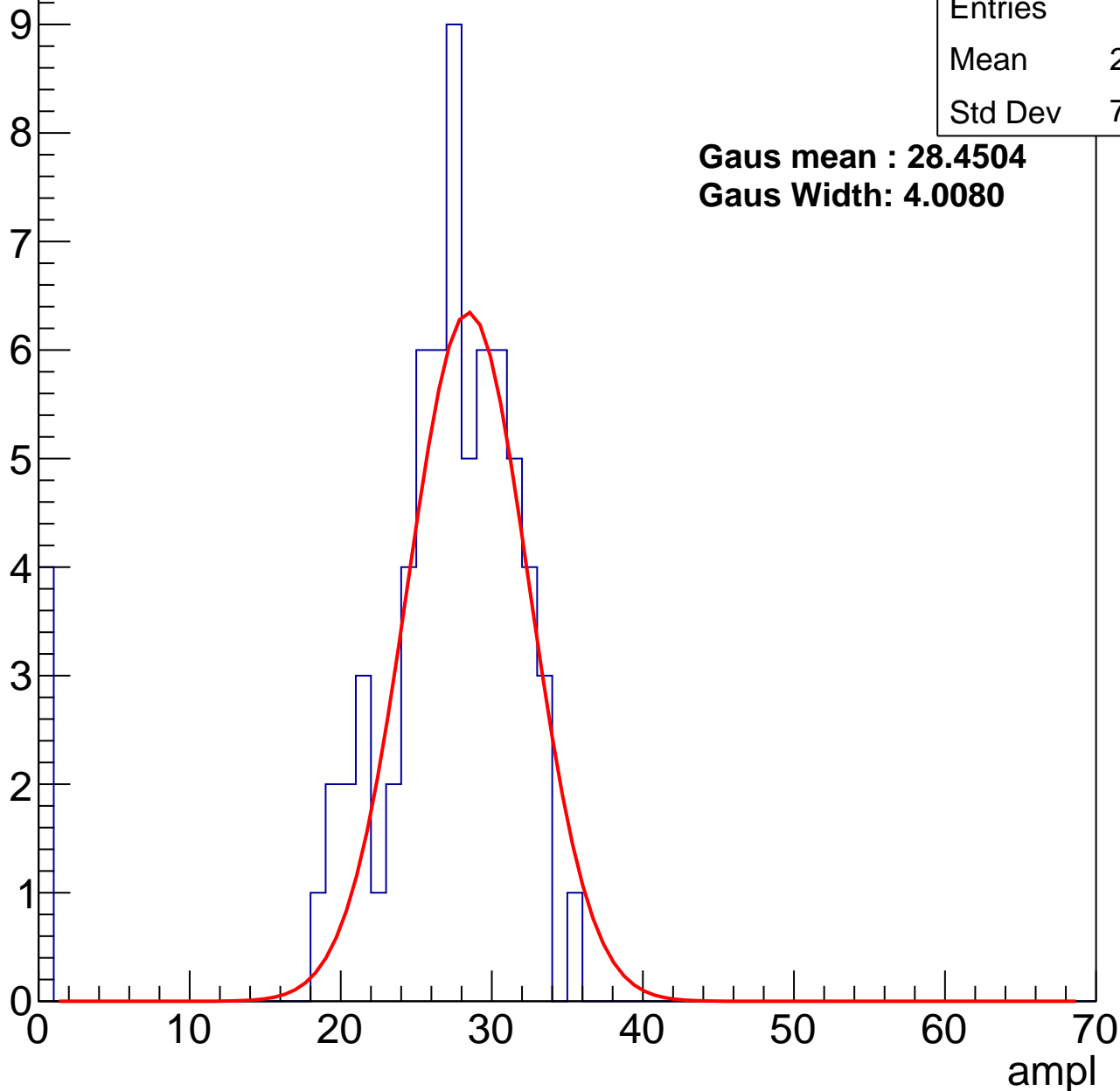
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	25.47
Std Dev	7.295

Gaus mean : 28.4504

Gaus Width: 4.0080



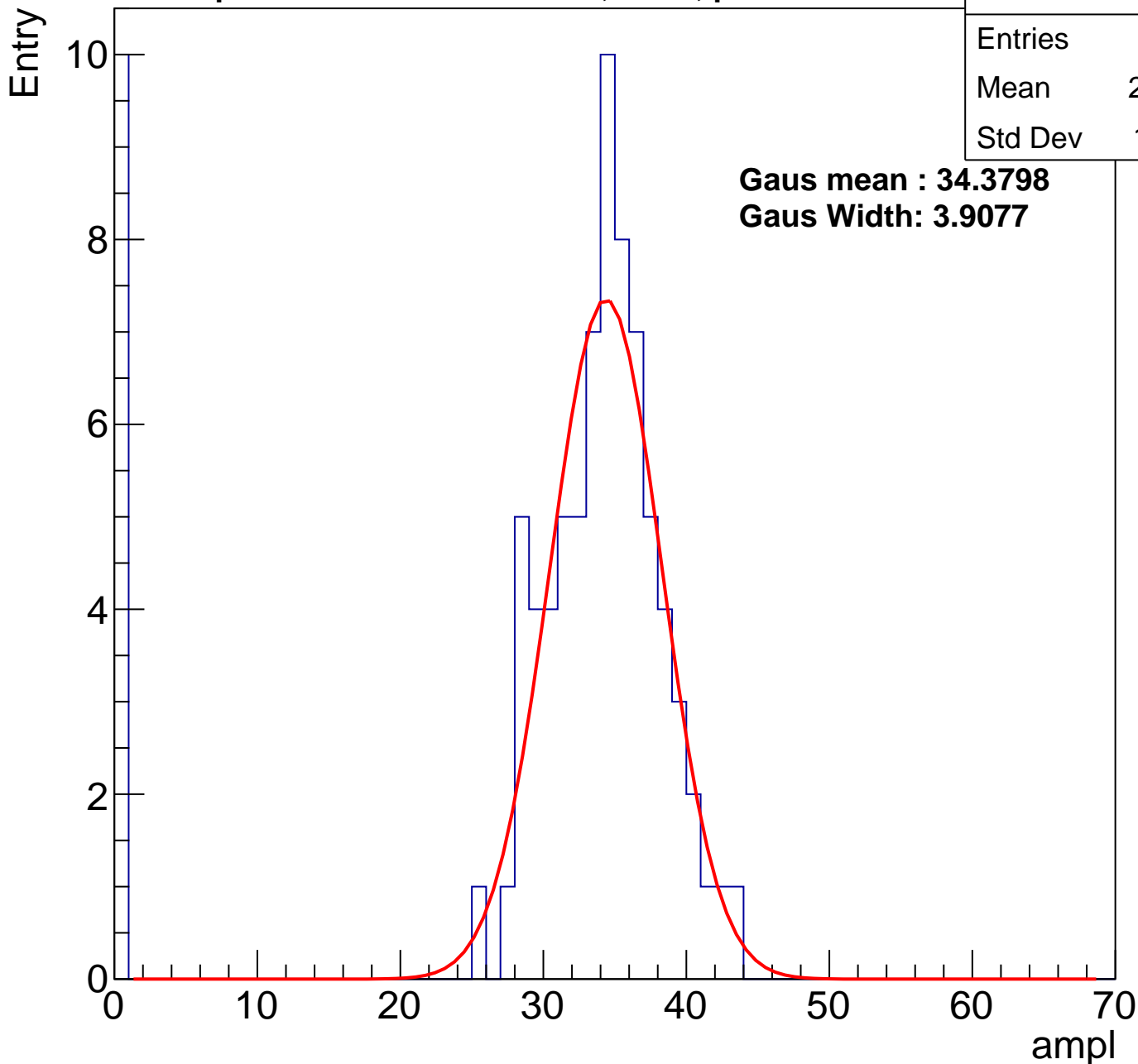
B1L103S, U24-ch26, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	29.83
Std Dev	11.51

Gaus mean : 34.3798

Gaus Width: 3.9077



B1L103S, U24-ch26, adc2

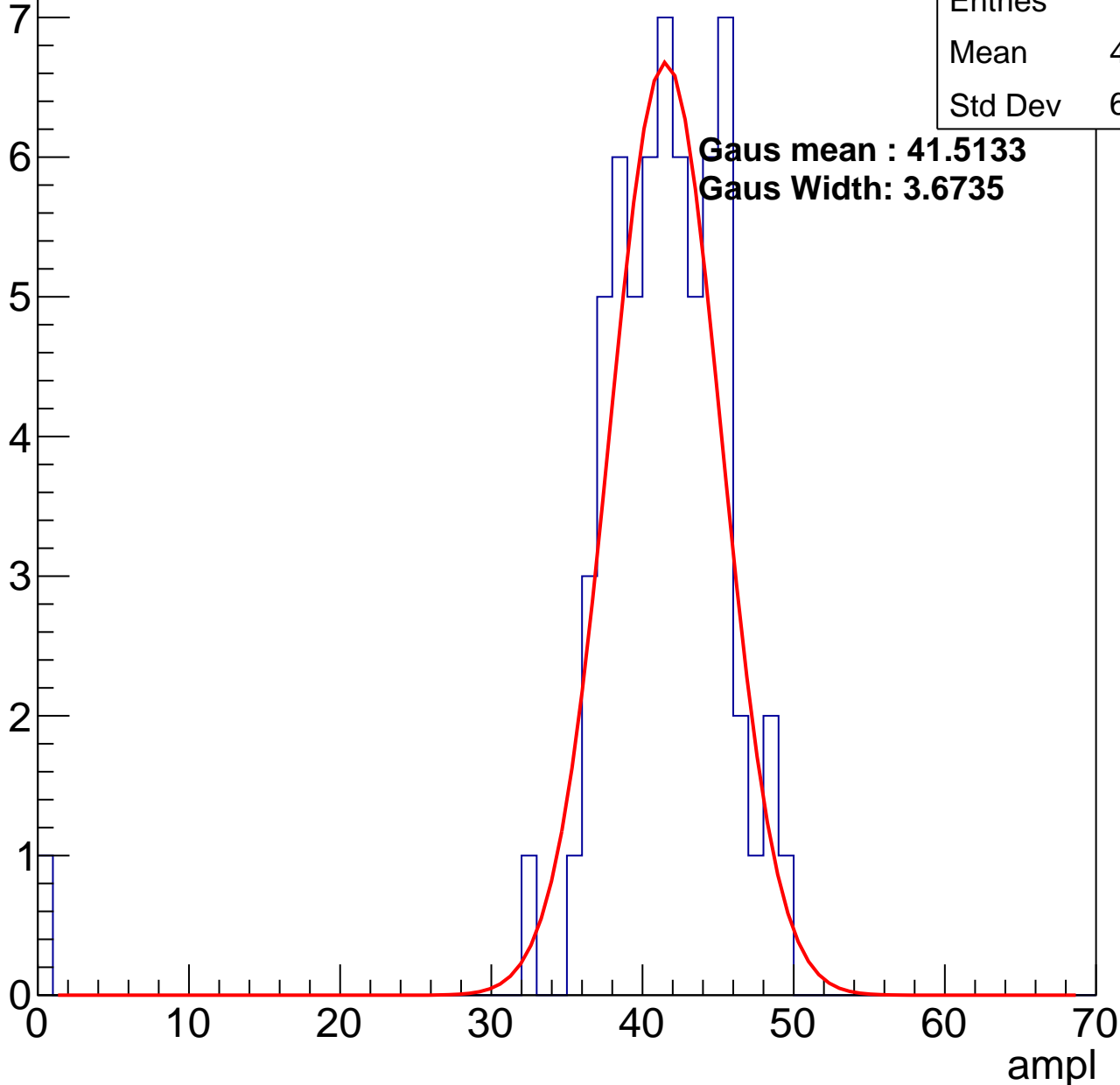
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	40.62
Std Dev	6.156

Gaus mean : 41.5133

Gaus Width: 3.6735

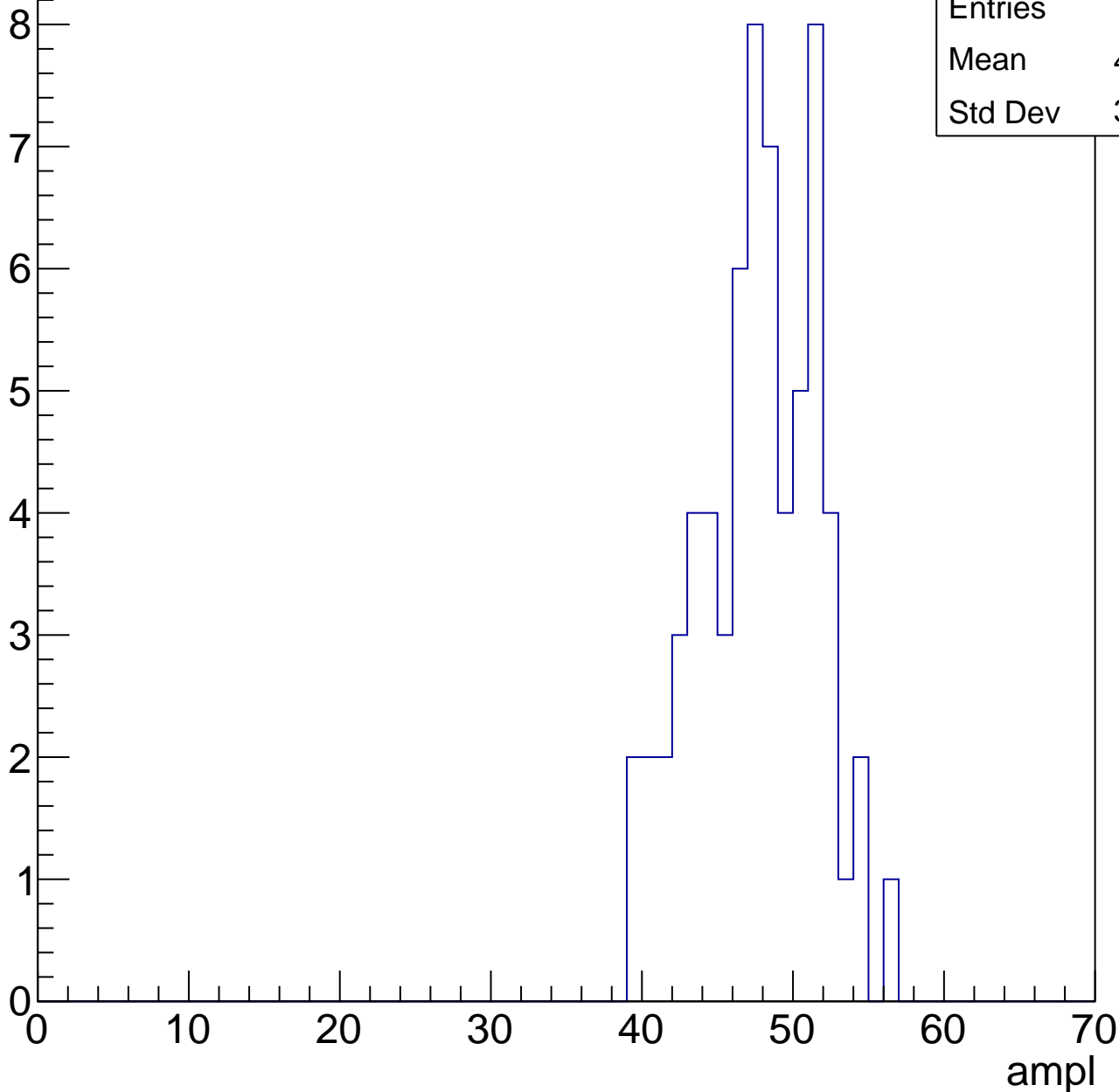


B1L103S, U24-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	47.21
Std Dev	3.891

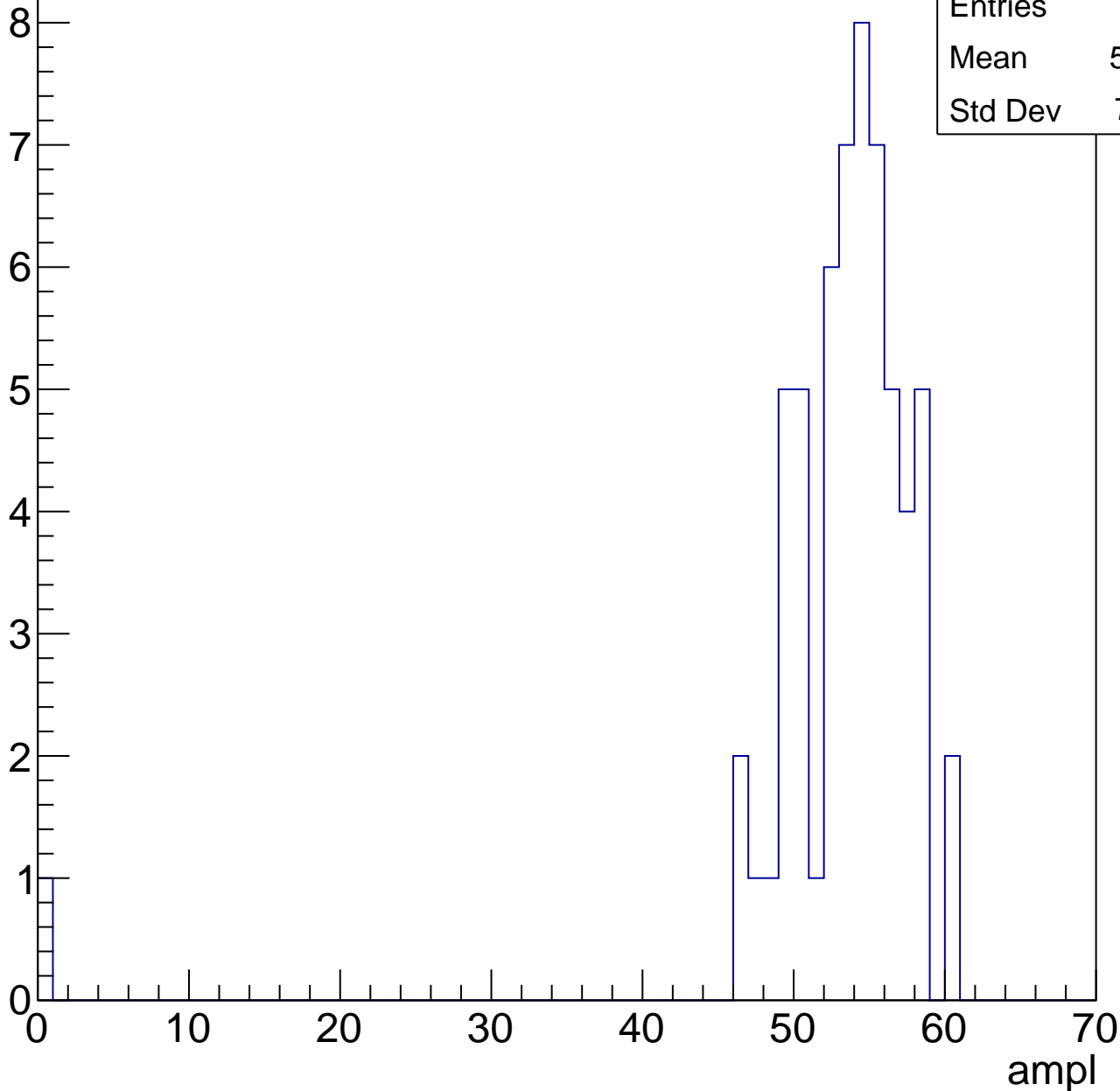


B1L103S, U24-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	52.52
Std Dev	7.591

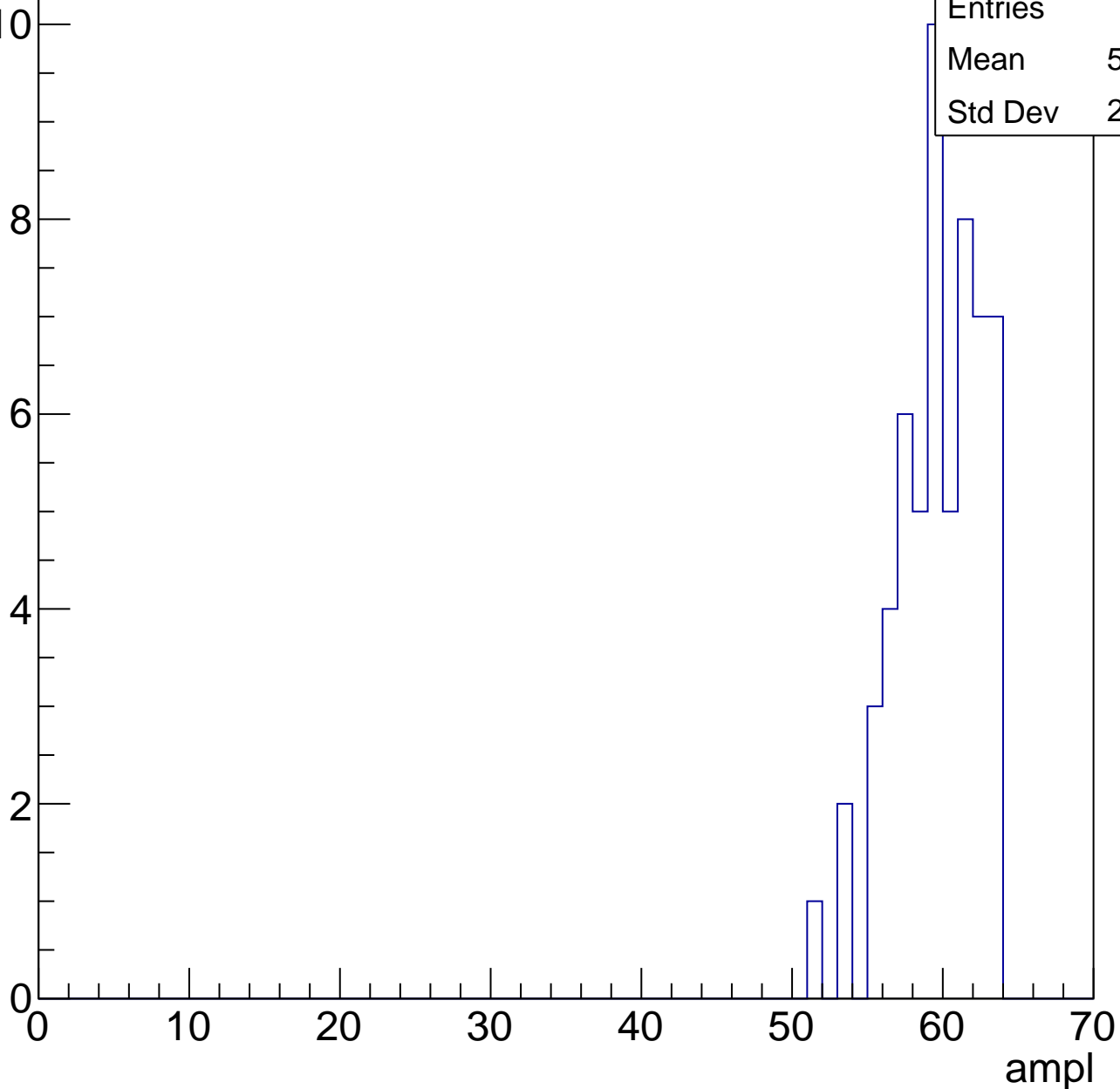


B1L103S, U24-ch26, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

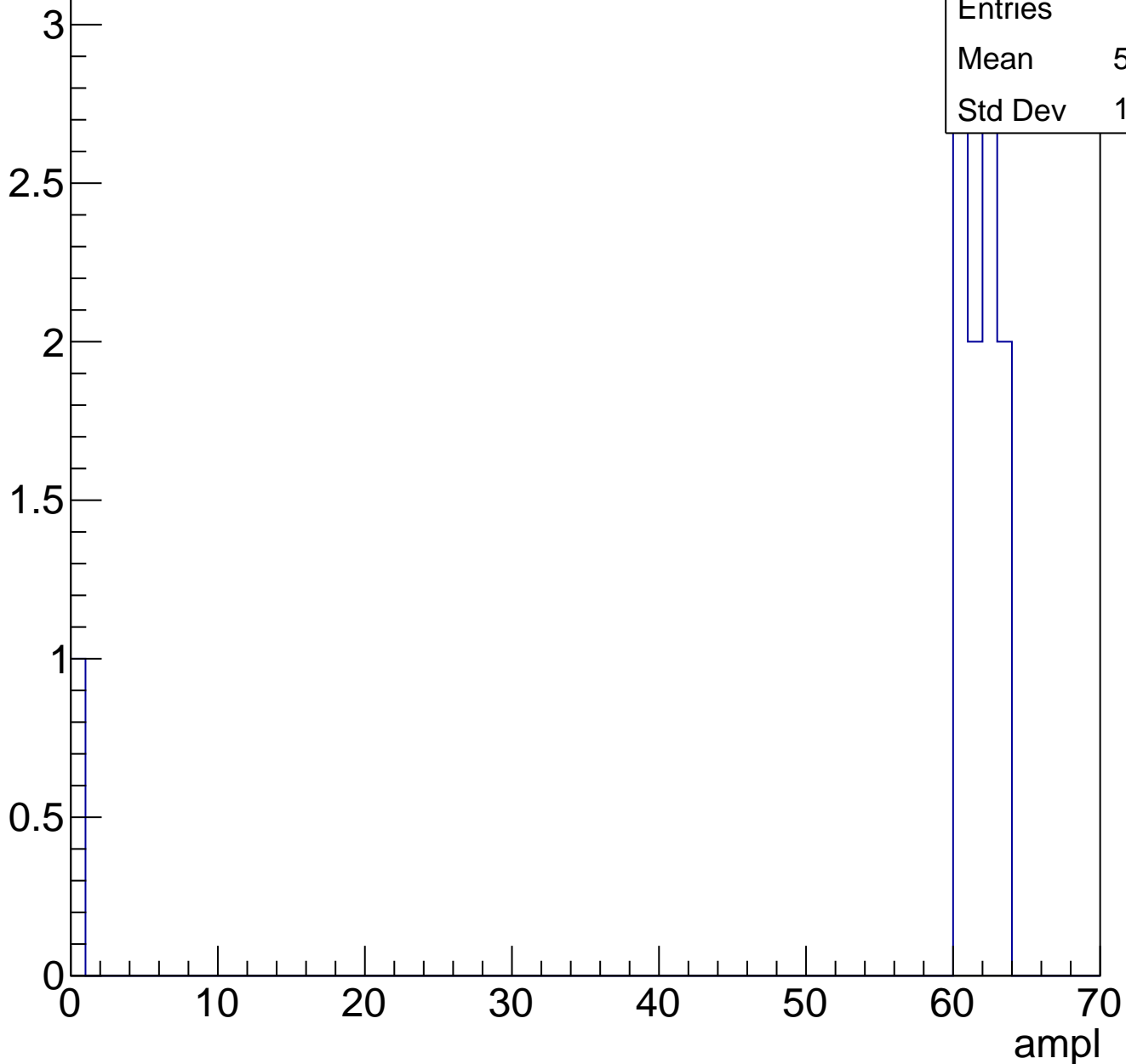
Entries	58
Mean	59.16
Std Dev	2.815



B1L103S, U24-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U24-ch27, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	21.48
Std Dev	14.1

Gaus mean : 30.6812

Gaus Width: 3.7450

Entry

25

20

15

10

5

0

0

10

20

30

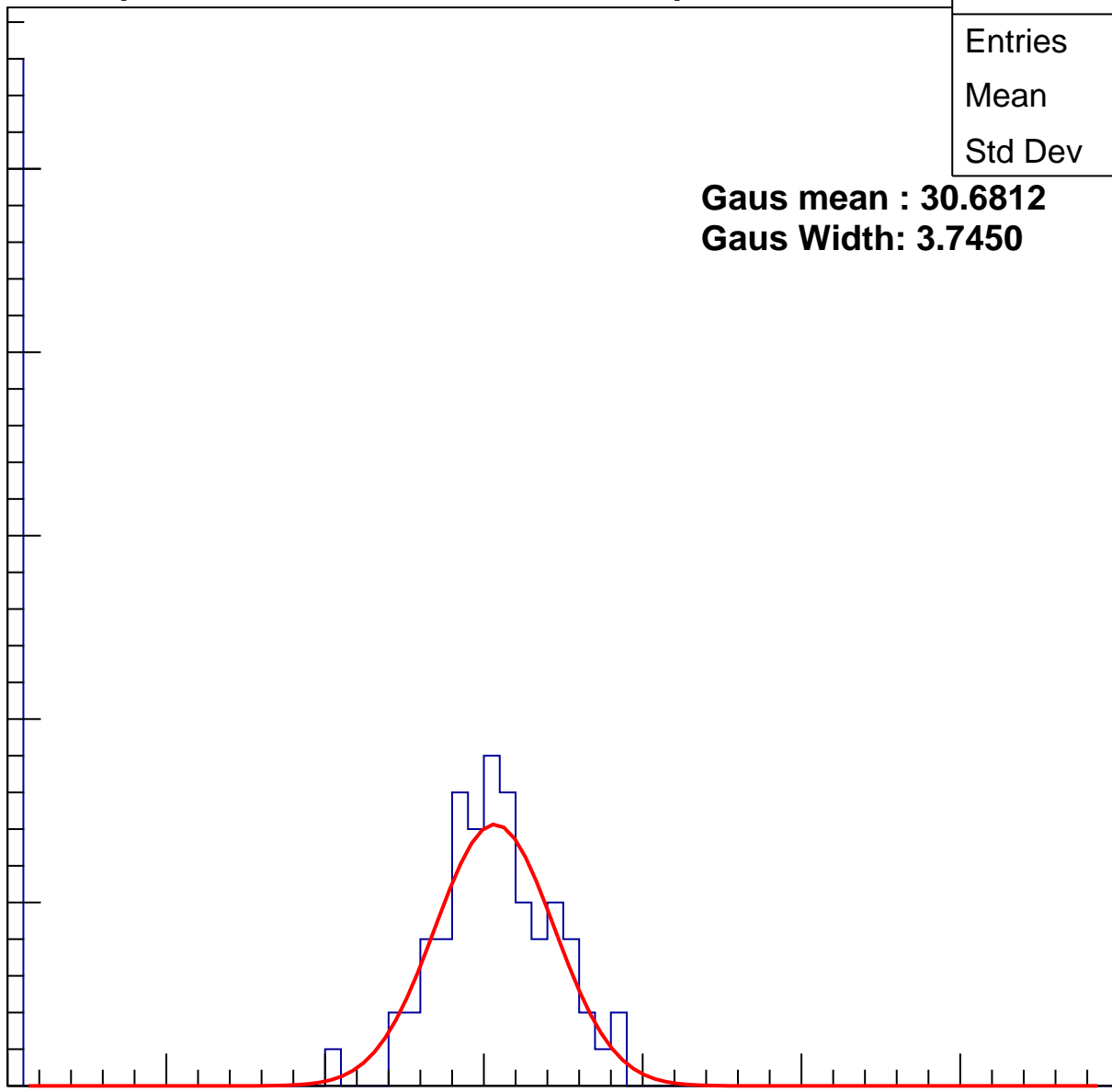
40

50

60

70

ampl



B1L103S, U24-ch27, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	34.85
Std Dev	10.13

Gaus mean : 38.0272

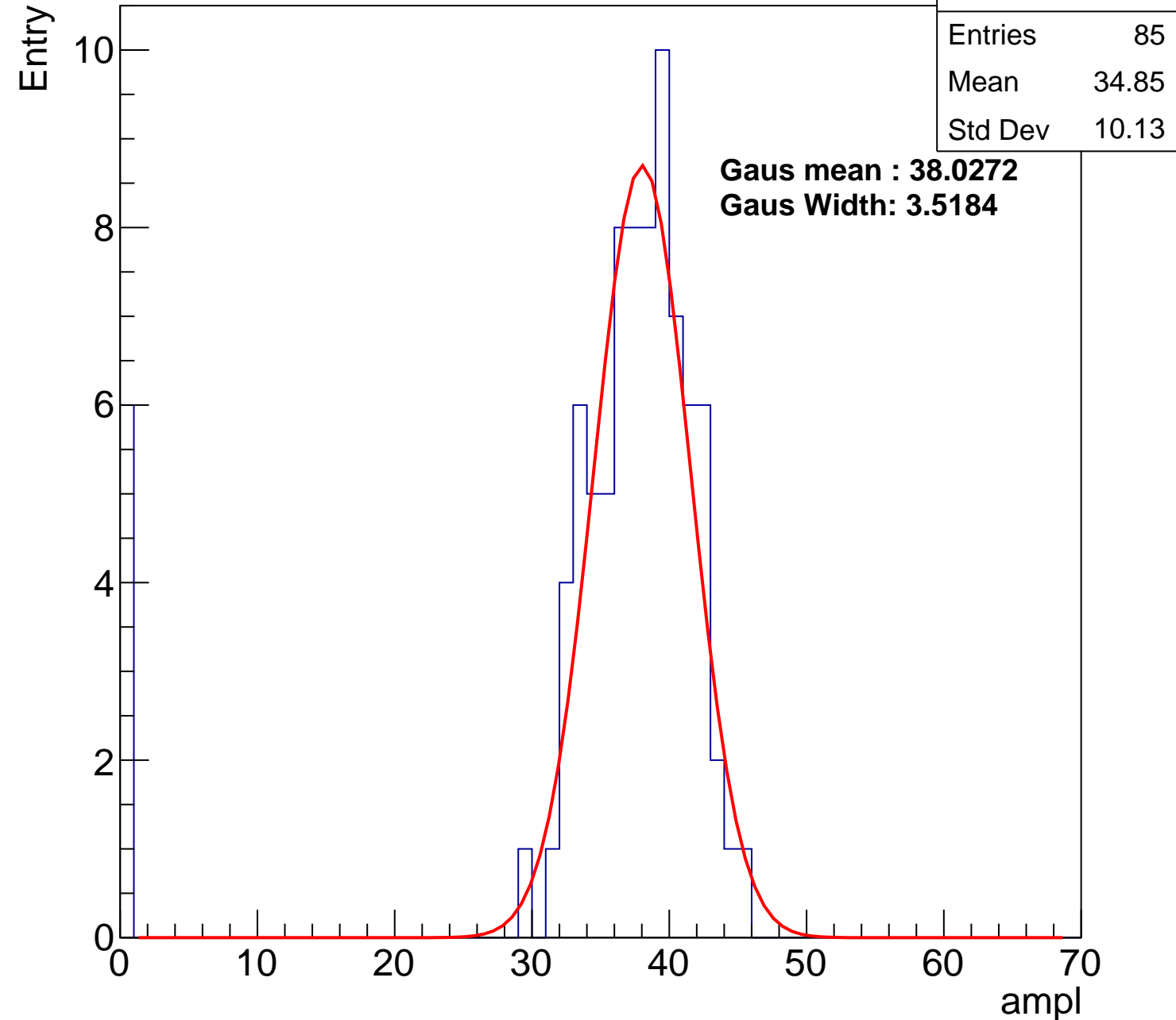
Gaus Width: 3.5184

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch27, adc2

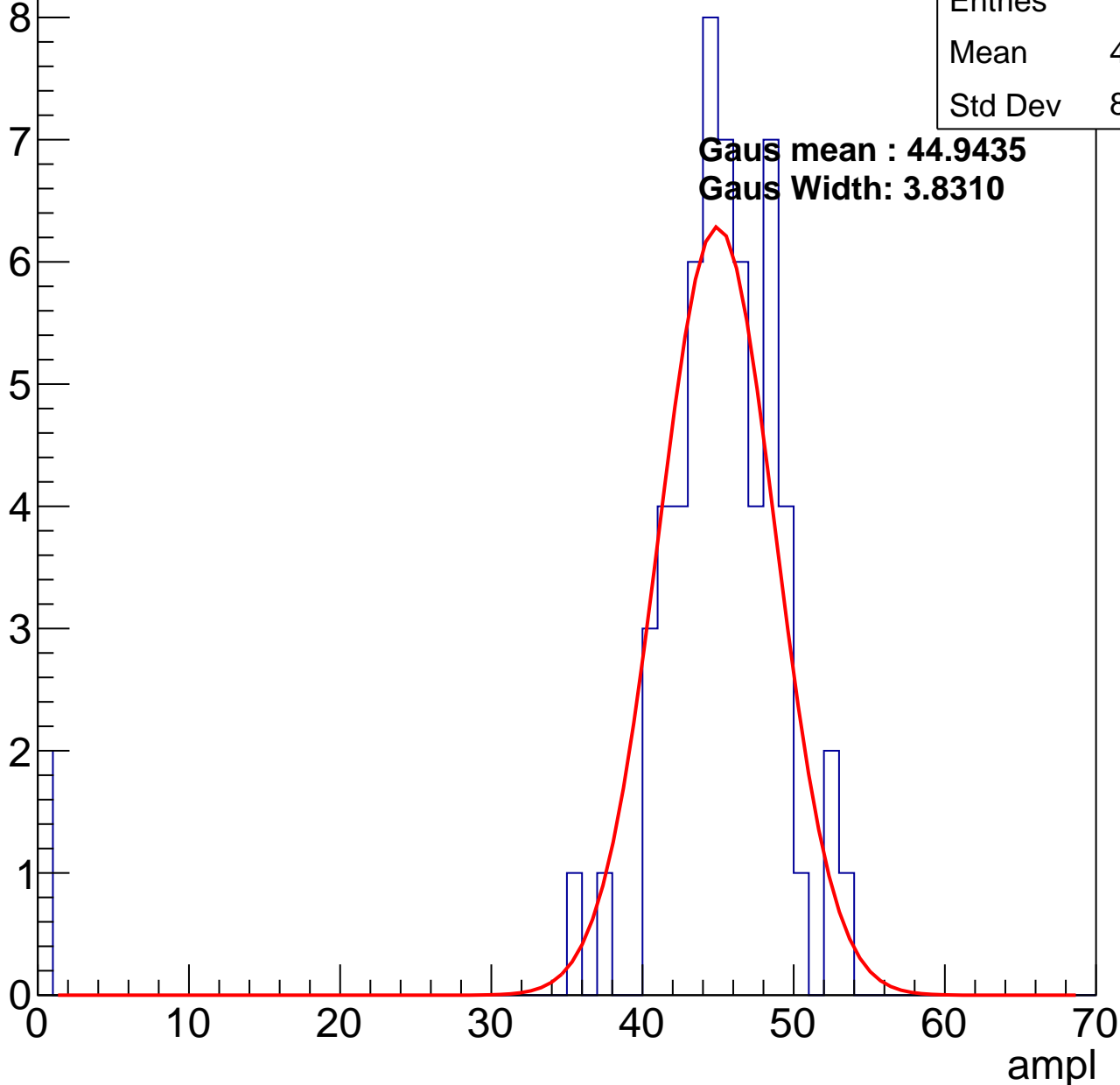
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	43.48
Std Dev	8.698

Gaus mean : 44.9435

Gaus Width: 3.8310



B1L103S, U24-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

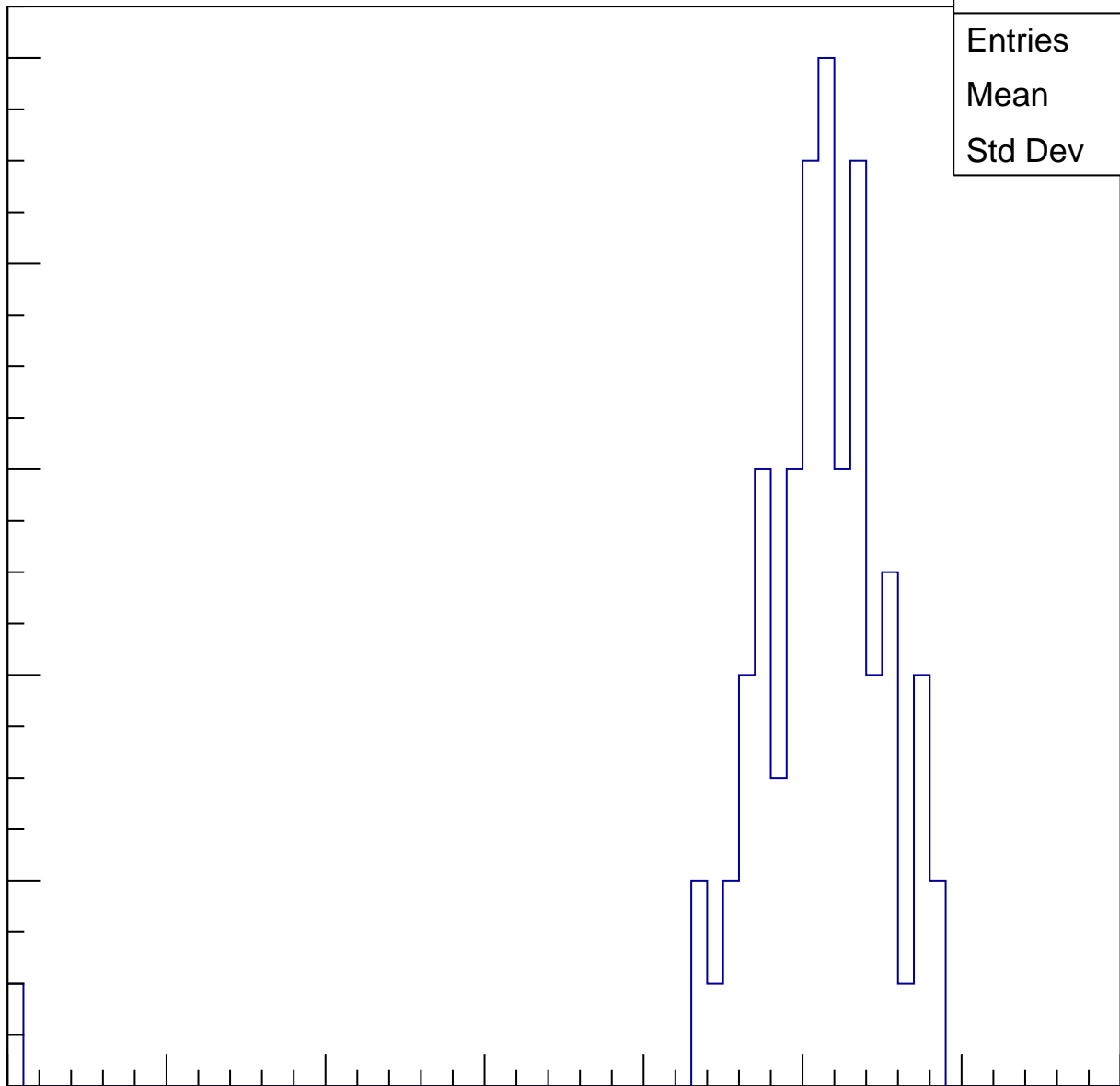
Entries	75
Mean	50.19
Std Dev	6.813

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

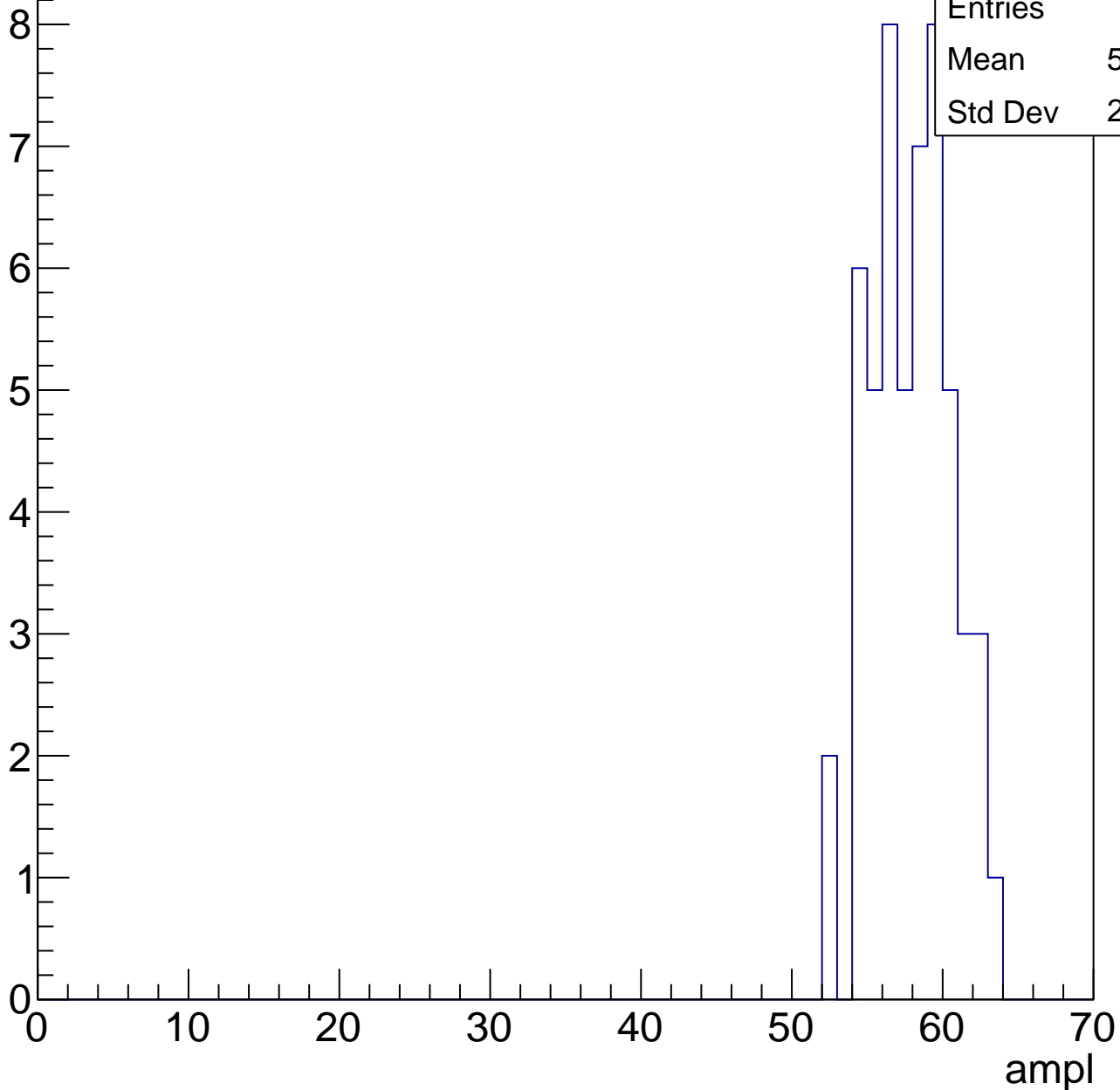


B1L103S, U24-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.47
Std Dev	2.618

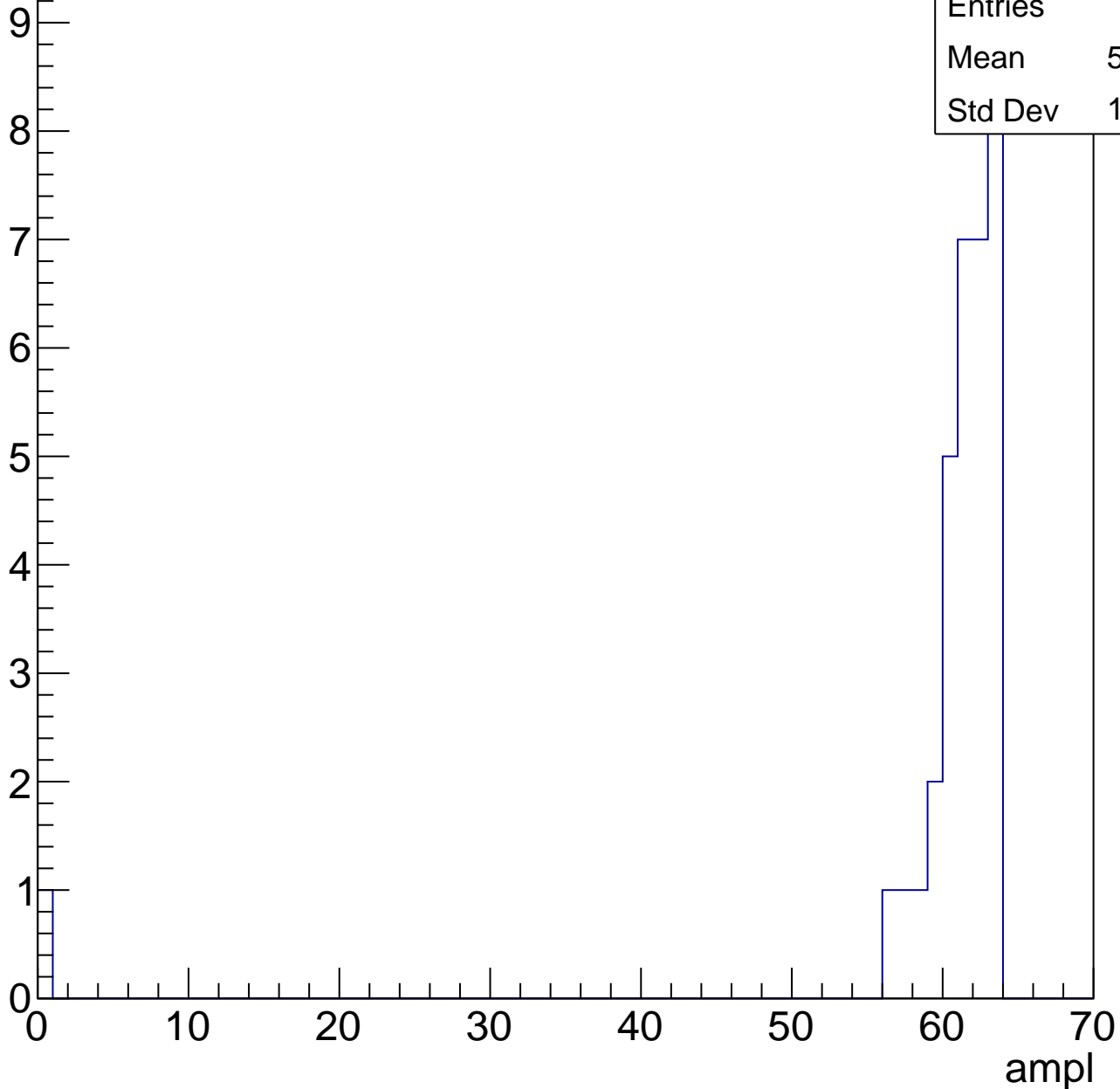


B1L103S, U24-ch27, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	34
Mean	59.32
Std Dev	10.48



B1L103S, U24-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



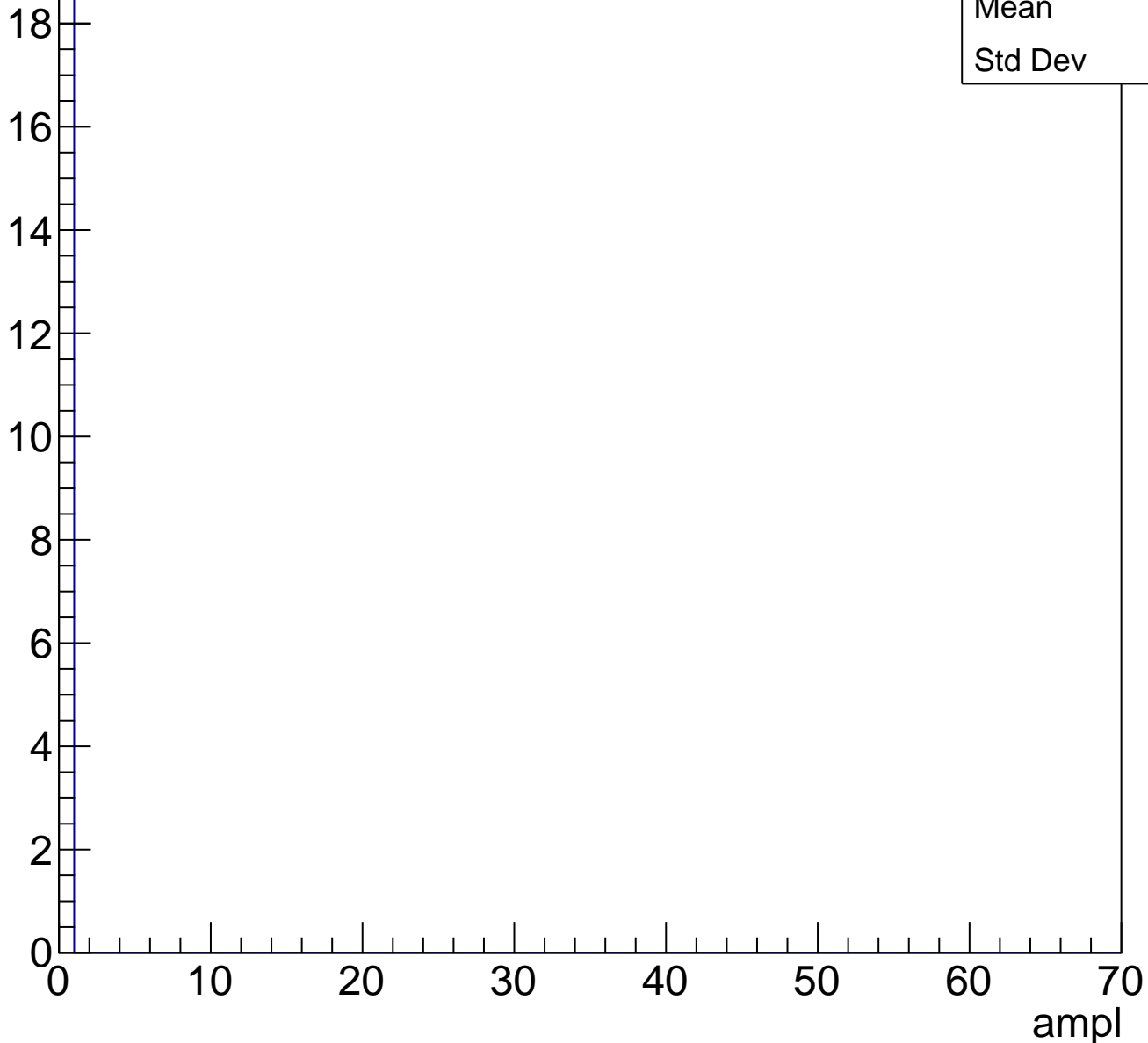
Entries	0
Mean	0
Std Dev	0

B1L103S, U24-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



B1L103S, U24-ch28, adc0

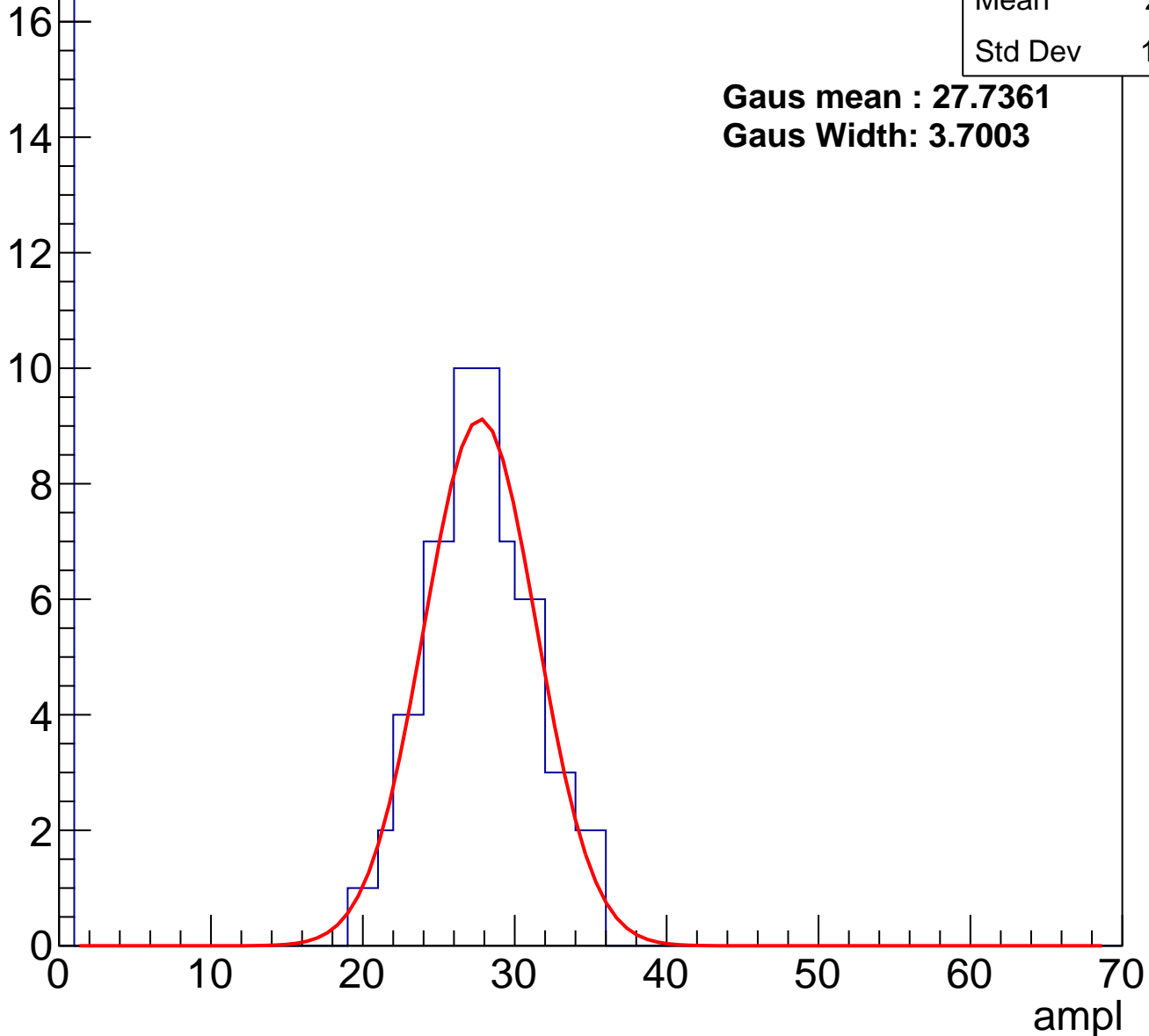
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	22.71
Std Dev	10.65

Gaus mean : 27.7361

Gaus Width: 3.7003

Entry



B1L103S, U24-ch28, adc1

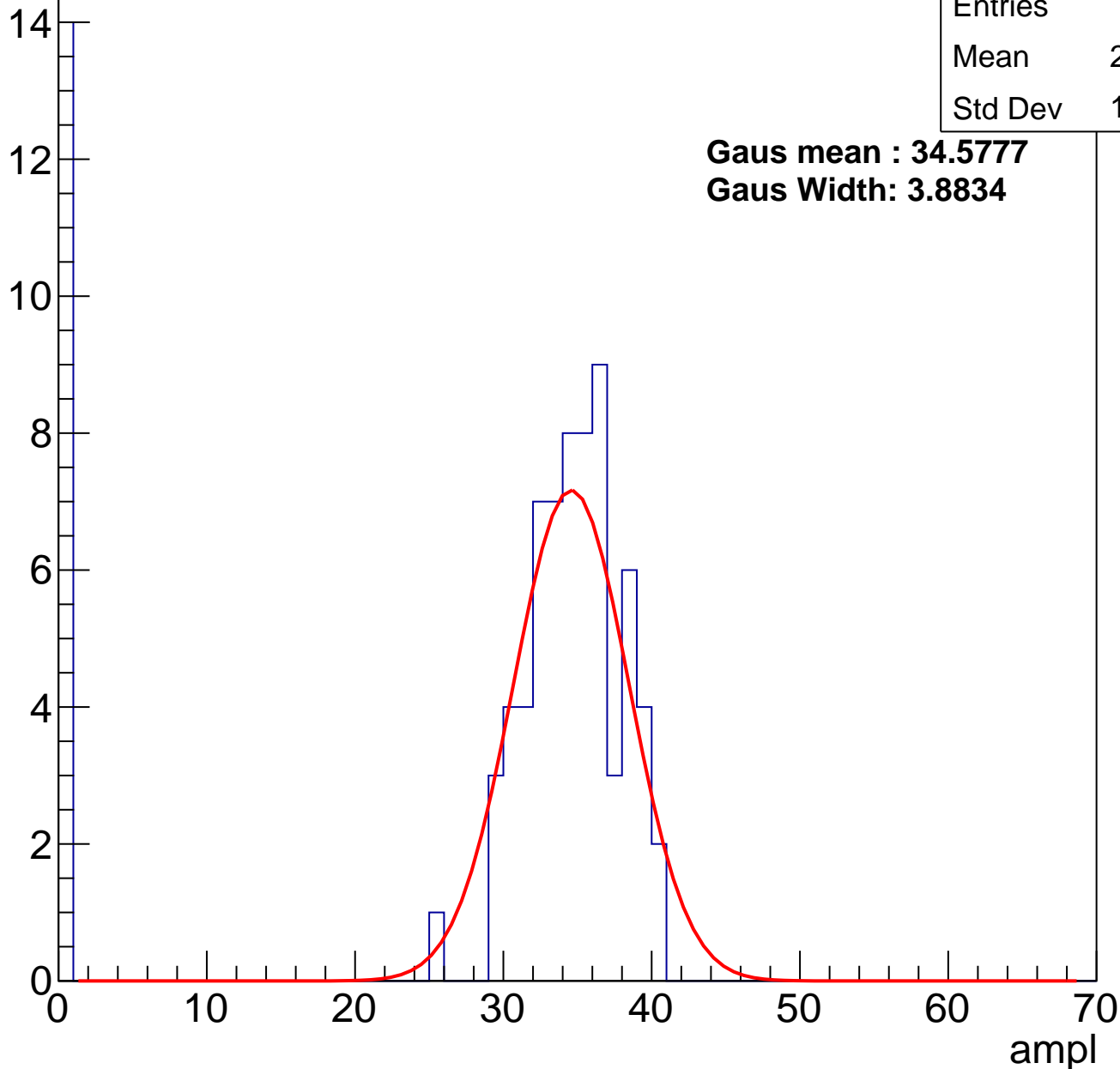
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	28.27
Std Dev	13.32

Gaus mean : 34.5777

Gaus Width: 3.8834

Entry



B1L103S, U24-ch28, adc2

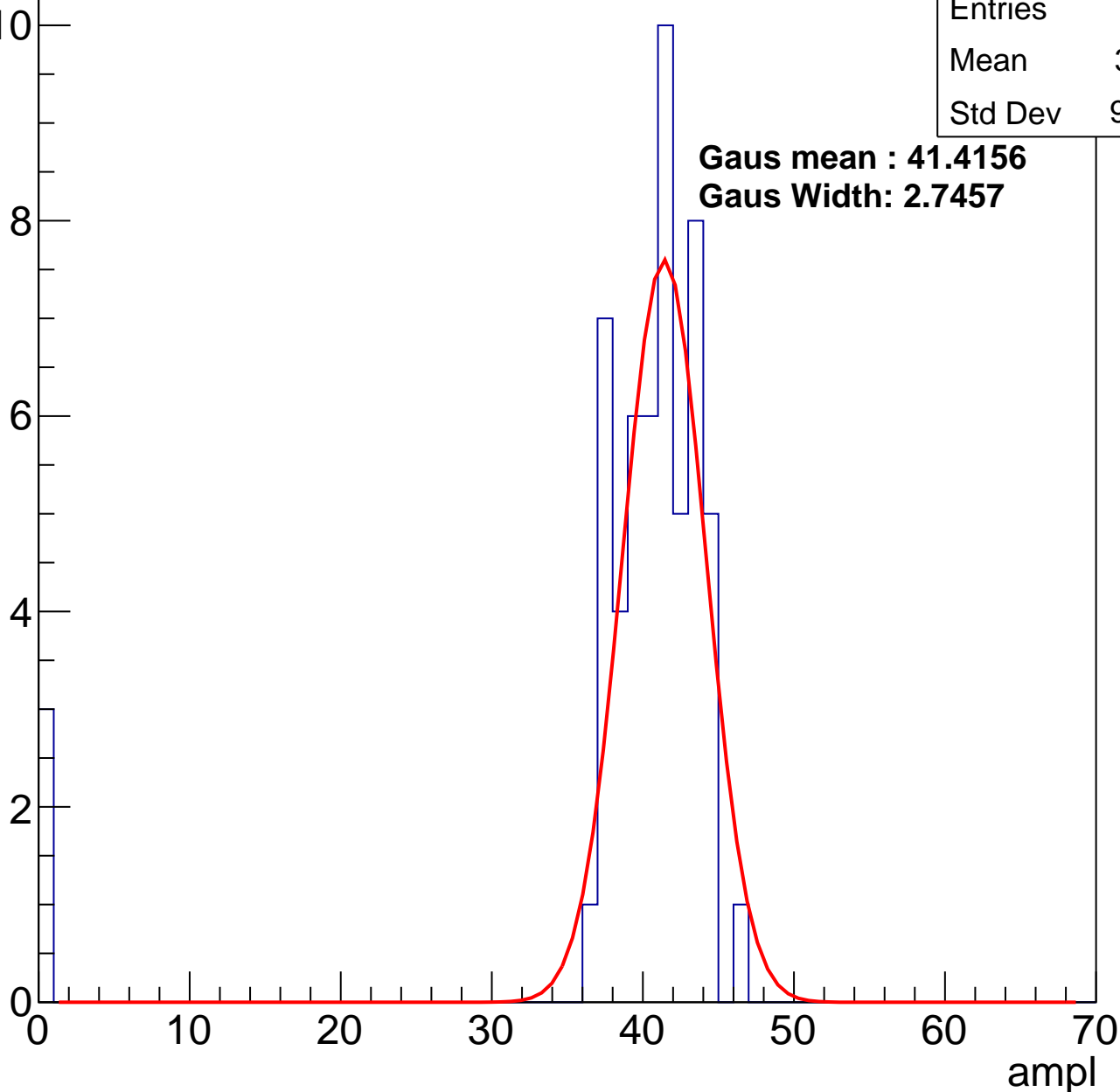
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	38.41
Std Dev	9.428

Gaus mean : 41.4156

Gaus Width: 2.7457

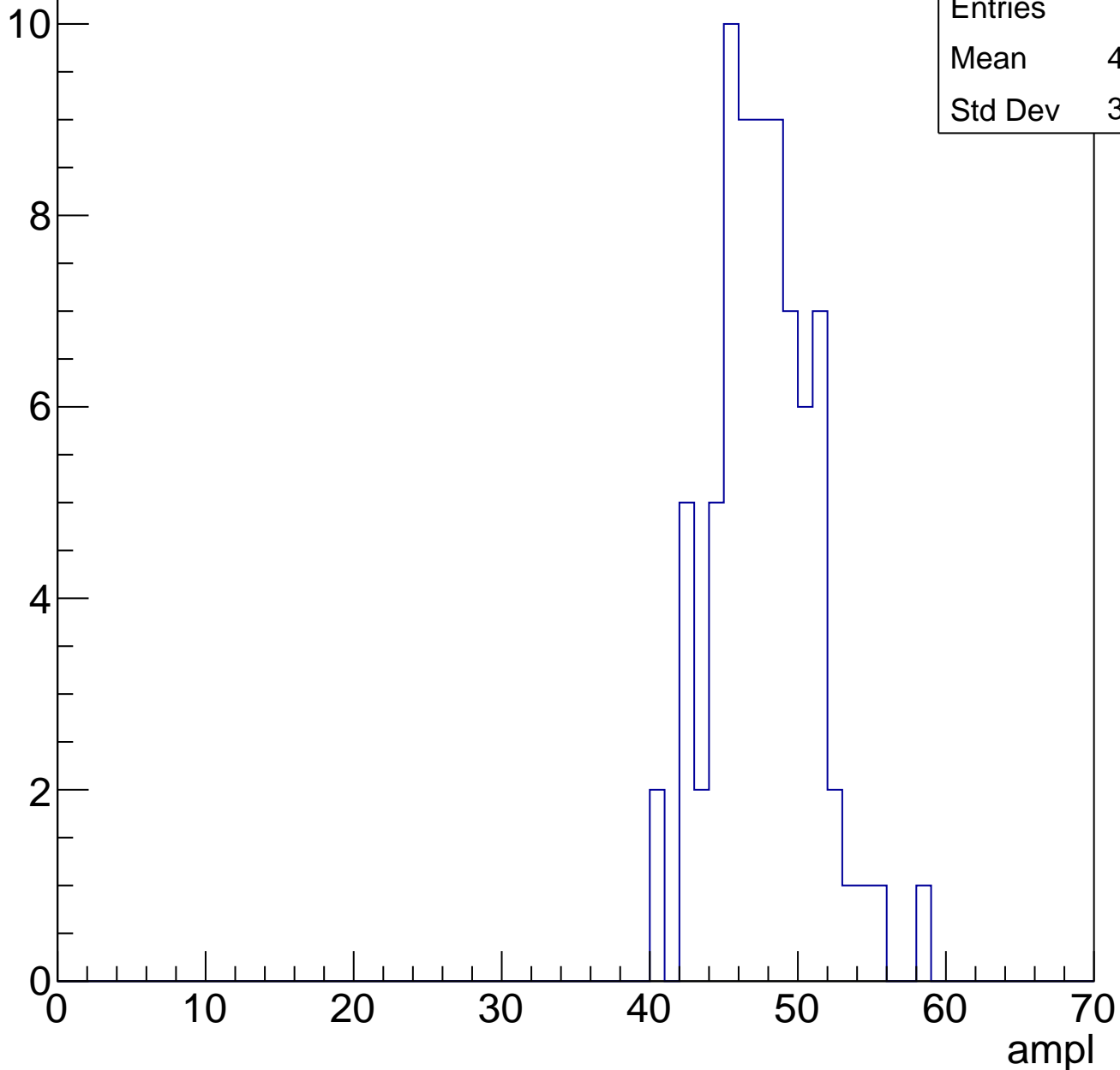


B1L103S, U24-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2

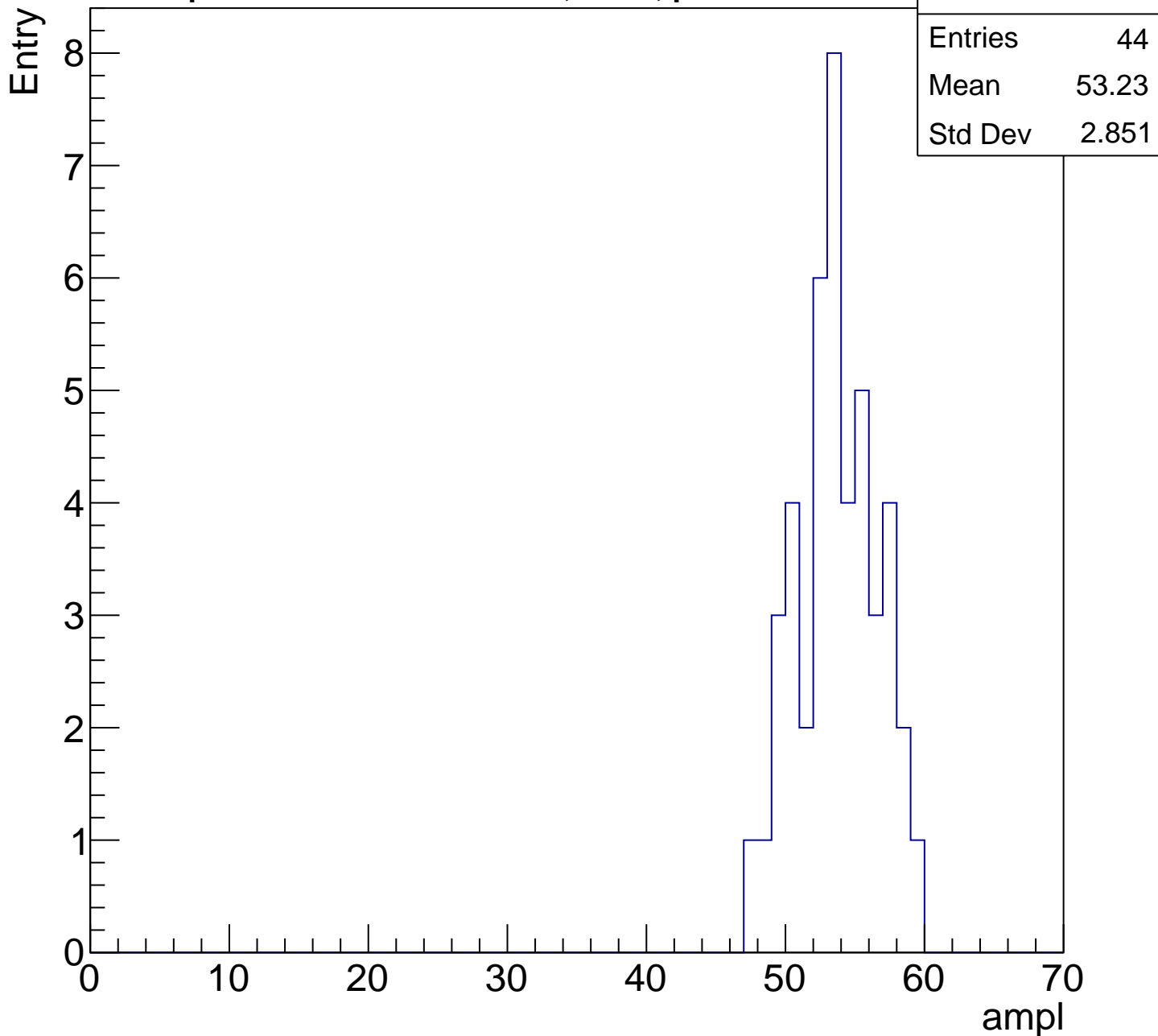
Entries	77
Mean	47.26
Std Dev	3.355

Entry



B1L103S, U24-ch28, adc4

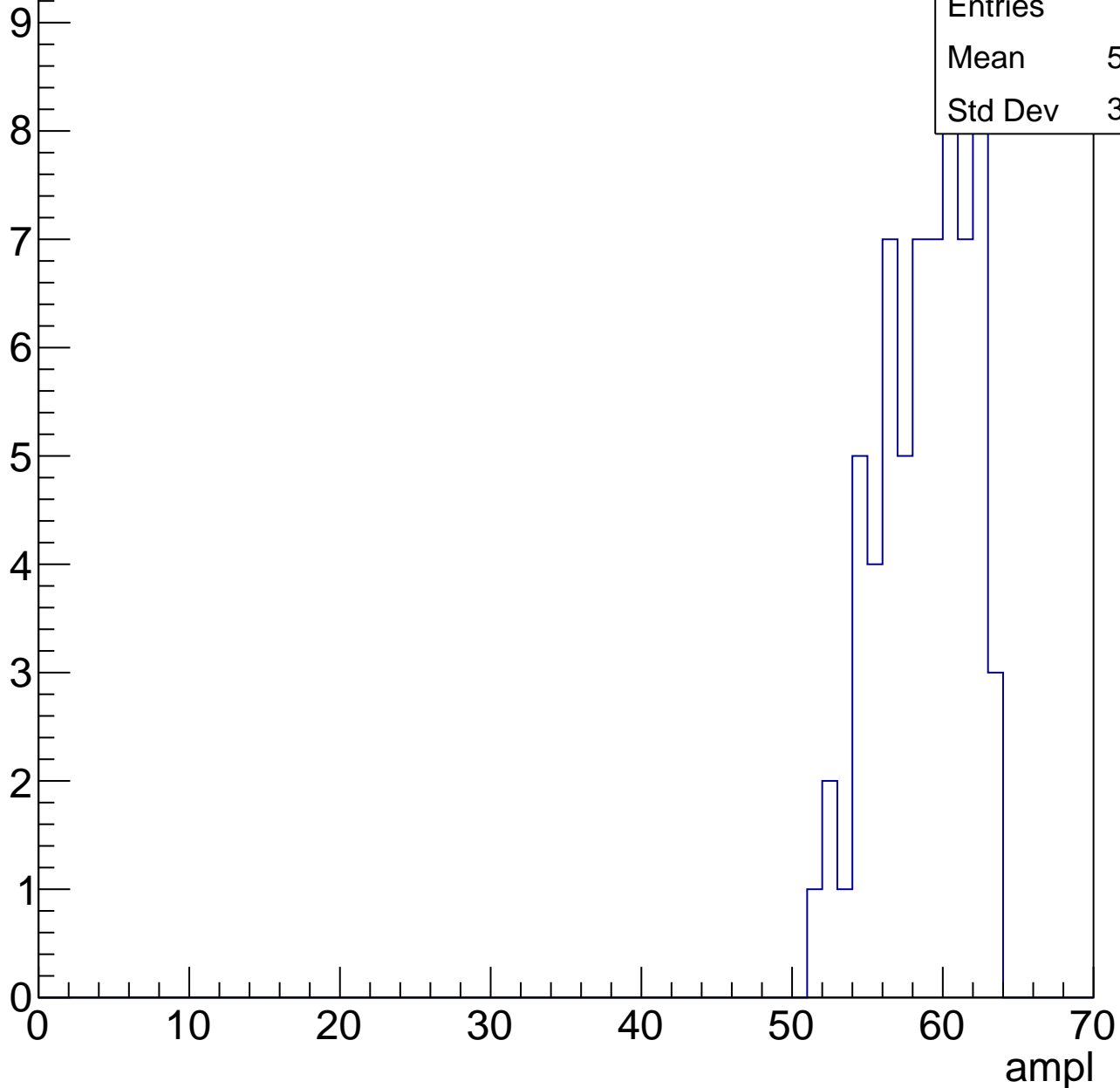
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U24-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

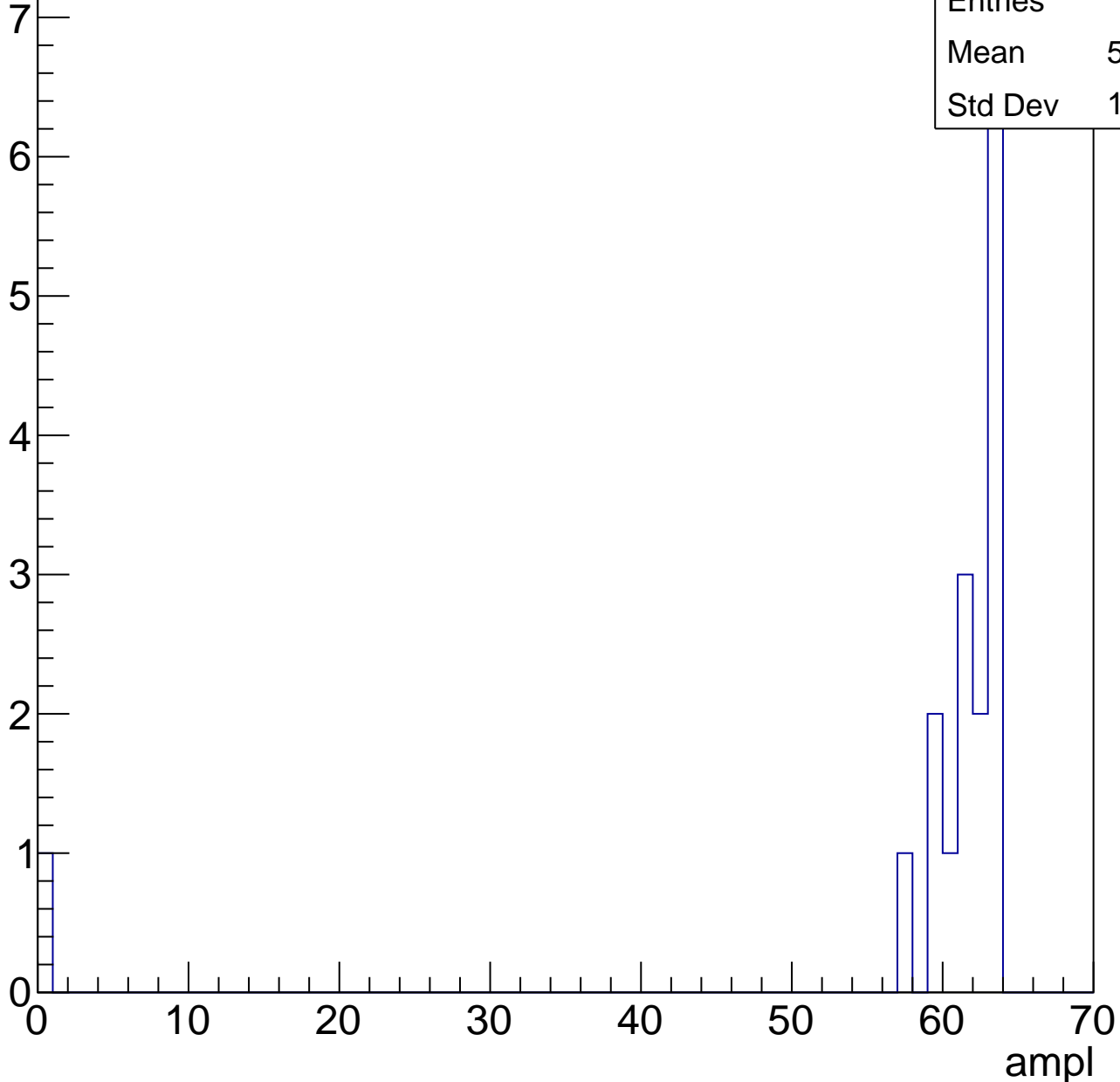


B1L103S, U24-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.82
Std Dev	14.56



B1L103S, U24-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch29, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	20.3
Std Dev	13.85

Gaus mean : 29.8999

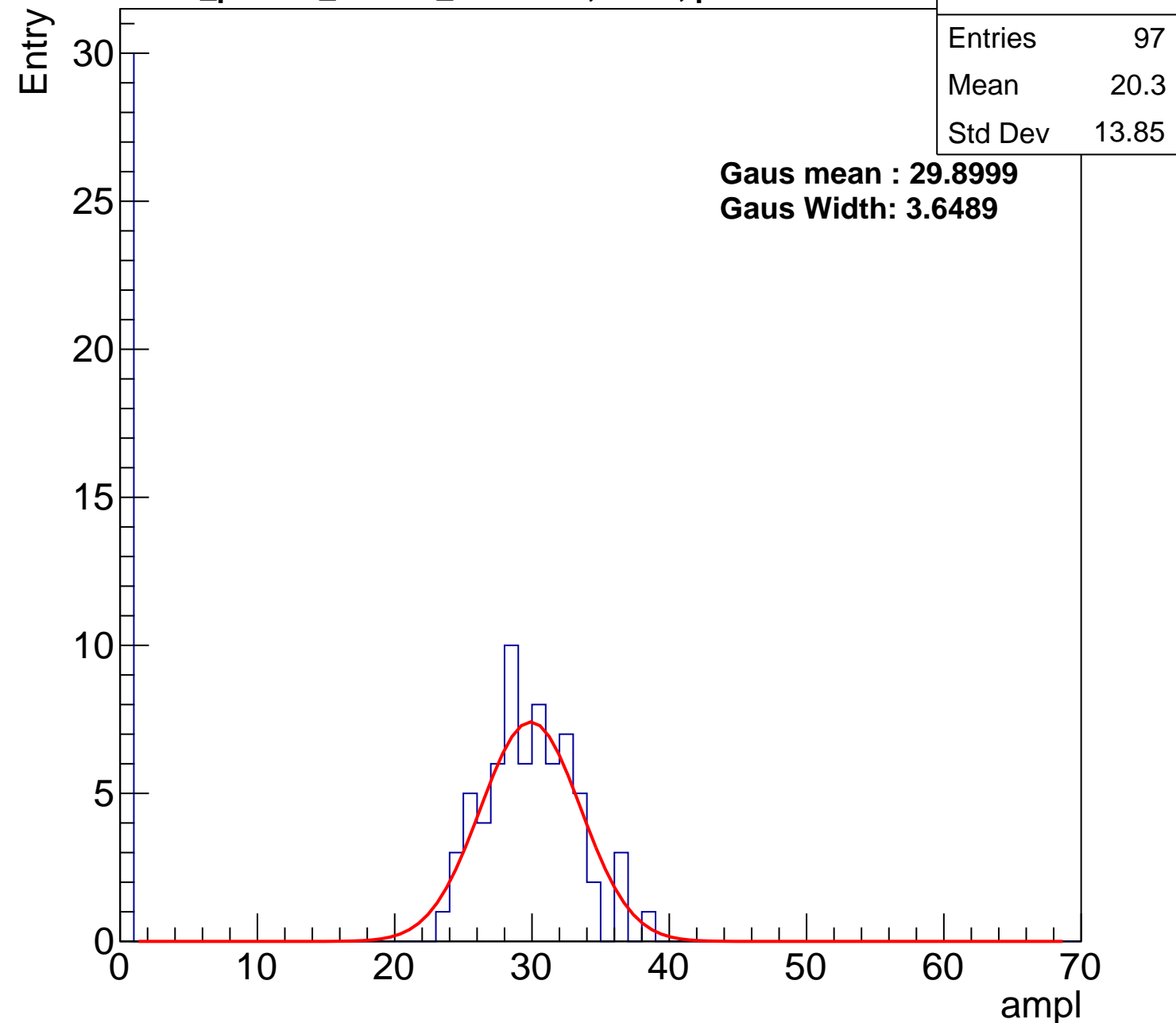
Gaus Width: 3.6489

Entry

30
25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch29, adc1

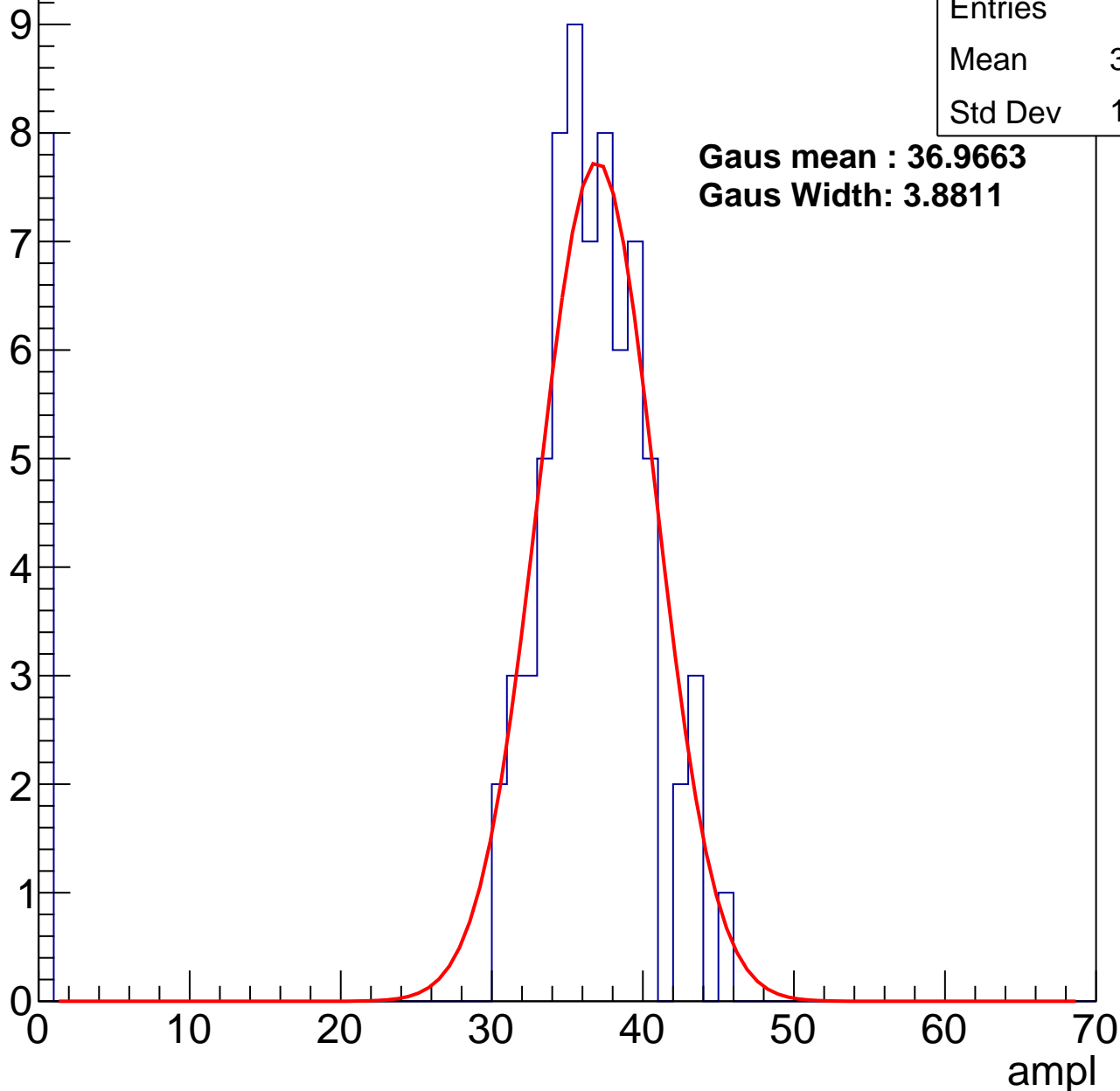
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	32.57
Std Dev	11.52

Gaus mean : 36.9663

Gaus Width: 3.8811



B1L103S, U24-ch29, adc2

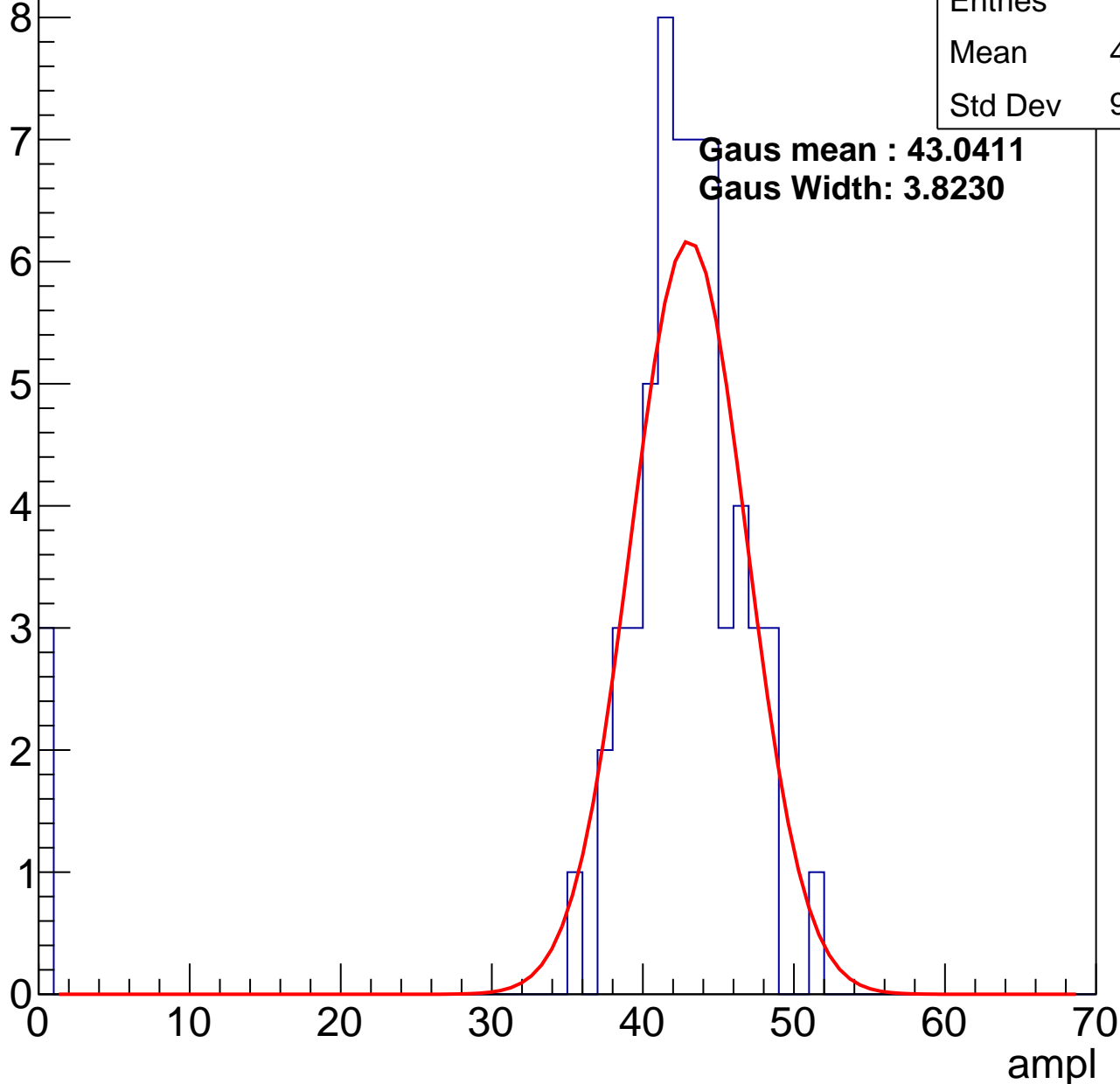
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	40.43
Std Dev	9.778

Gaus mean : 43.0411

Gaus Width: 3.8230



B1L103S, U24-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	47.01
Std Dev	10.96

Entry

10

8

6

4

2

0

ampl

0

10

20

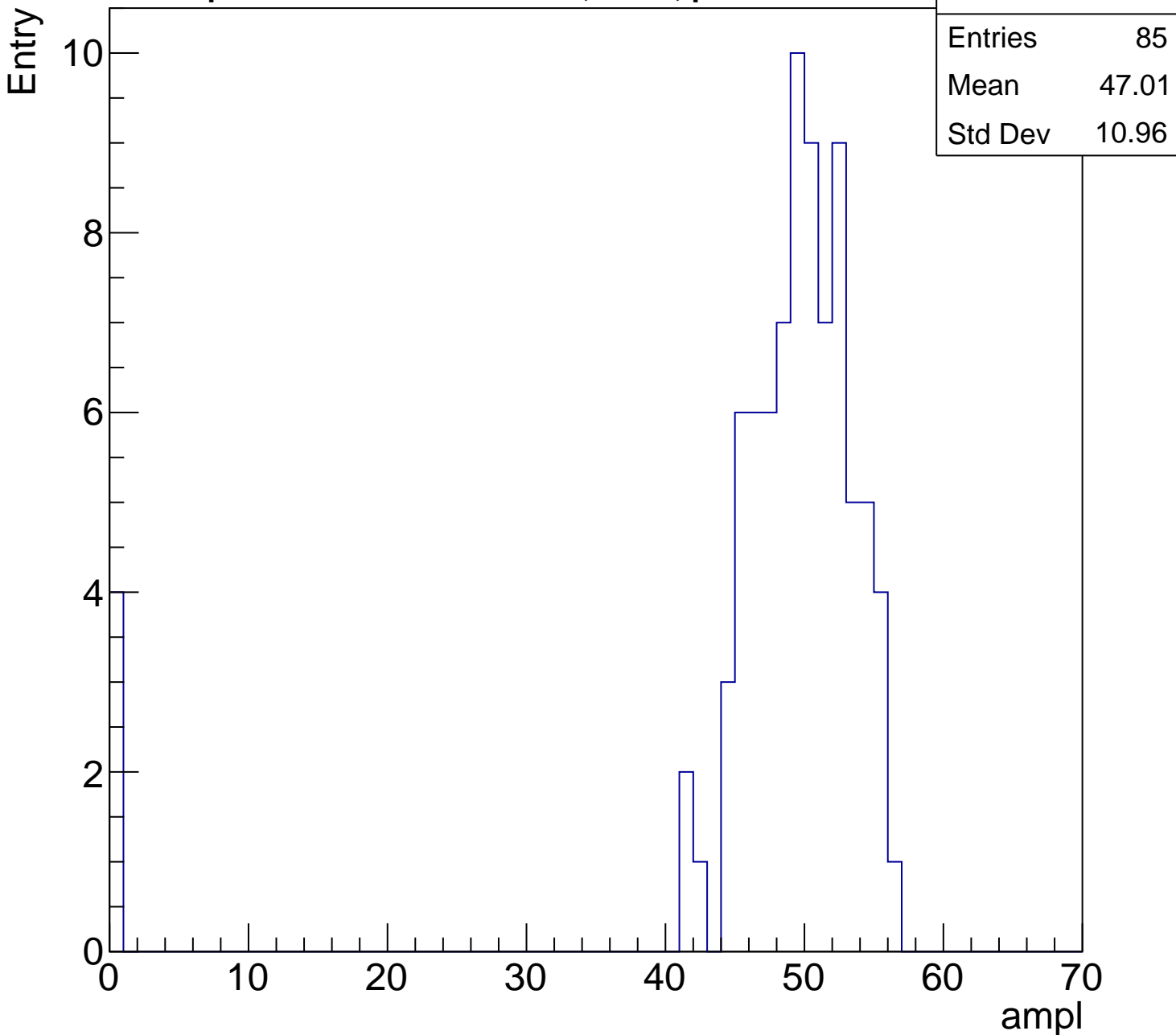
30

40

50

60

70

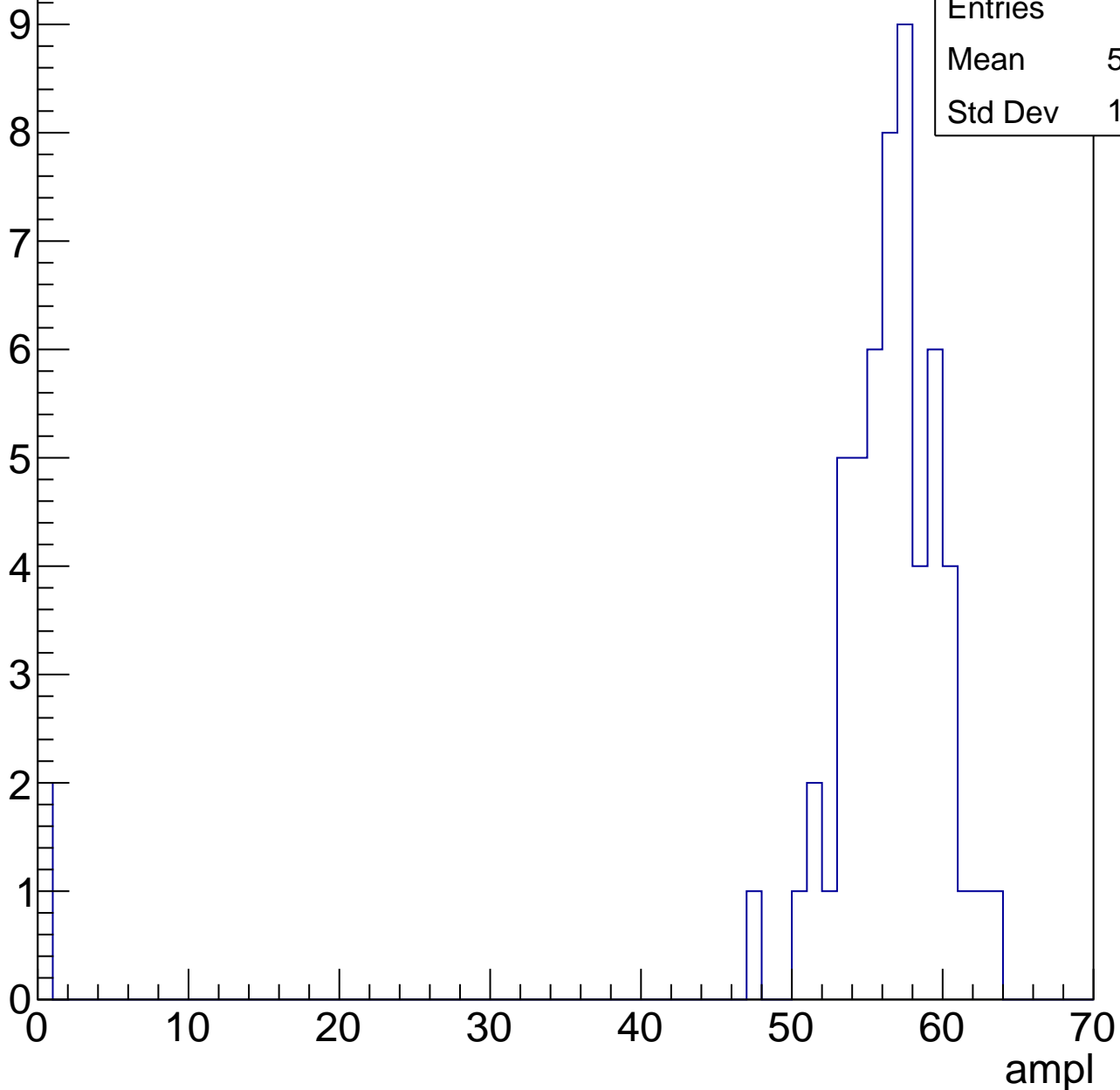


B1L103S, U24-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.19
Std Dev	10.75

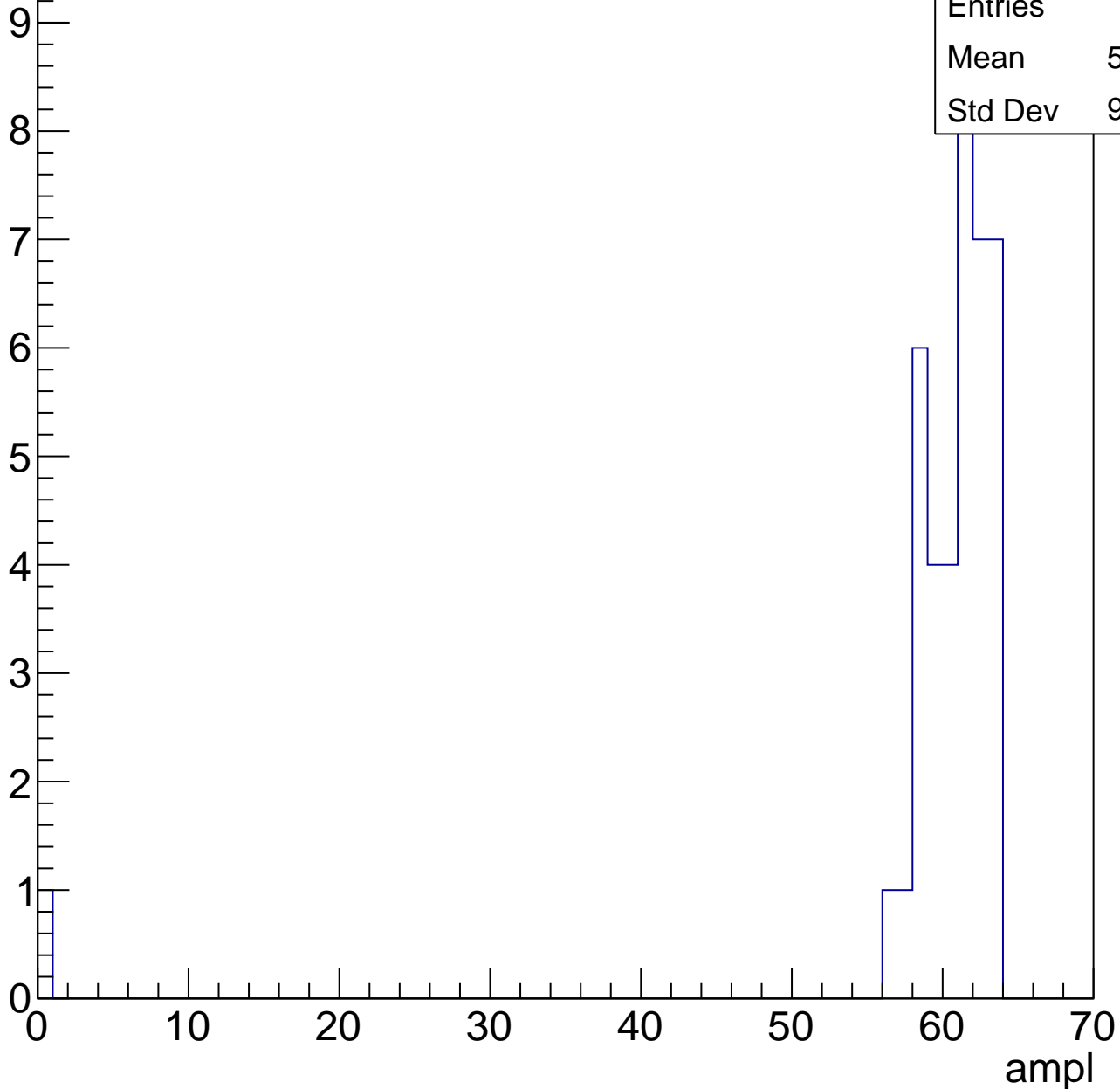


B1L103S, U24-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	59.02
Std Dev	9.637



B1L103S, U24-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	6
Mean	61.67
Std Dev	1.247

B1L103S, U24-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch30, adc0

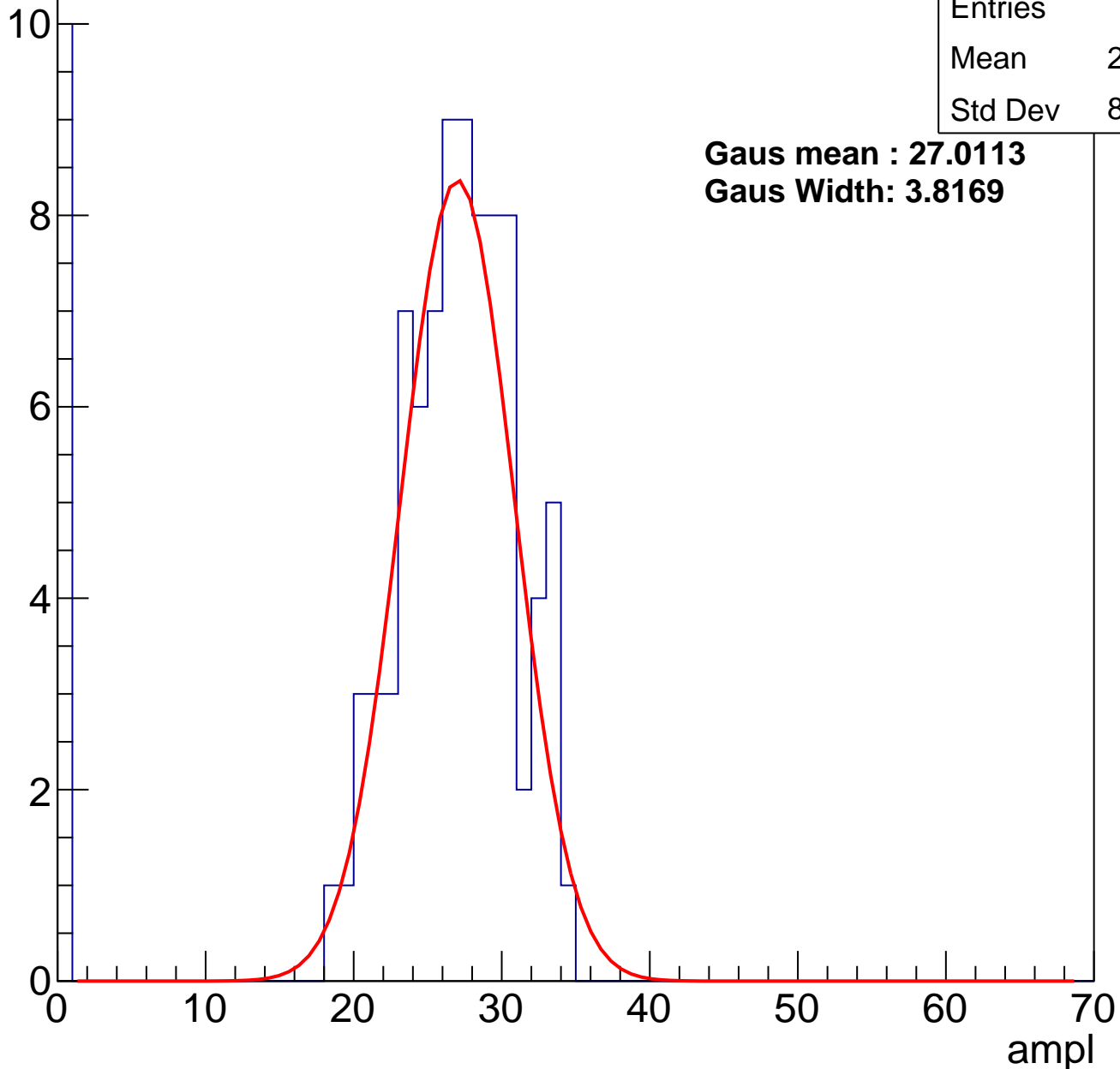
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	23.87
Std Dev	8.893

Gaus mean : 27.0113

Gaus Width: 3.8169

Entry



B1L103S, U24-ch30, adc1

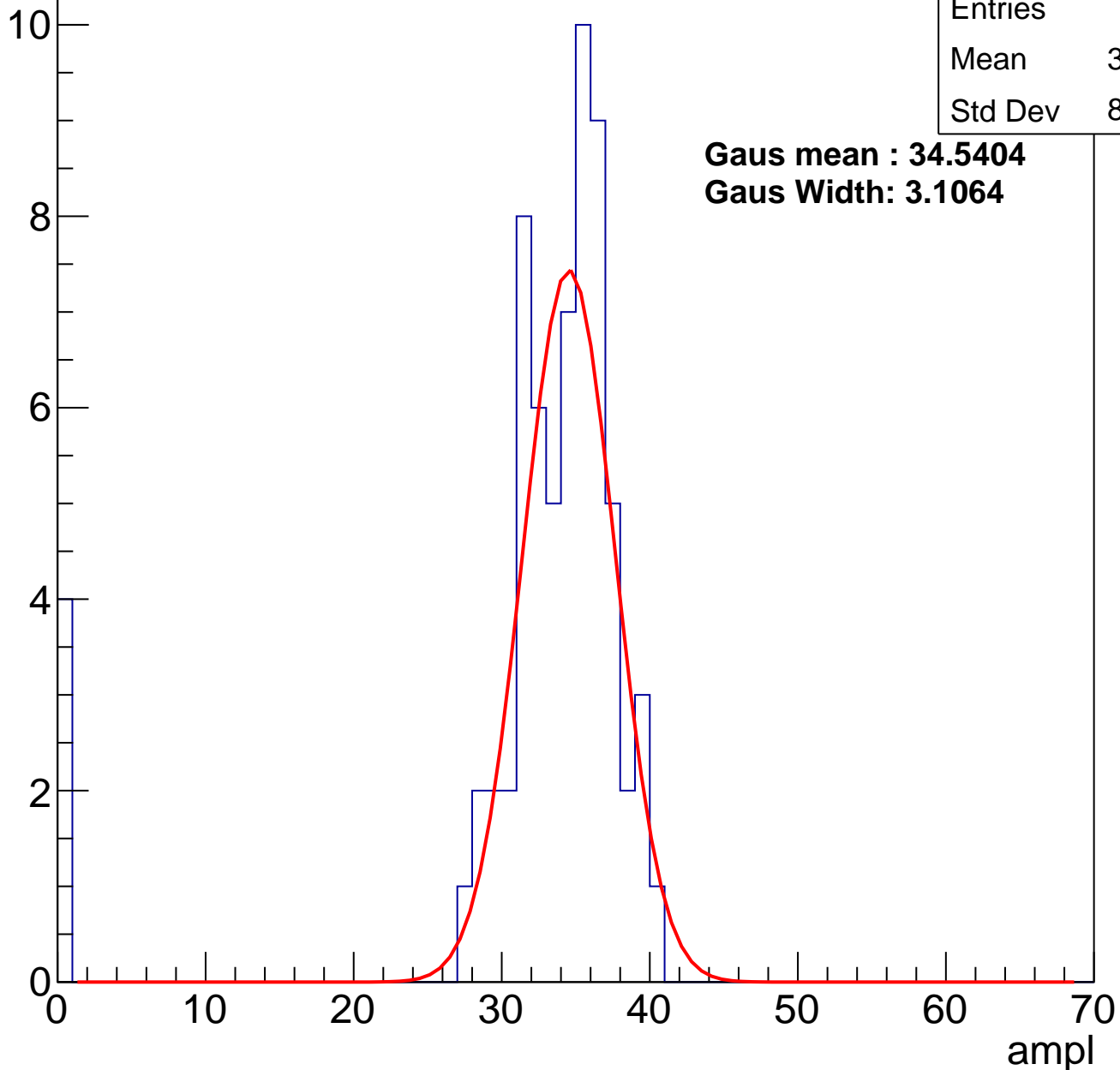
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	31.88
Std Dev	8.518

Gaus mean : 34.5404

Gaus Width: 3.1064

Entry



B1L103S, U24-ch30, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	34.51
Std Dev	14.76

Gaus mean : 41.3091

Gaus Width: 2.8110

Entry

10

8

6

4

2

0

0

10

20

30

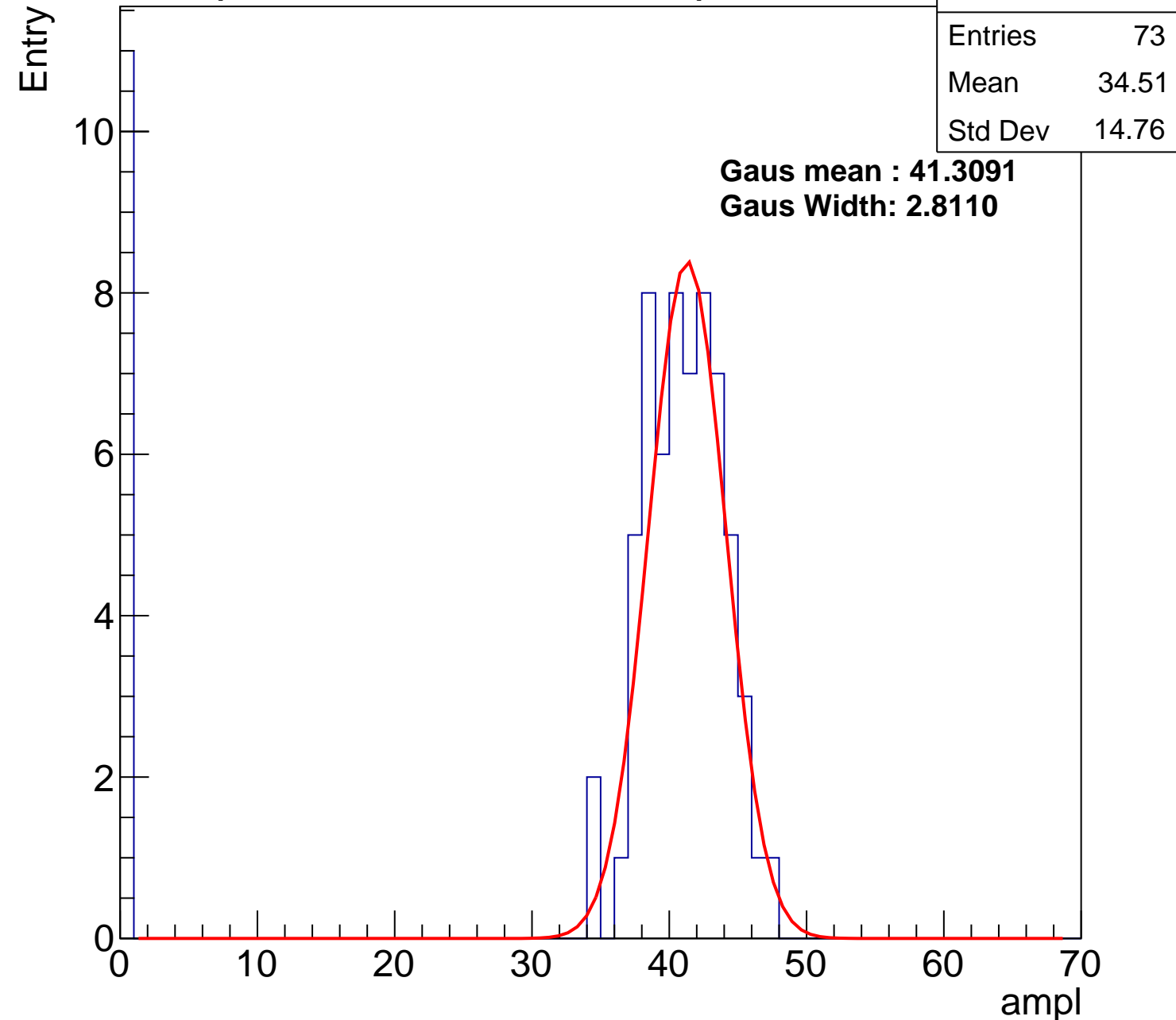
40

50

60

70

ampl

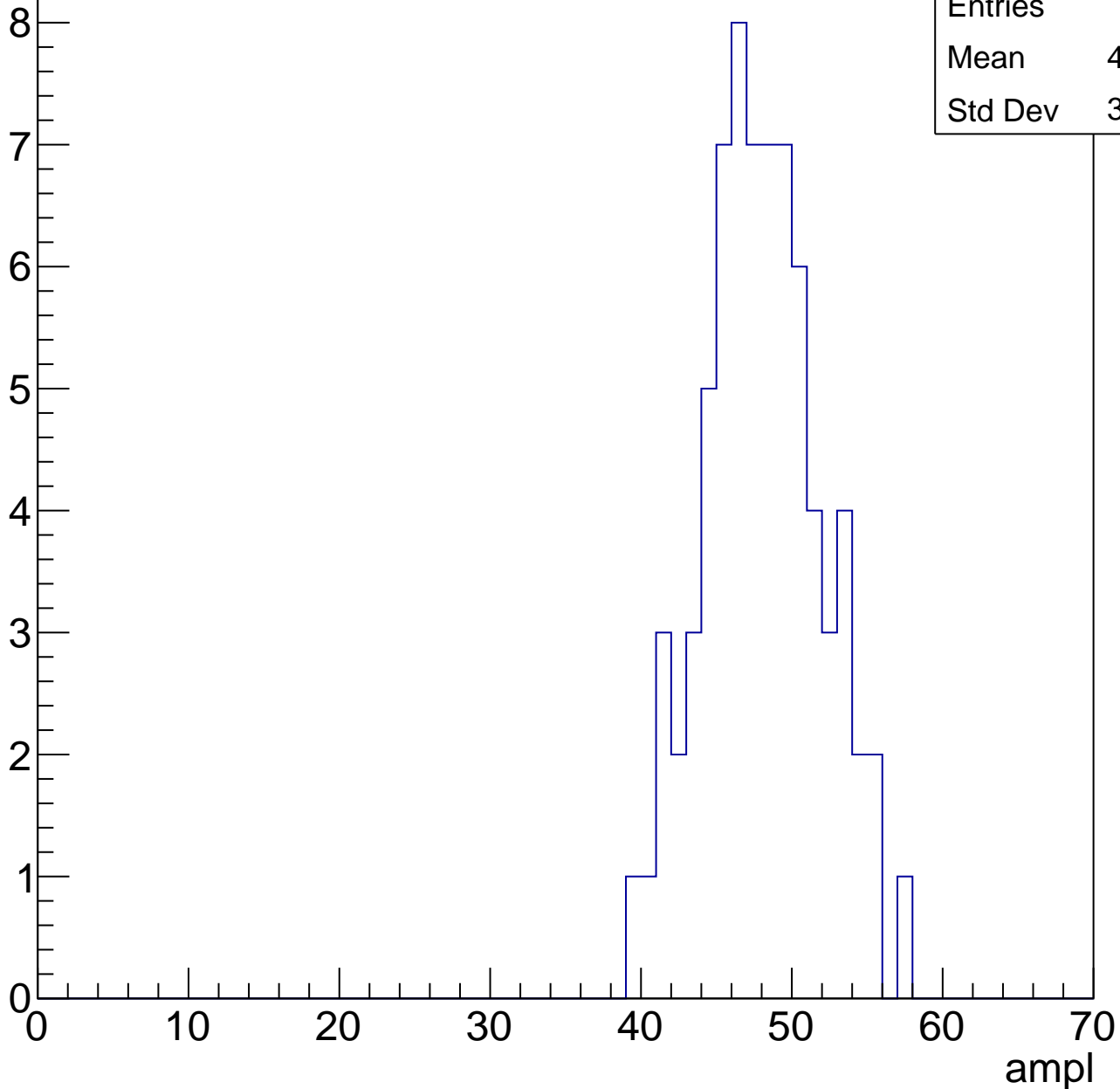


B1L103S, U24-ch30, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	47.58
Std Dev	3.835

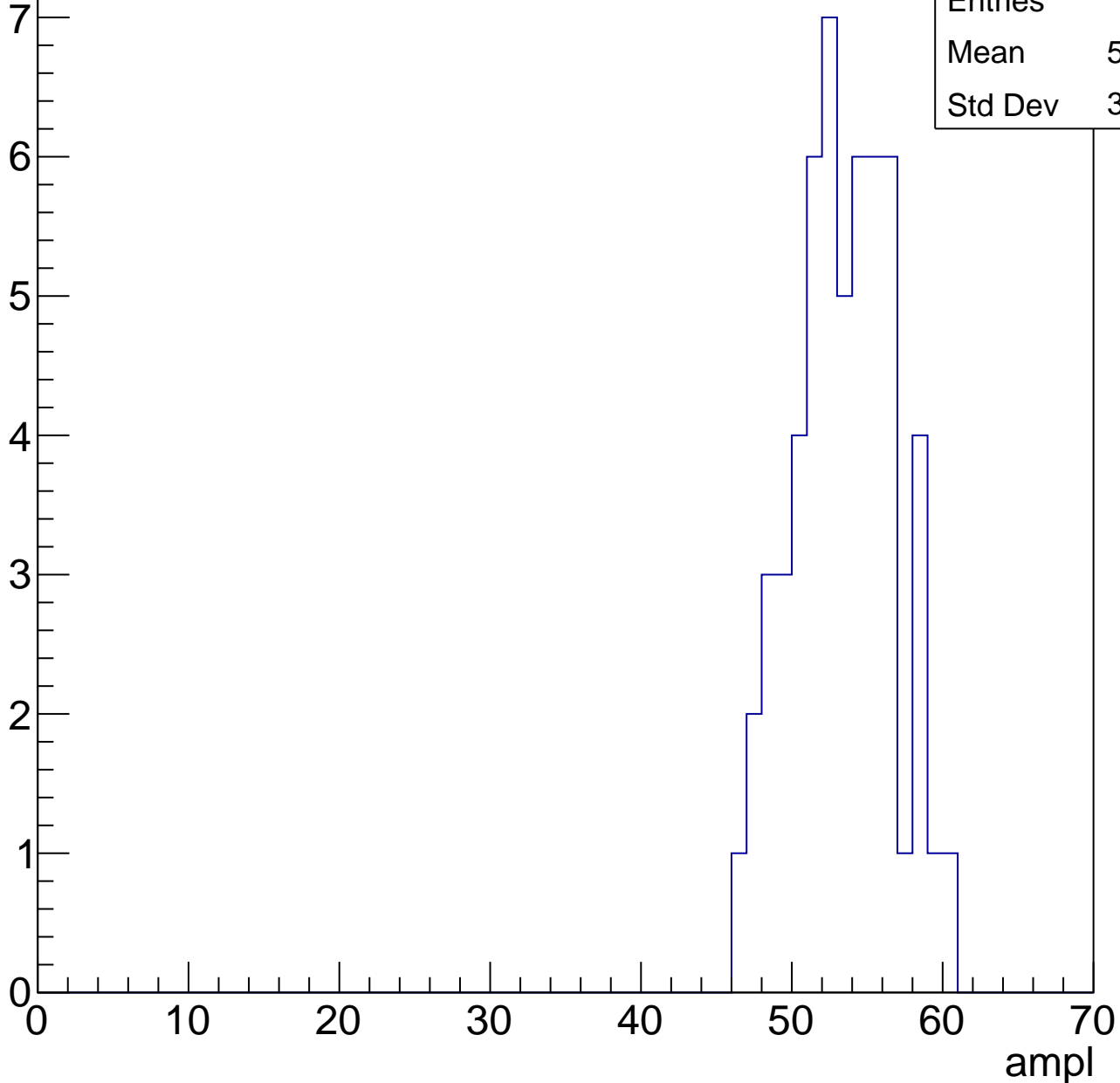


B1L103S, U24-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

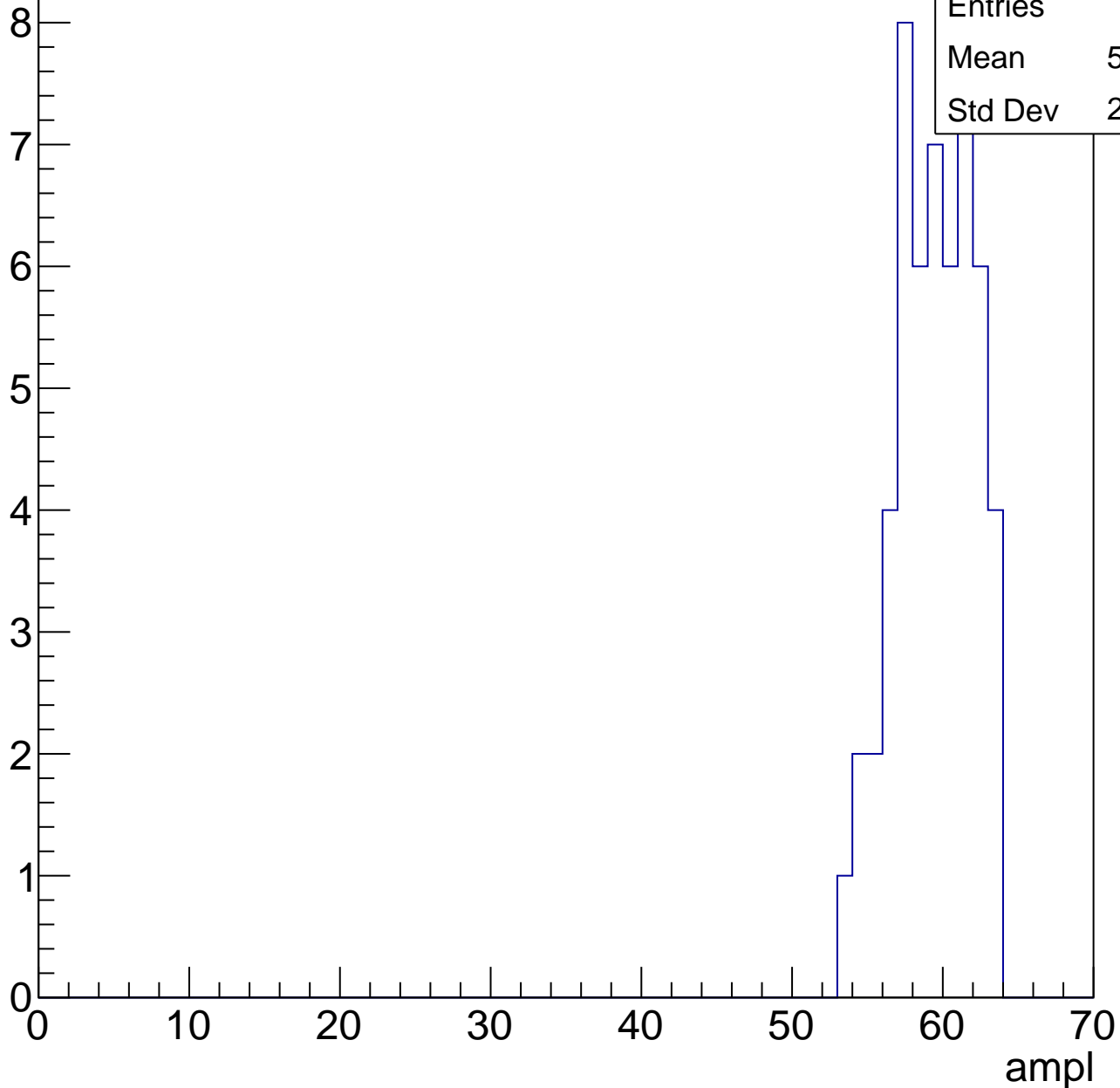
Entries	56
Mean	52.93
Std Dev	3.262



B1L103S, U24-ch30, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



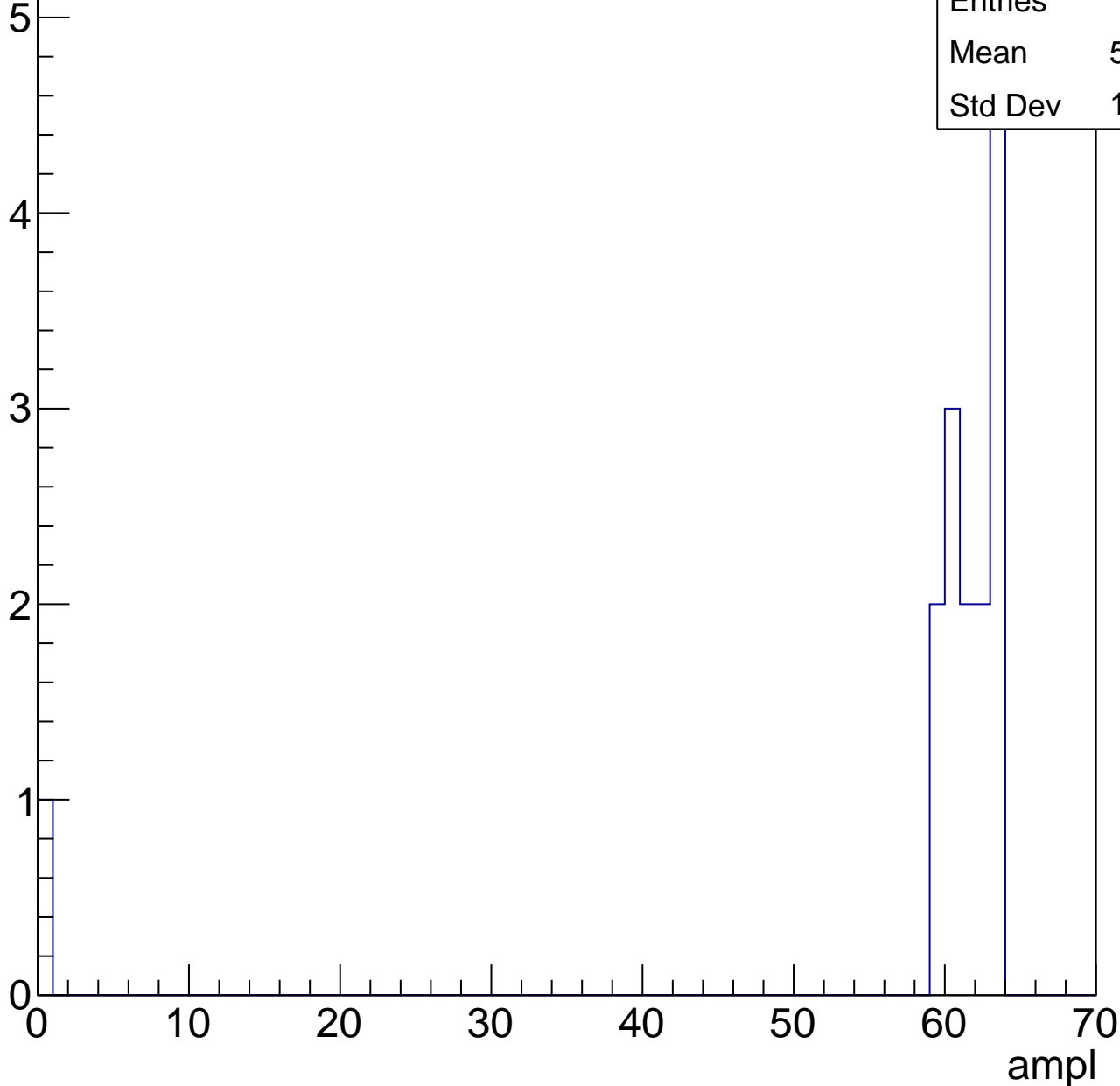
Entries	54
Mean	58.96
Std Dev	2.538

B1L103S, U24-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.27
Std Dev	15.37



B1L103S, U24-ch30, adc7

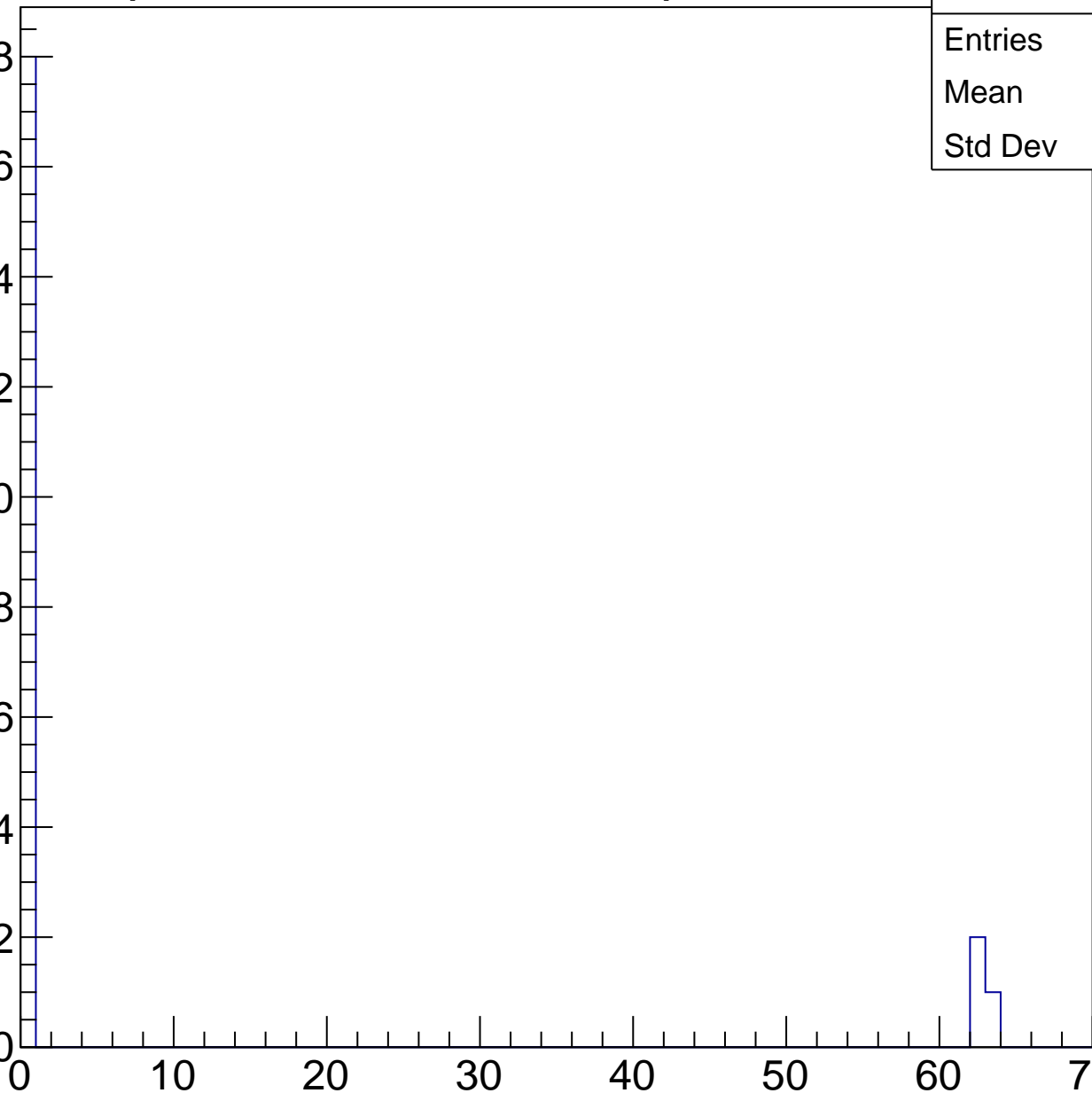
calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	21
Mean	8.905
Std Dev	21.81

ampl



B1L103S, U24-ch31, adc0

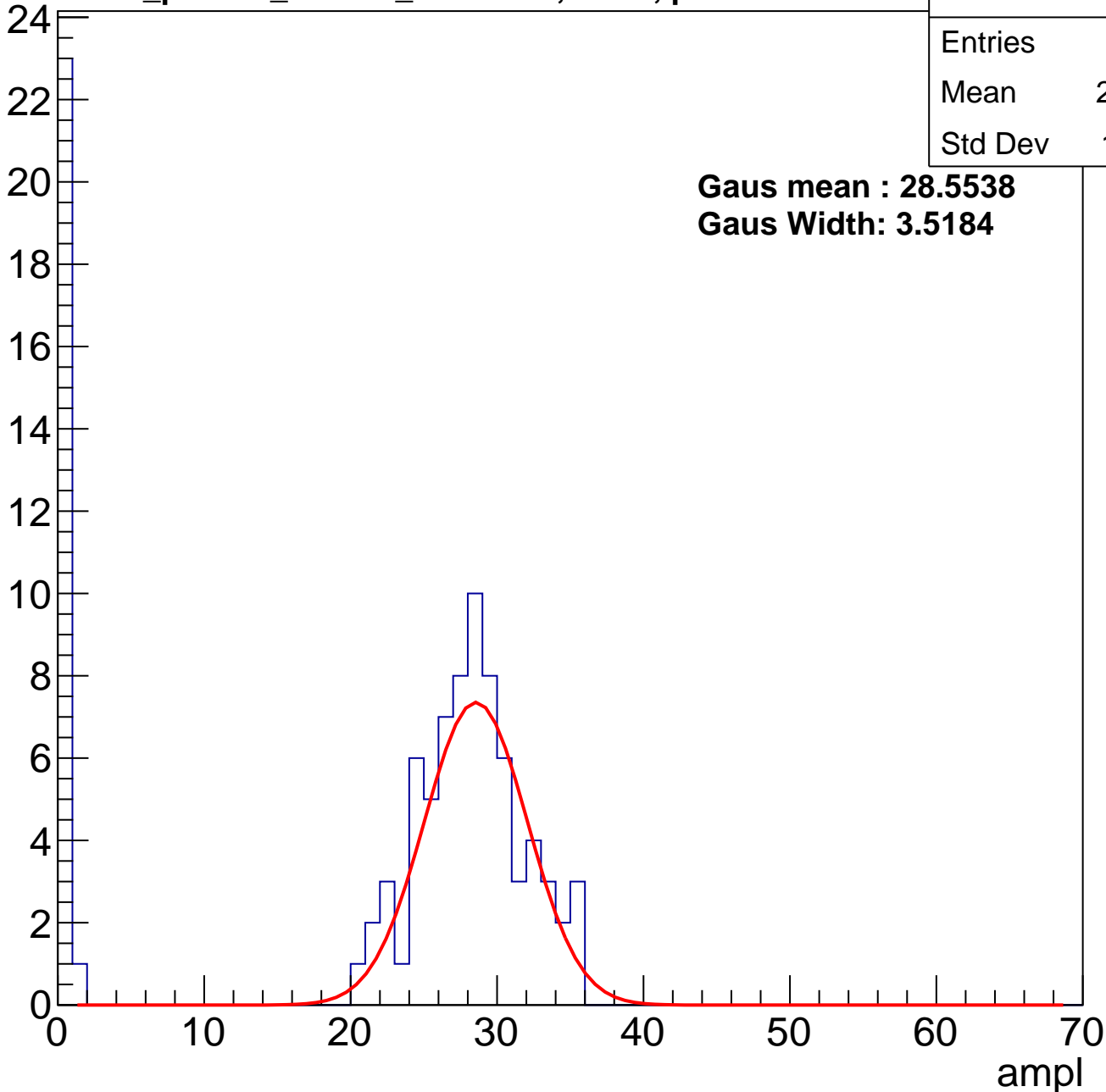
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	20.88
Std Dev	12.41

Gaus mean : 28.5538

Gaus Width: 3.5184

Entry



B1L103S, U24-ch31, adc1

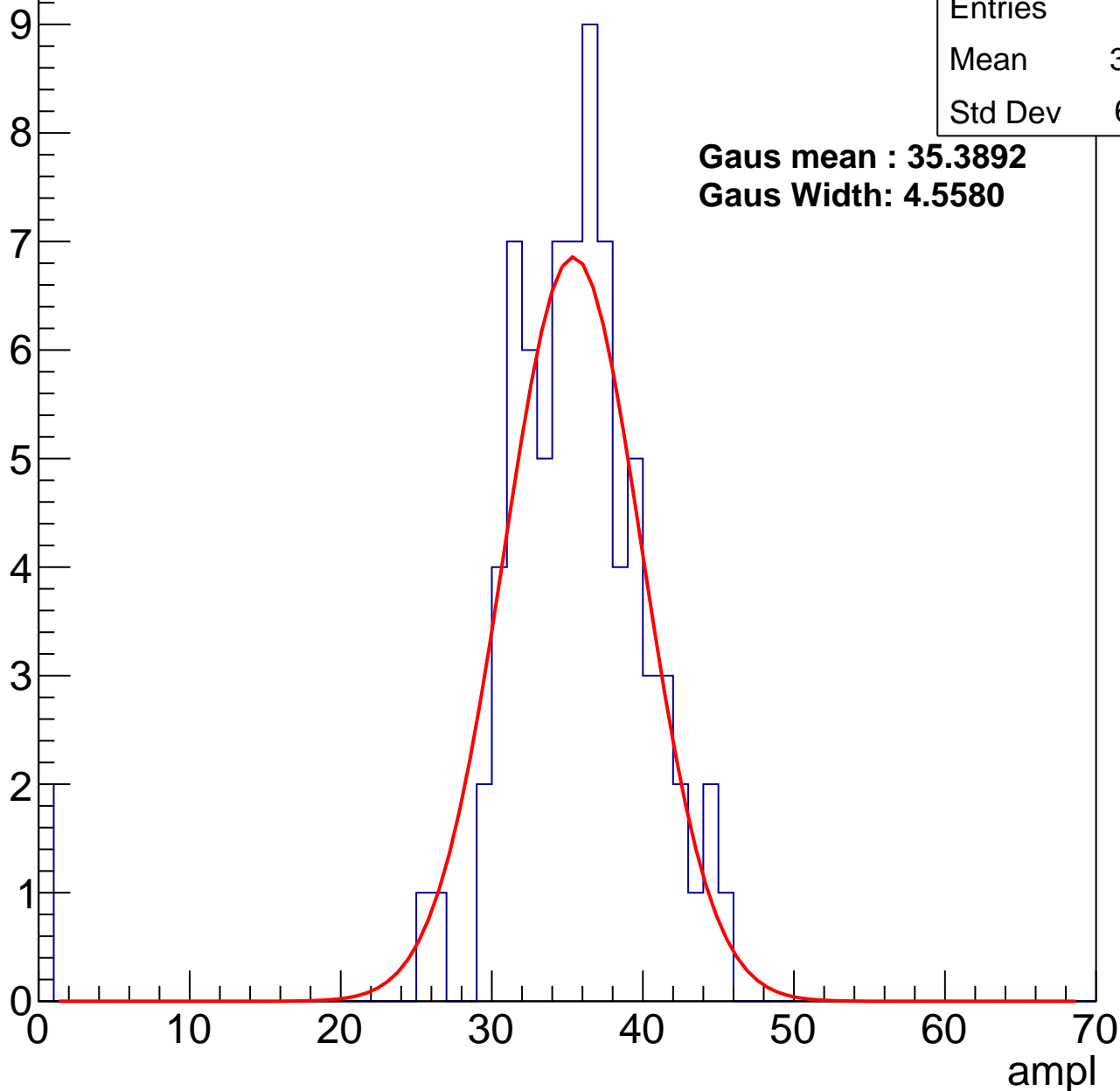
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	34.42
Std Dev	6.871

Gaus mean : 35.3892

Gaus Width: 4.5580



B1L103S, U24-ch31, adc2

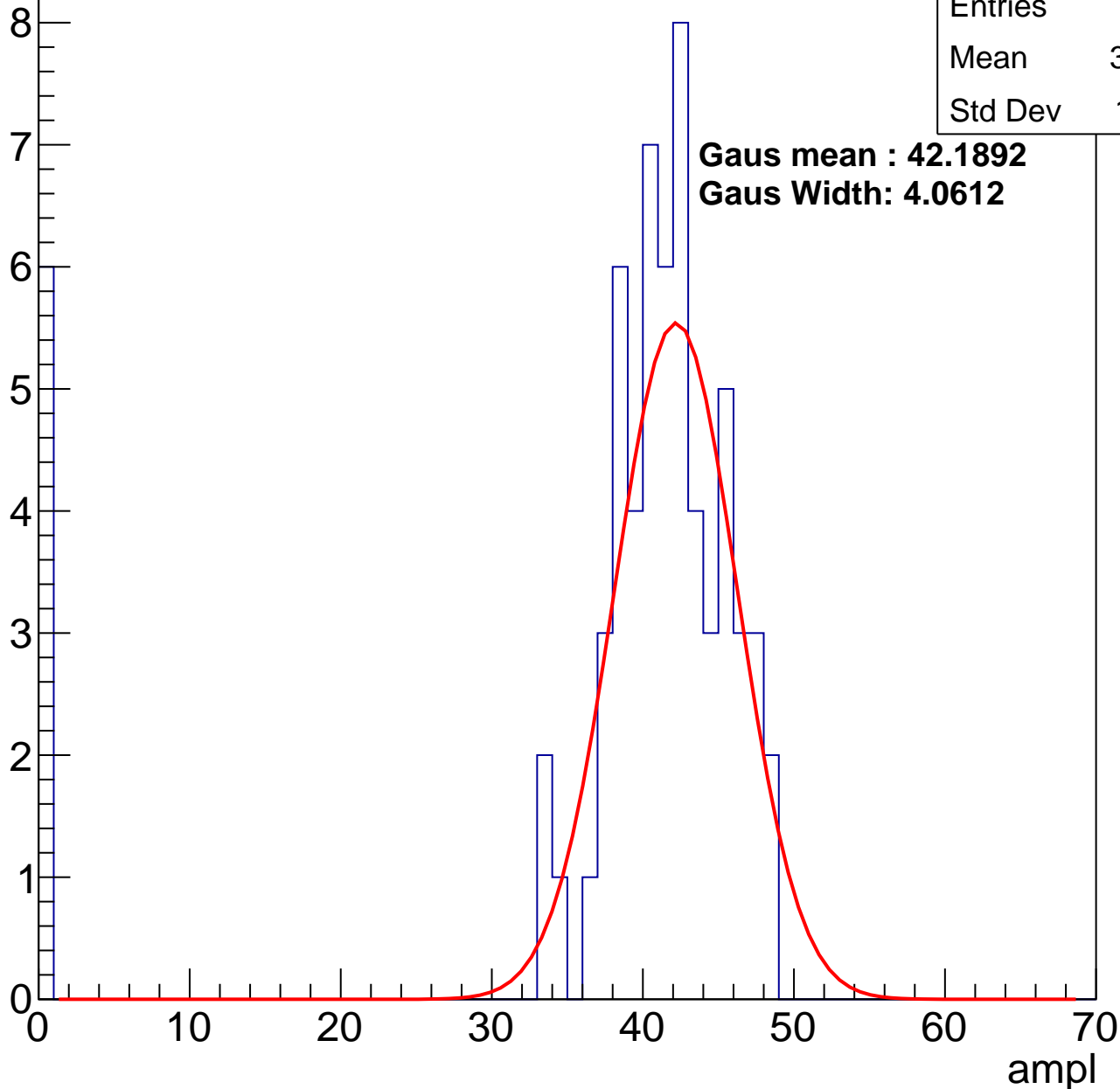
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.45
Std Dev	12.51

Gaus mean : 42.1892

Gaus Width: 4.0612

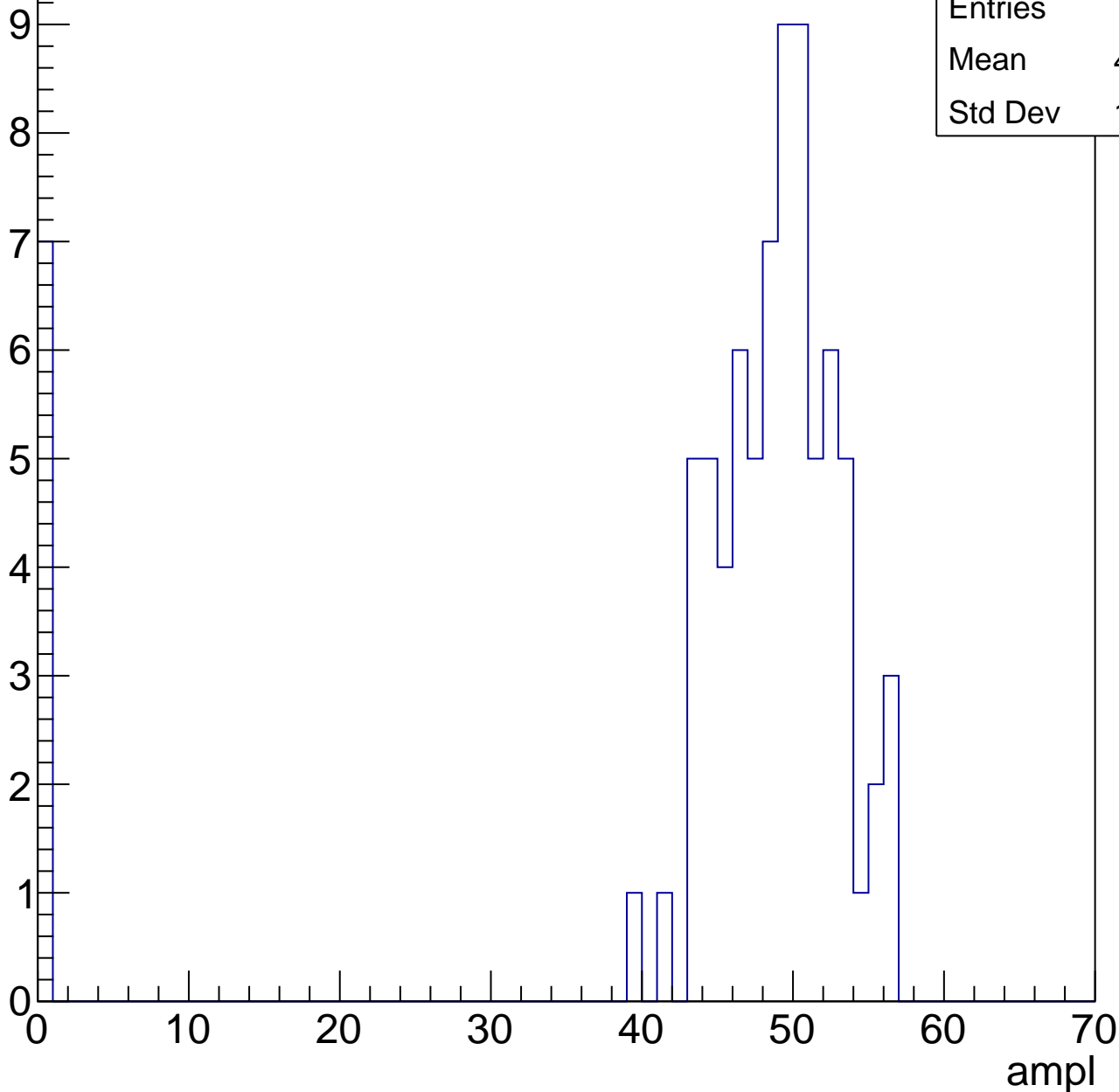


B1L103S, U24-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	44.41
Std Dev	14.11

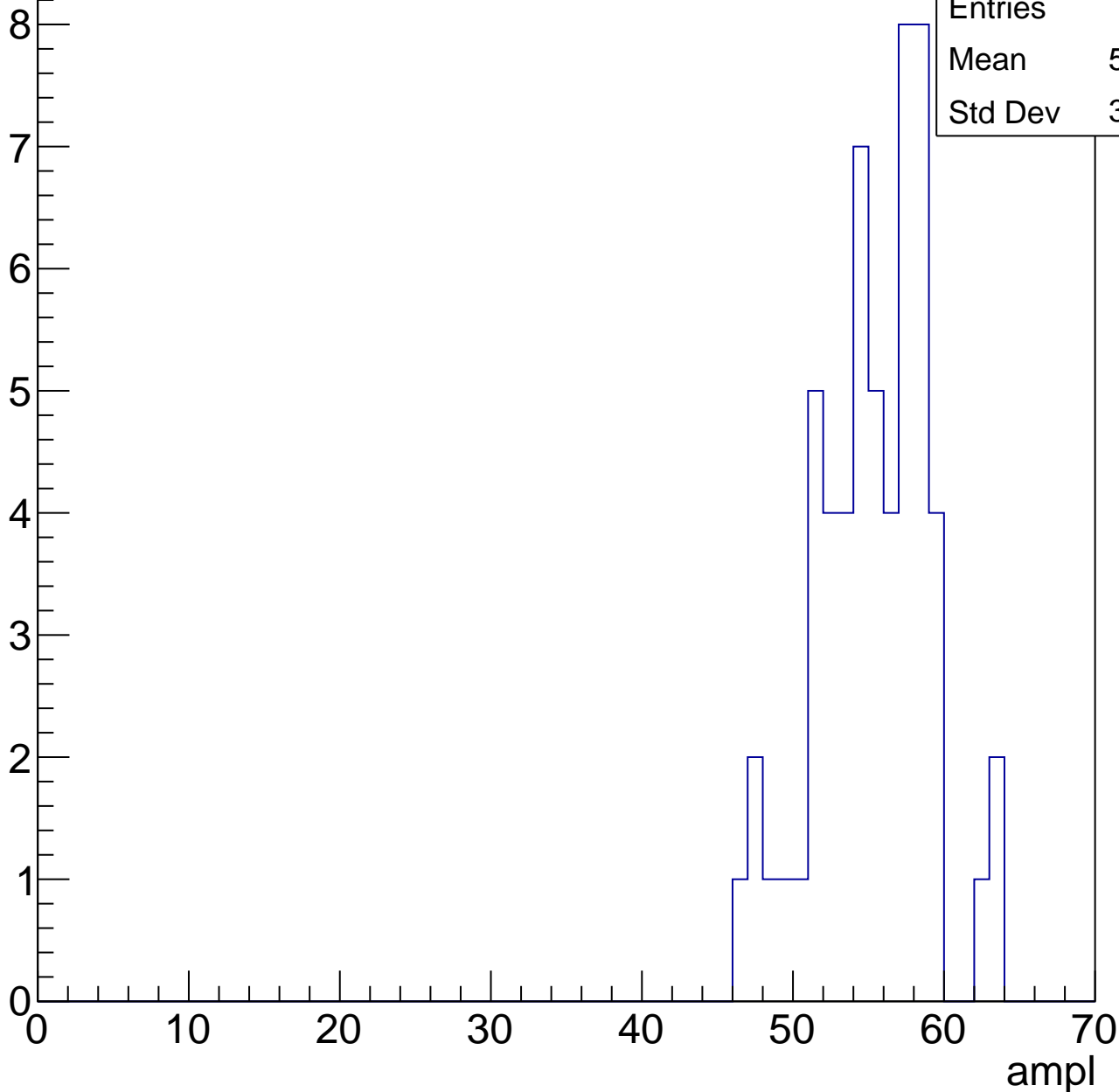


B1L103S, U24-ch31, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	54.88
Std Dev	3.728

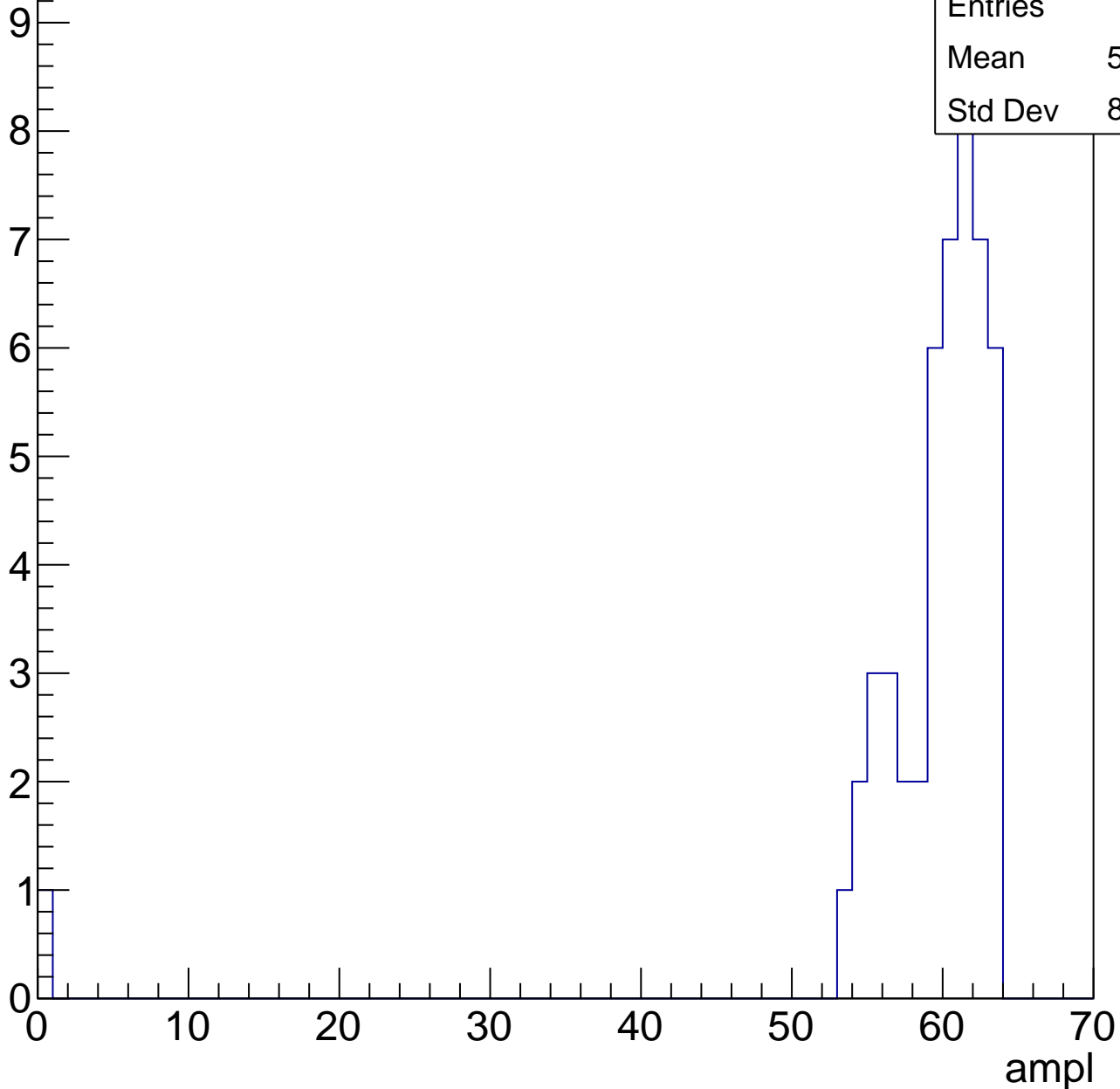


B1L103S, U24-ch31, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

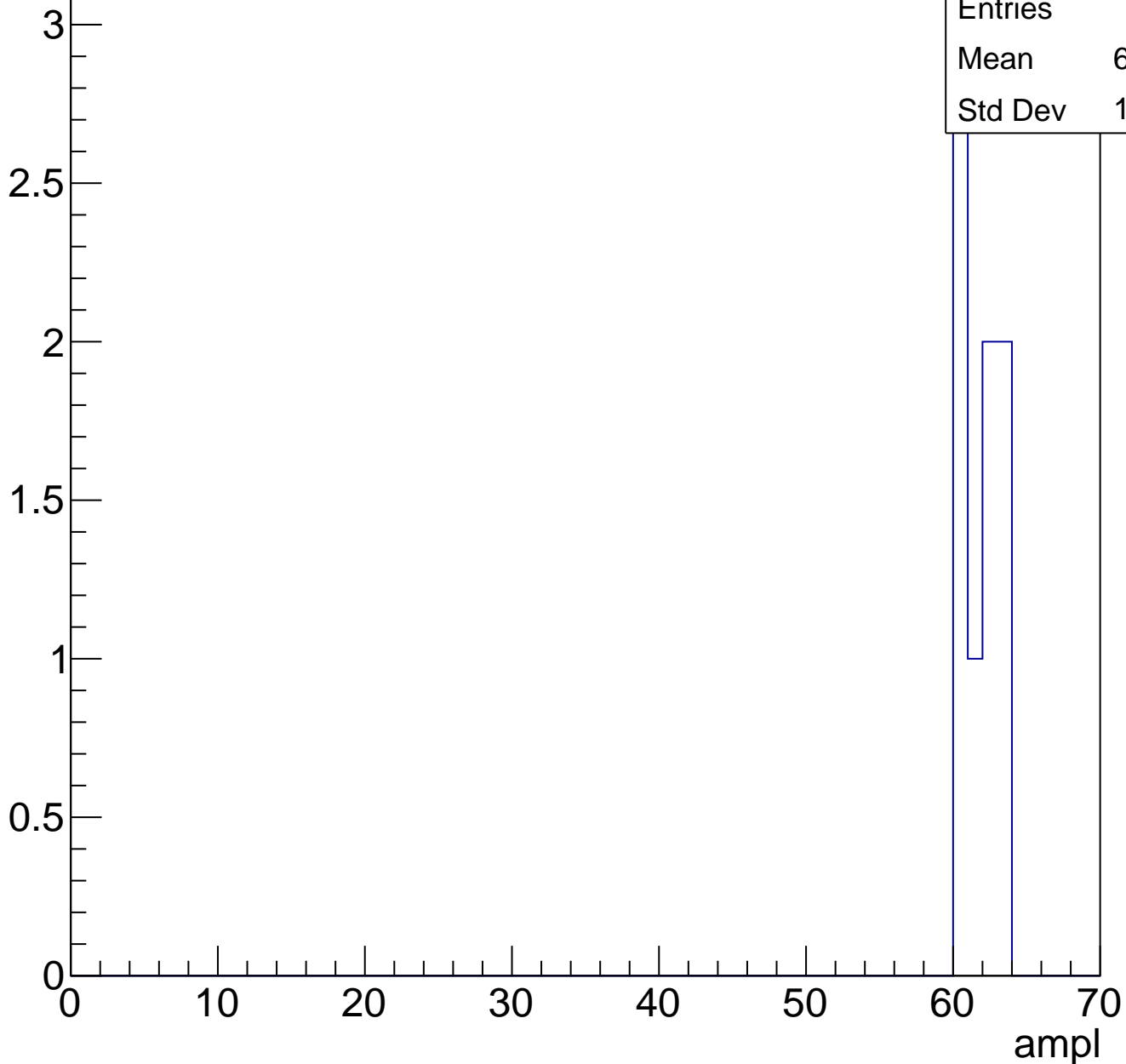
Entries	49
Mean	58.35
Std Dev	8.845



B1L103S, U24-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch31, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch32, adc0

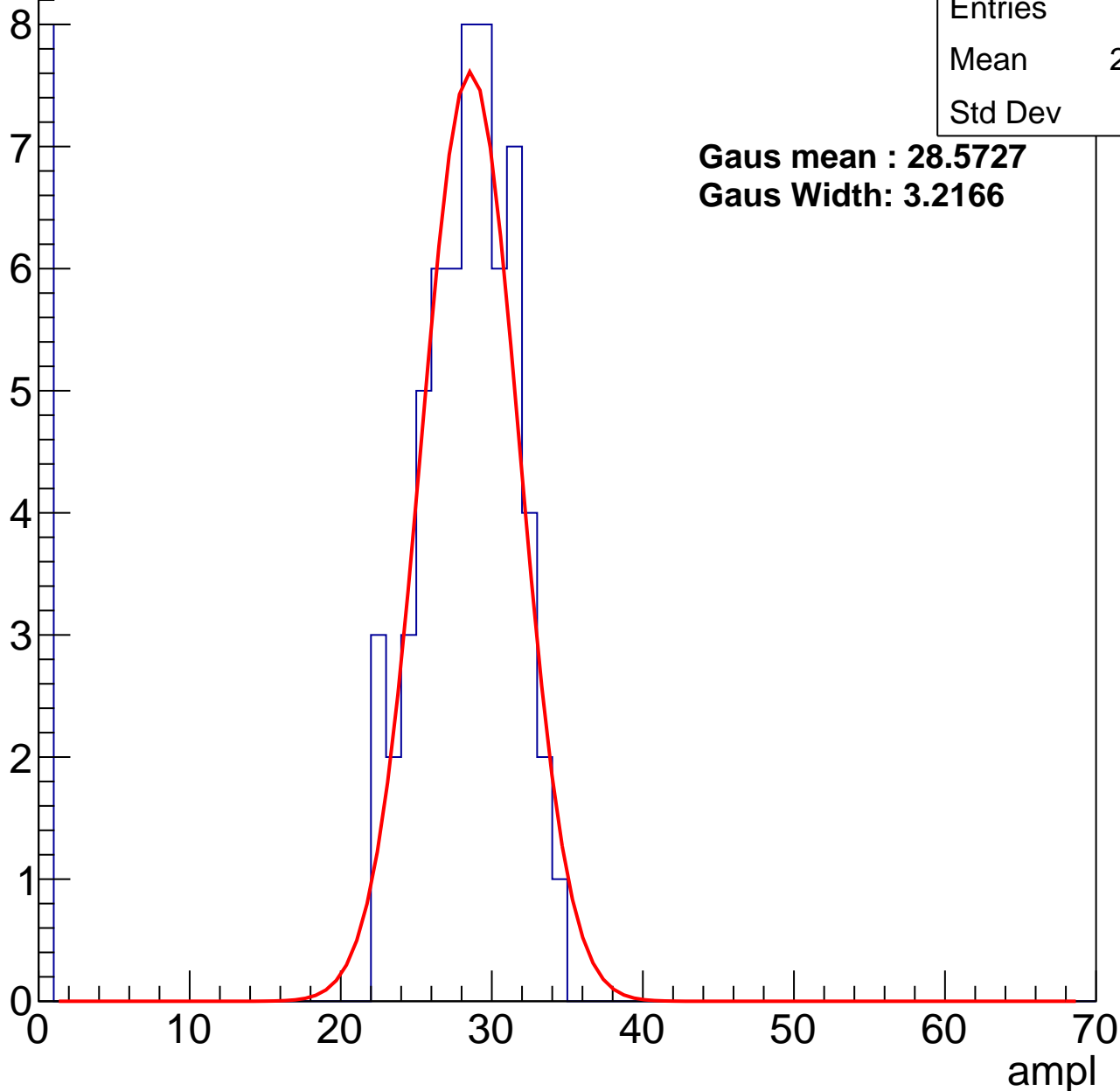
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	24.75
Std Dev	9.38

Gaus mean : 28.5727

Gaus Width: 3.2166



B1L103S, U24-ch32, adc1

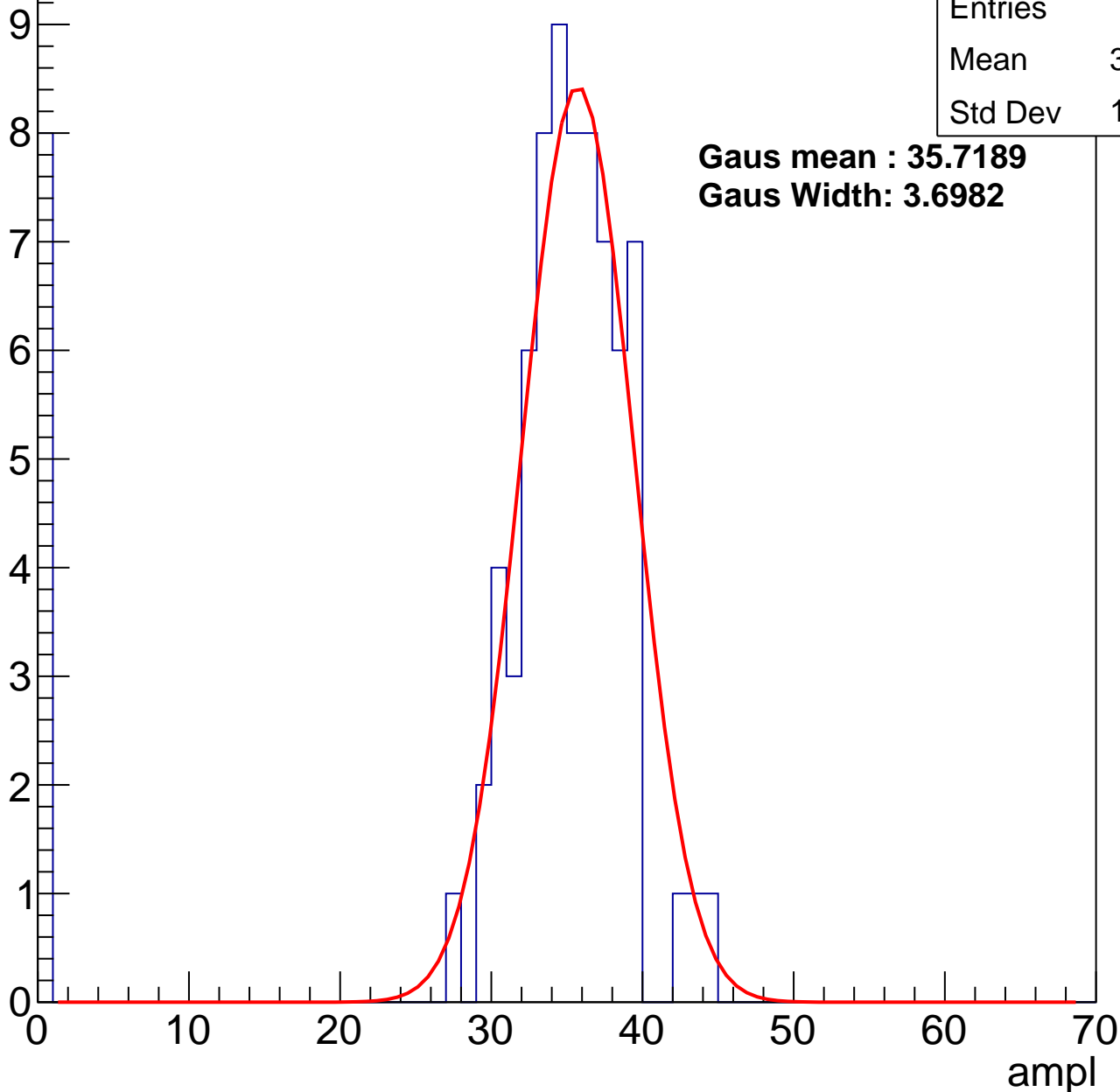
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	31.46
Std Dev	10.94

Gaus mean : 35.7189

Gaus Width: 3.6982



B1L103S, U24-ch32, adc2

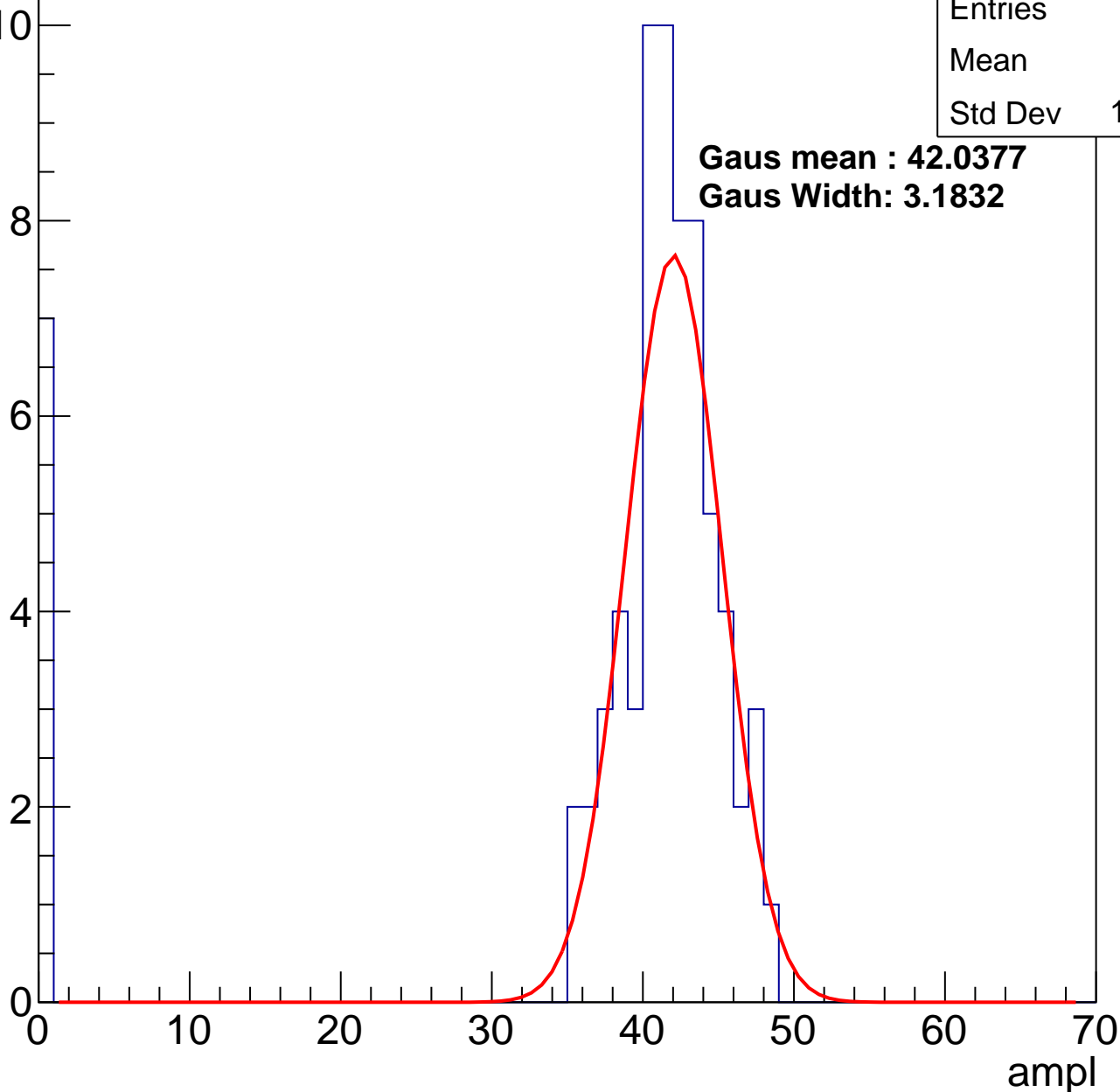
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	37.4
Std Dev	12.59

Gaus mean : 42.0377

Gaus Width: 3.1832



B1L103S, U24-ch32, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	48.95
Std Dev	3.243

Entry

10

8

6

4

2

0

0

10

20

30

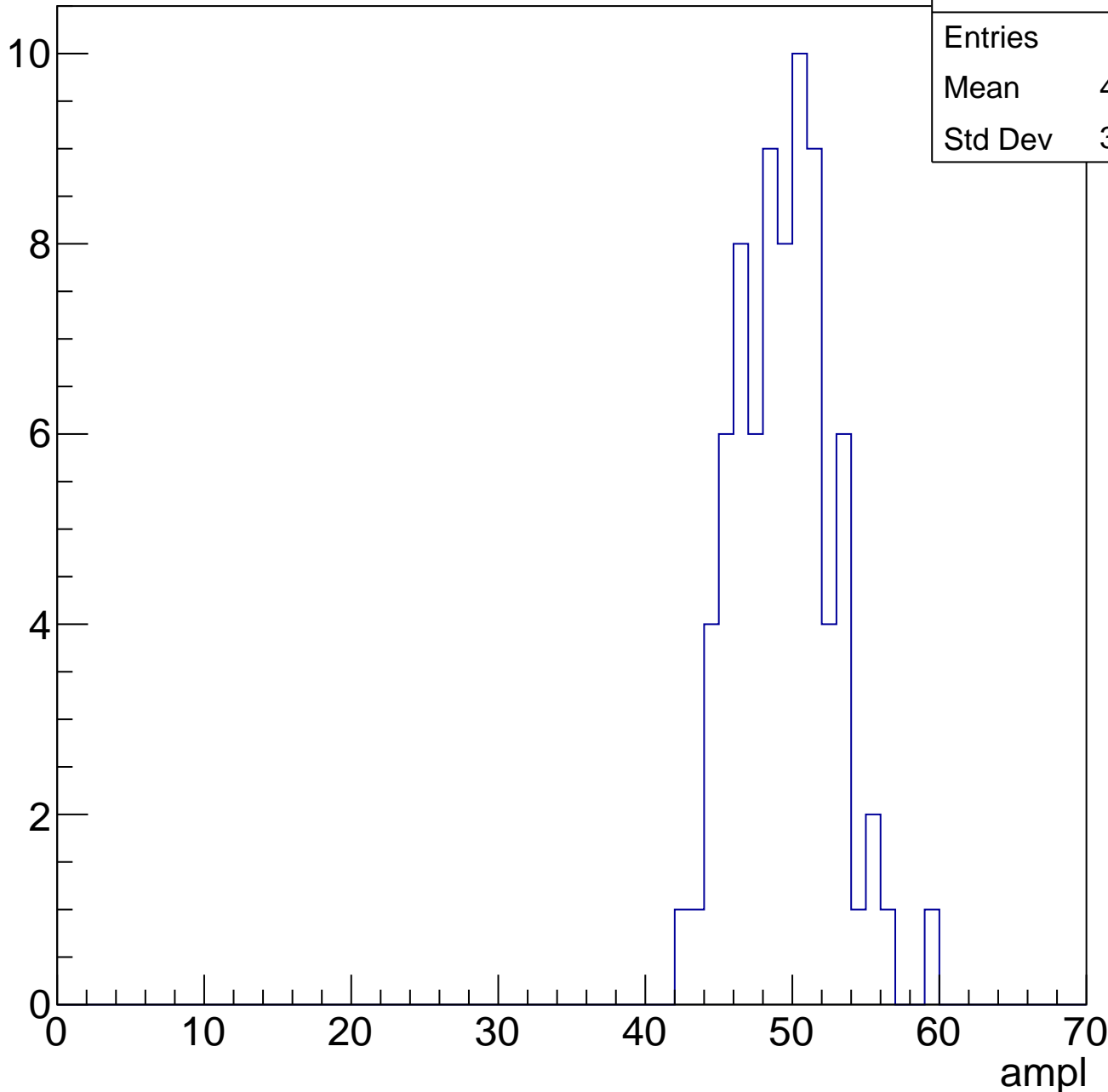
40

50

60

ampl

70

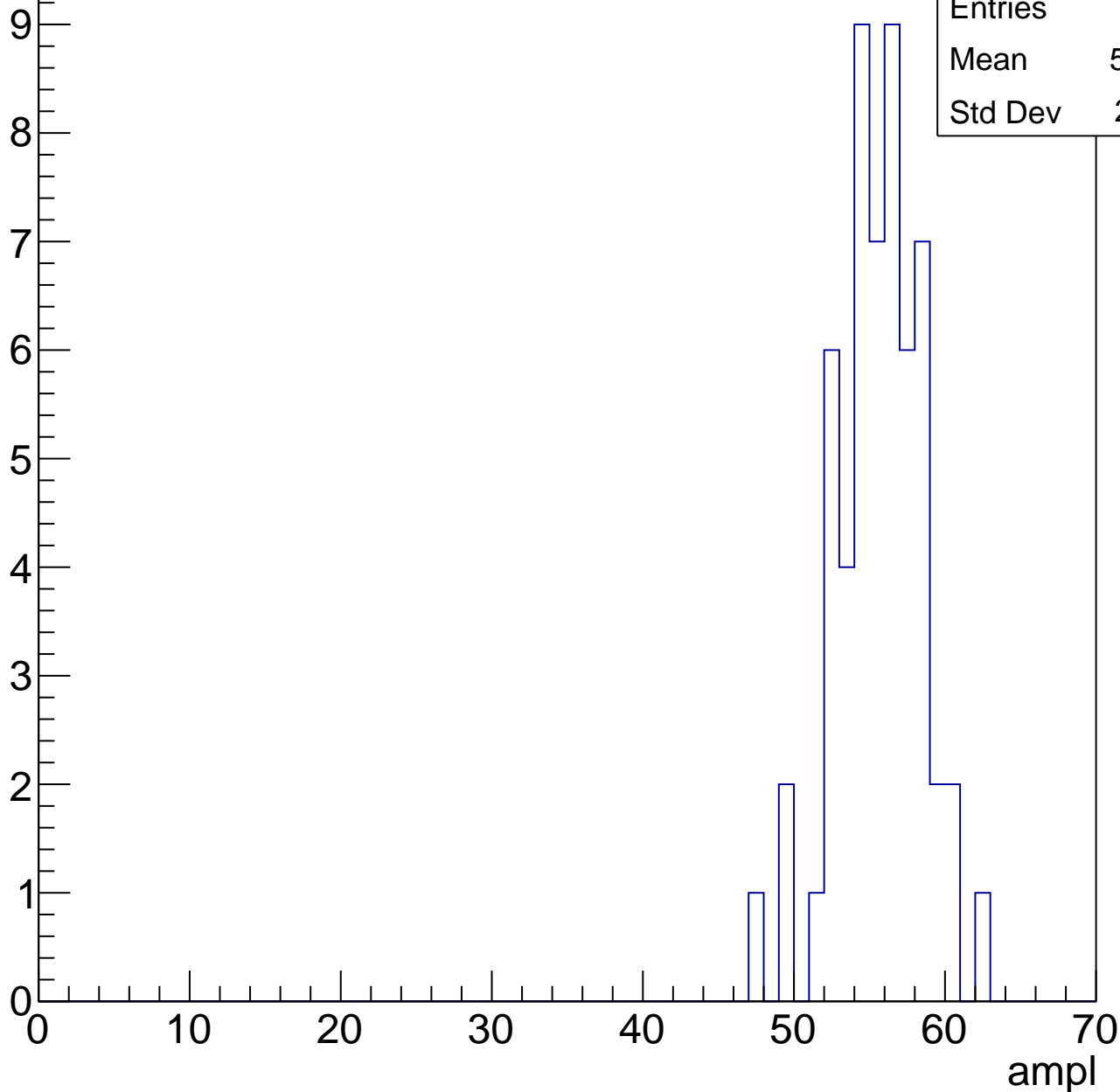


B1L103S, U24-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.14
Std Dev	2.831

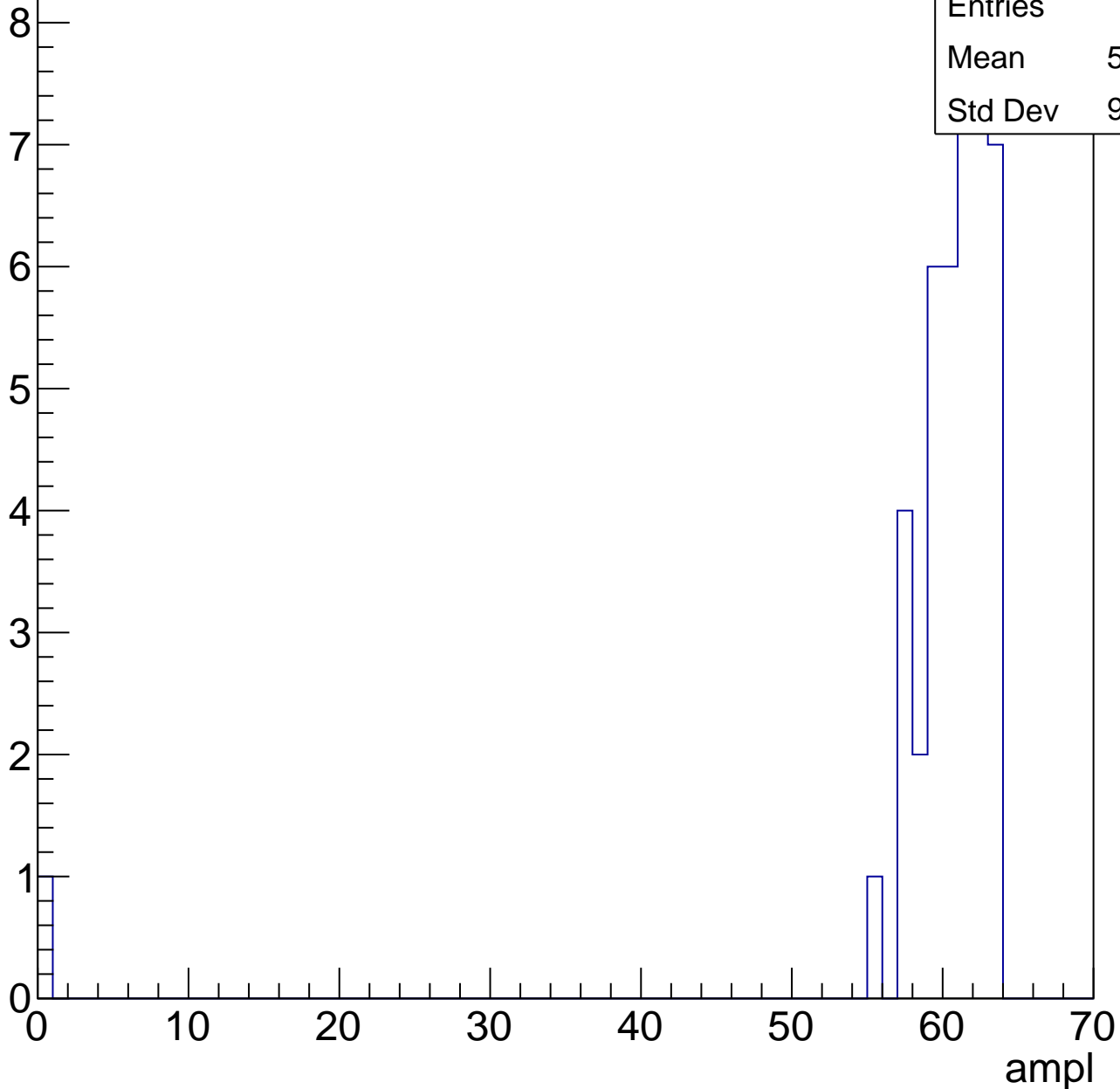


B1L103S, U24-ch32, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	59.02
Std Dev	9.322



B1L103S, U24-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

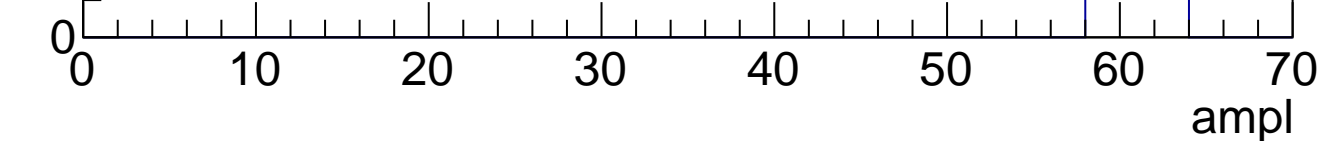
10

Mean

61

Std Dev

1.673



B1L103S, U24-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch33, adc0

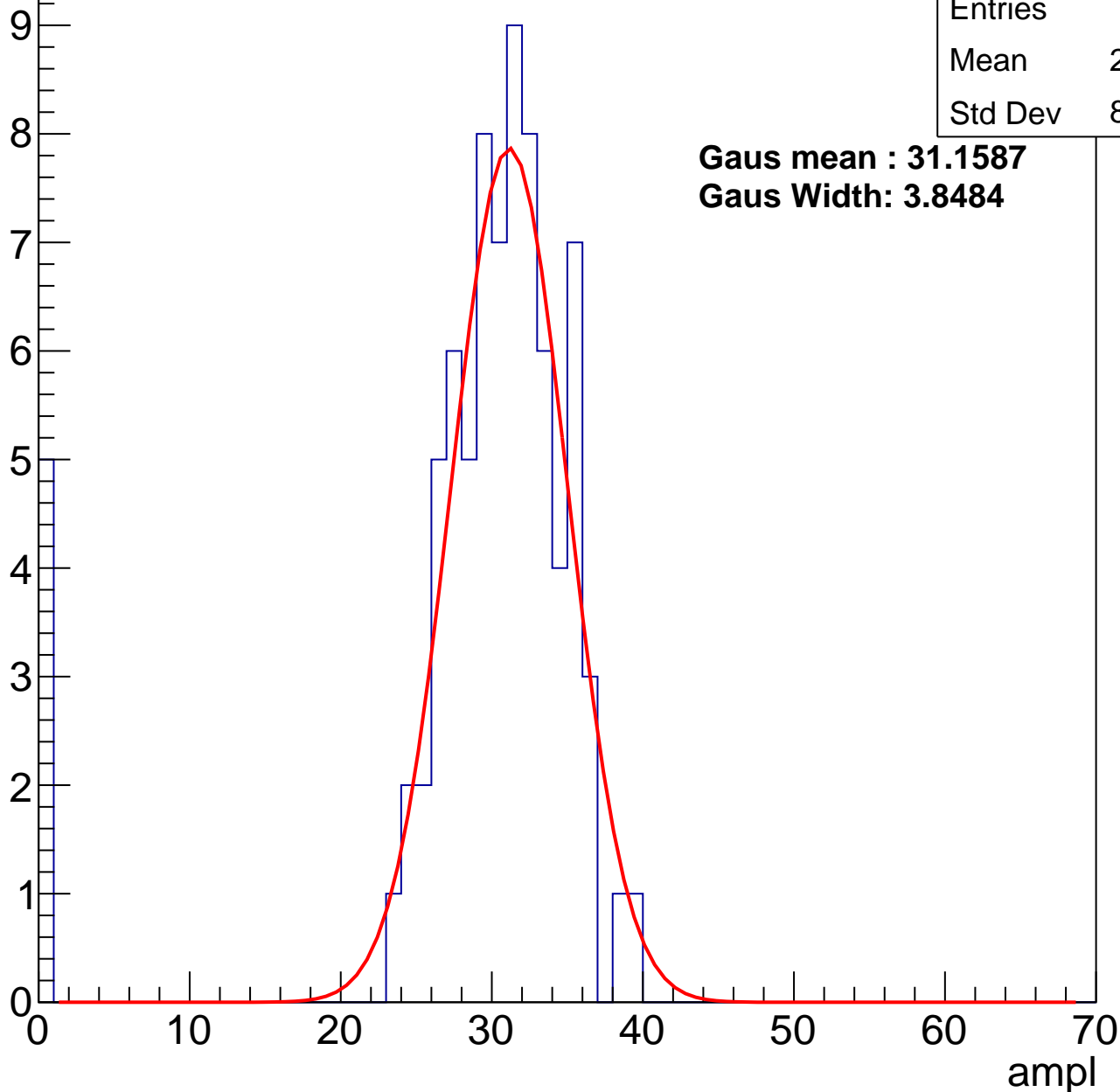
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	28.68
Std Dev	8.124

Gaus mean : 31.1587

Gaus Width: 3.8484



B1L103S, U24-ch33, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	34.47
Std Dev	11.37

Gaus mean : 38.6200

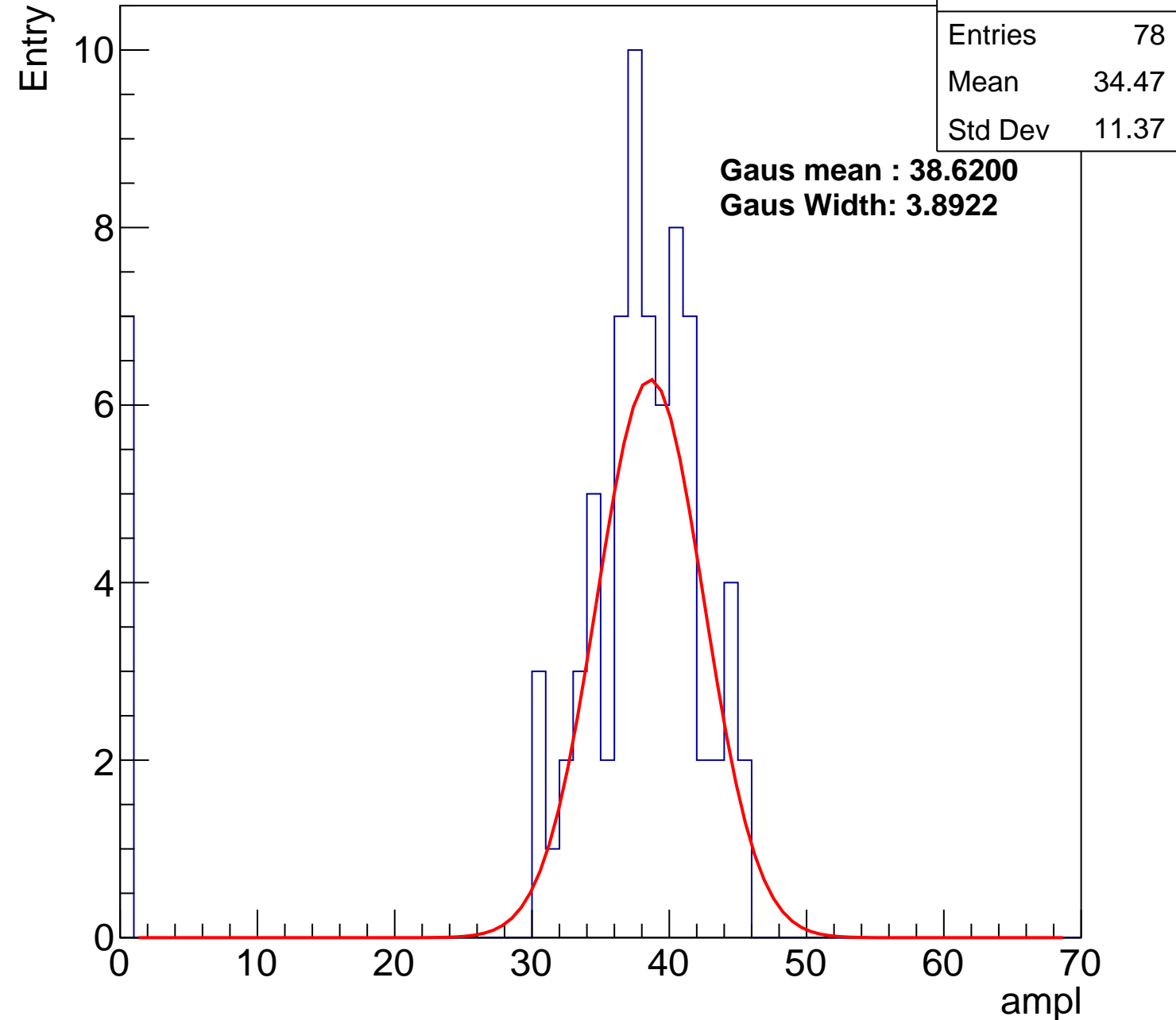
Gaus Width: 3.8922

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch33, adc2

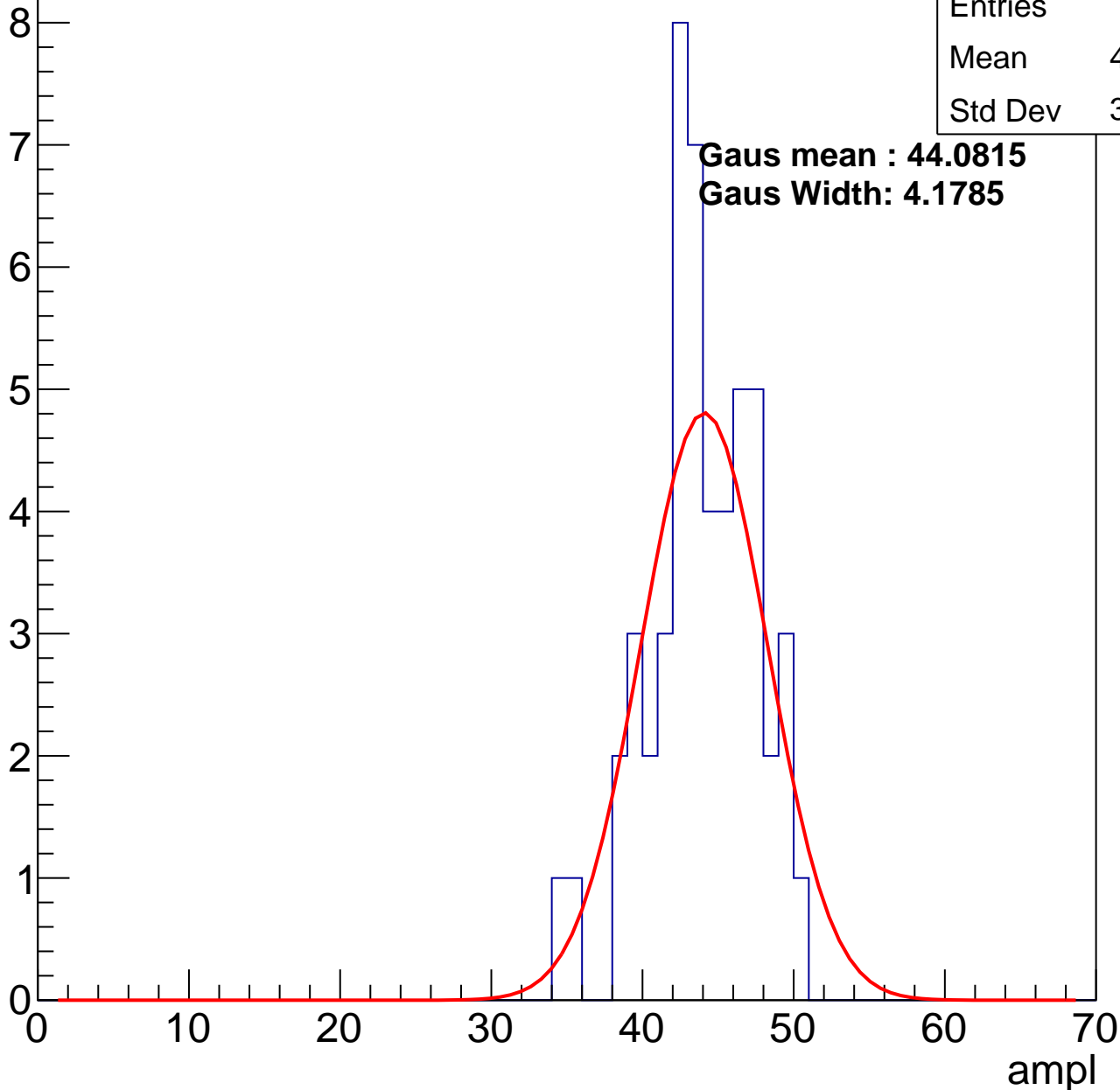
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	43.45
Std Dev	3.494

Gaus mean : 44.0815

Gaus Width: 4.1785

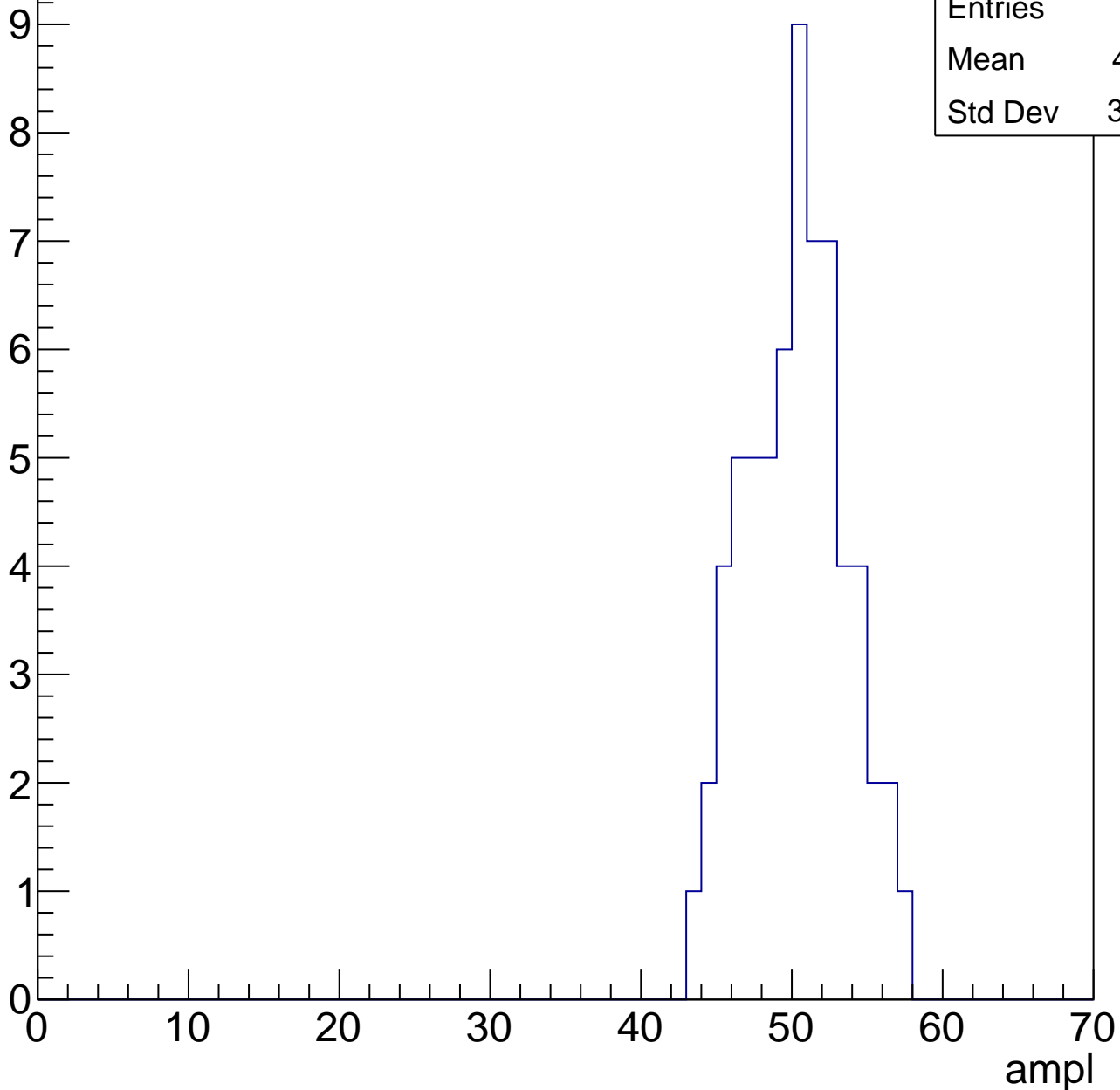


B1L103S, U24-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.81
Std Dev	3.249

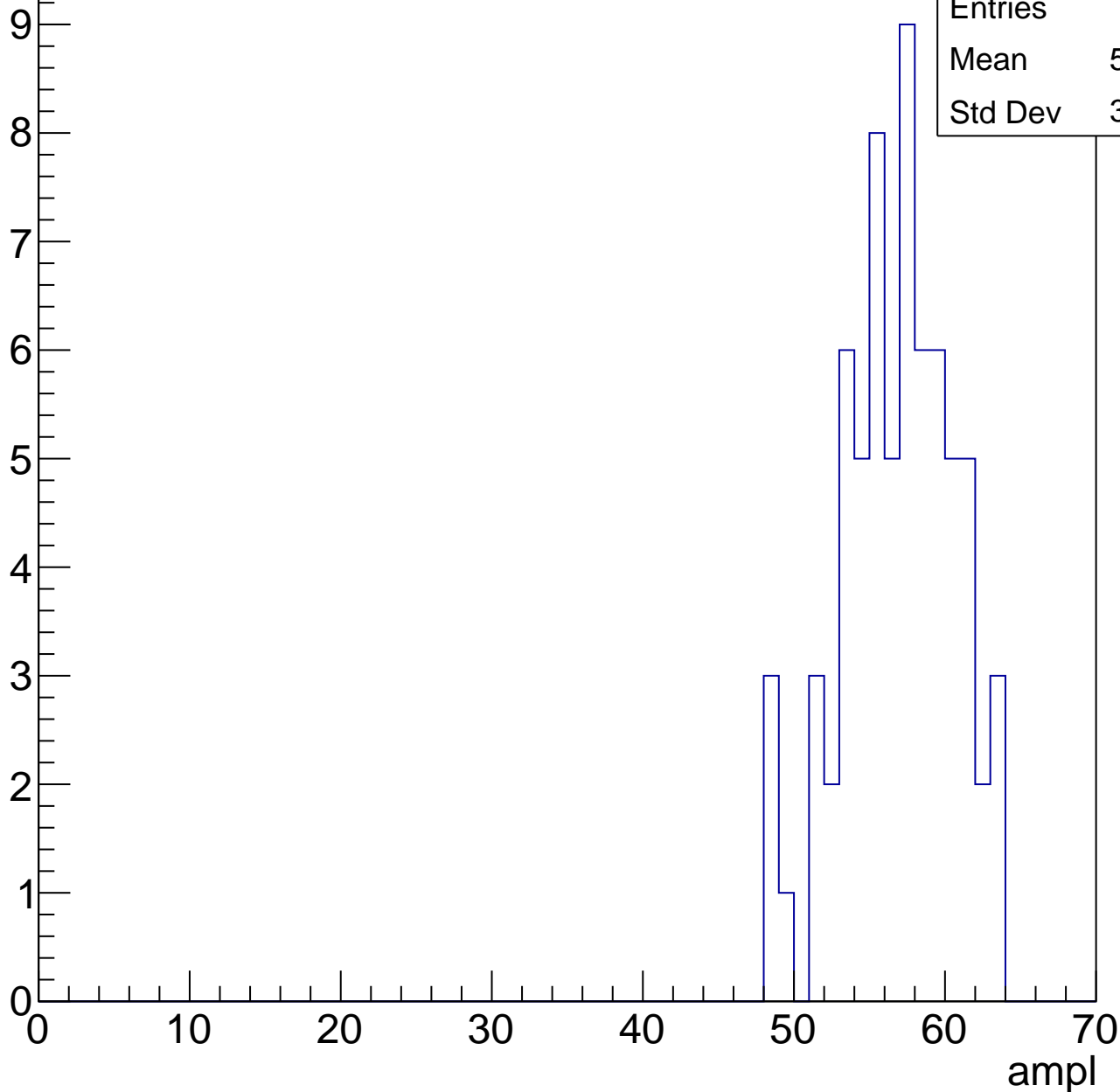


B1L103S, U24-ch33, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	56.39
Std Dev	3.672

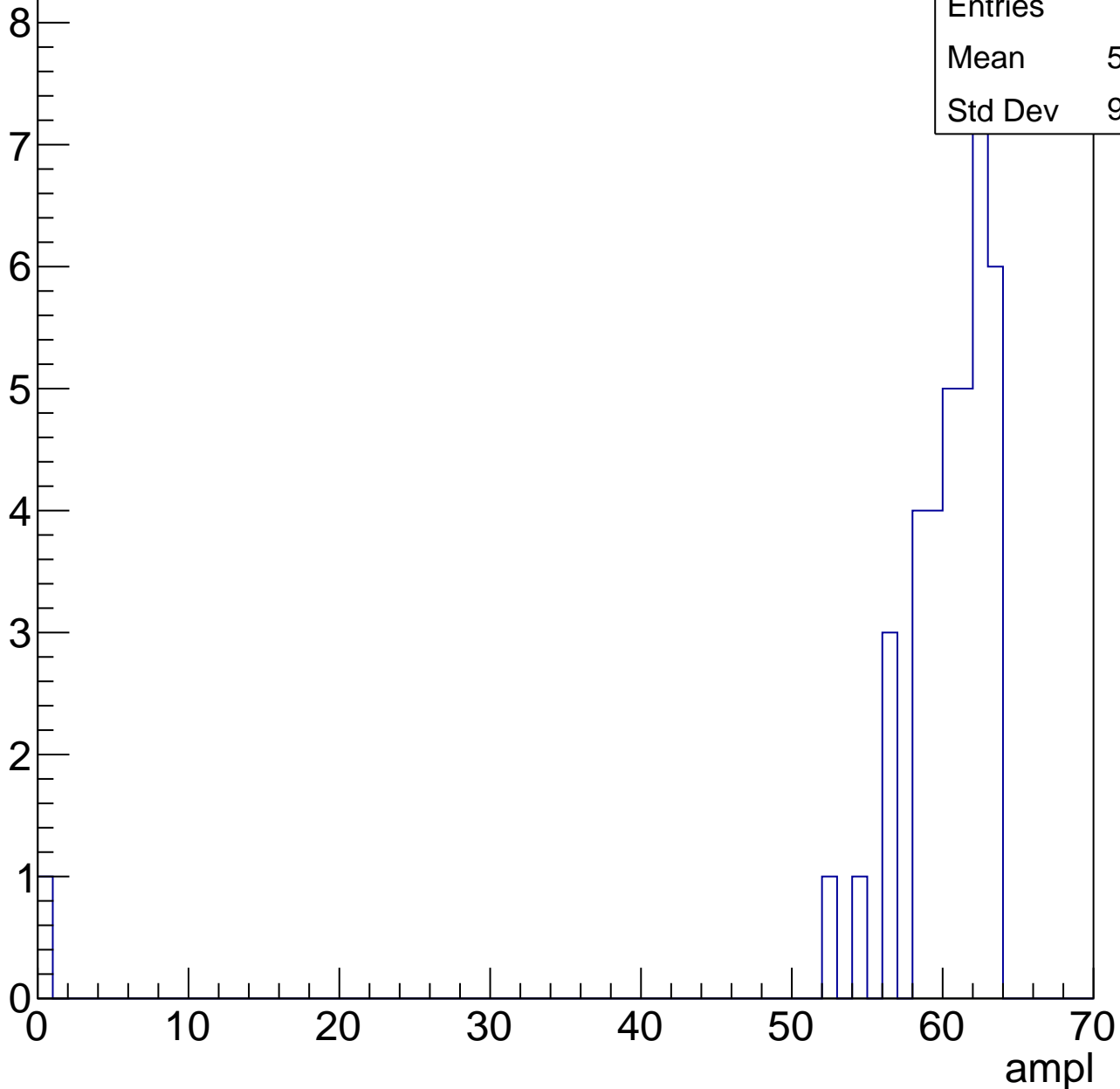


B1L103S, U24-ch33, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.45
Std Dev	9.957



B1L103S, U24-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch34, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	23.27
Std Dev	10.69

Gaus mean : 27.9759

Gaus Width: 3.4946

Entry

12

10

8

6

4

2

0

0

10

20

30

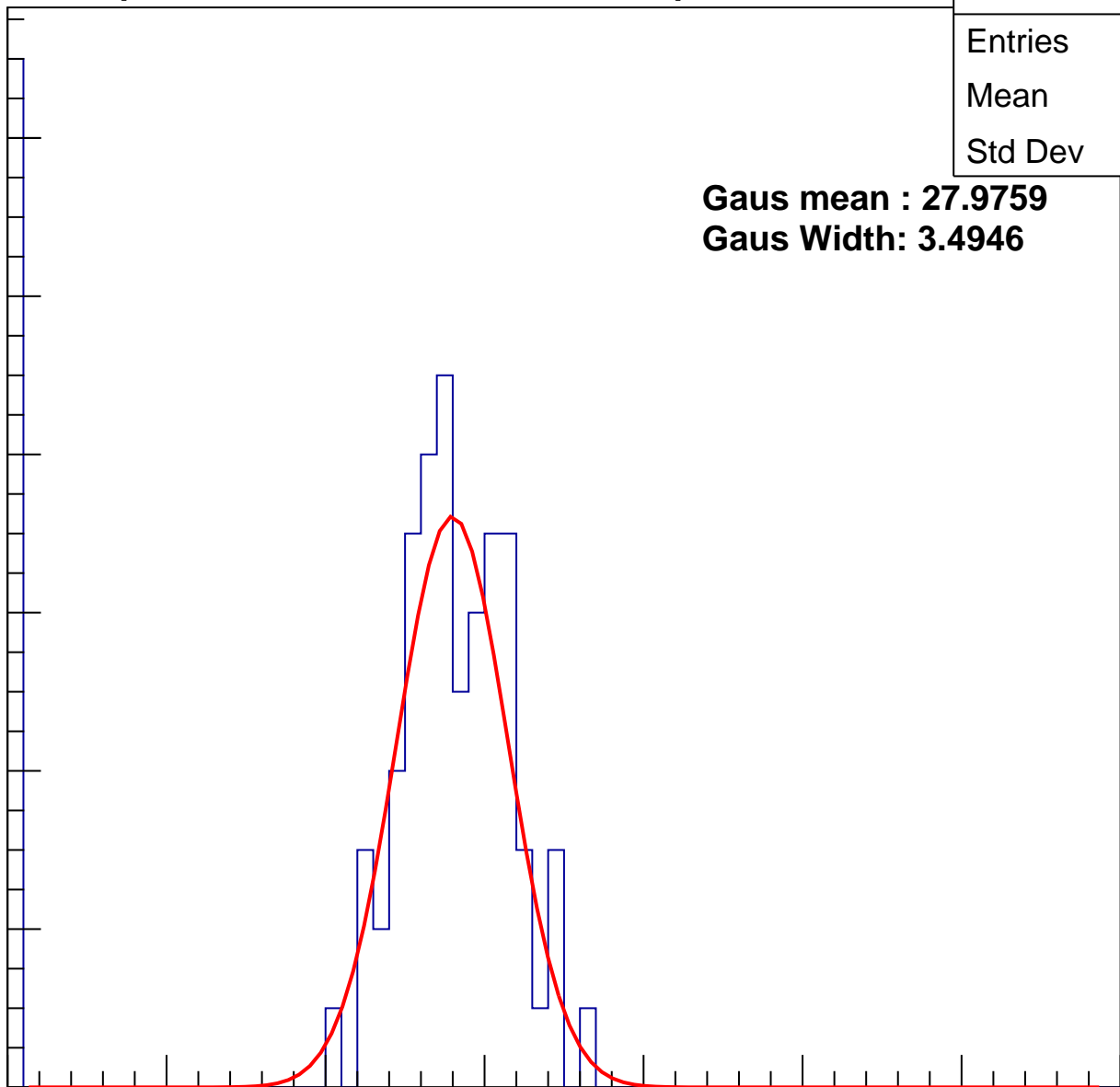
40

50

60

70

ampl



B1L103S, U24-ch34, adc1

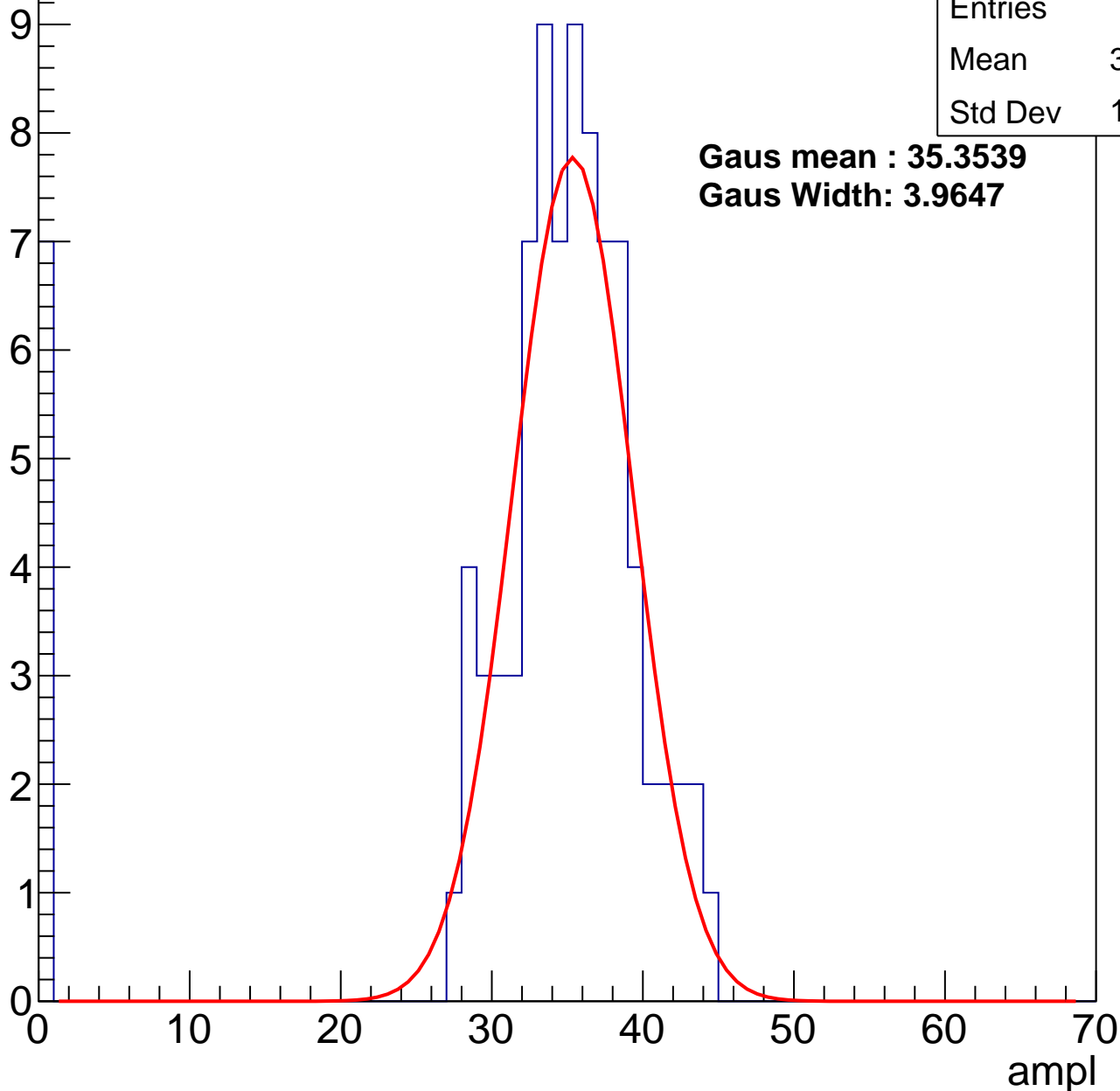
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	32.14
Std Dev	10.14

Gaus mean : 35.3539

Gaus Width: 3.9647



B1L103S, U24-ch34, adc2

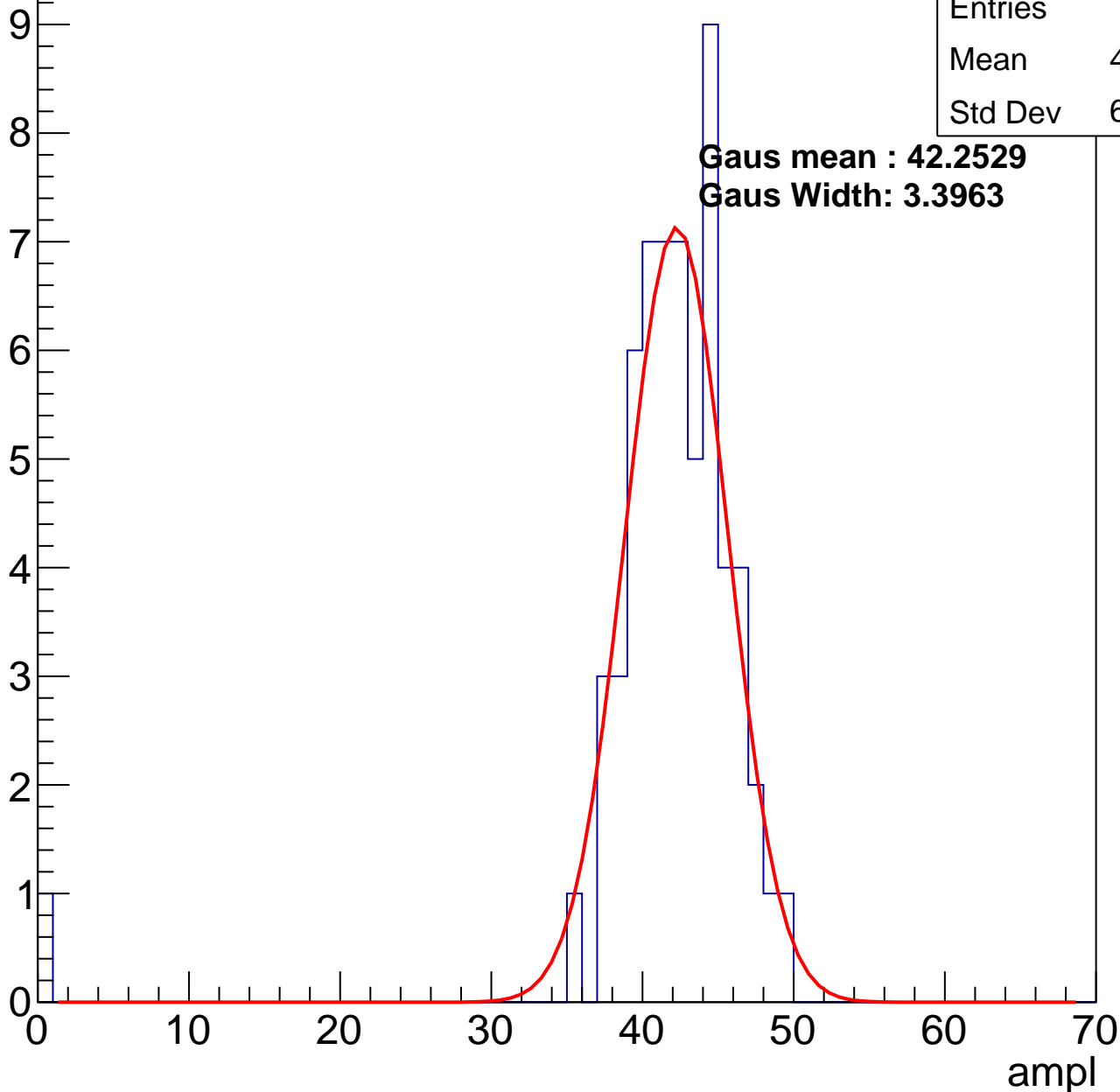
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	41.33
Std Dev	6.105

Gaus mean : 42.2529

Gaus Width: 3.3963

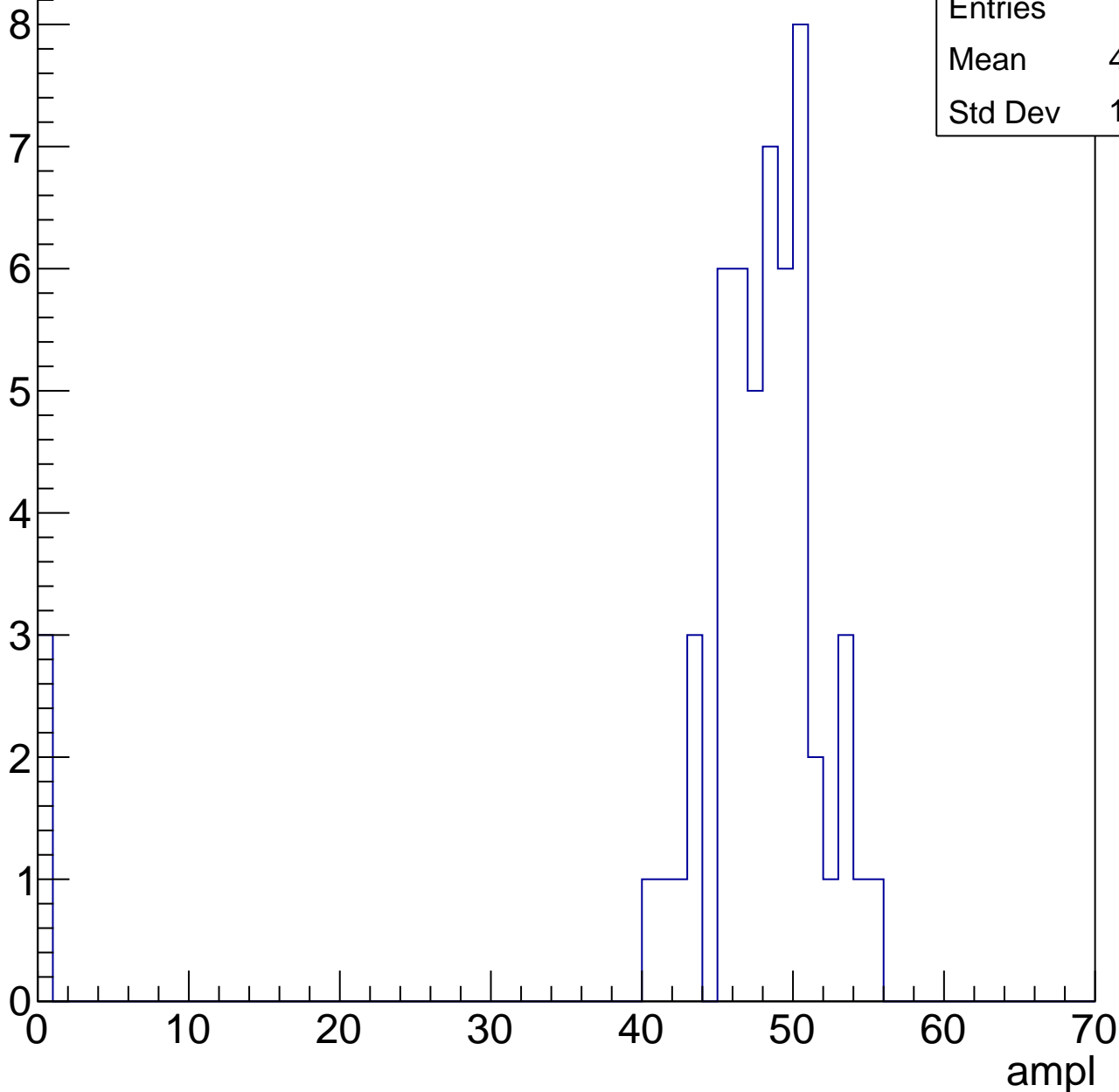


B1L103S, U24-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	45.18
Std Dev	11.29

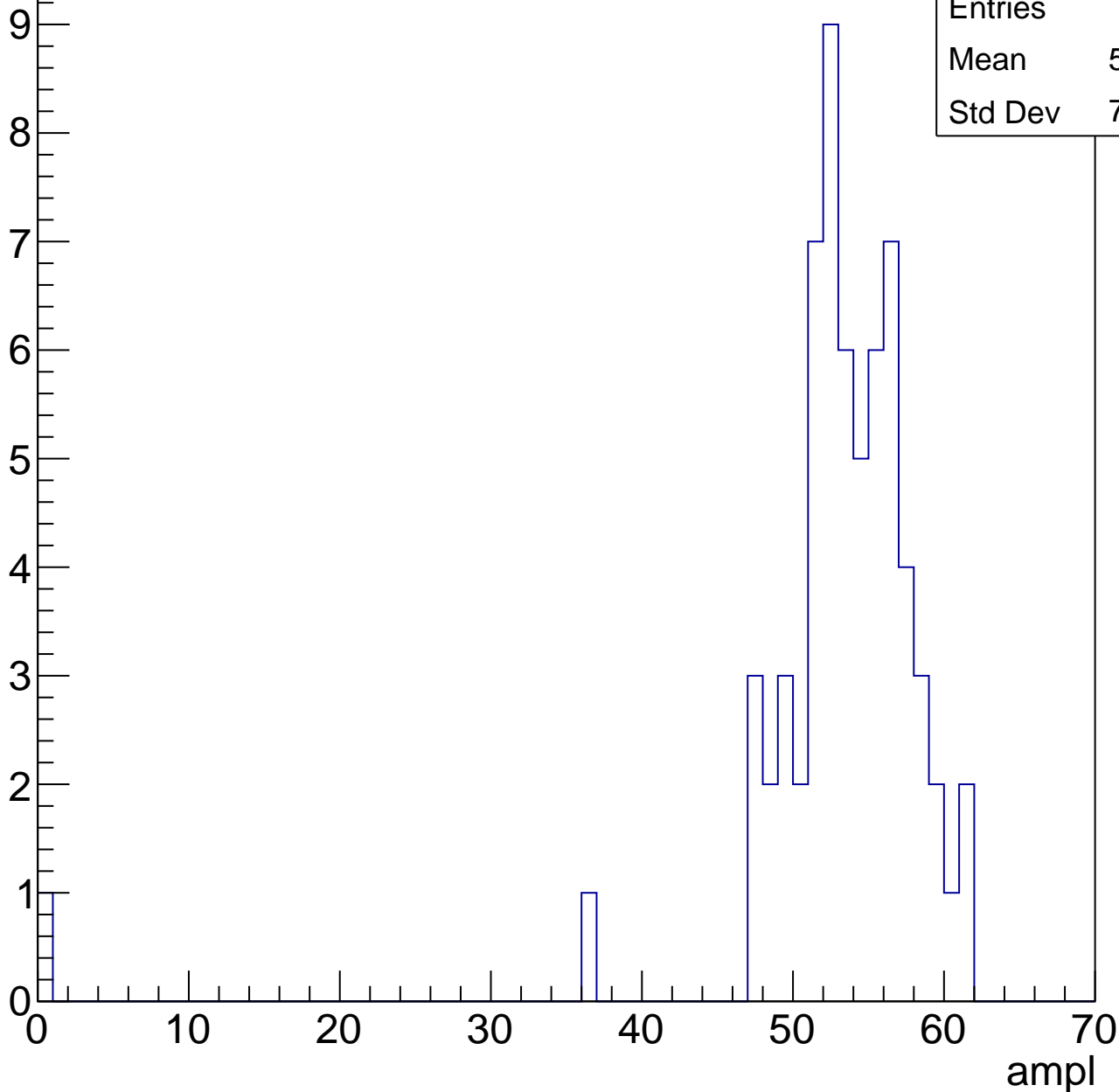


B1L103S, U24-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	52.45
Std Dev	7.726

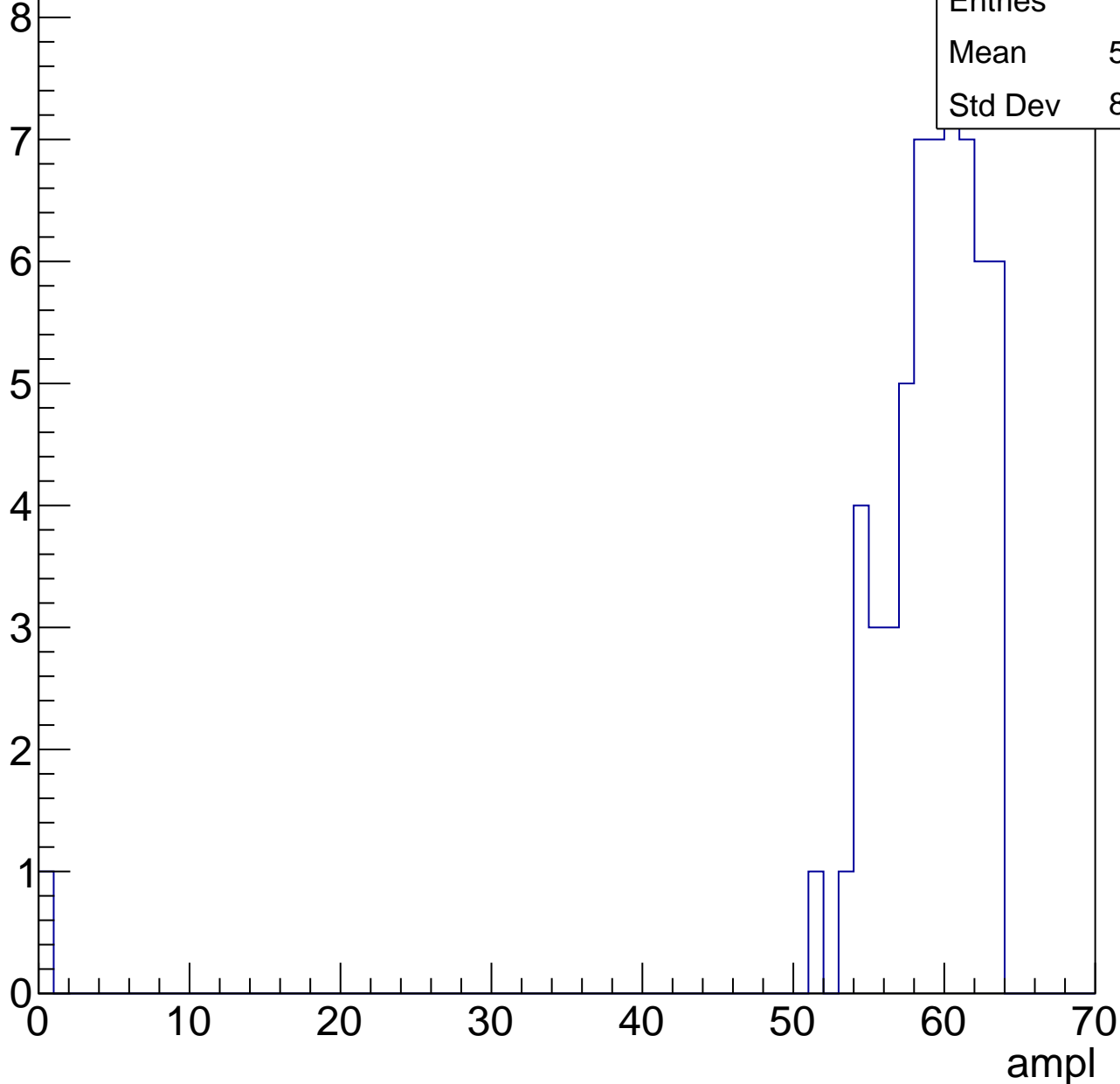


B1L103S, U24-ch34, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

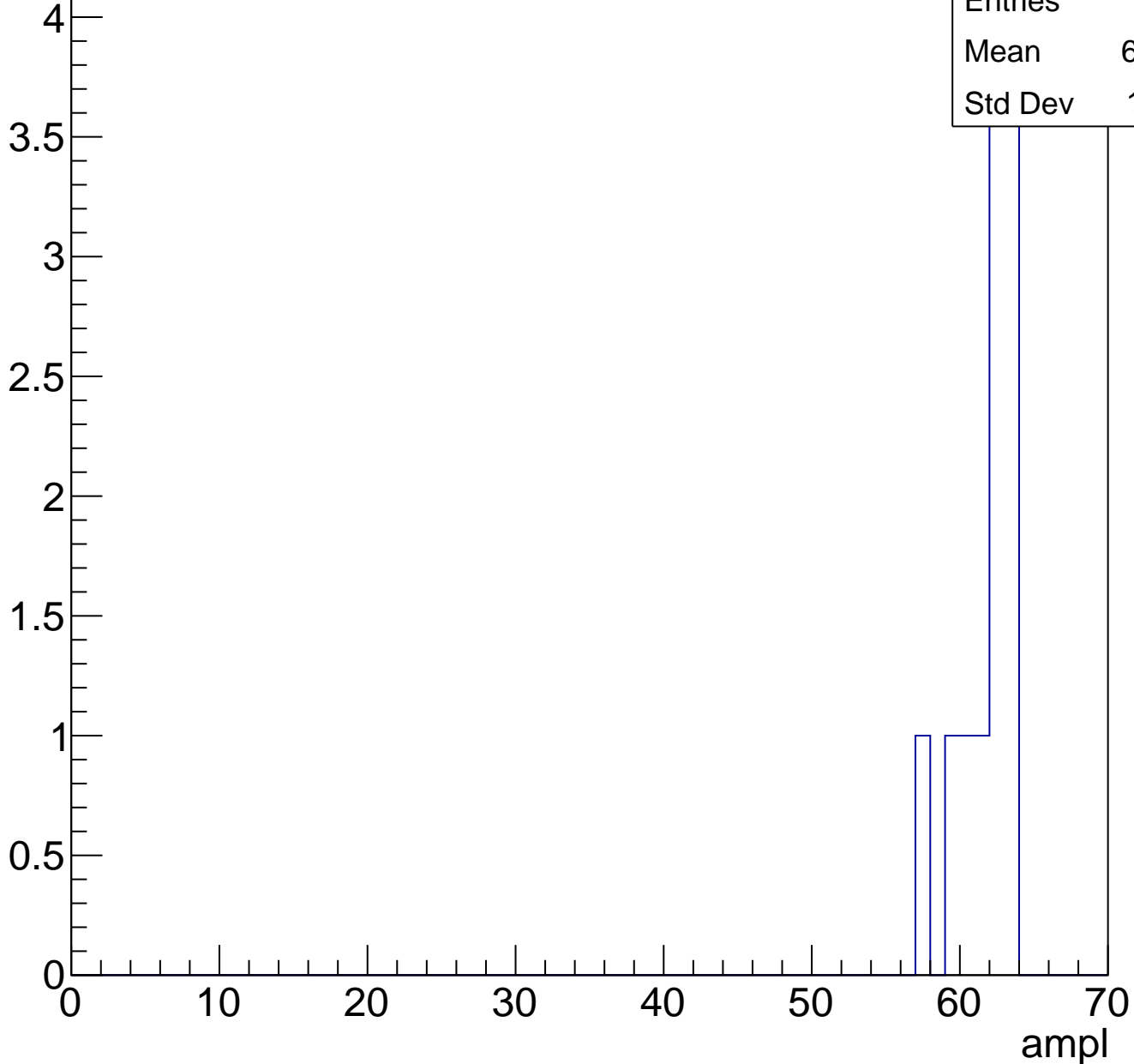
Entries	59
Mean	57.86
Std Dev	8.123



B1L103S, U24-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

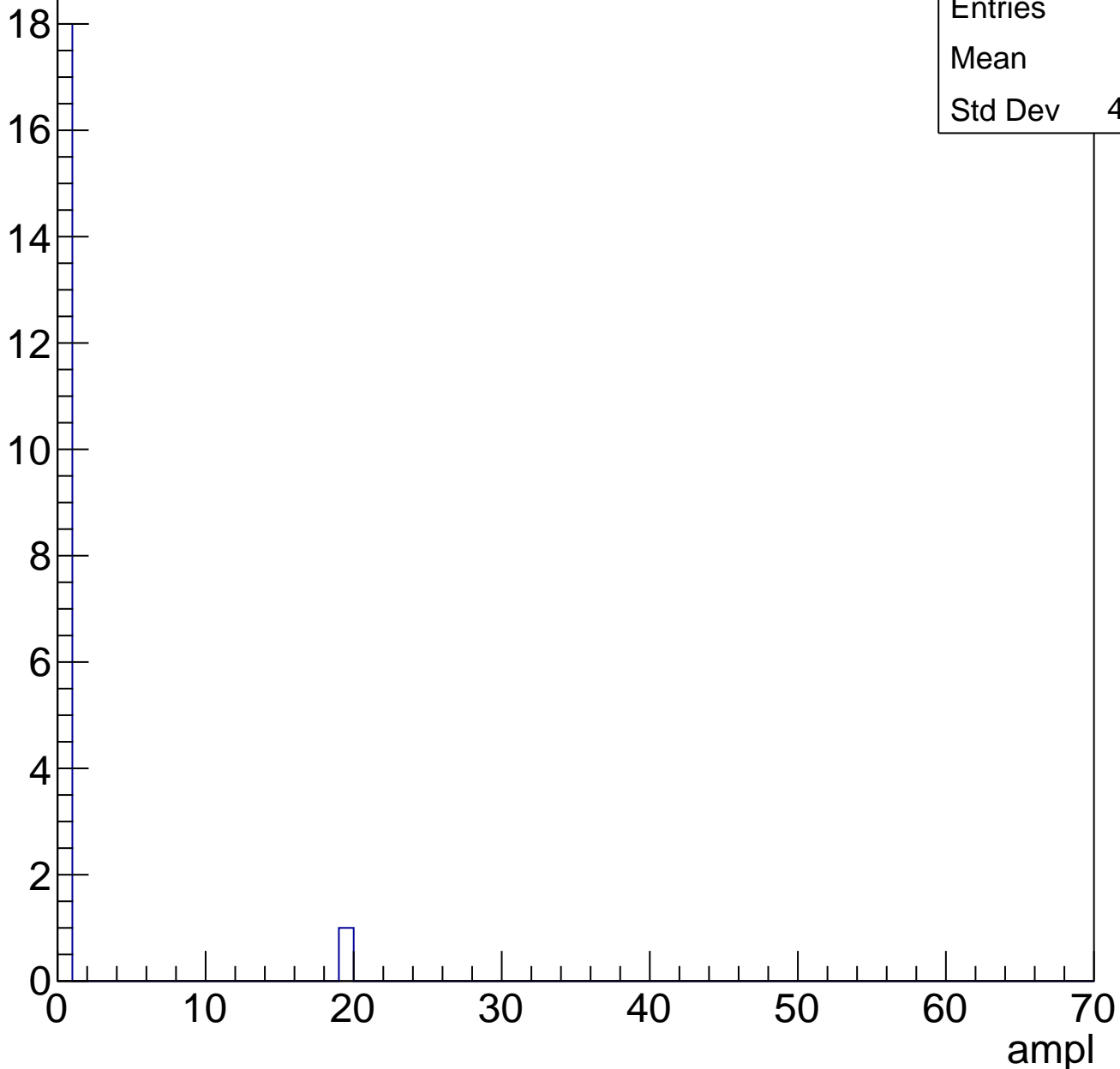


B1L103S, U24-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1
Std Dev	4.243

Entry



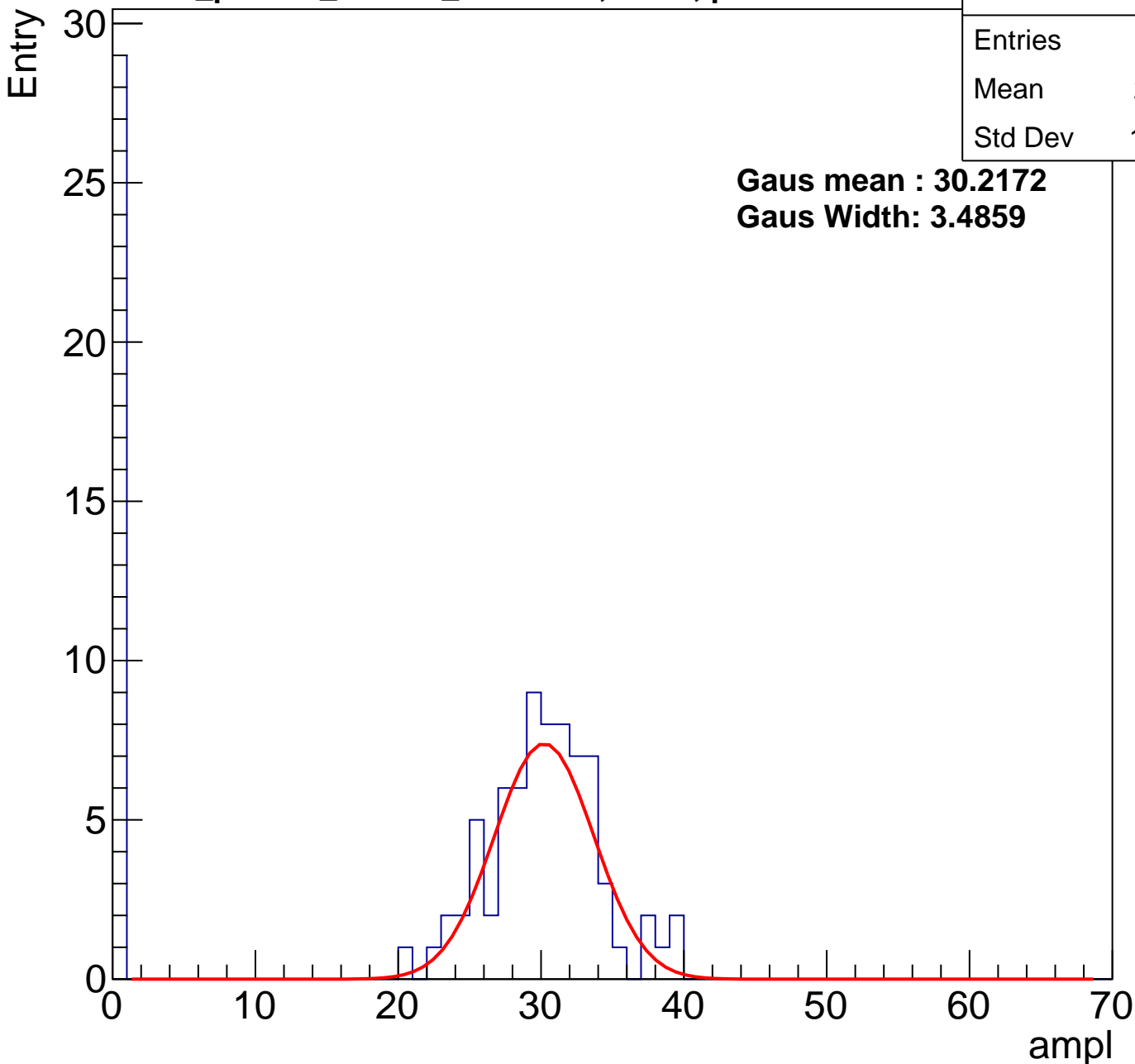
B1L103S, U24-ch35, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	21.31
Std Dev	13.82

Gaus mean : 30.2172

Gaus Width: 3.4859



B1L103S, U24-ch35, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	30.08
Std Dev	13.94

Gaus mean : 36.8105

Gaus Width: 3.1274

Entry

12

10

8

6

4

2

0

0

10

20

30

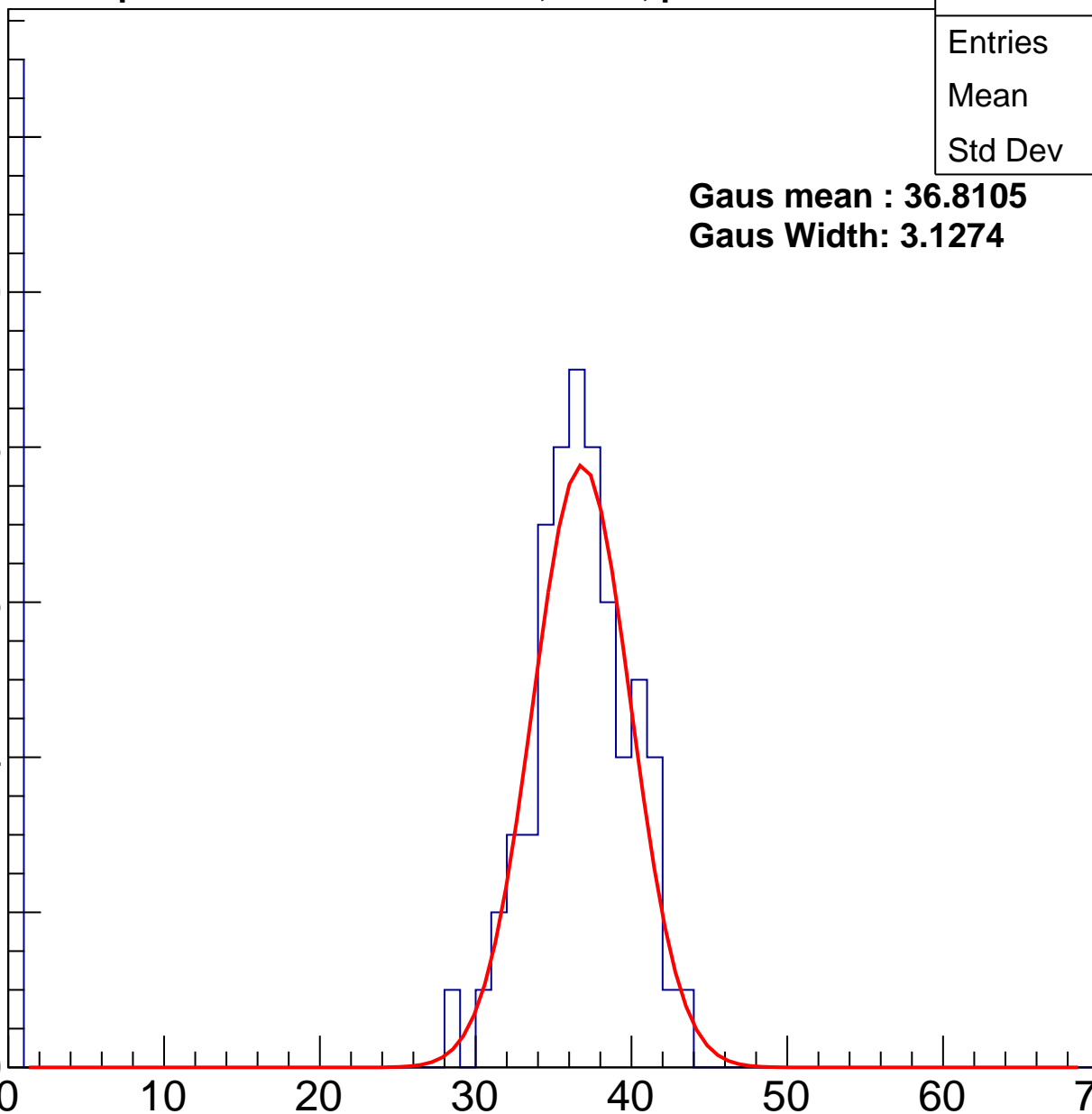
40

50

60

70

ampl



B1L103S, U24-ch35, adc2

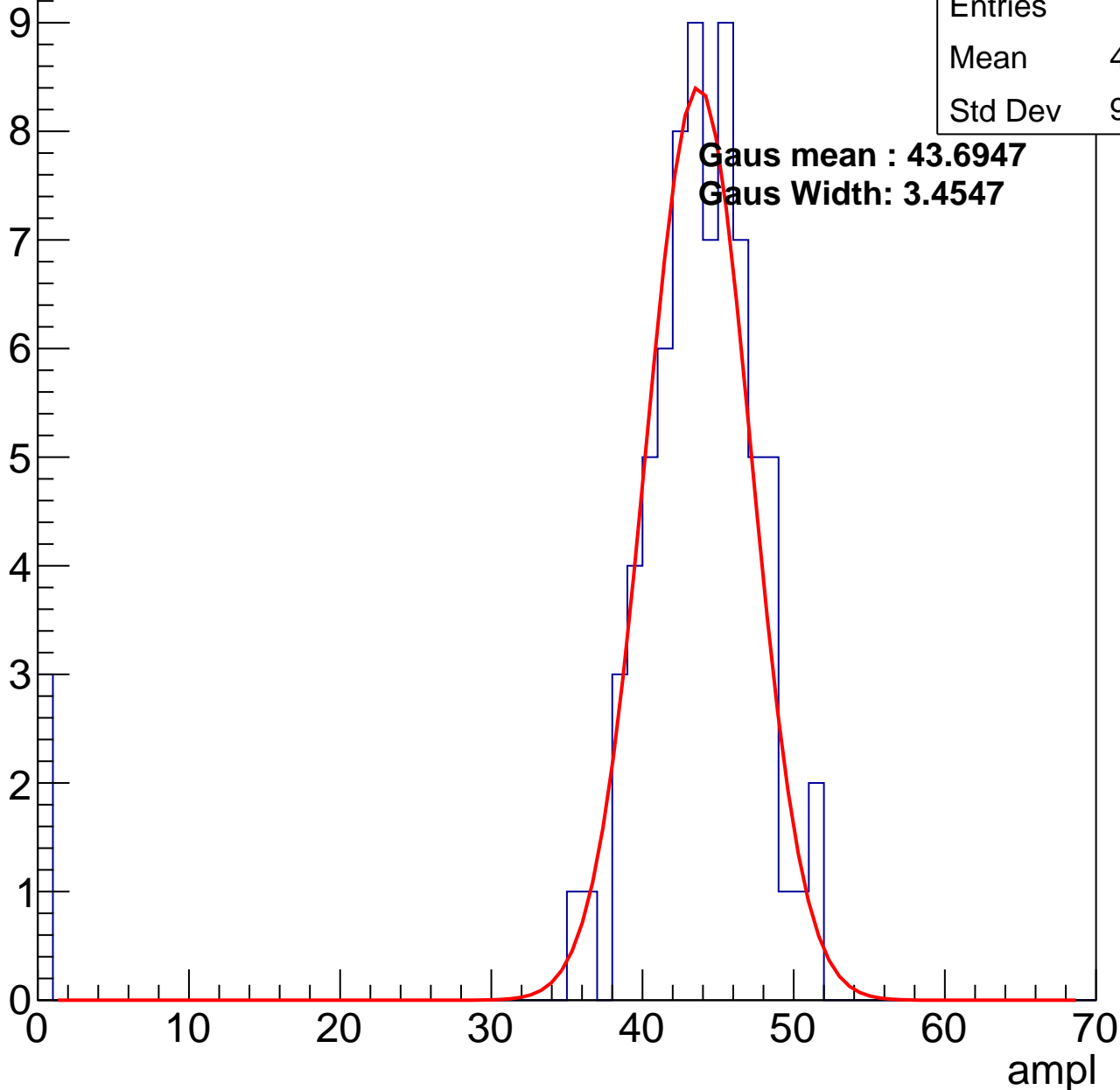
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	41.83
Std Dev	9.042

Gaus mean : 43.6947

Gaus Width: 3.4547

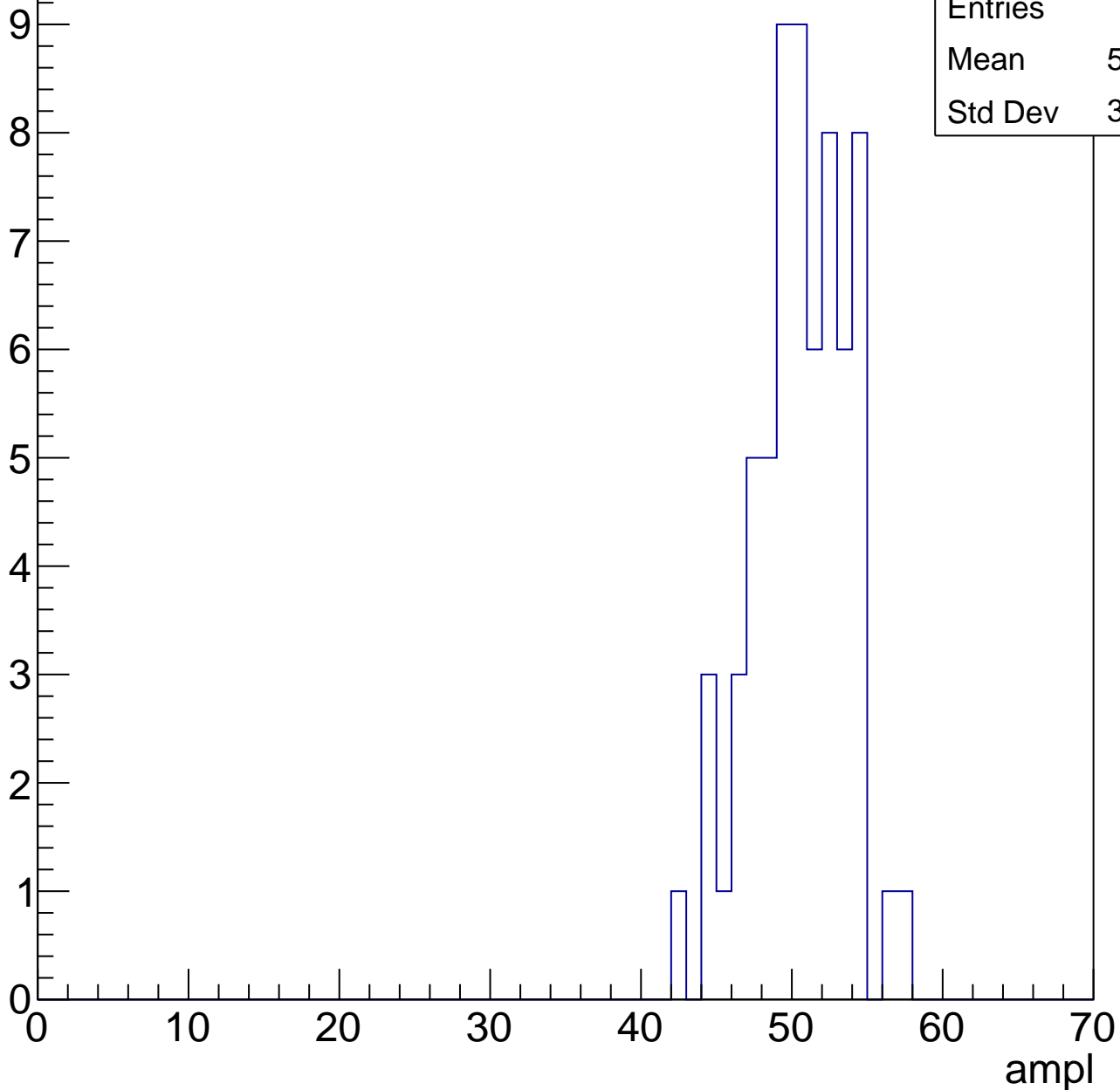


B1L103S, U24-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	50.12
Std Dev	3.072

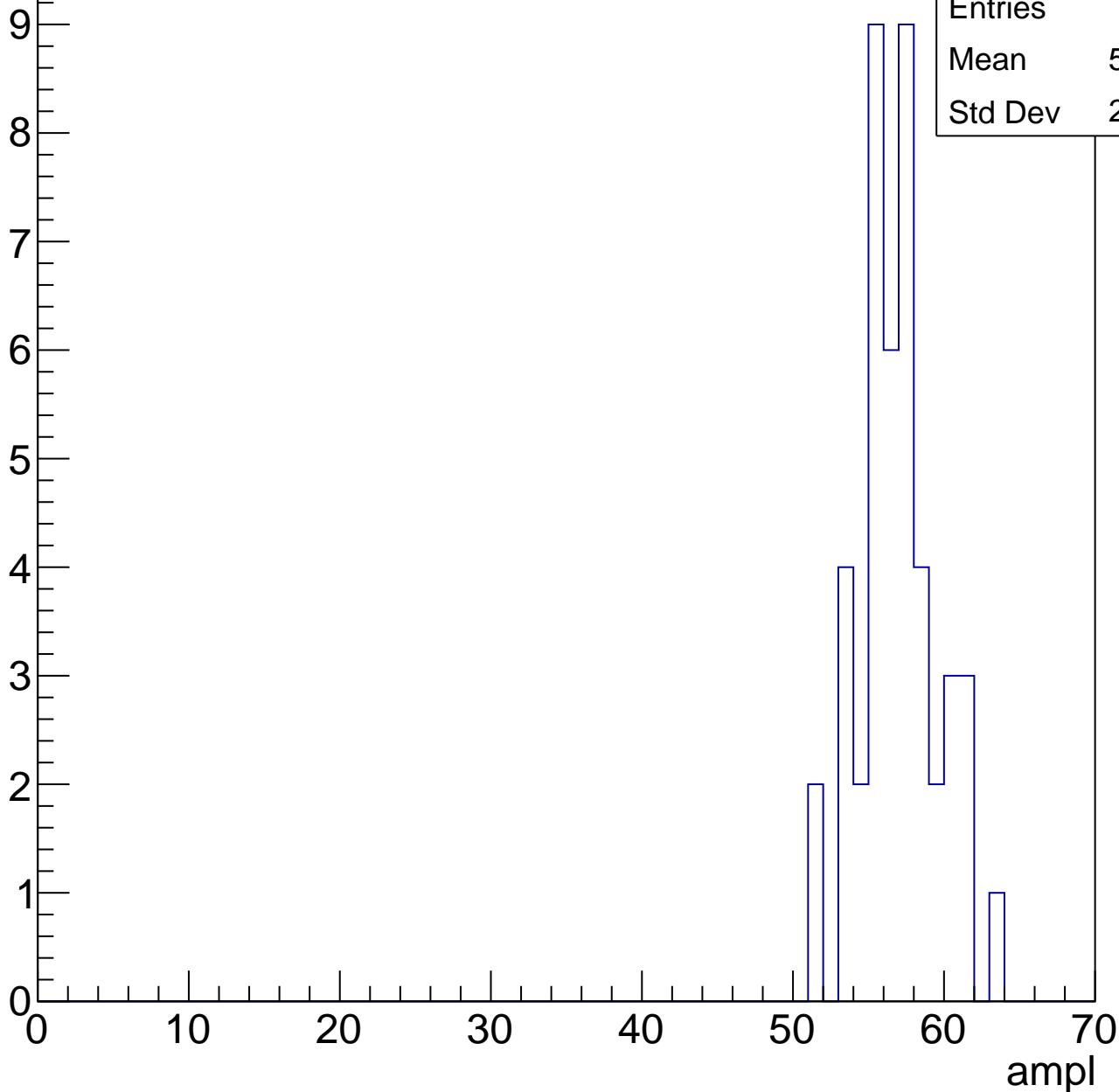


B1L103S, U24-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

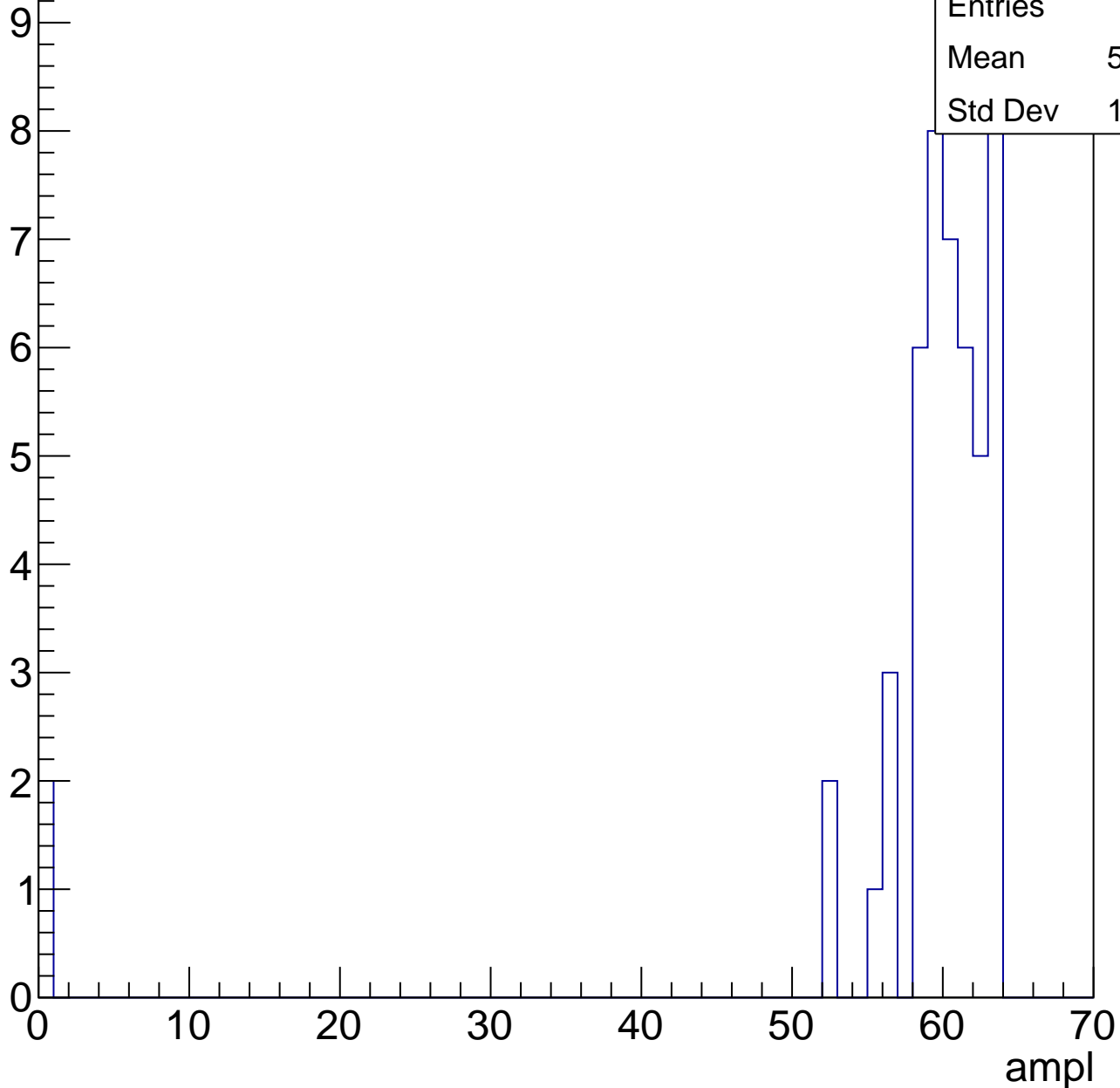
Entries	45
Mean	56.49
Std Dev	2.613



B1L103S, U24-ch35, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

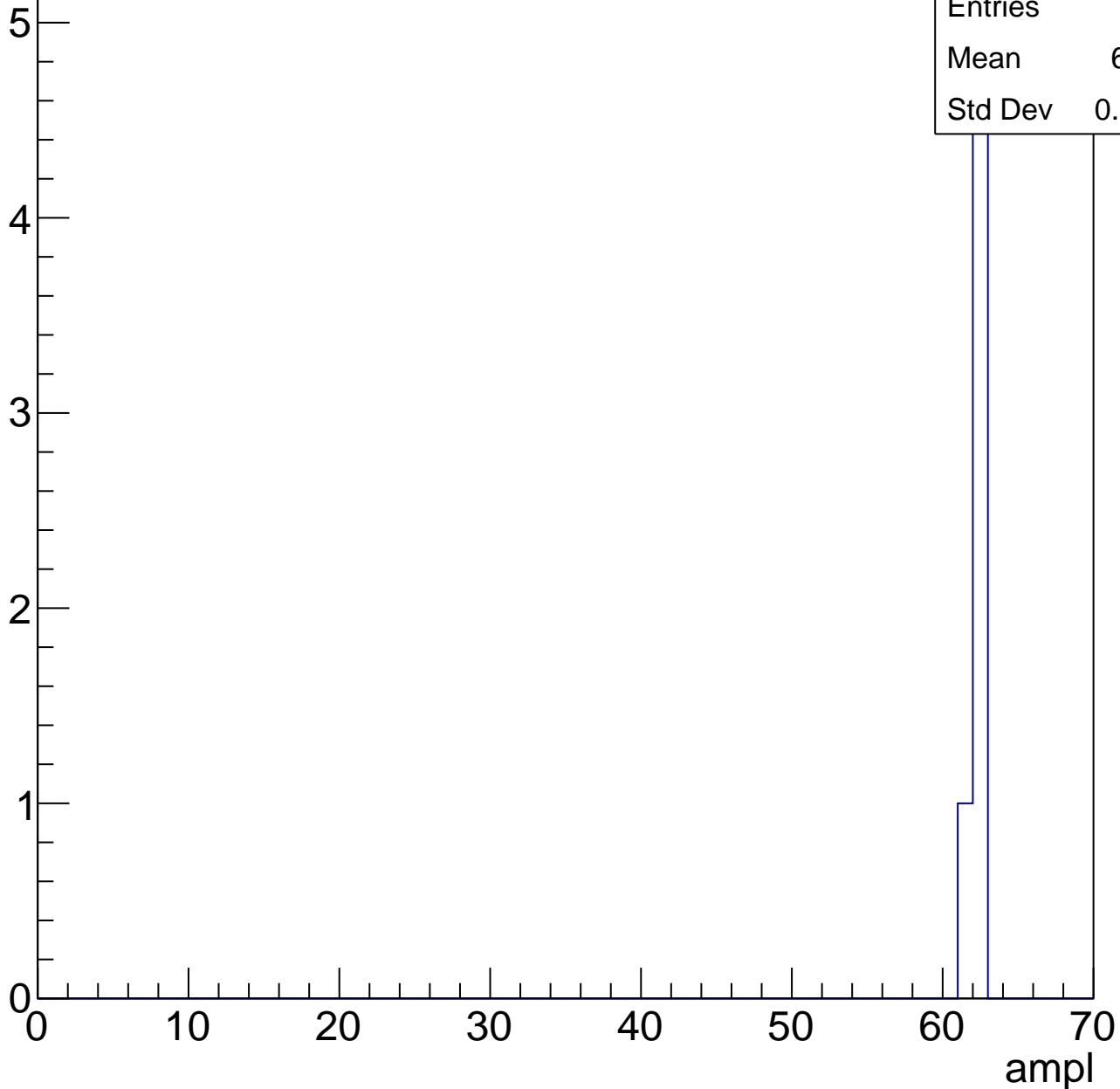


B1L103S, U24-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	6
Mean	61.83
Std Dev	0.3727



B1L103S, U24-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U24-ch36, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	24.19
Std Dev	10.1

Gaus mean : 29.1577

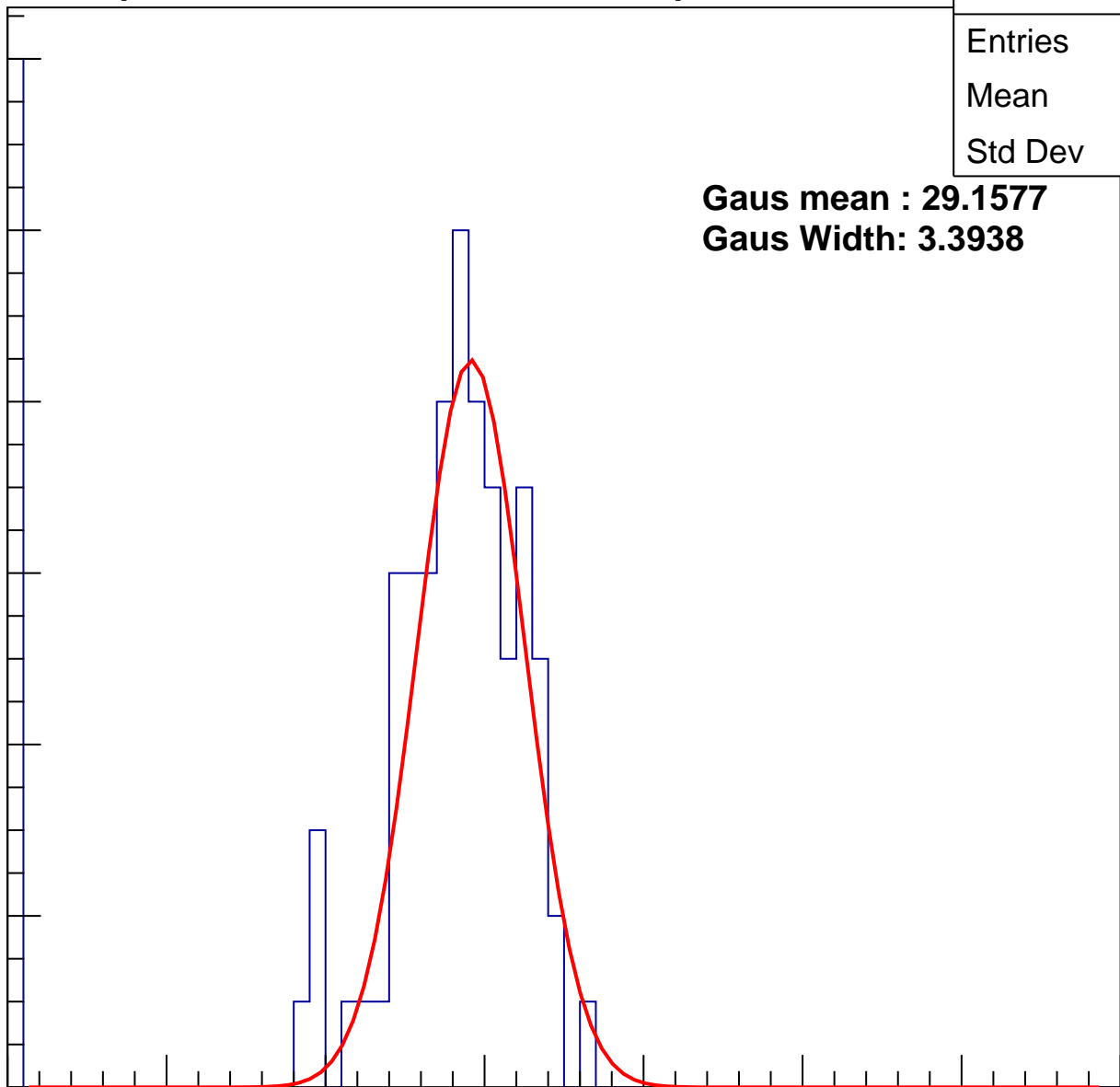
Gaus Width: 3.3938

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch36, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	29.82
Std Dev	13.09

Gaus mean : 36.0160

Gaus Width: 4.4163

Entry

10

8

6

4

2

0

0

10

20

30

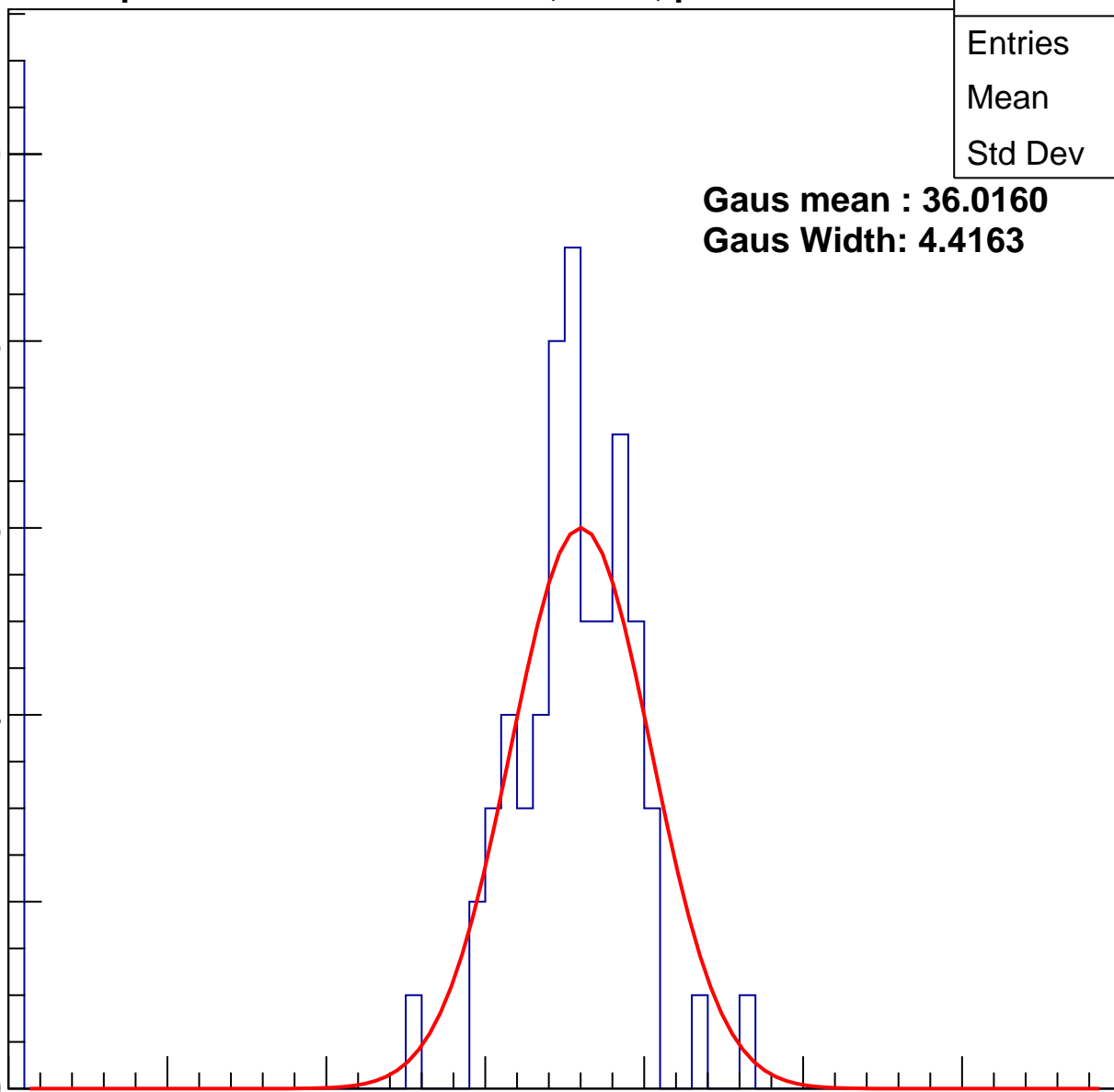
40

50

60

70

ampl



B1L103S, U24-ch36, adc2

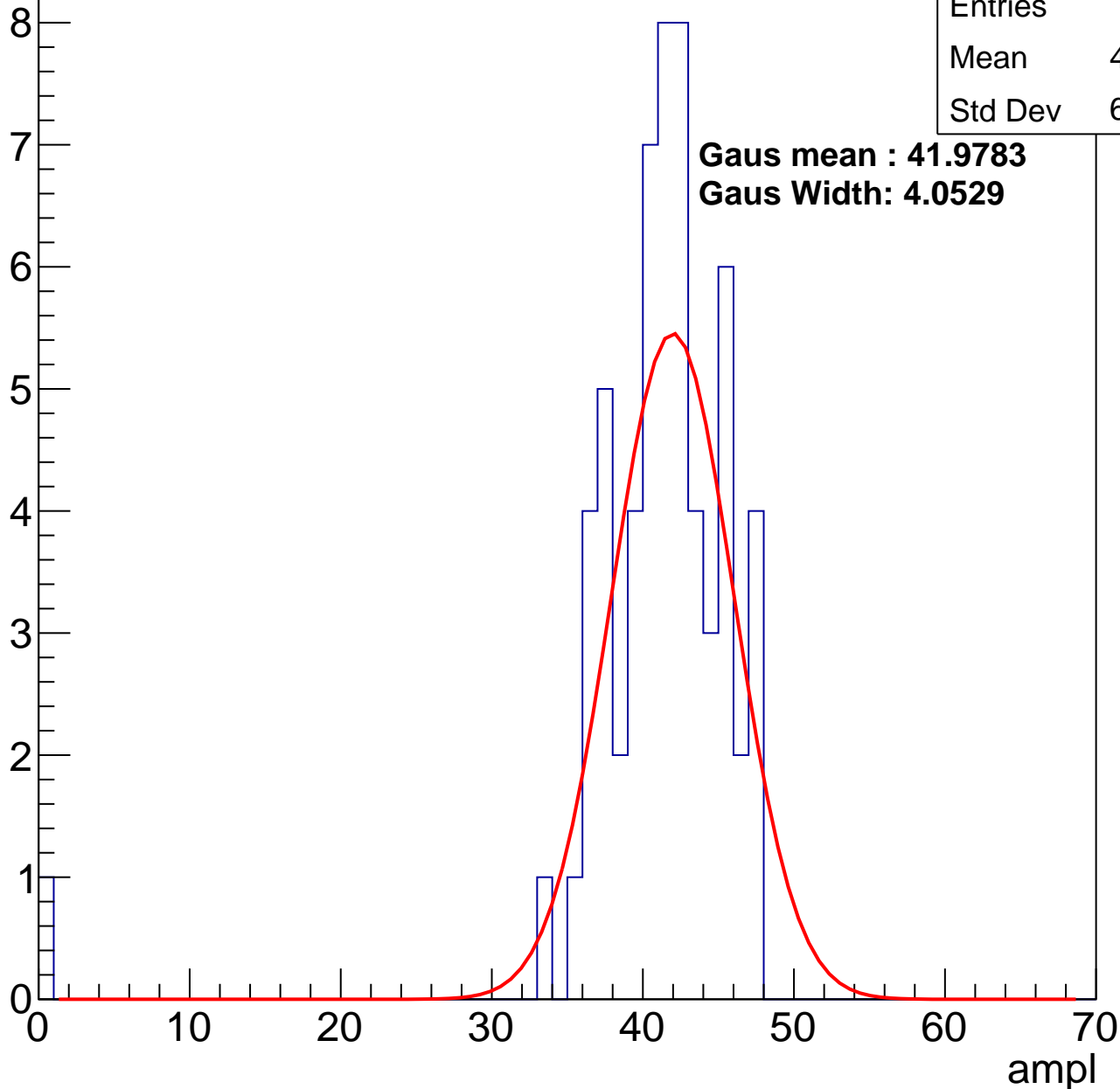
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	40.45
Std Dev	6.225

Gaus mean : 41.9783

Gaus Width: 4.0529

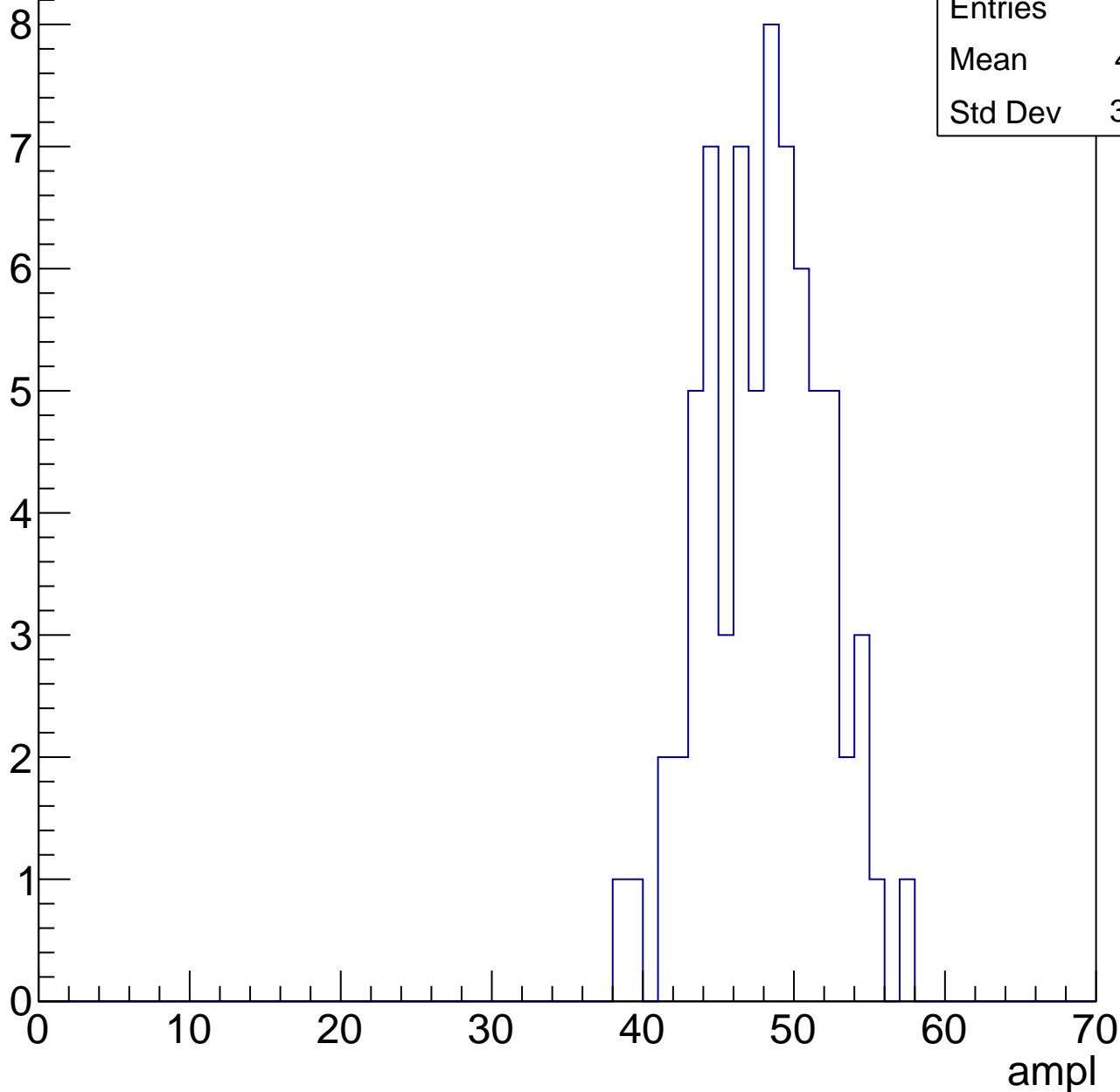


B1L103S, U24-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	47.61
Std Dev	3.902

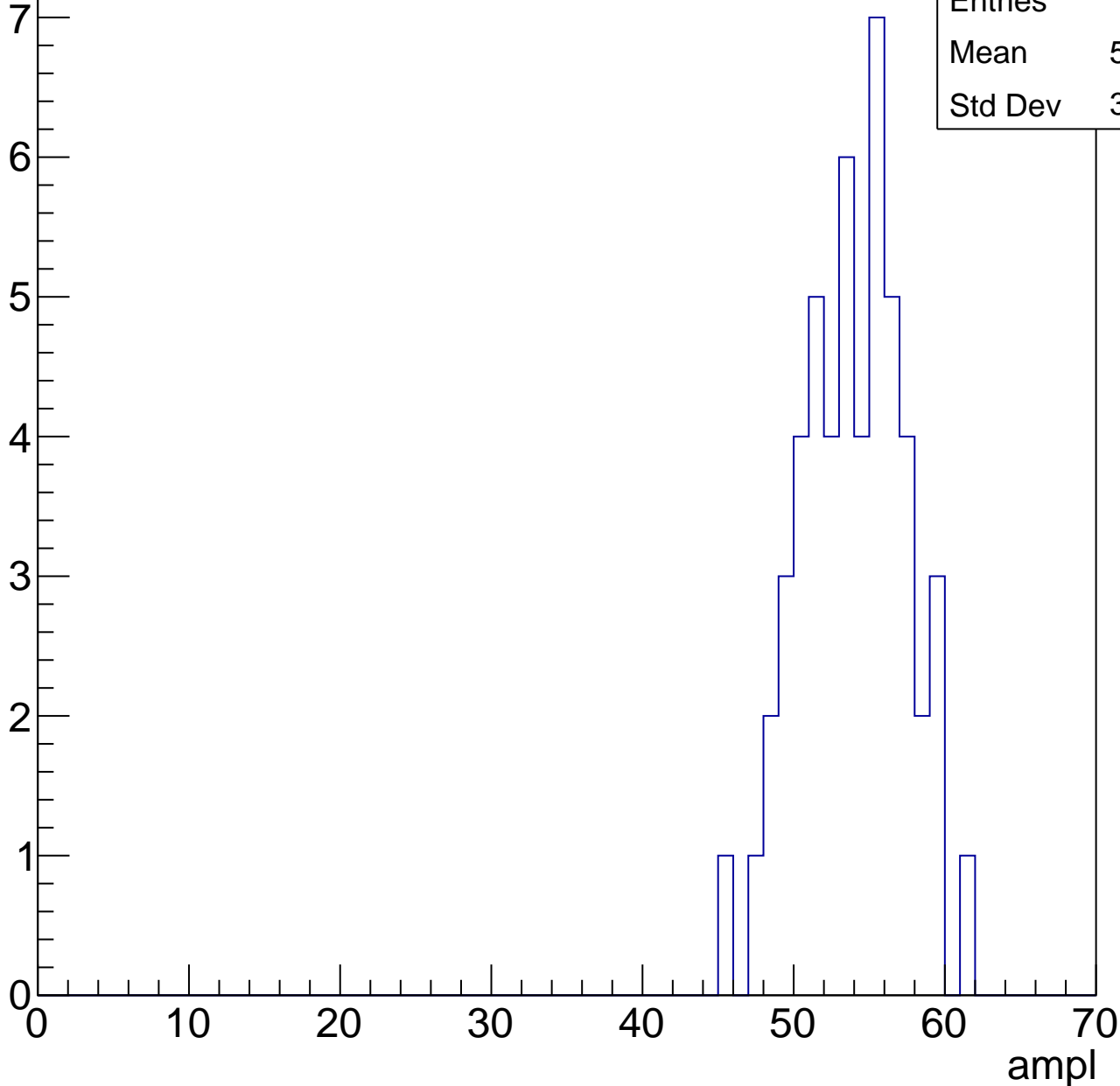


B1L103S, U24-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

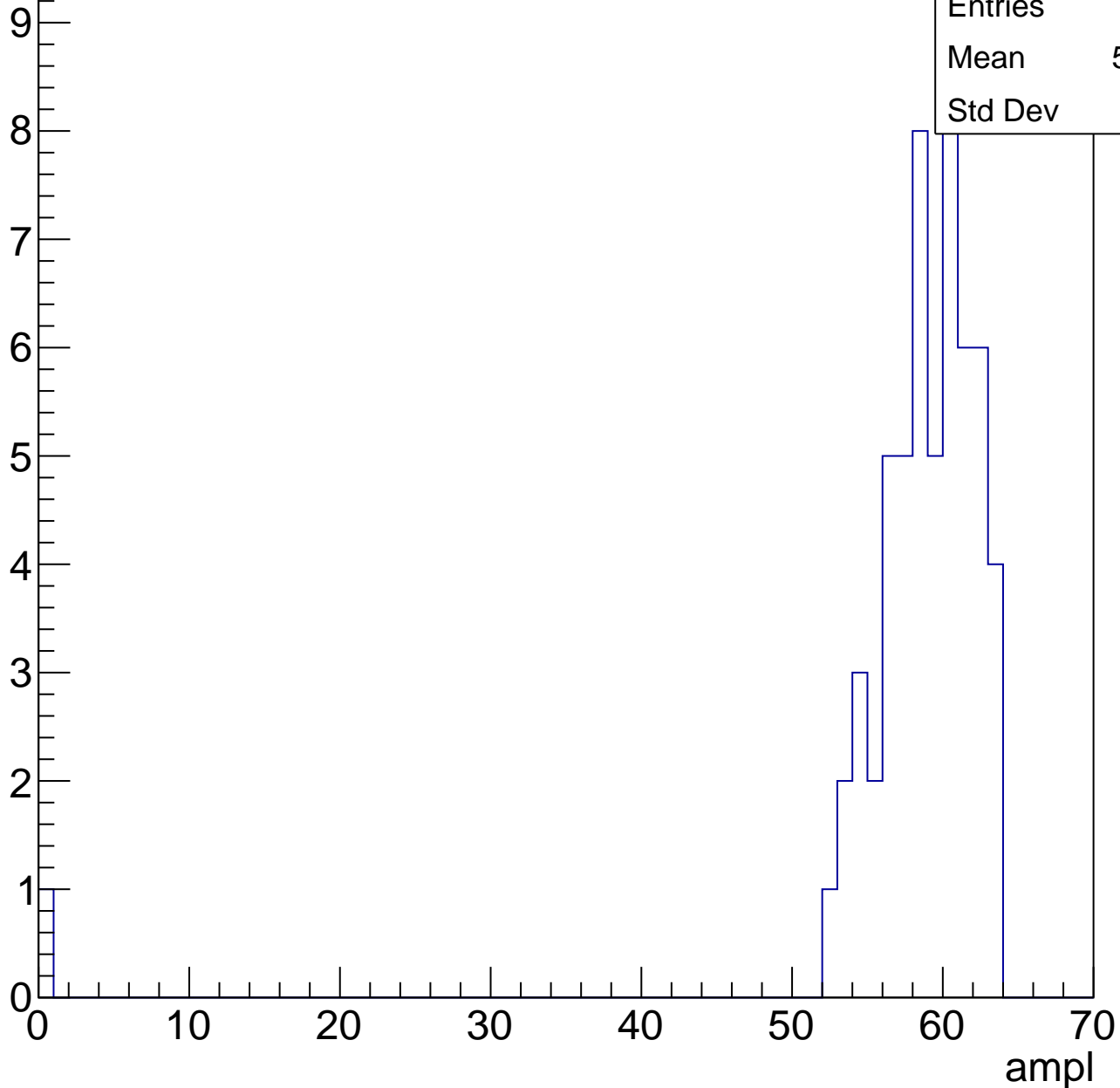
Entries	52
Mean	53.44
Std Dev	3.427



B1L103S, U24-ch36, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

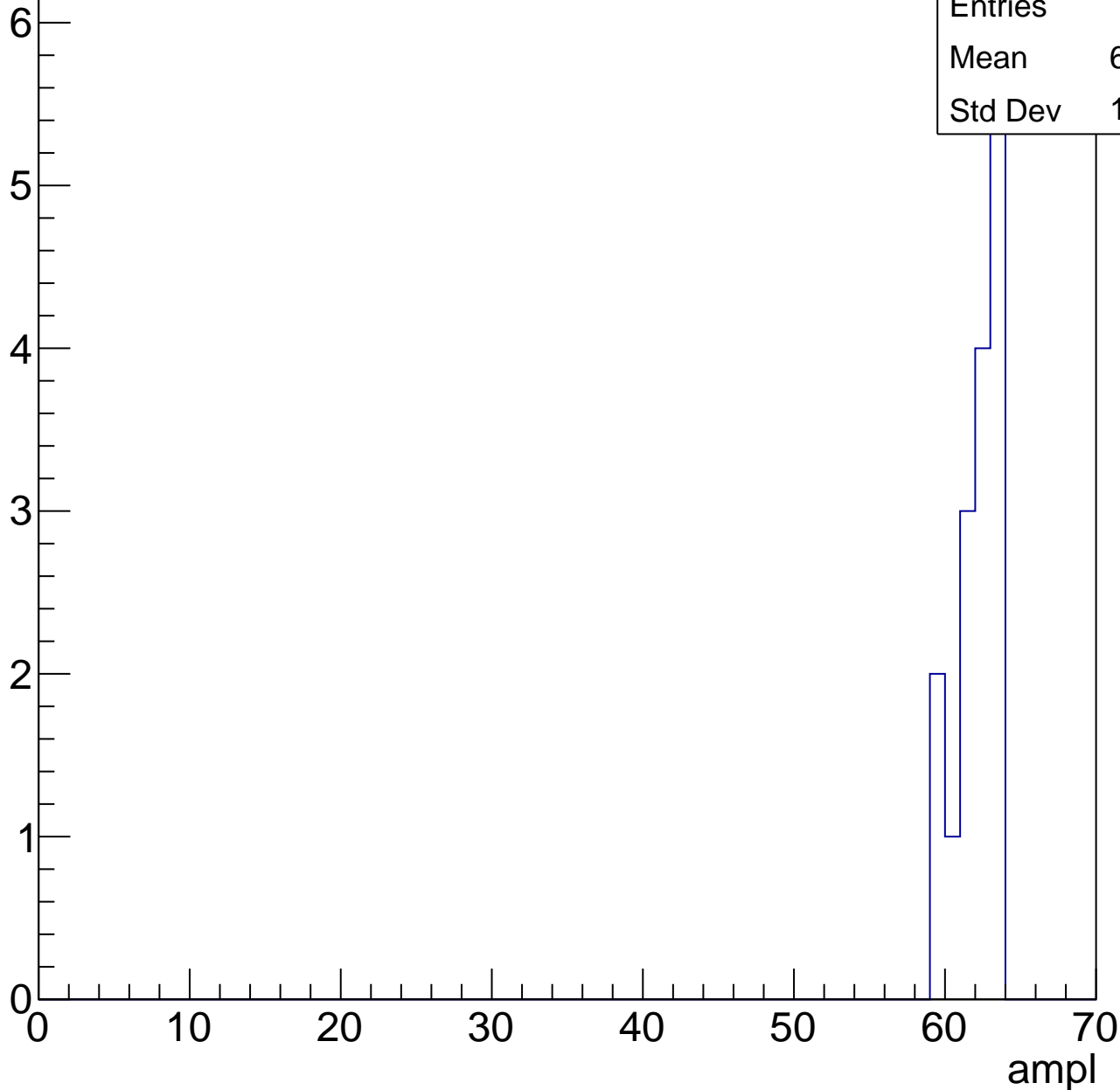


B1L103S, U24-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.69
Std Dev	1.356

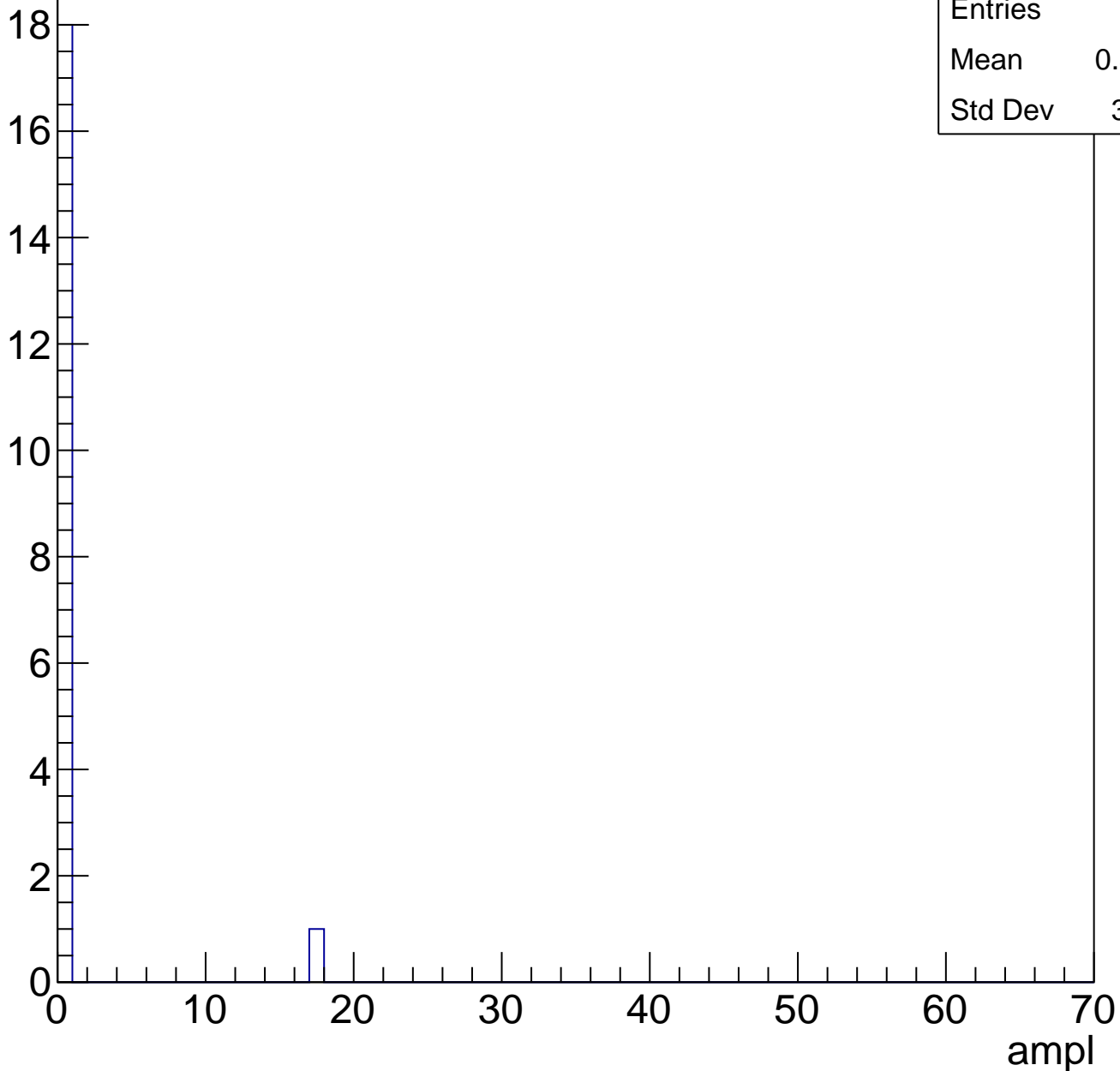


B1L103S, U24-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0.8947
Std Dev	3.796

Entry



B1L103S, U24-ch37, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	24.95
Std Dev	10.52

Gaus mean : 29.3452

Gaus Width: 3.9108

Entry

12

10

8

6

4

2

0

0

10

20

30

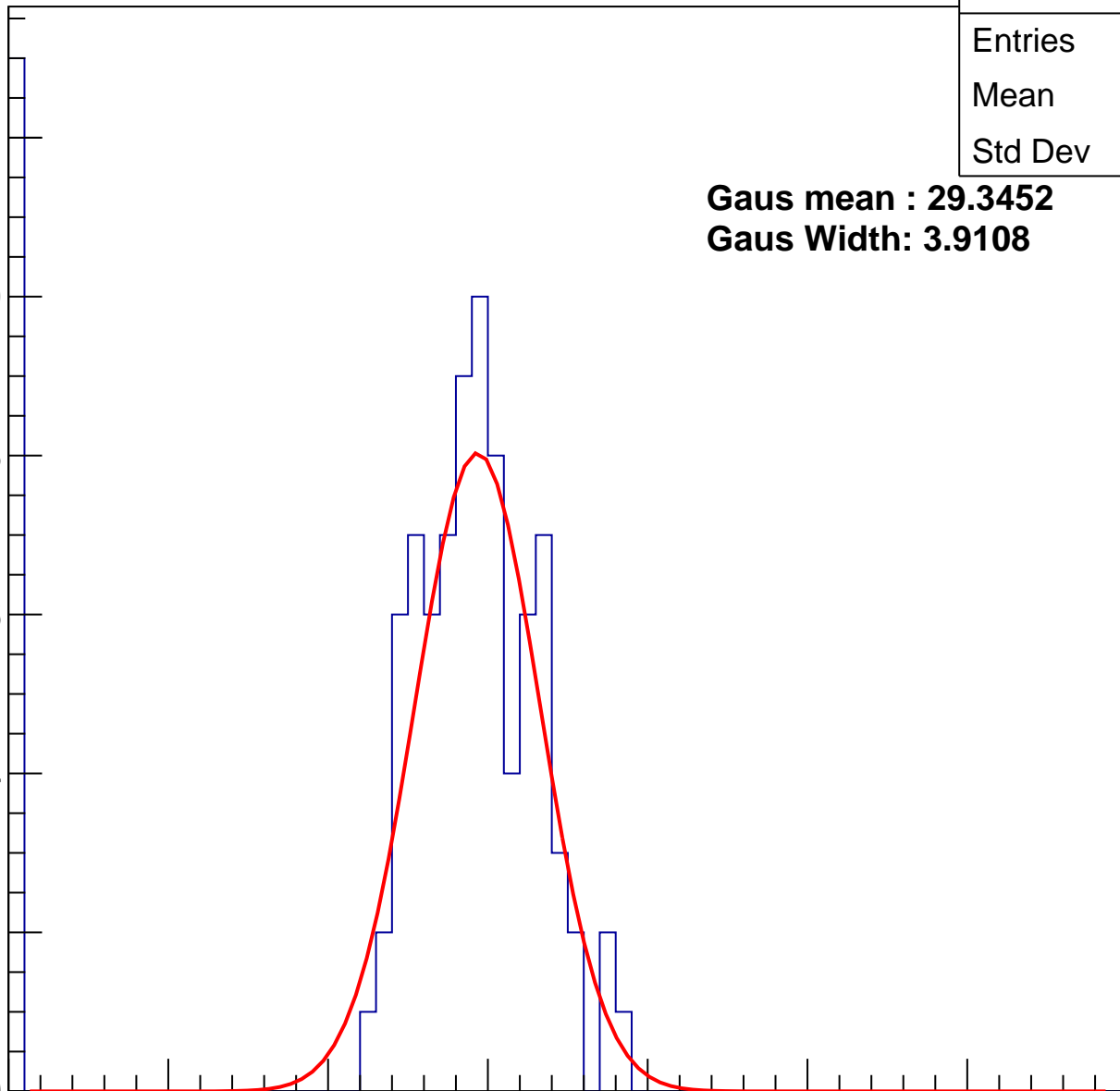
40

50

60

70

ampl



B1L103S, U24-ch37, adc1

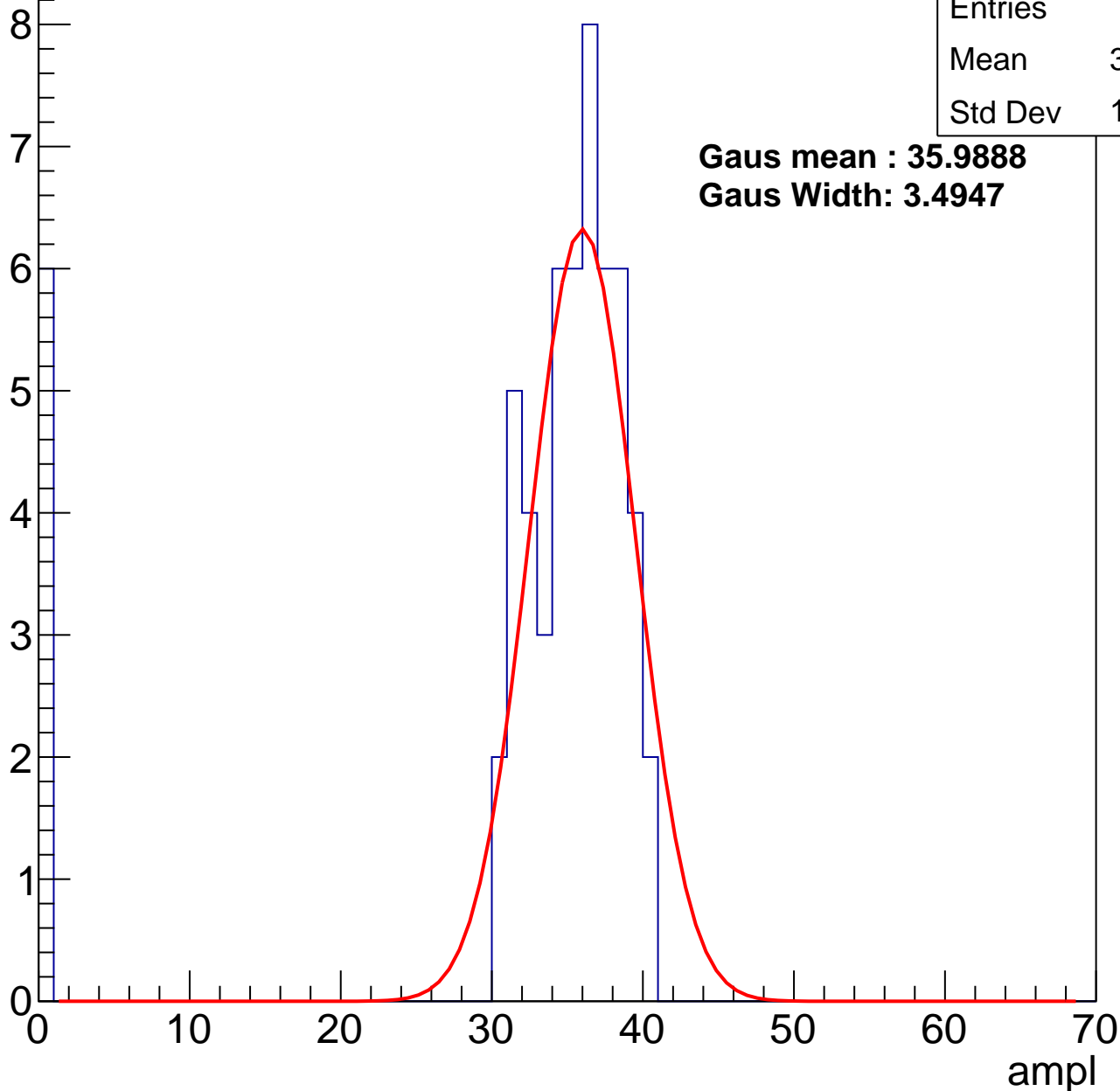
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	31.55
Std Dev	11.02

Gaus mean : 35.9888

Gaus Width: 3.4947



B1L103S, U24-ch37, adc2

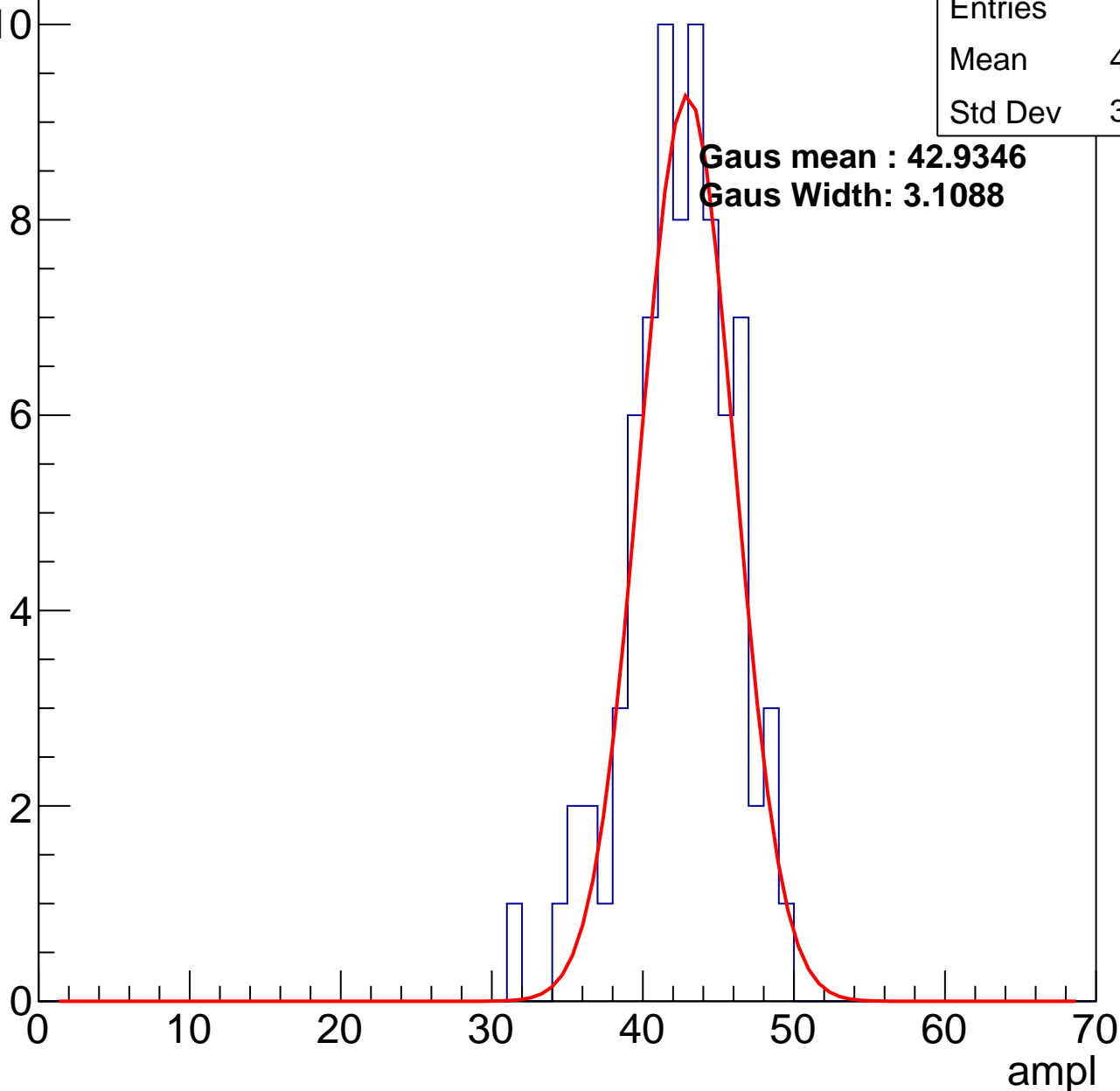
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	42.04
Std Dev	3.462

Gaus mean : 42.9346

Gaus Width: 3.1088

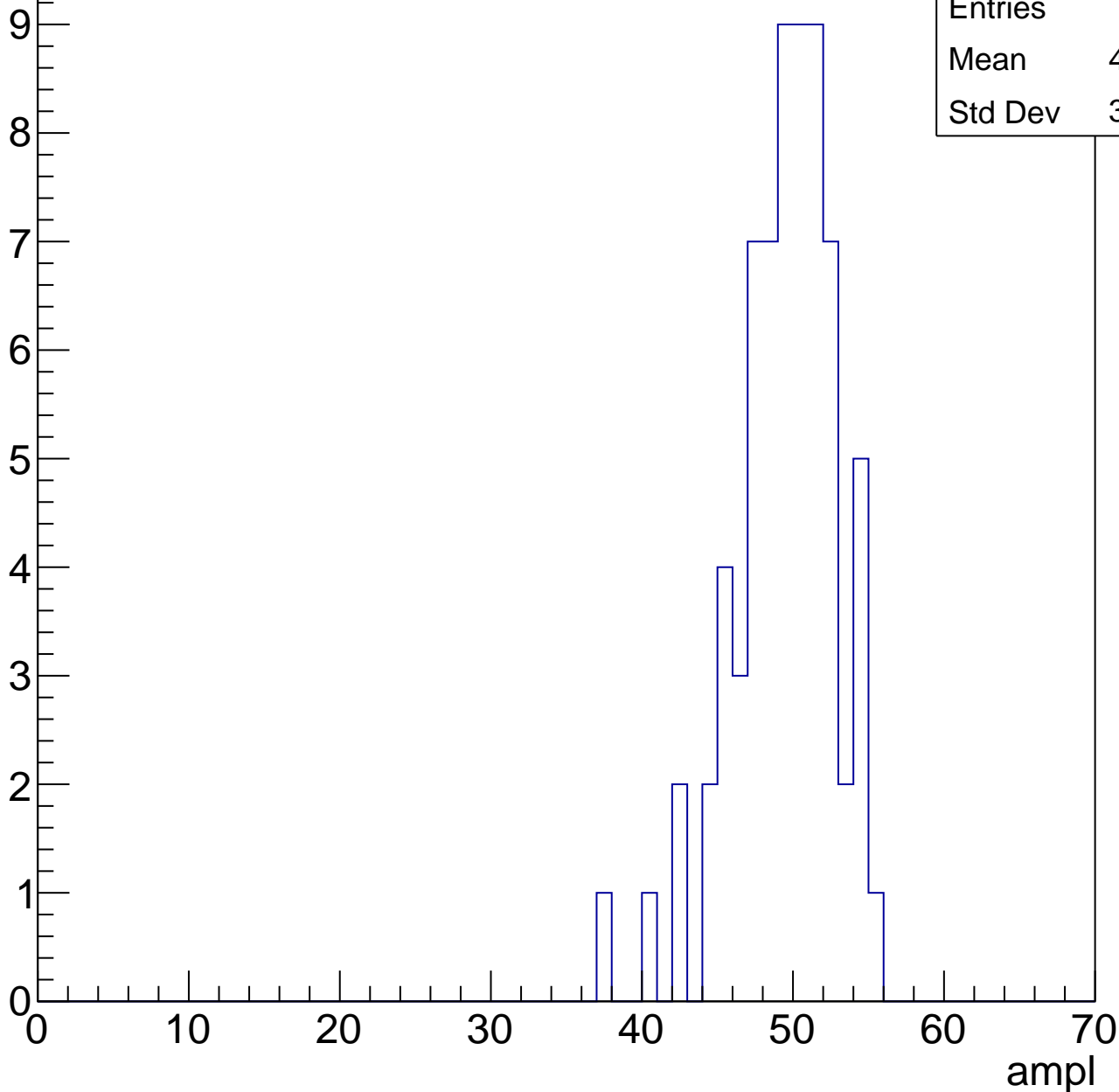


B1L103S, U24-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	48.94
Std Dev	3.405

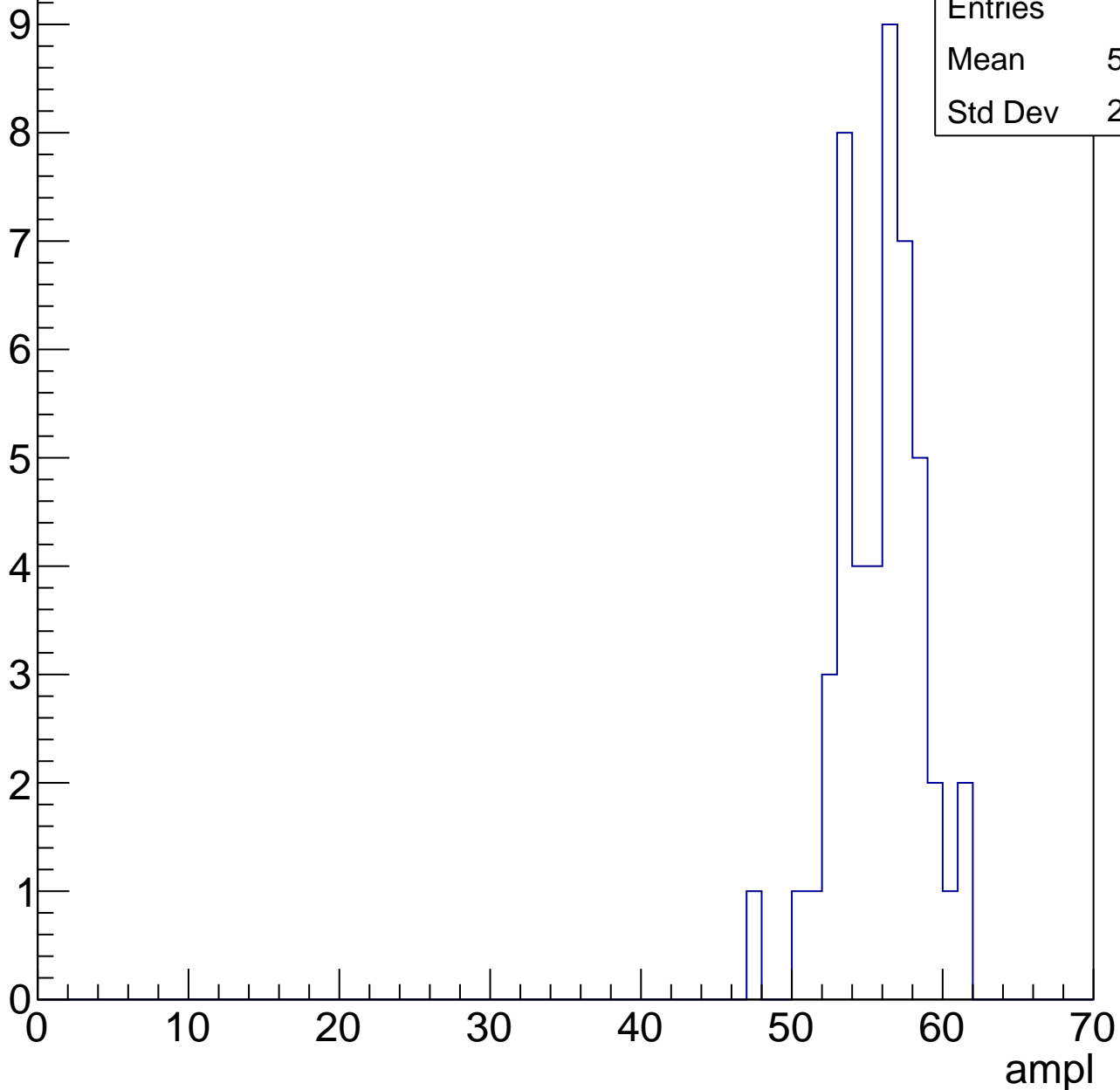


B1L103S, U24-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	55.35
Std Dev	2.788



B1L103S, U24-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

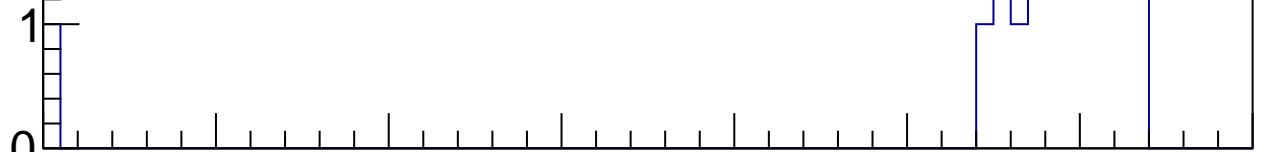
Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	58.76
Std Dev	8.156

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch37, adc6

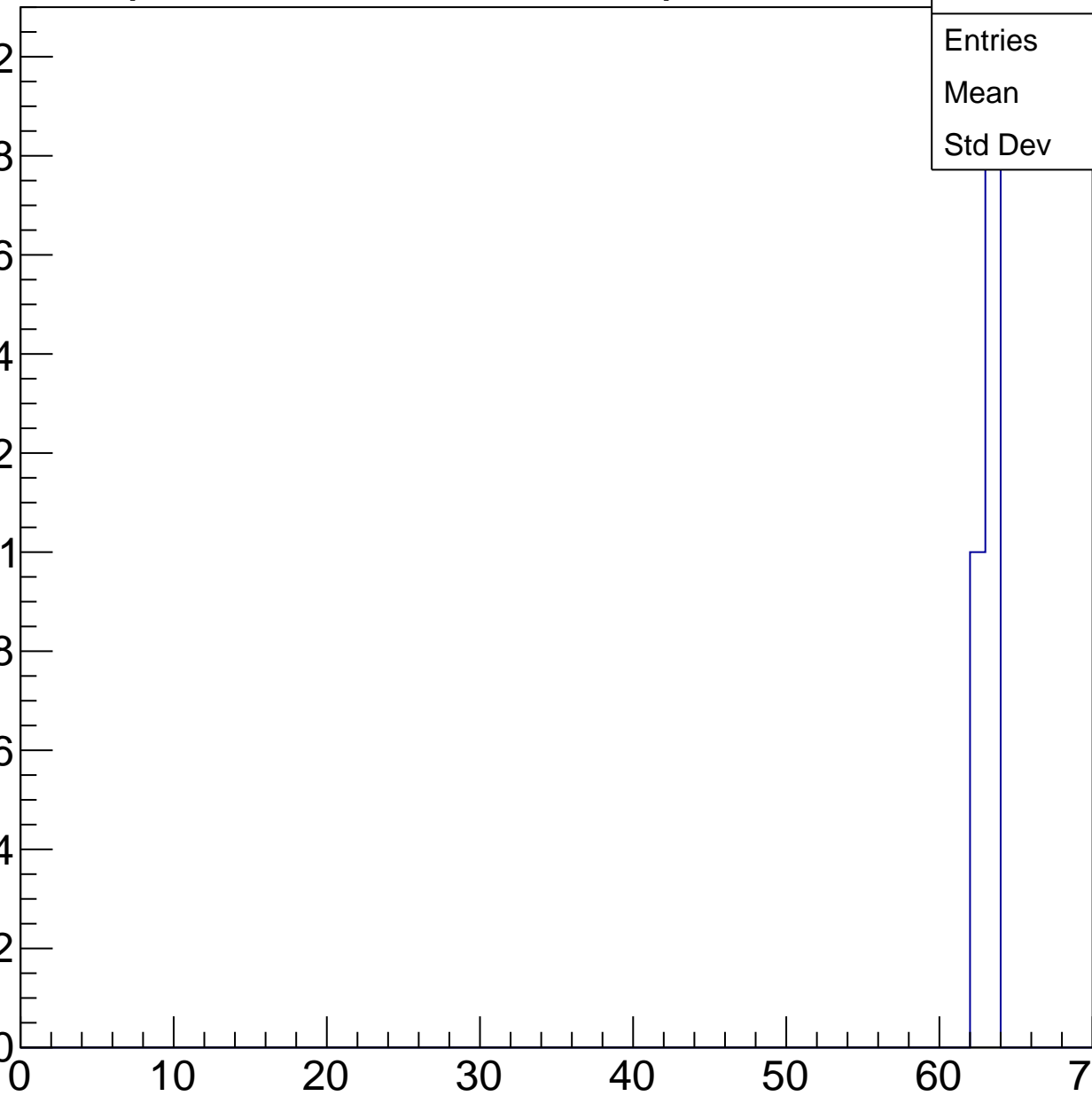
calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L103S, U24-ch37, adc7

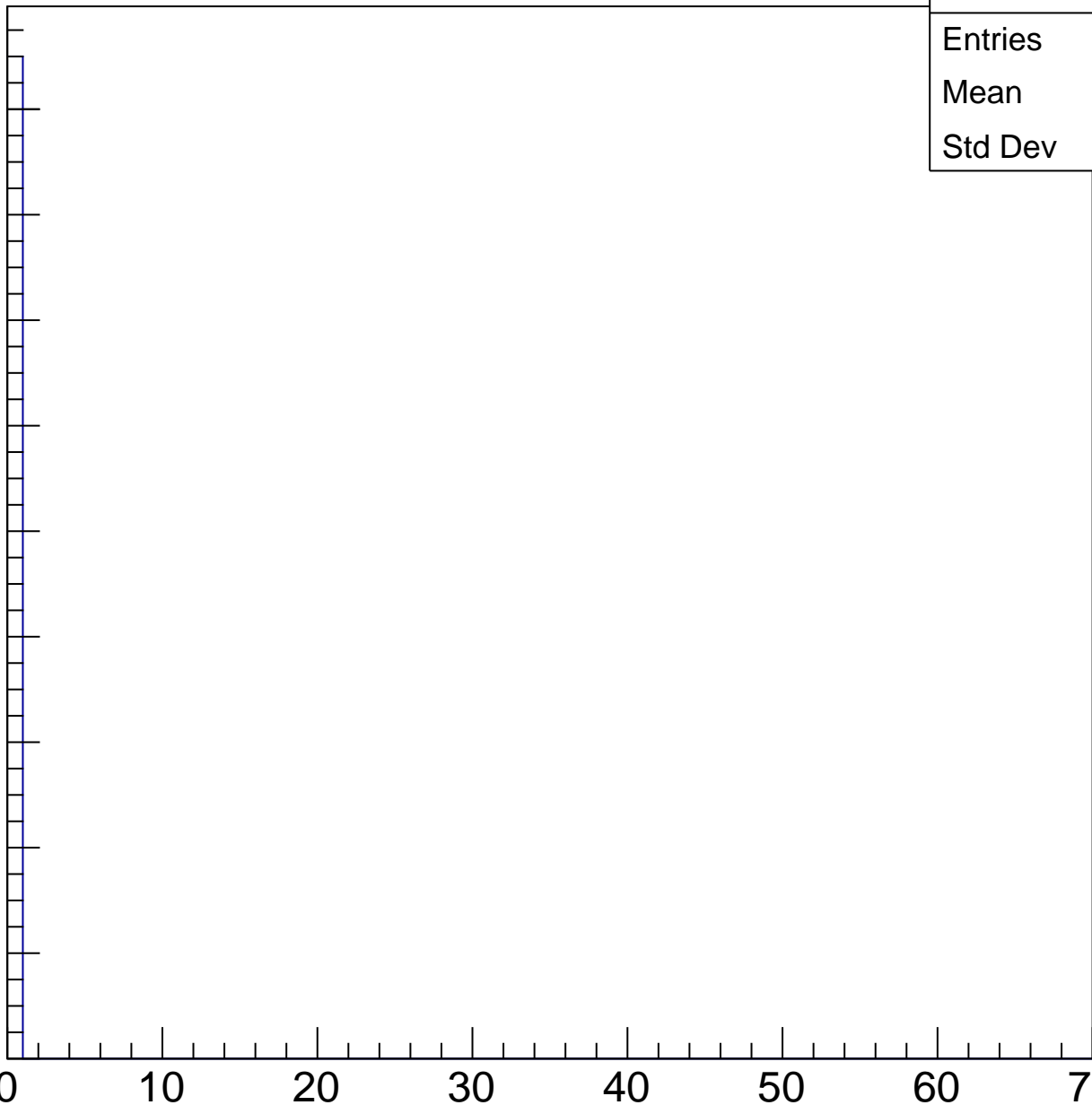
calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl



B1L103S, U24-ch38, adc0

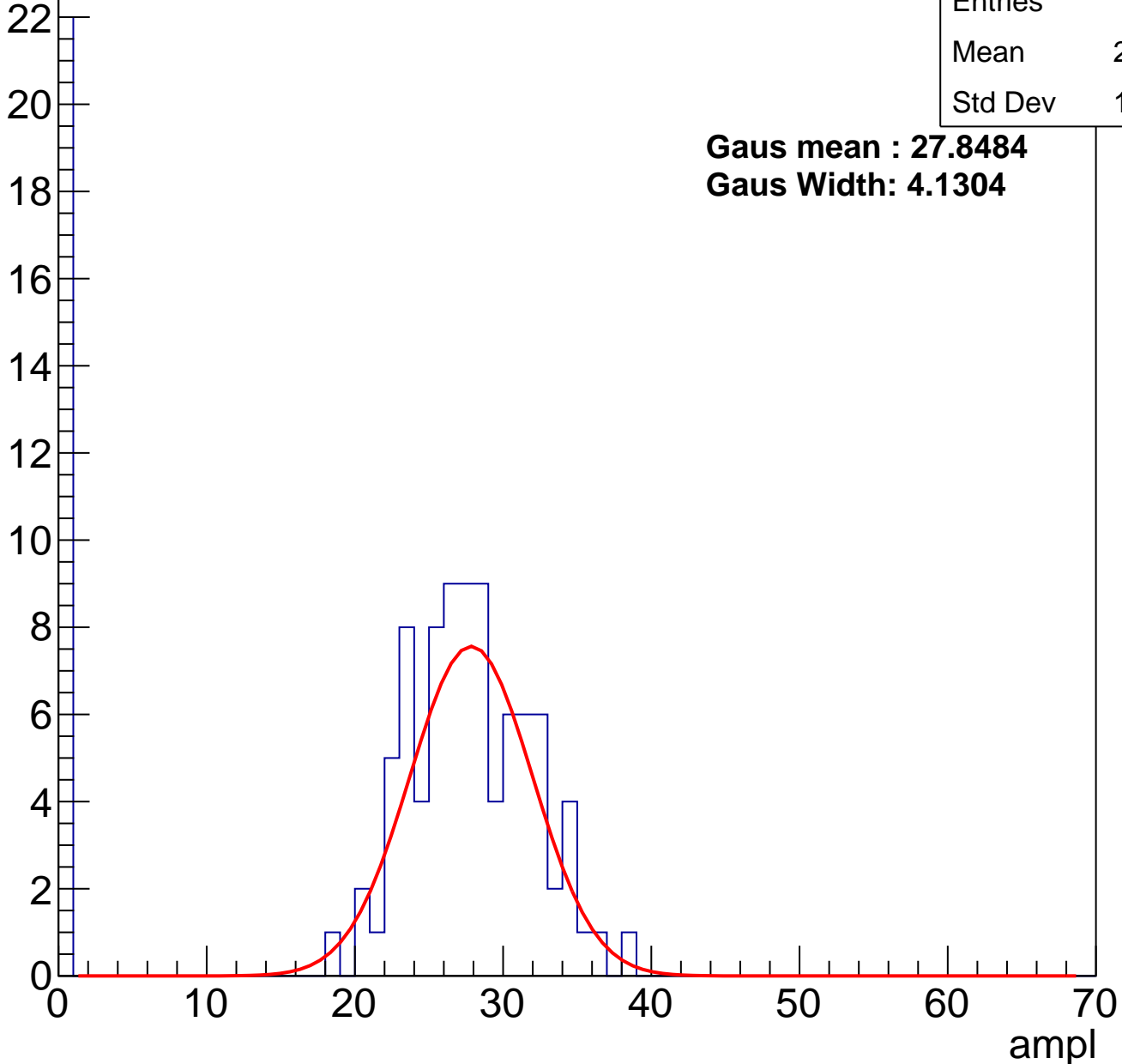
calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	21.86
Std Dev	11.56

Gaus mean : 27.8484

Gaus Width: 4.1304

Entry



B1L103S, U24-ch38, adc1

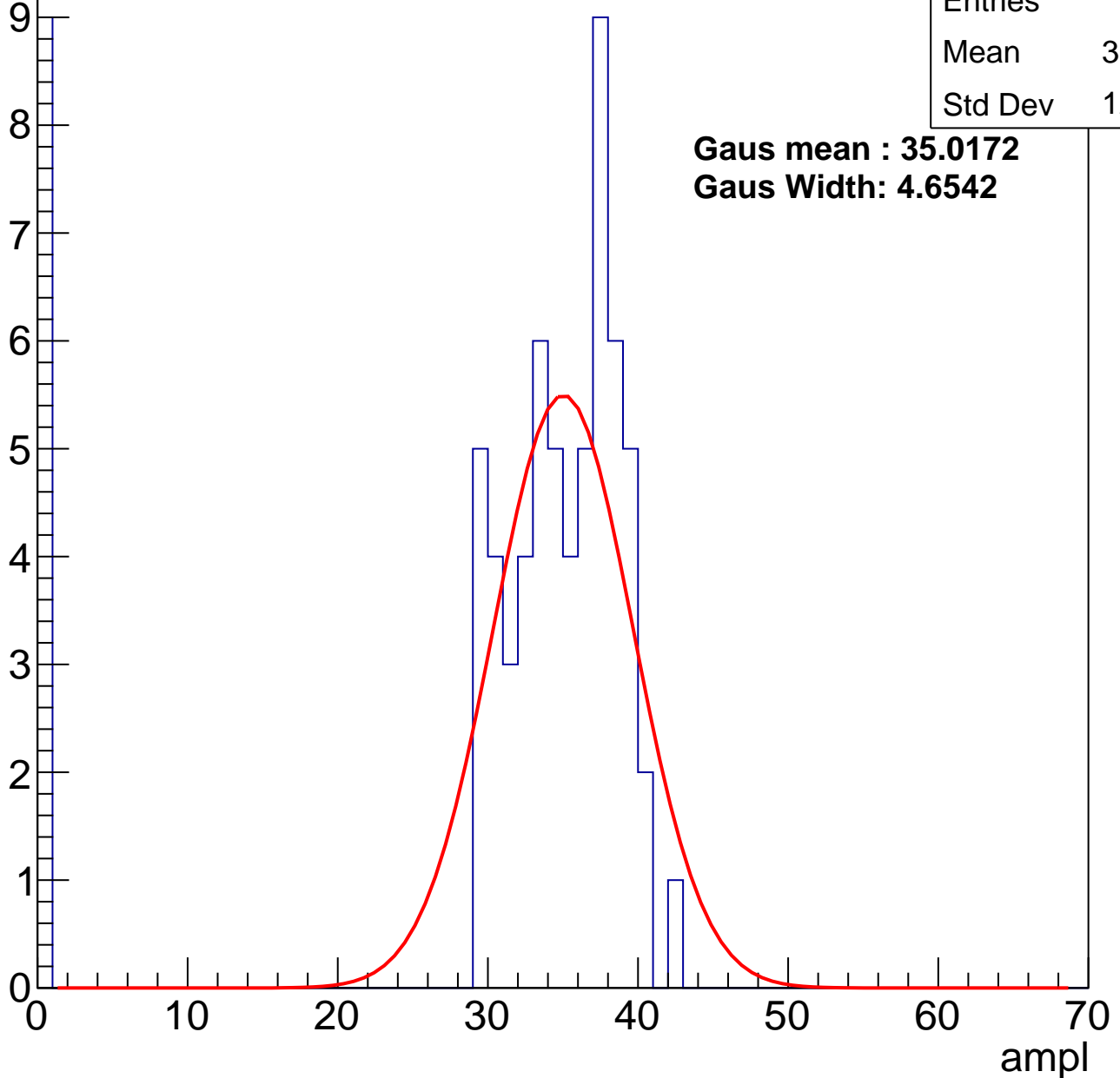
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	30.18
Std Dev	12.19

Gaus mean : 35.0172

Gaus Width: 4.6542



B1L103S, U24-ch38, adc2

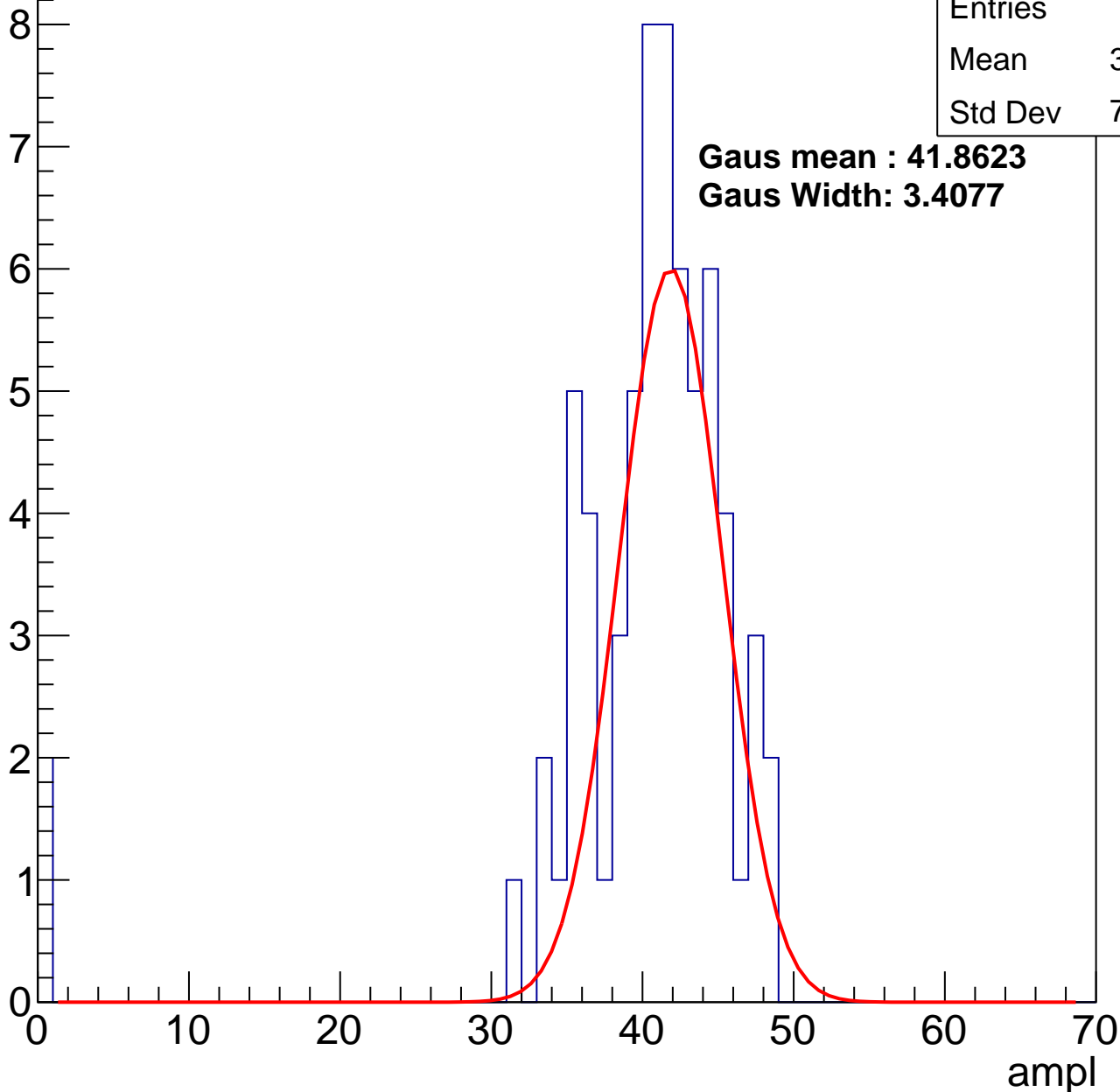
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	39.37
Std Dev	7.906

Gaus mean : 41.8623

Gaus Width: 3.4077

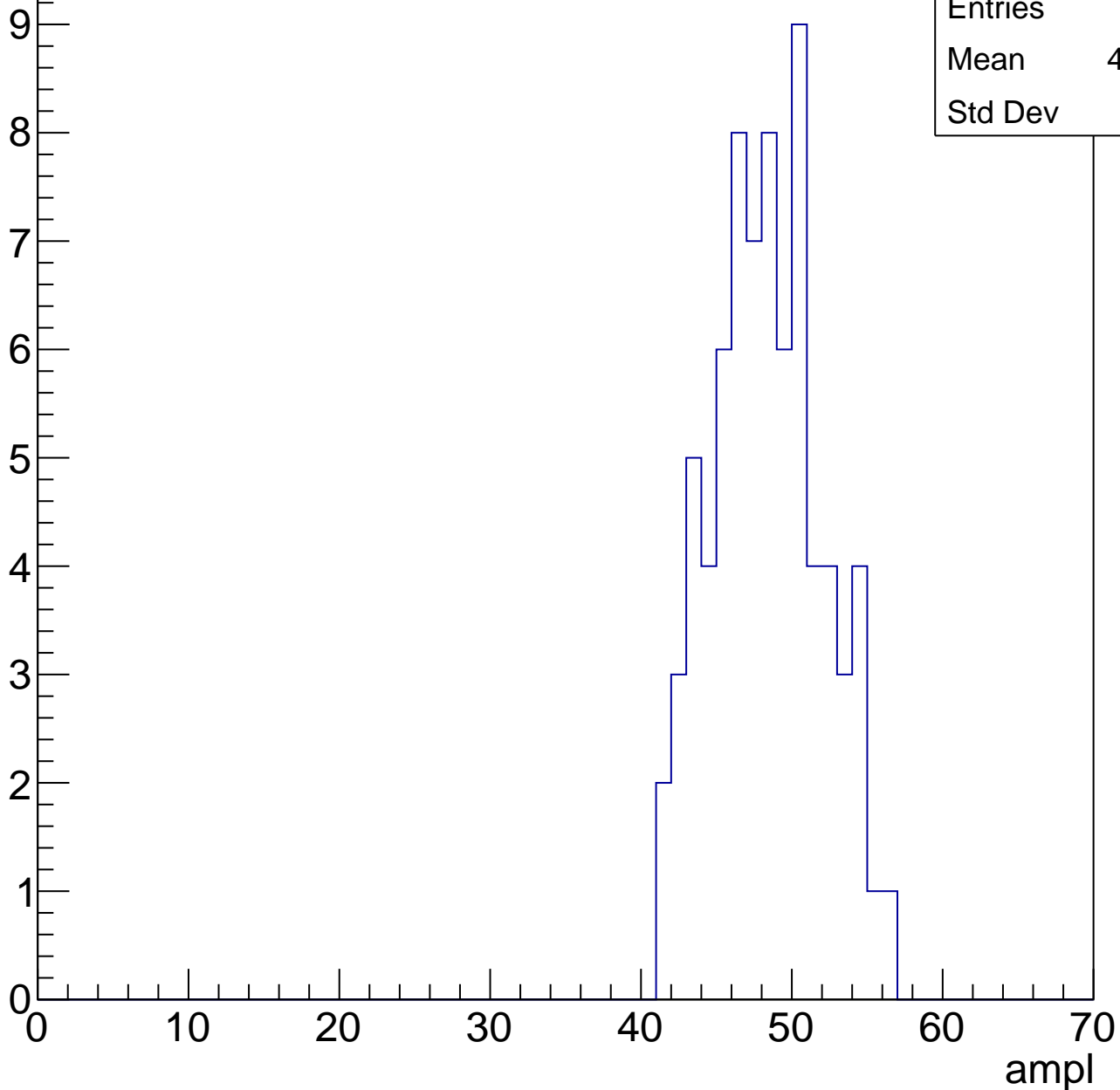


B1L103S, U24-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	47.89
Std Dev	3.58

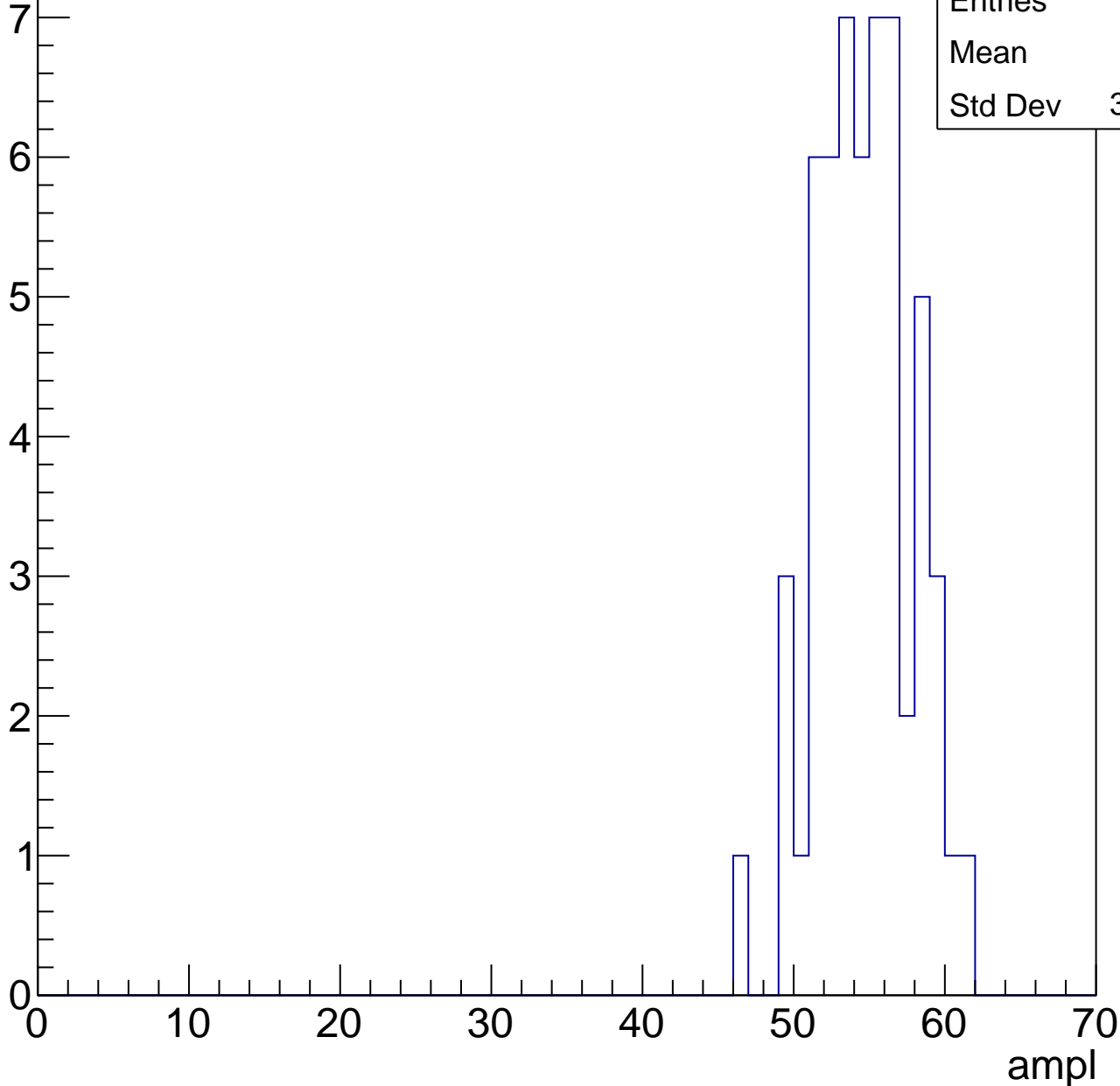


B1L103S, U24-ch38, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

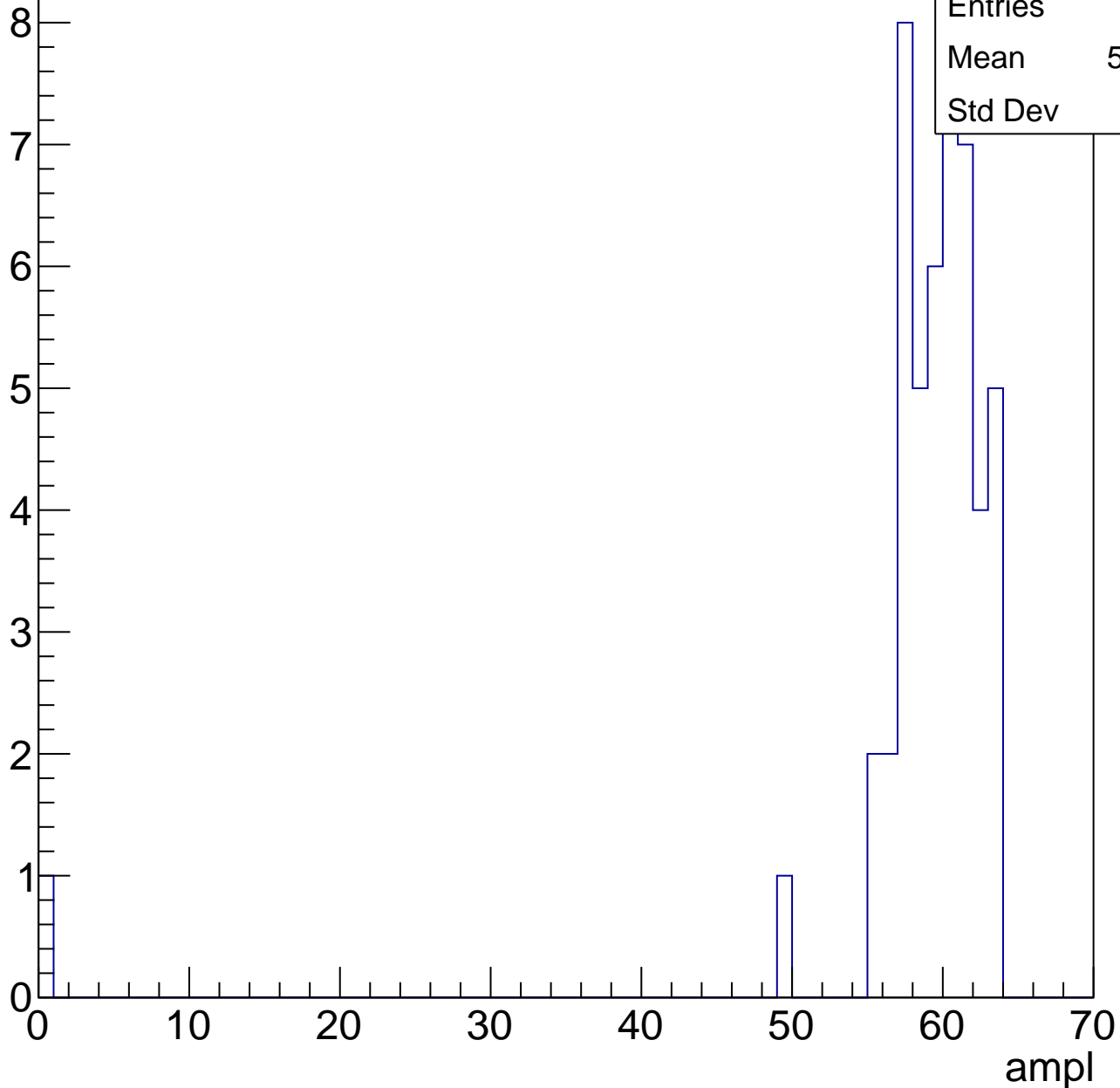
Entries	56
Mean	54.2
Std Dev	3.079



B1L103S, U24-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

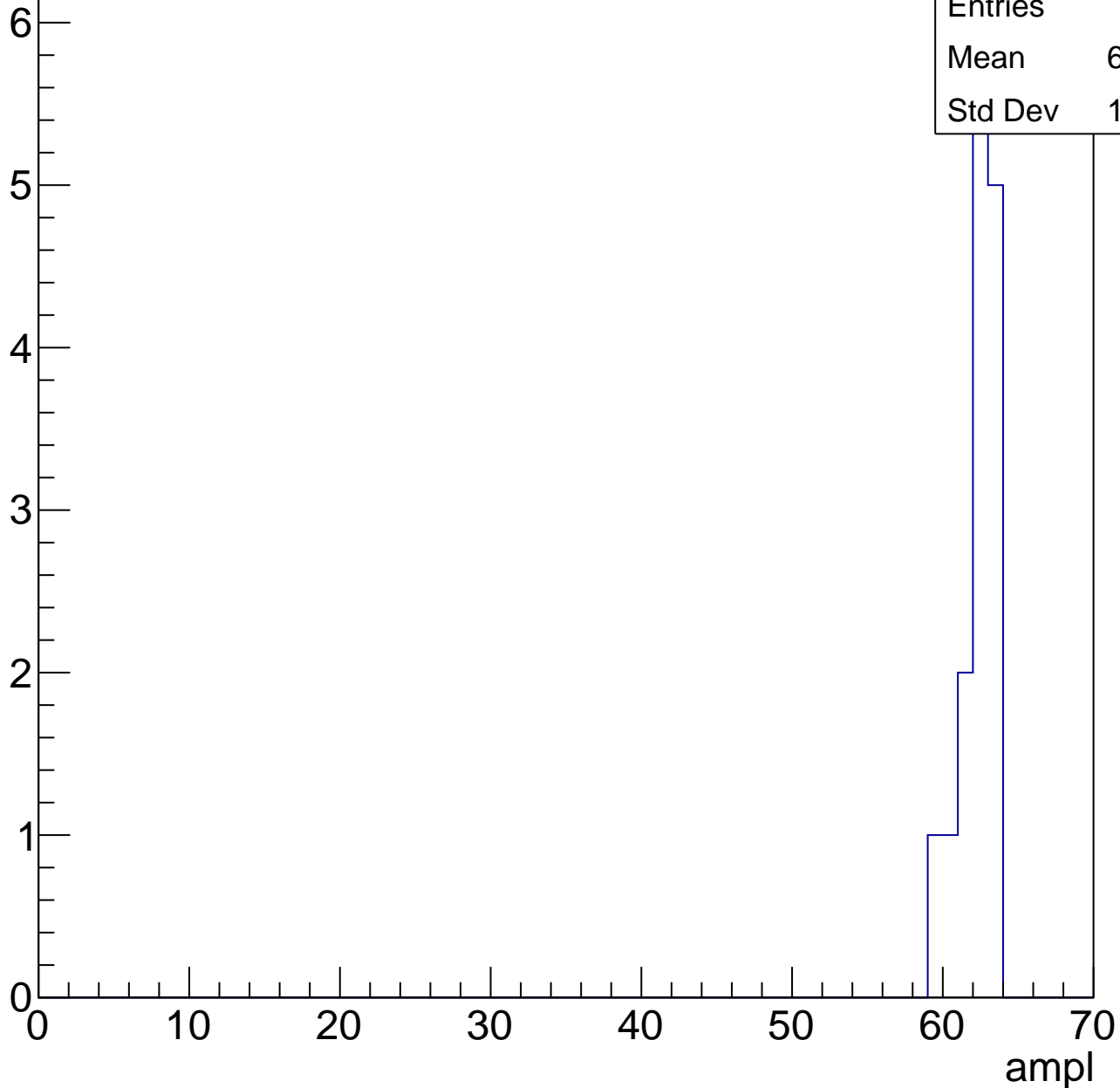
Entry



B1L103S, U24-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch39, adc0

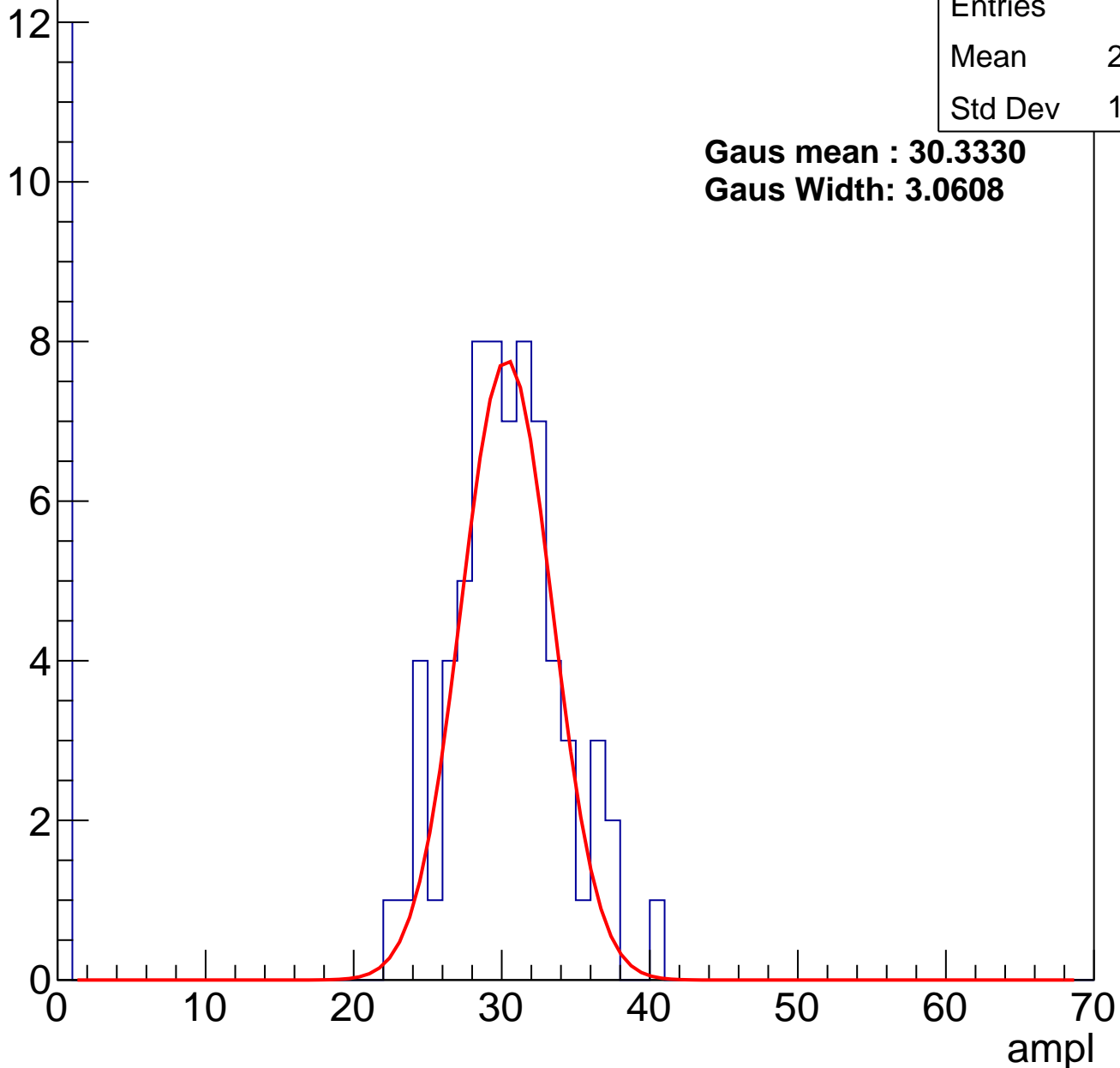
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	25.43
Std Dev	11.18

Gaus mean : 30.3330

Gaus Width: 3.0608

Entry



B1L103S, U24-ch39, adc1

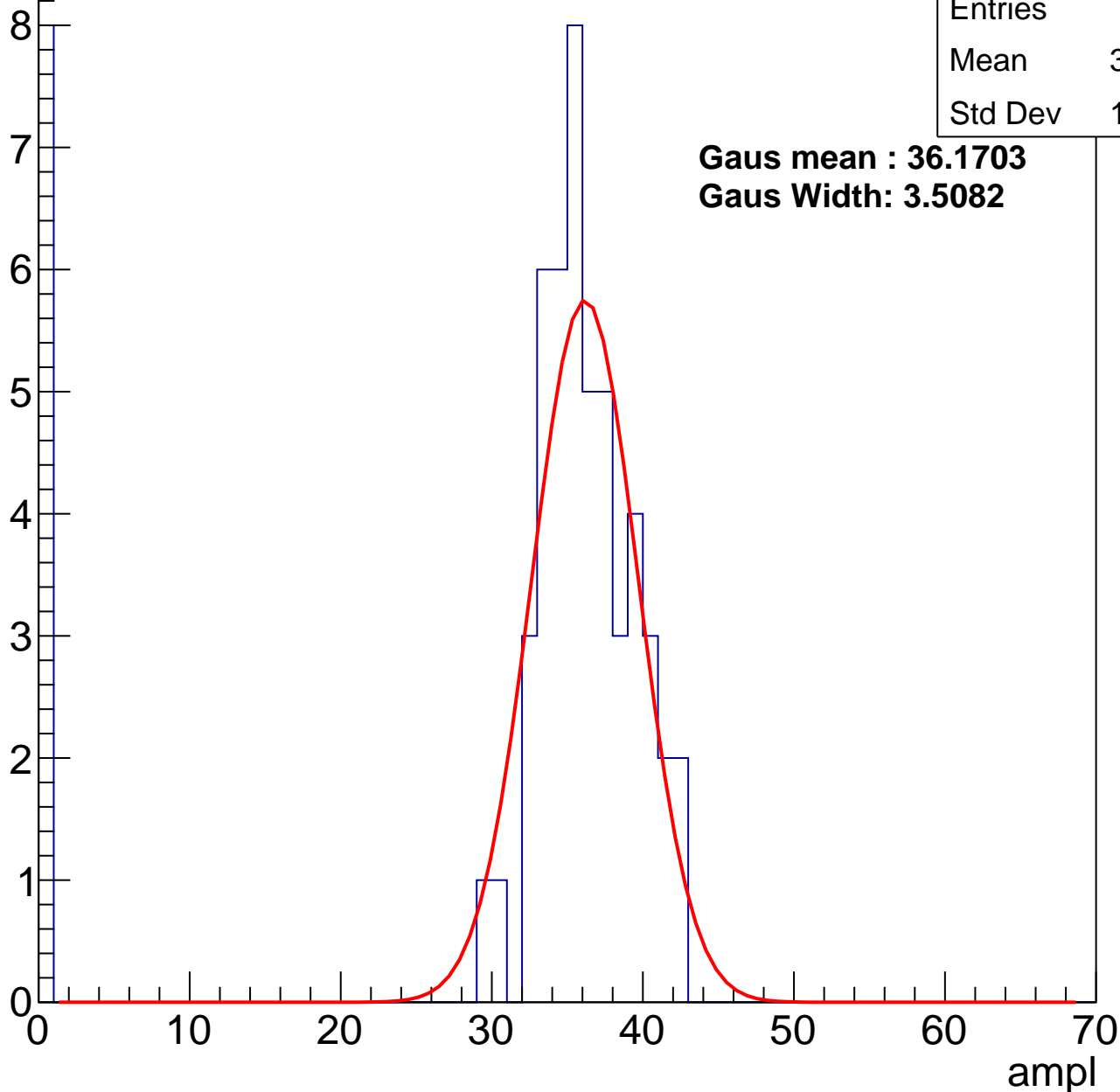
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	30.84
Std Dev	12.77

Gaus mean : 36.1703

Gaus Width: 3.5082



B1L103S, U24-ch39, adc2

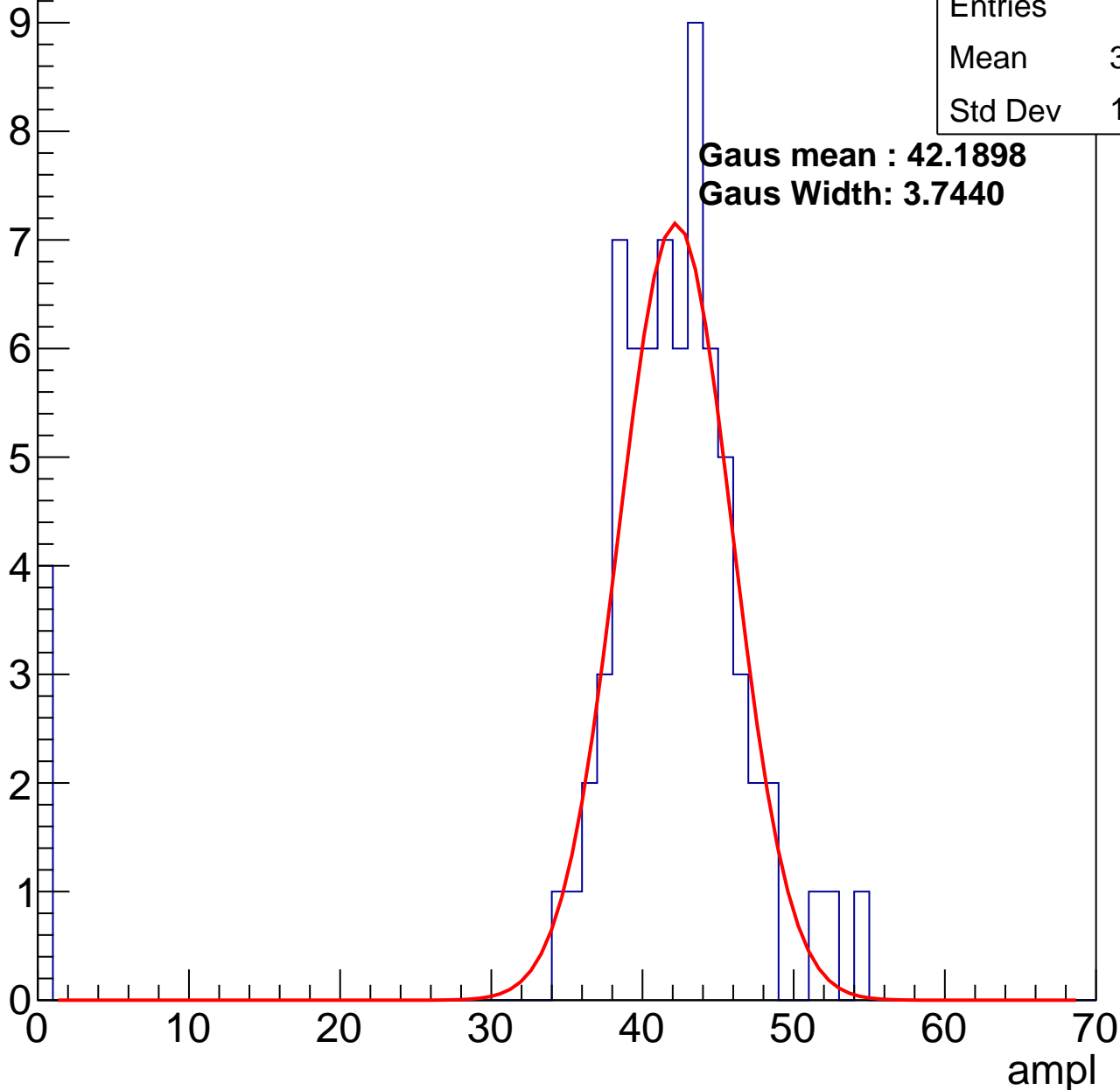
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	39.62
Std Dev	10.25

Gaus mean : 42.1898

Gaus Width: 3.7440

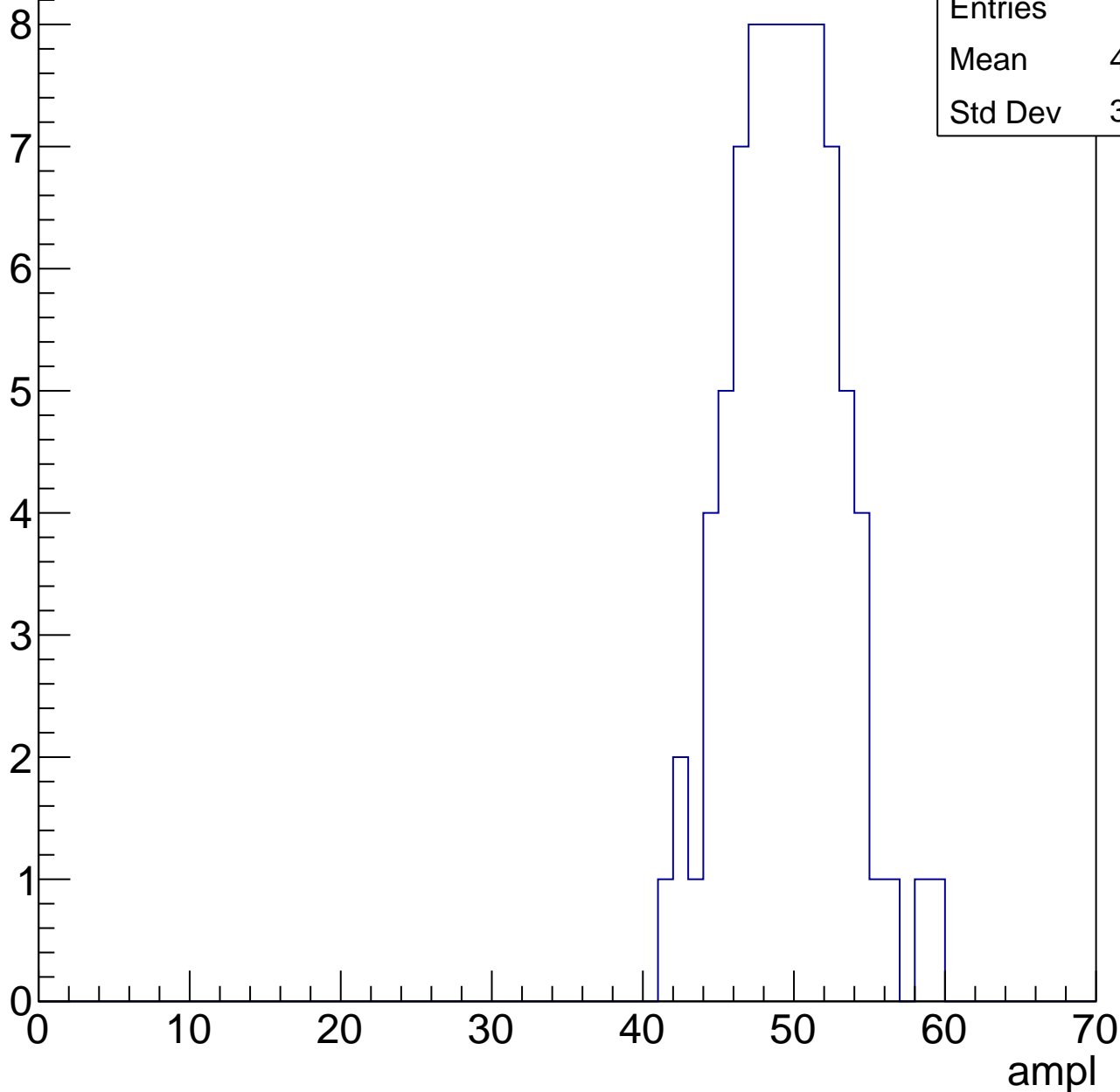


B1L103S, U24-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	49.05
Std Dev	3.588

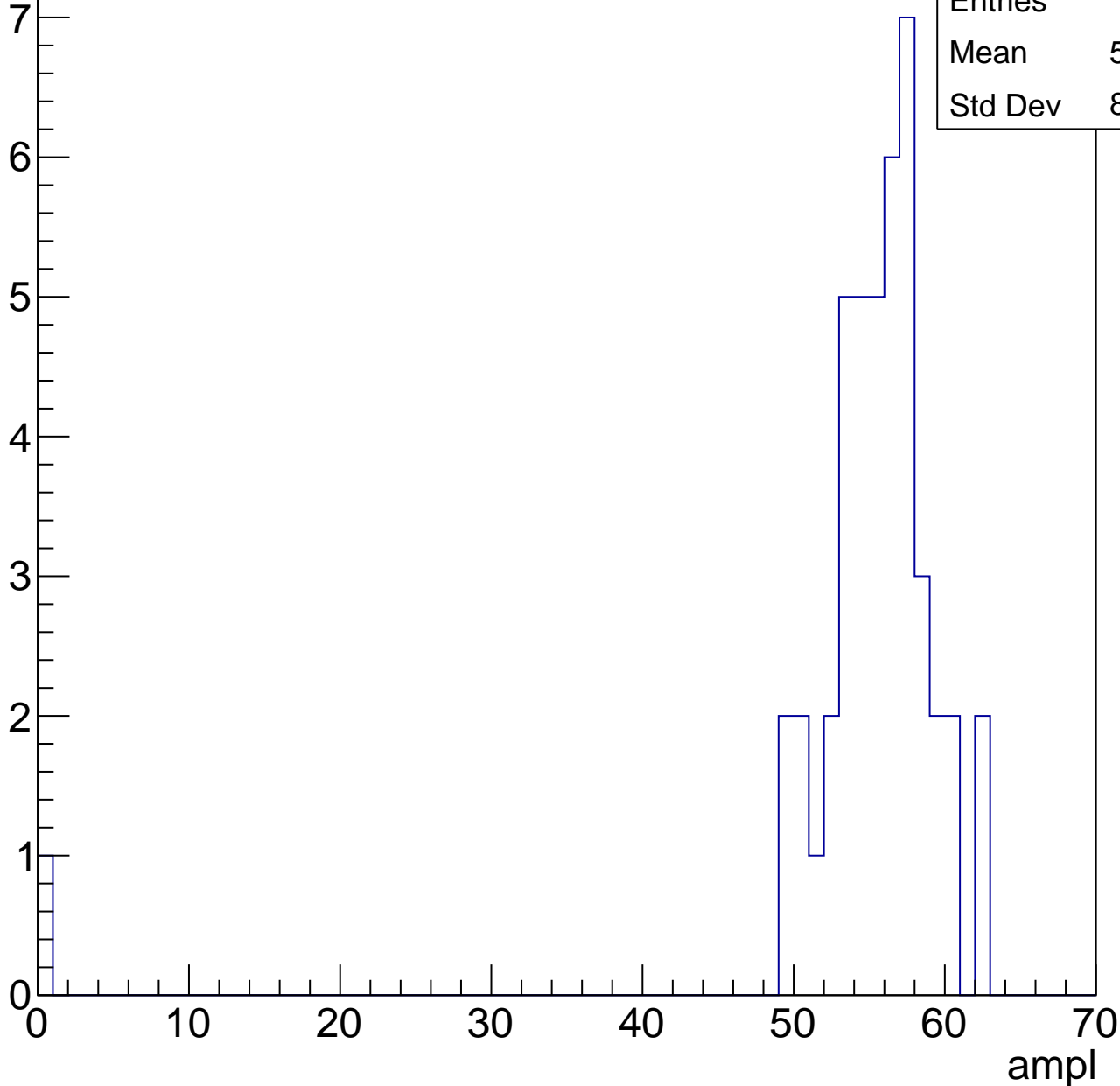


B1L103S, U24-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	54.09
Std Dev	8.705

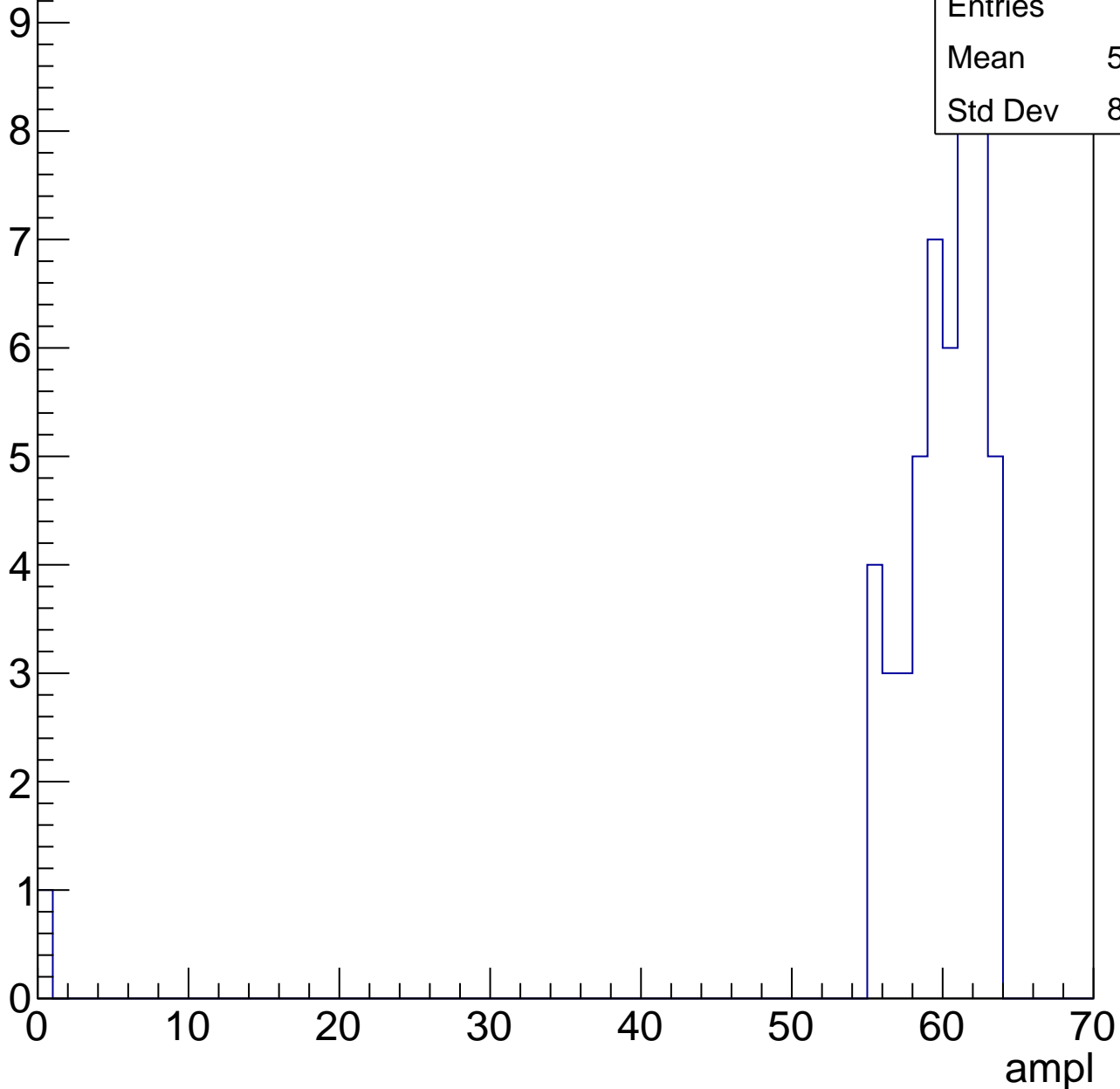


B1L103S, U24-ch39, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.47
Std Dev	8.596

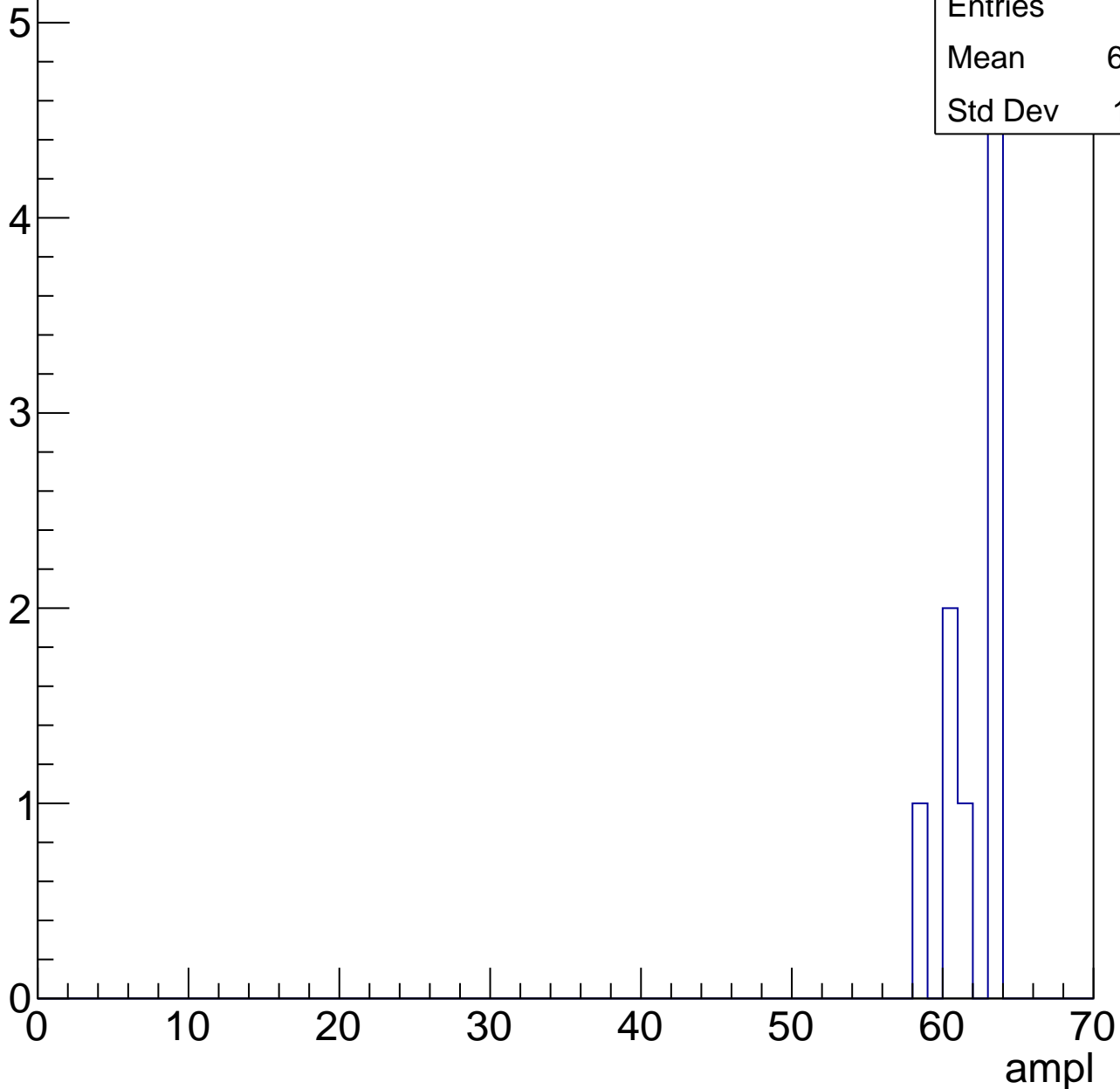


B1L103S, U24-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	61.56
Std Dev	1.771



B1L103S, U24-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch40, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	26.49
Std Dev	8.16

Gaus mean : 29.2742

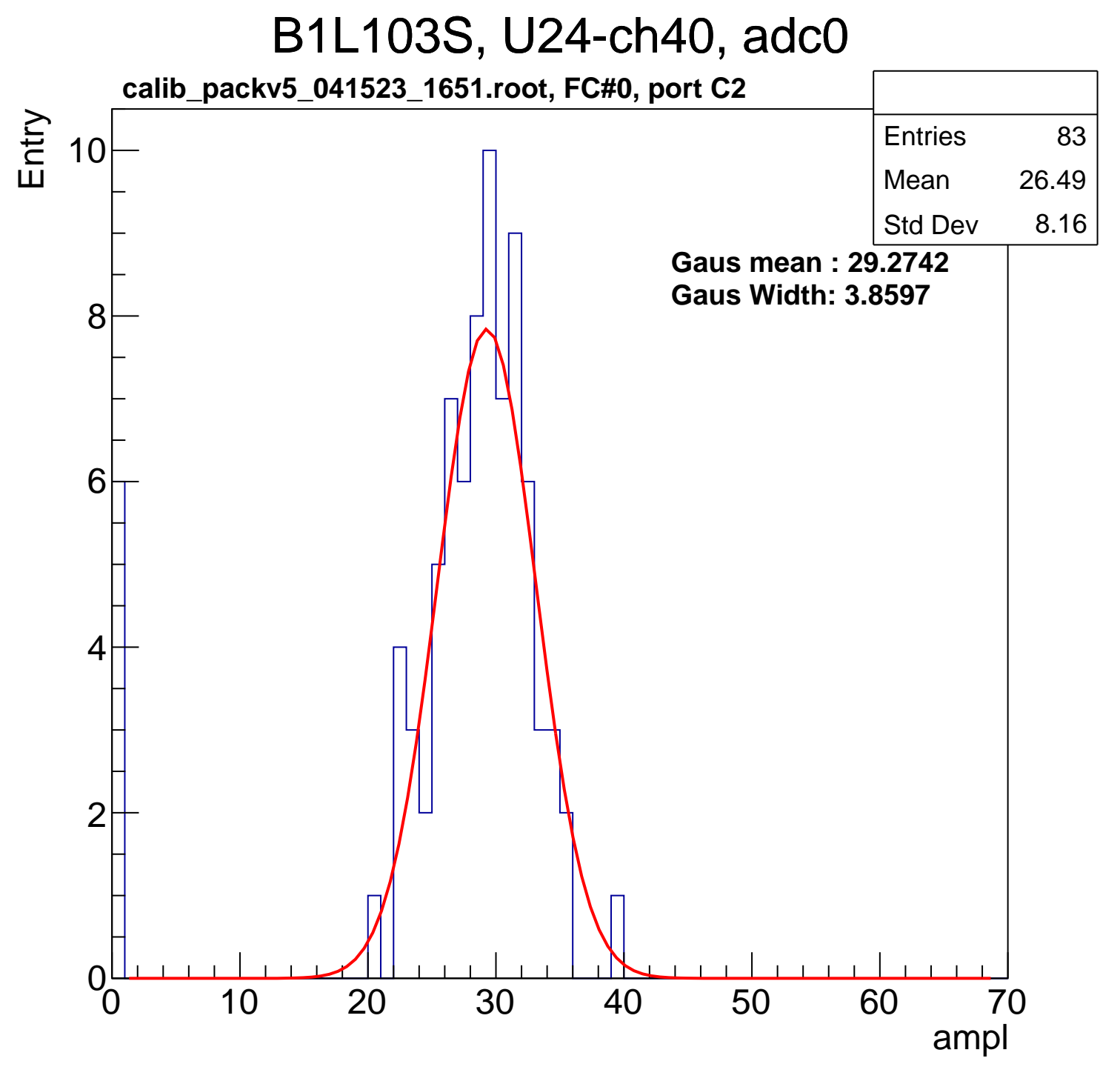
Gaus Width: 3.8597

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch40, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	31.62
Std Dev	12.41

Gaus mean : 36.5325

Gaus Width: 3.2179

Entry

10

8

6

4

2

0

0

10

20

30

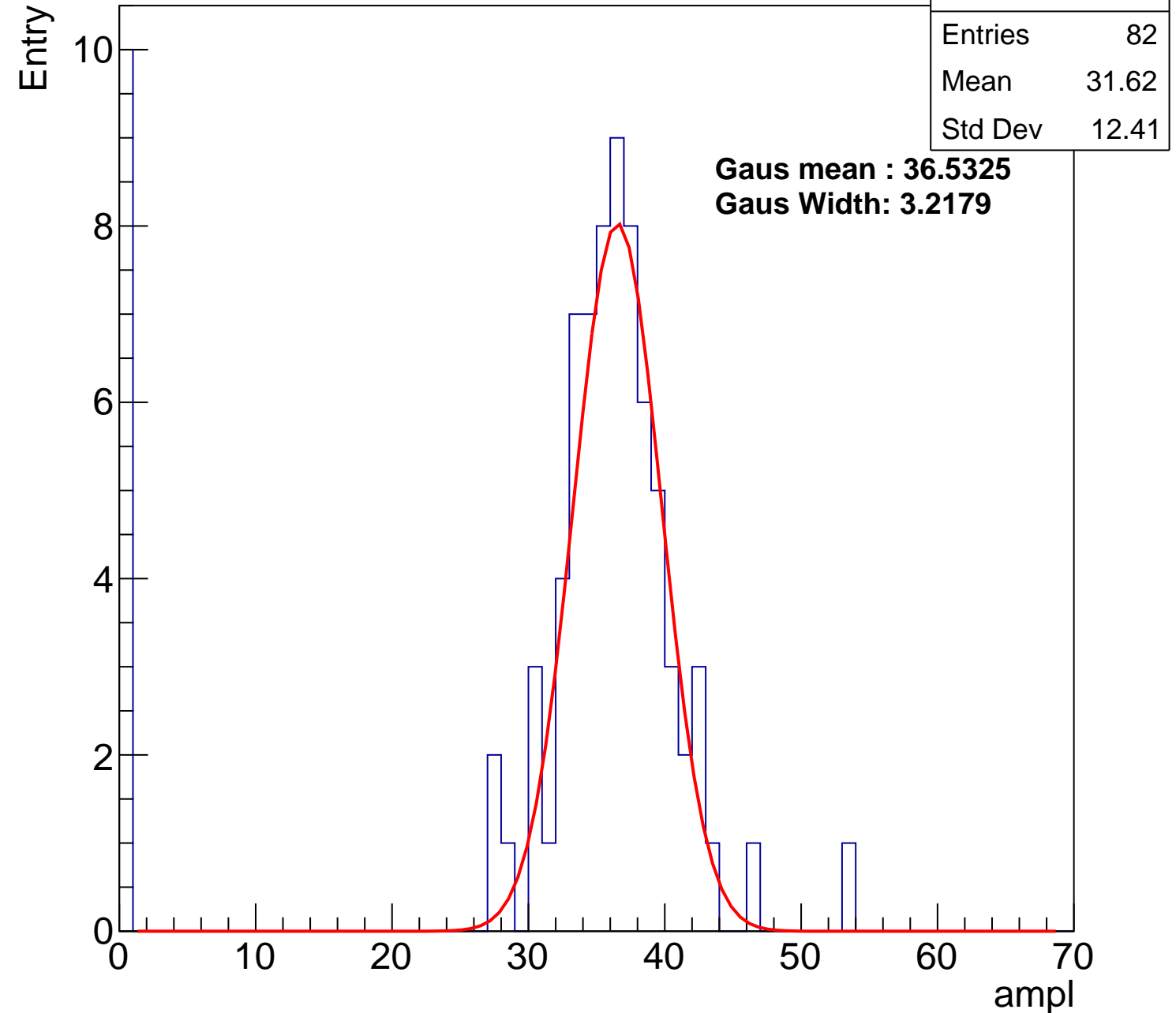
40

50

60

70

ampl



B1L103S, U24-ch40, adc2

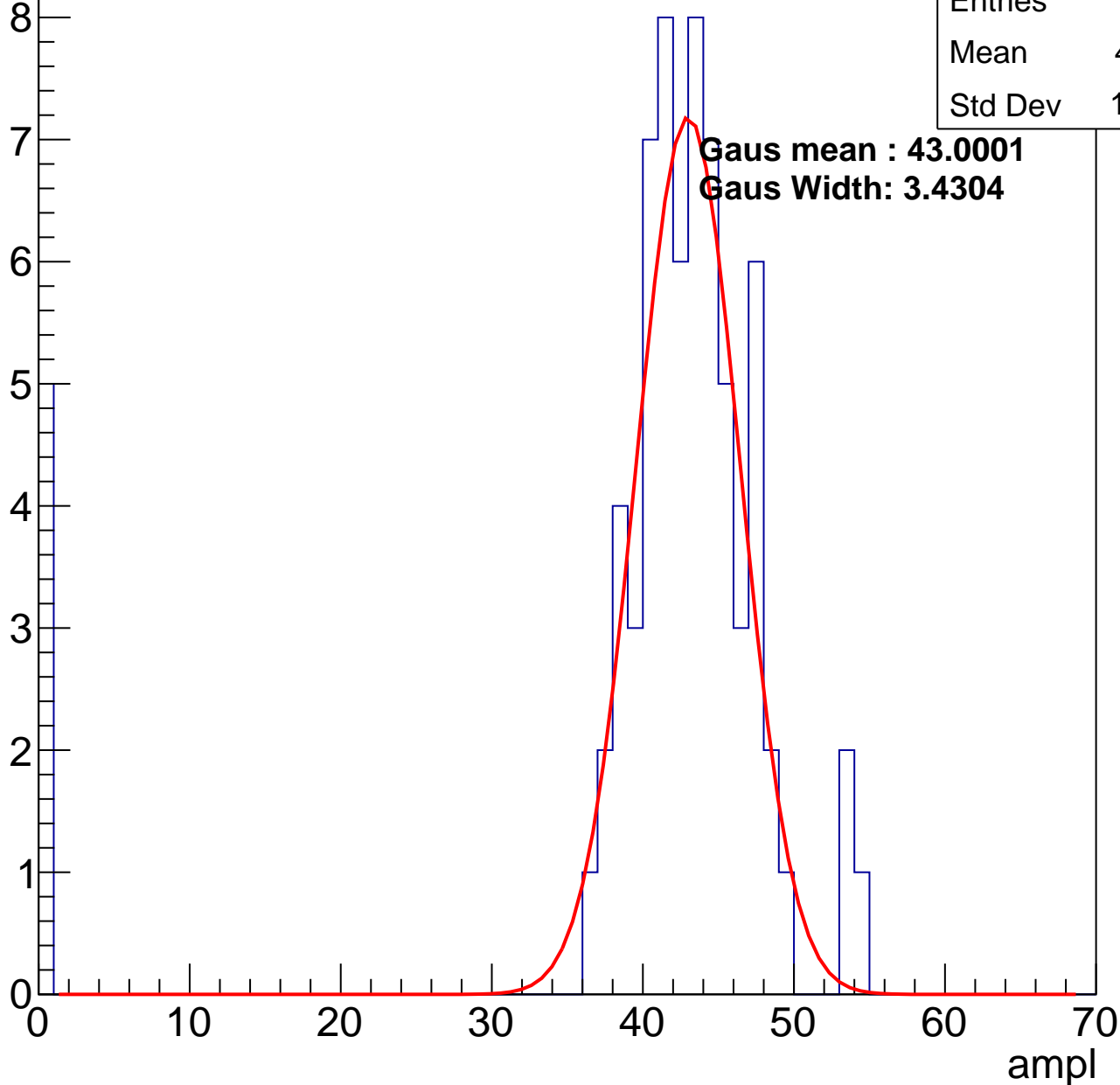
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	40.01
Std Dev	11.59

Gaus mean : 43.0001

Gaus Width: 3.4304

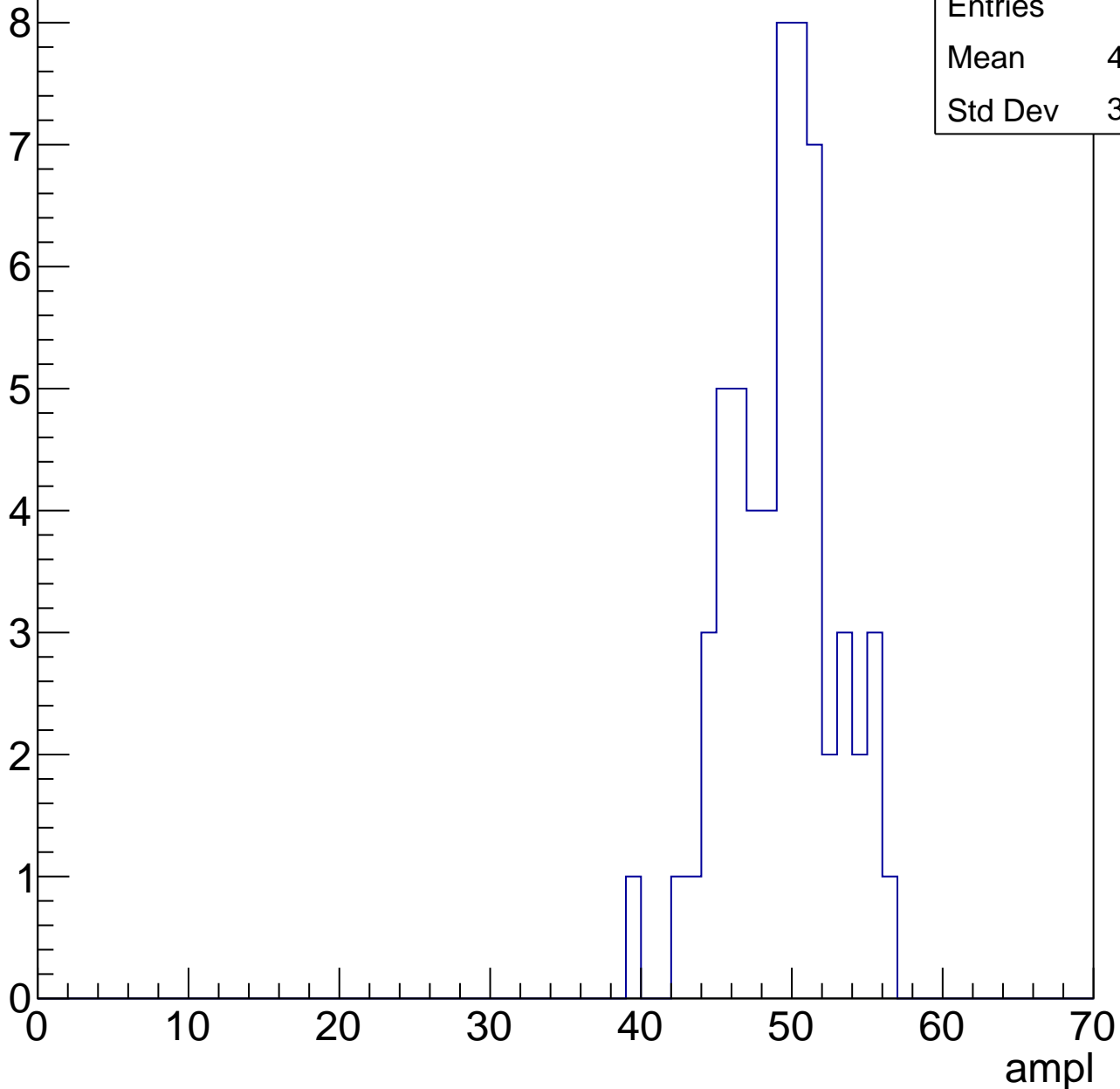


B1L103S, U24-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	48.83
Std Dev	3.504

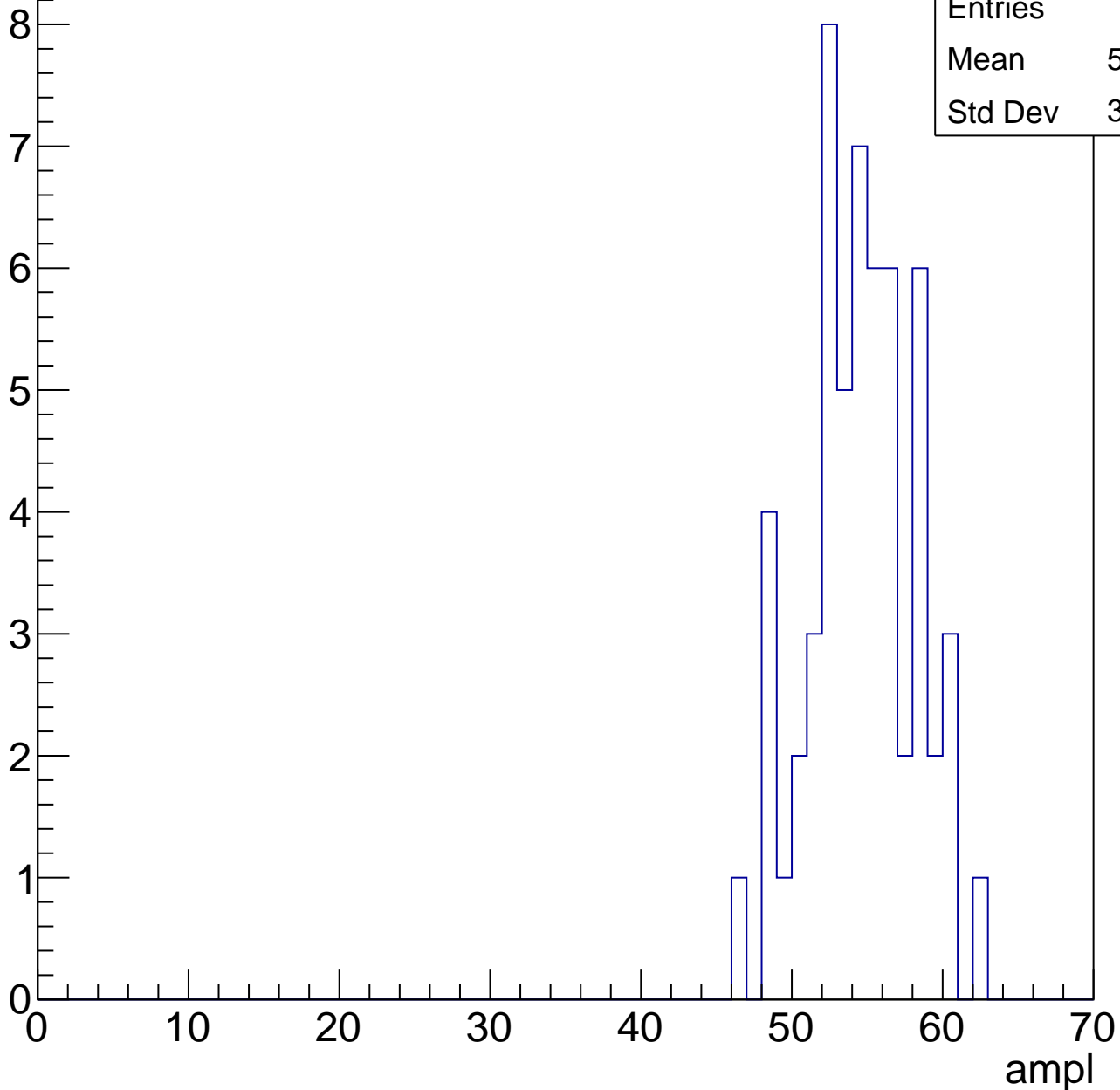


B1L103S, U24-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.16
Std Dev	3.488

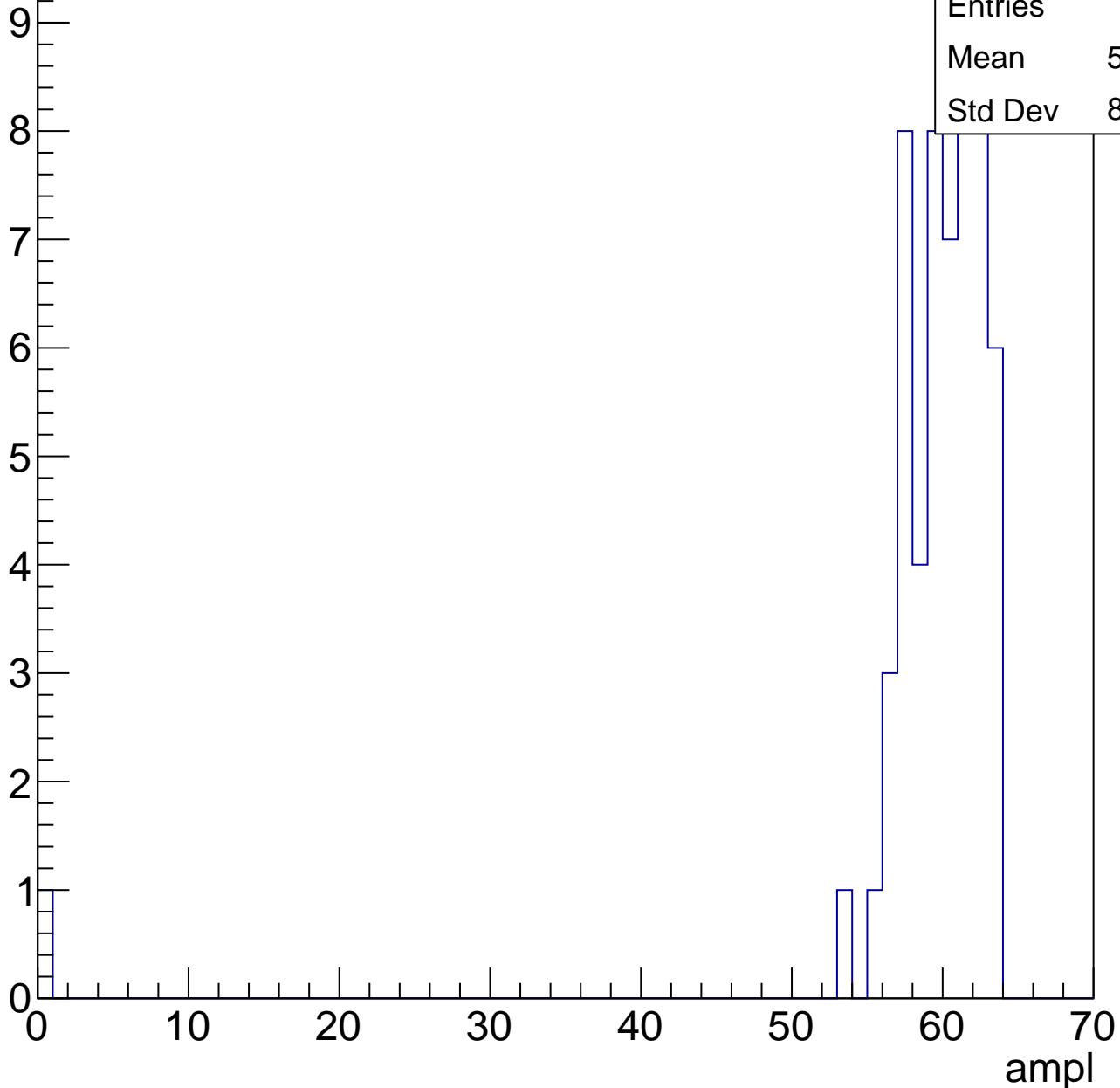


B1L103S, U24-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

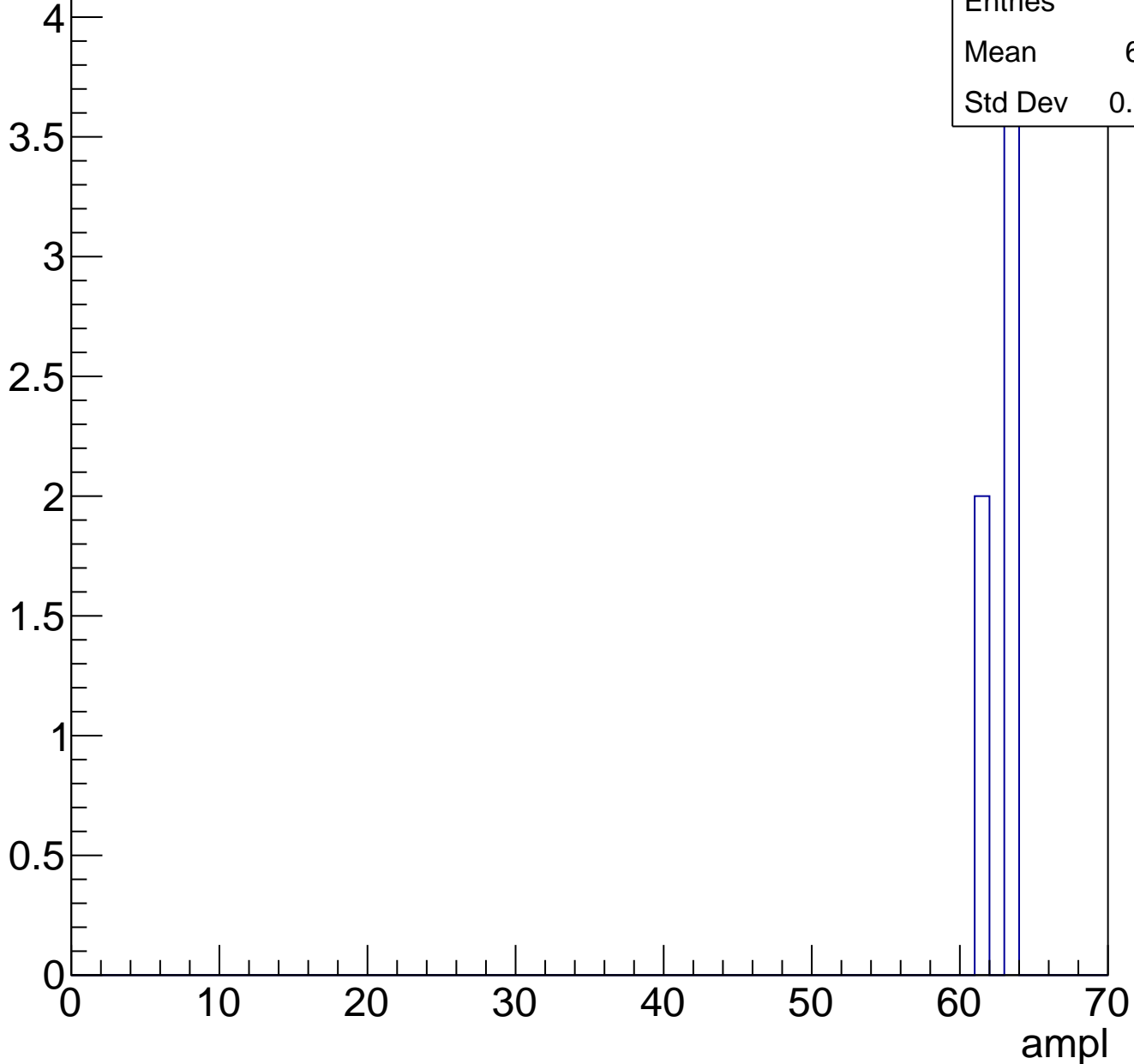
Entries	56
Mean	58.57
Std Dev	8.239



B1L103S, U24-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch41, adc0

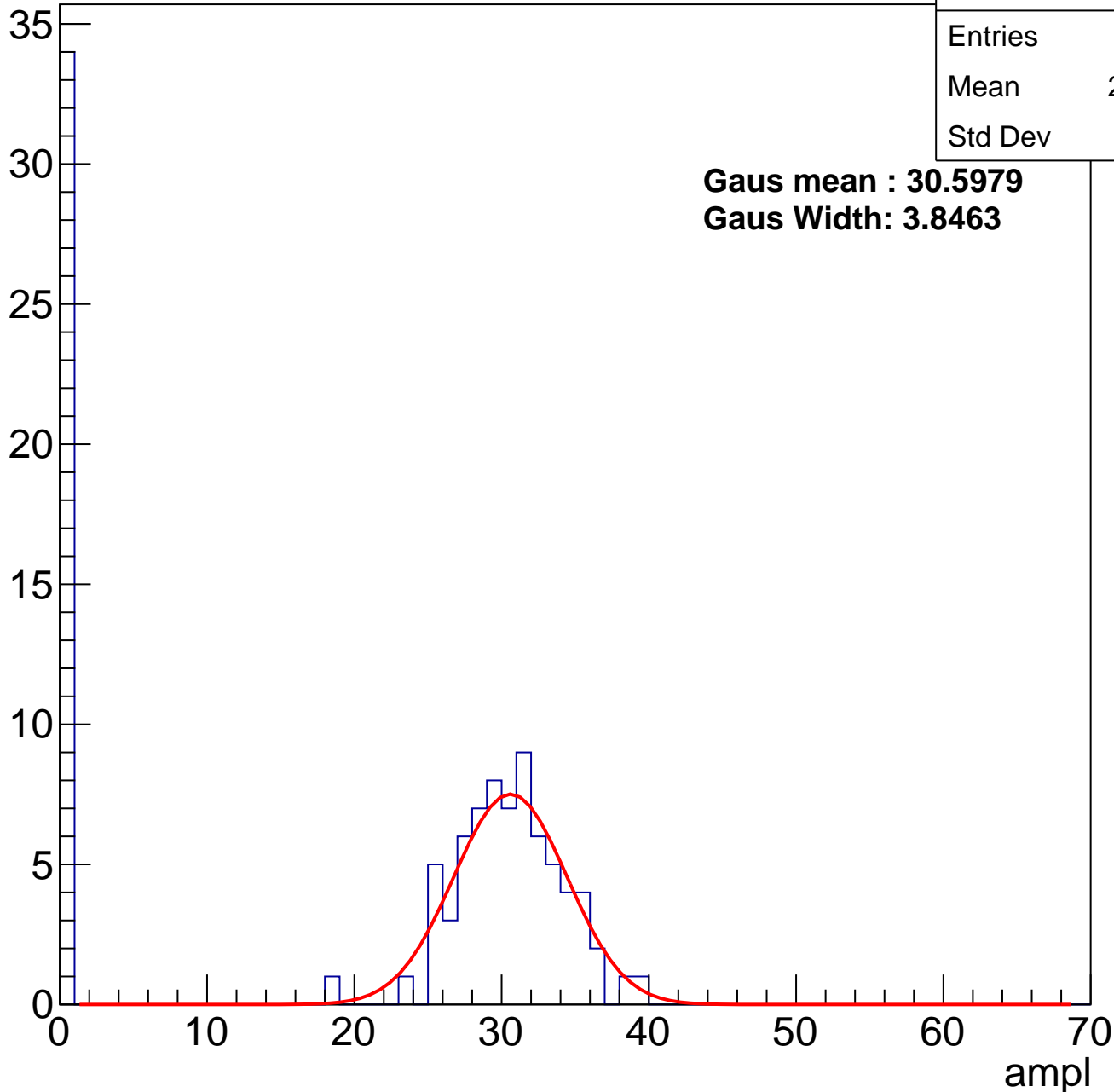
calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	20.24
Std Dev	14.41

Gaus mean : 30.5979

Gaus Width: 3.8463

Entry

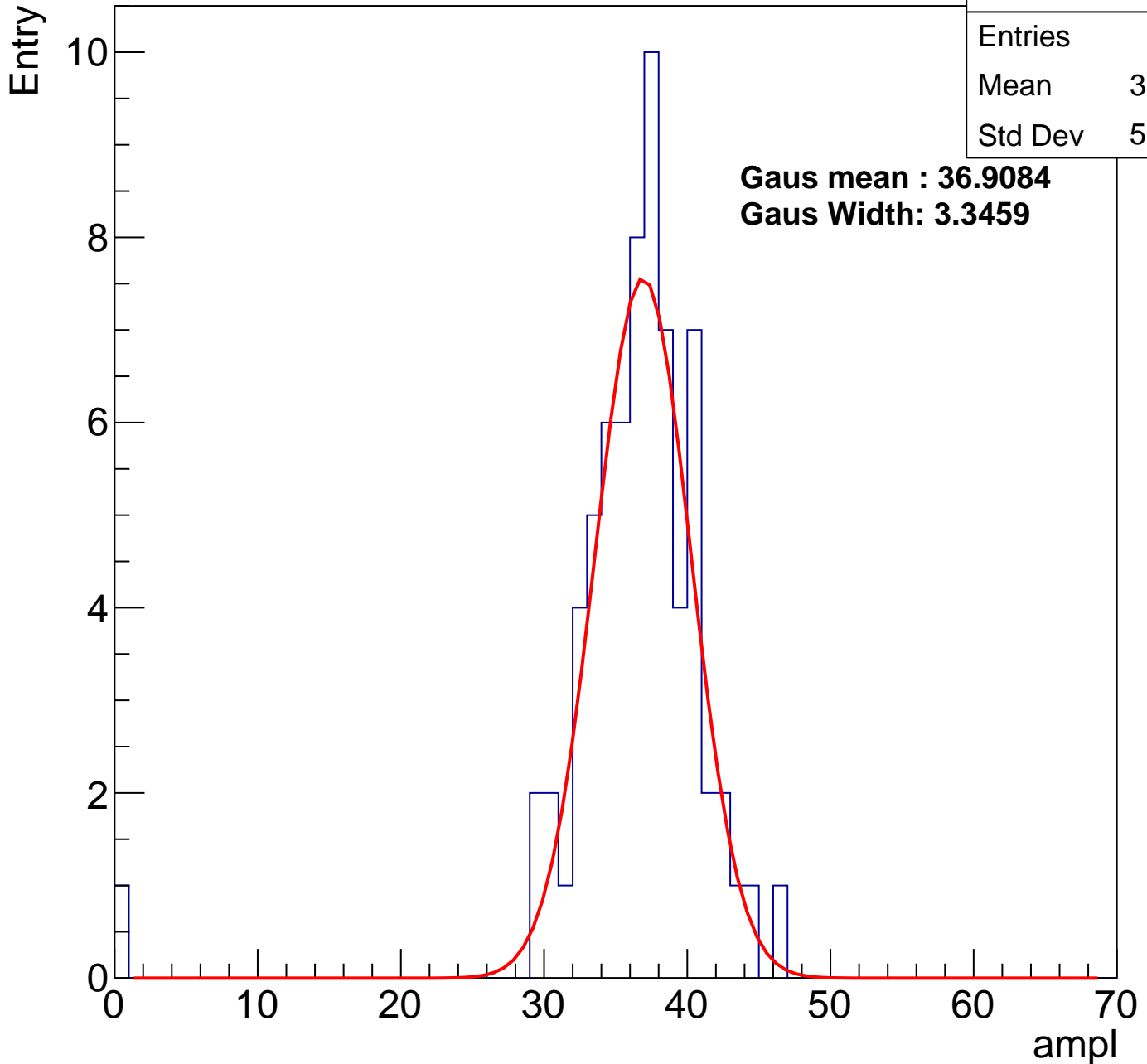


B1L103S, U24-ch41, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	35.93
Std Dev	5.543

Gaus mean : 36.9084
Gaus Width: 3.3459



B1L103S, U24-ch41, adc2

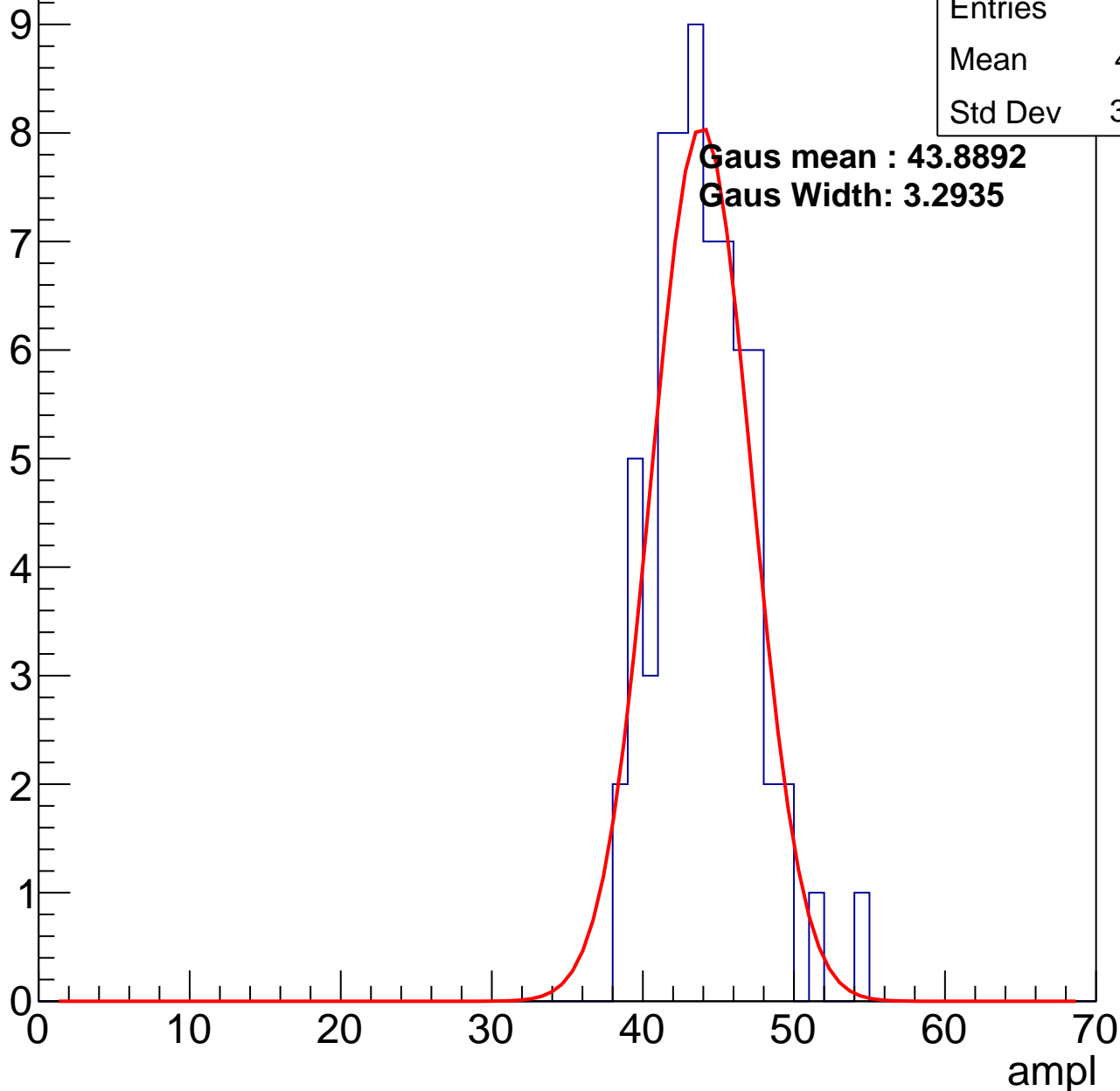
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	43.61
Std Dev	3.148

Gaus mean : 43.8892

Gaus Width: 3.2935



B1L103S, U24-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

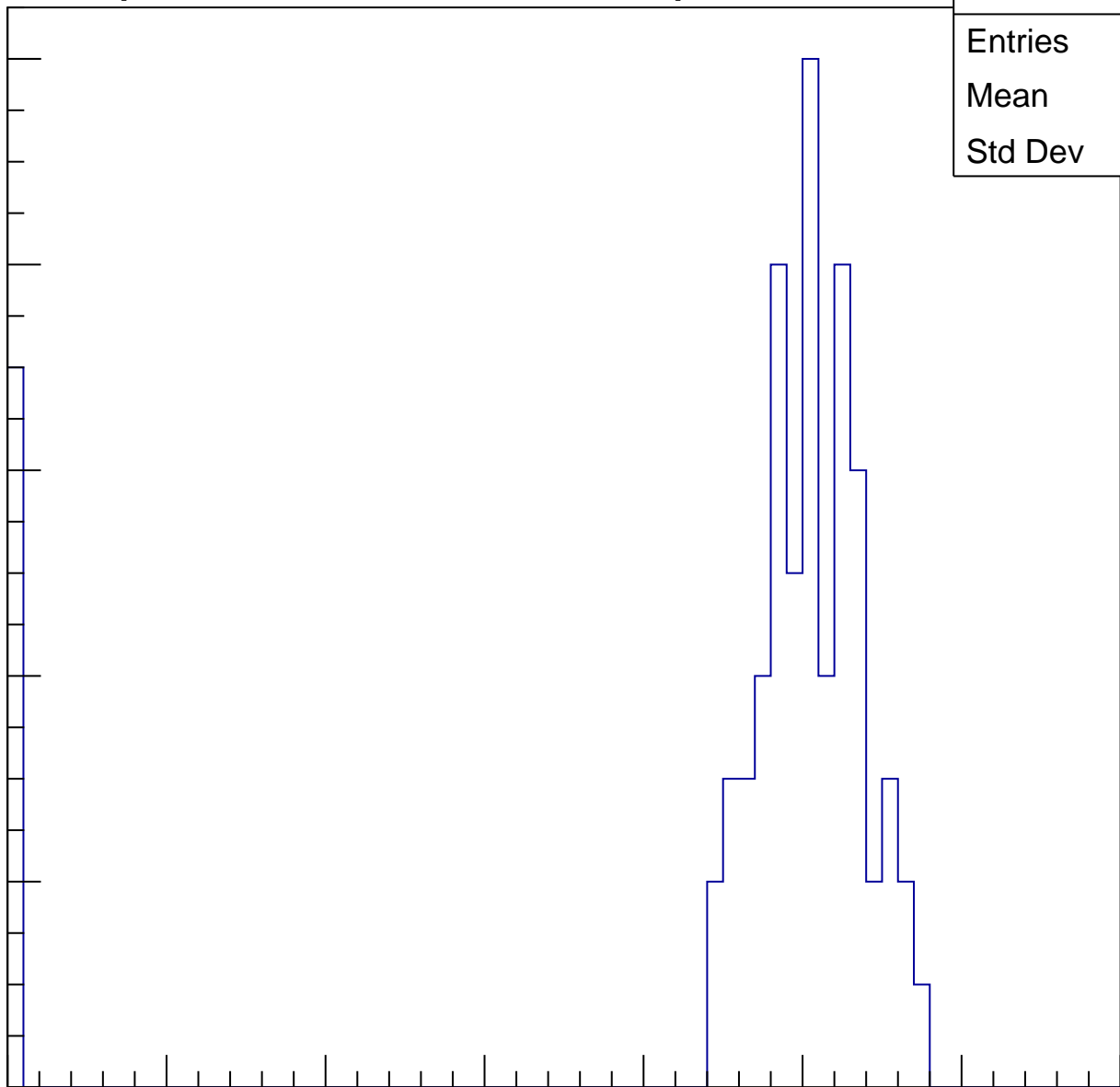
Entries	68
Mean	44.97
Std Dev	15.51

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

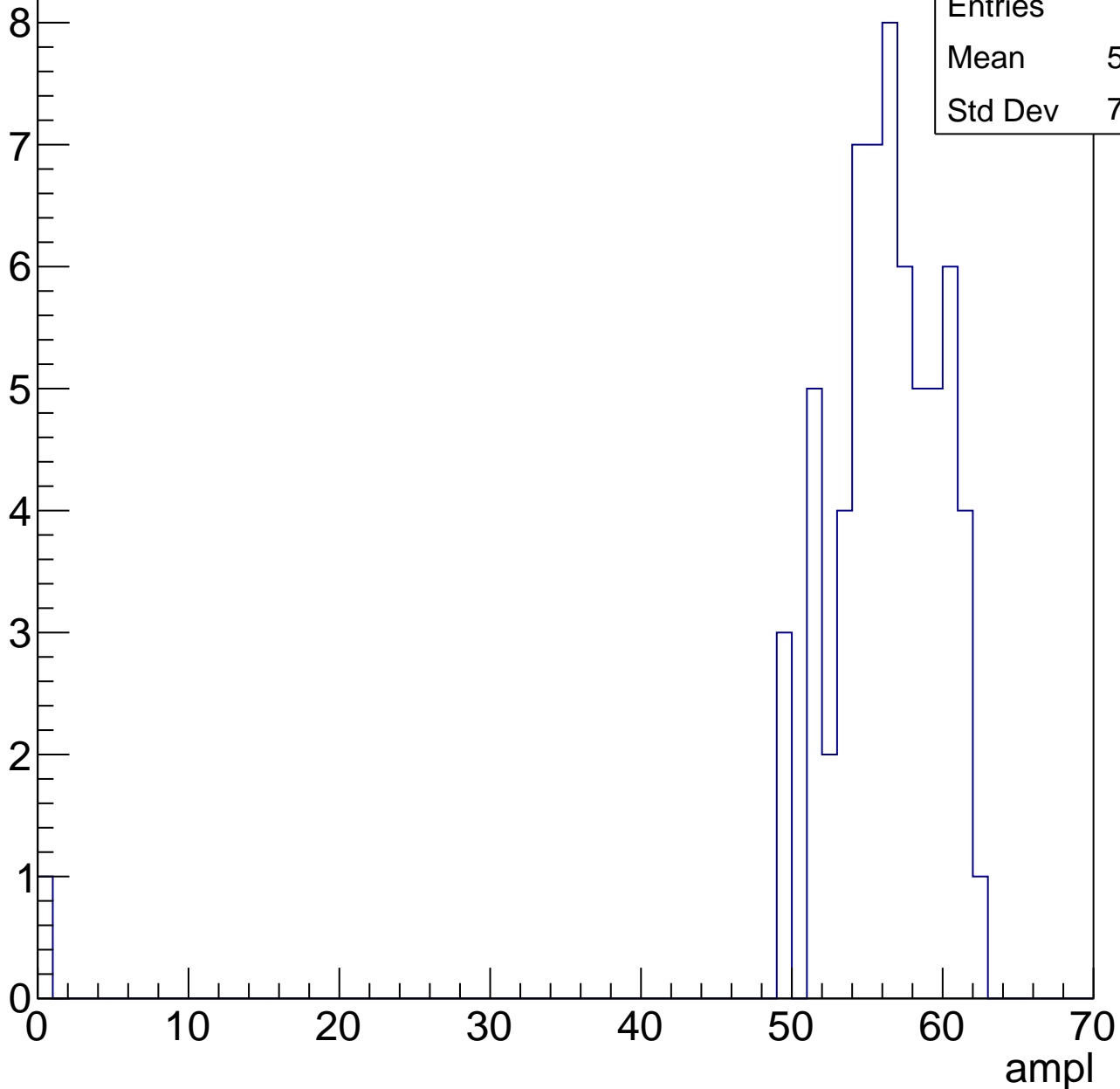


B1L103S, U24-ch41, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	55.03
Std Dev	7.659

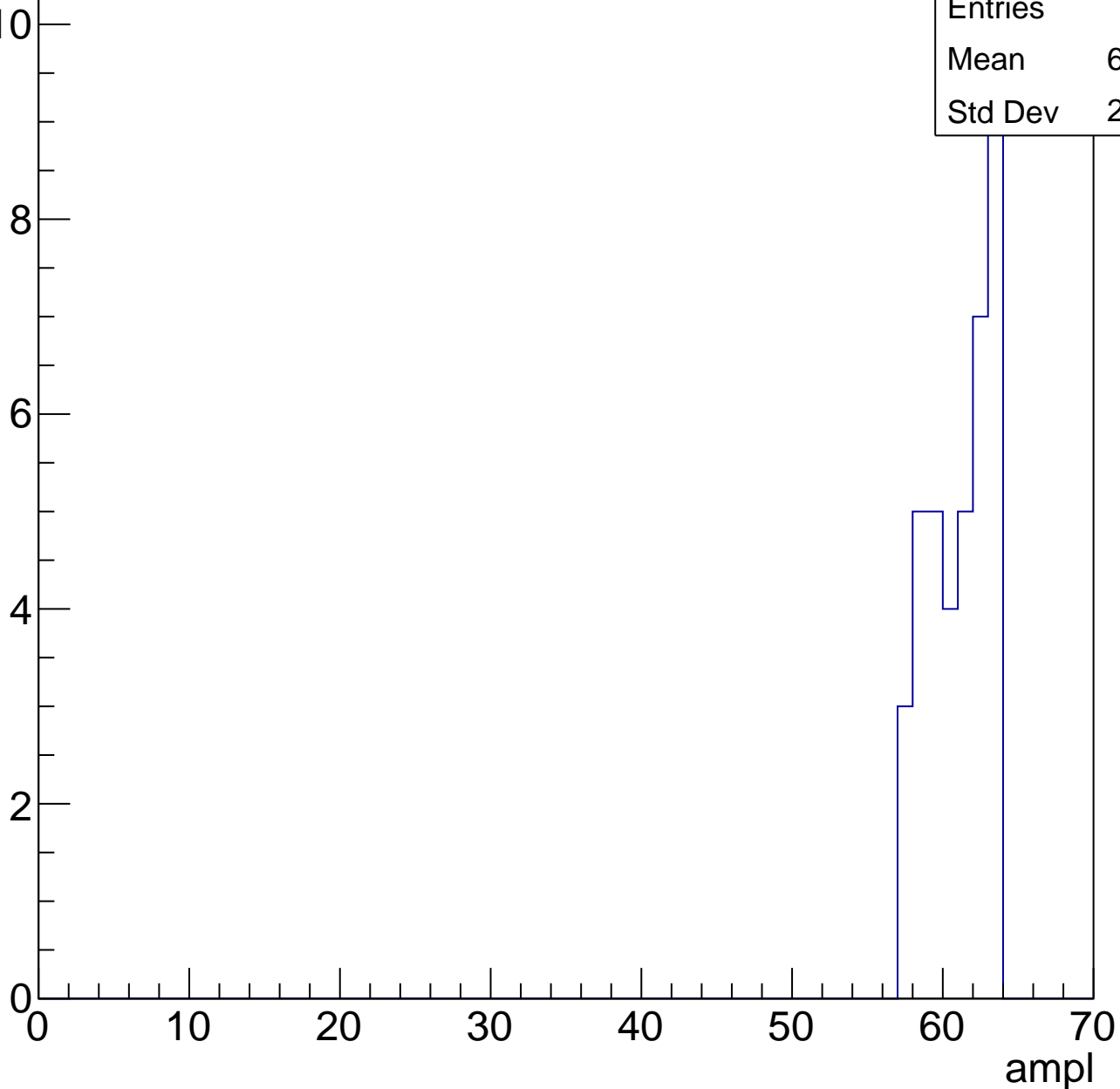


B1L103S, U24-ch41, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

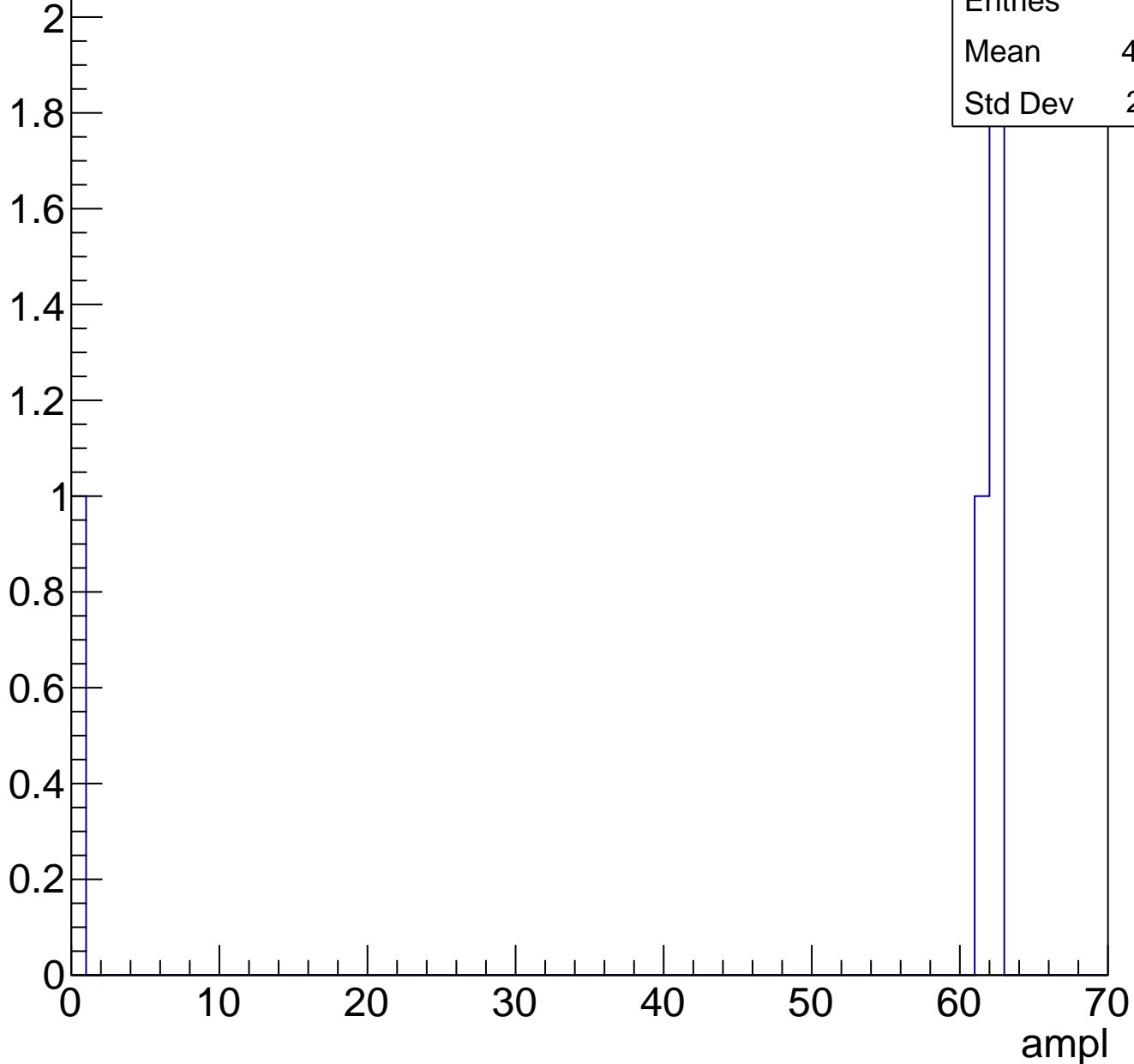
Entries	39
Mean	60.64
Std Dev	2.019



B1L103S, U24-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch42, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	23.56
Std Dev	10.23

Gaus mean : 27.8813

Gaus Width: 3.8665

Entry

12

10

8

6

4

2

0

0

10

20

30

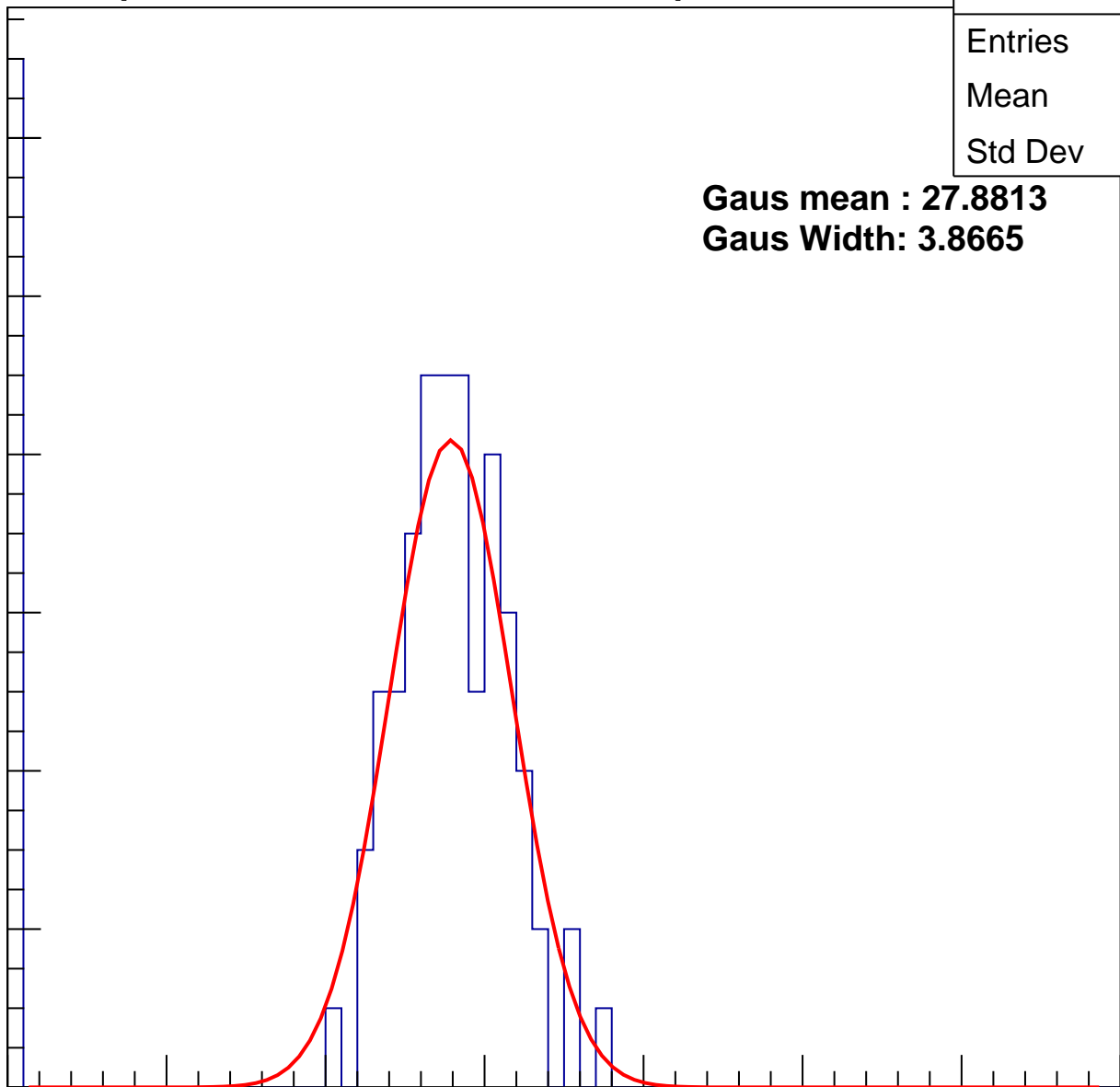
40

50

60

70

ampl

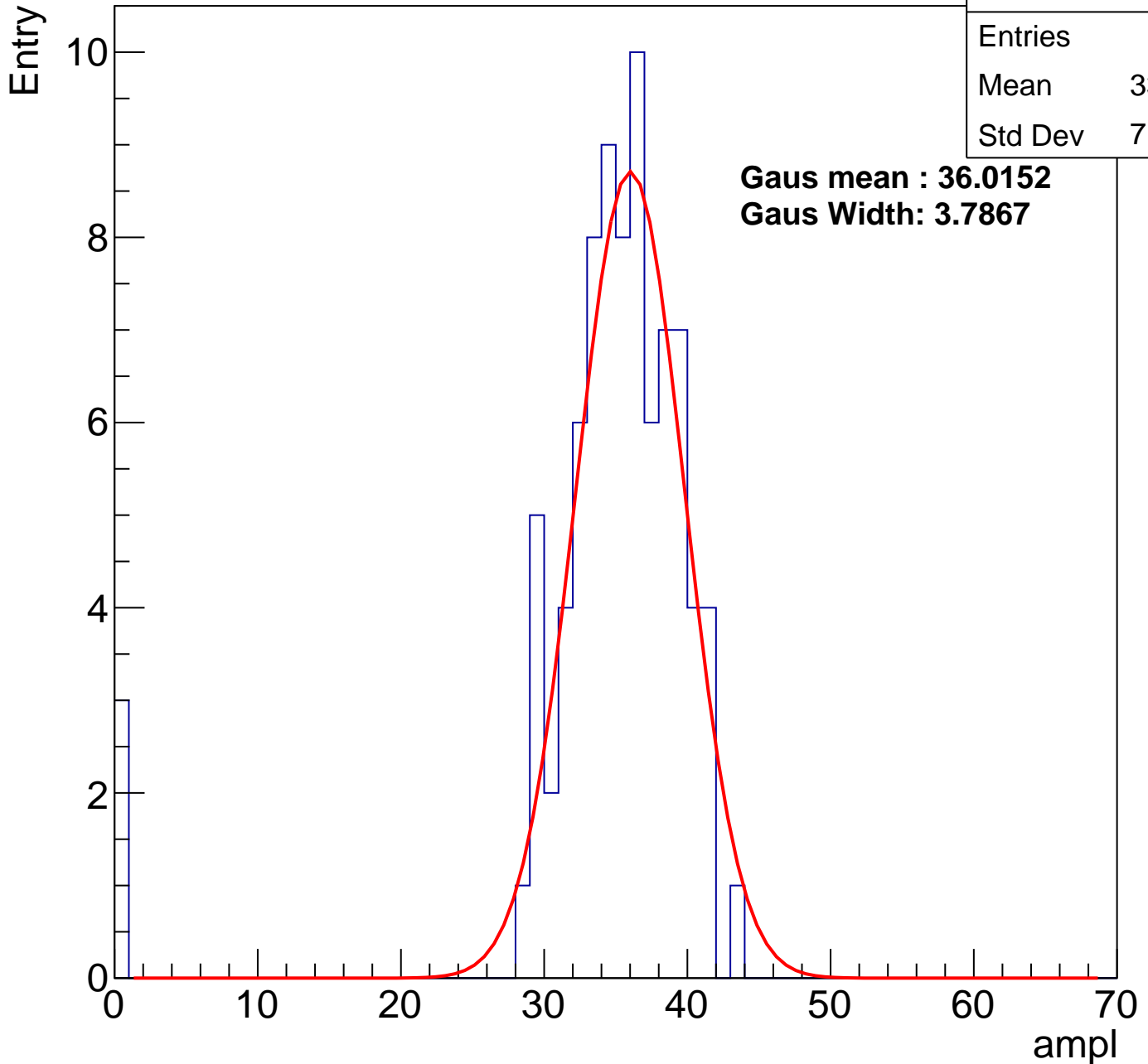


B1L103S, U24-ch42, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	33.96
Std Dev	7.307

Gaus mean : 36.0152
Gaus Width: 3.7867



B1L103S, U24-ch42, adc2

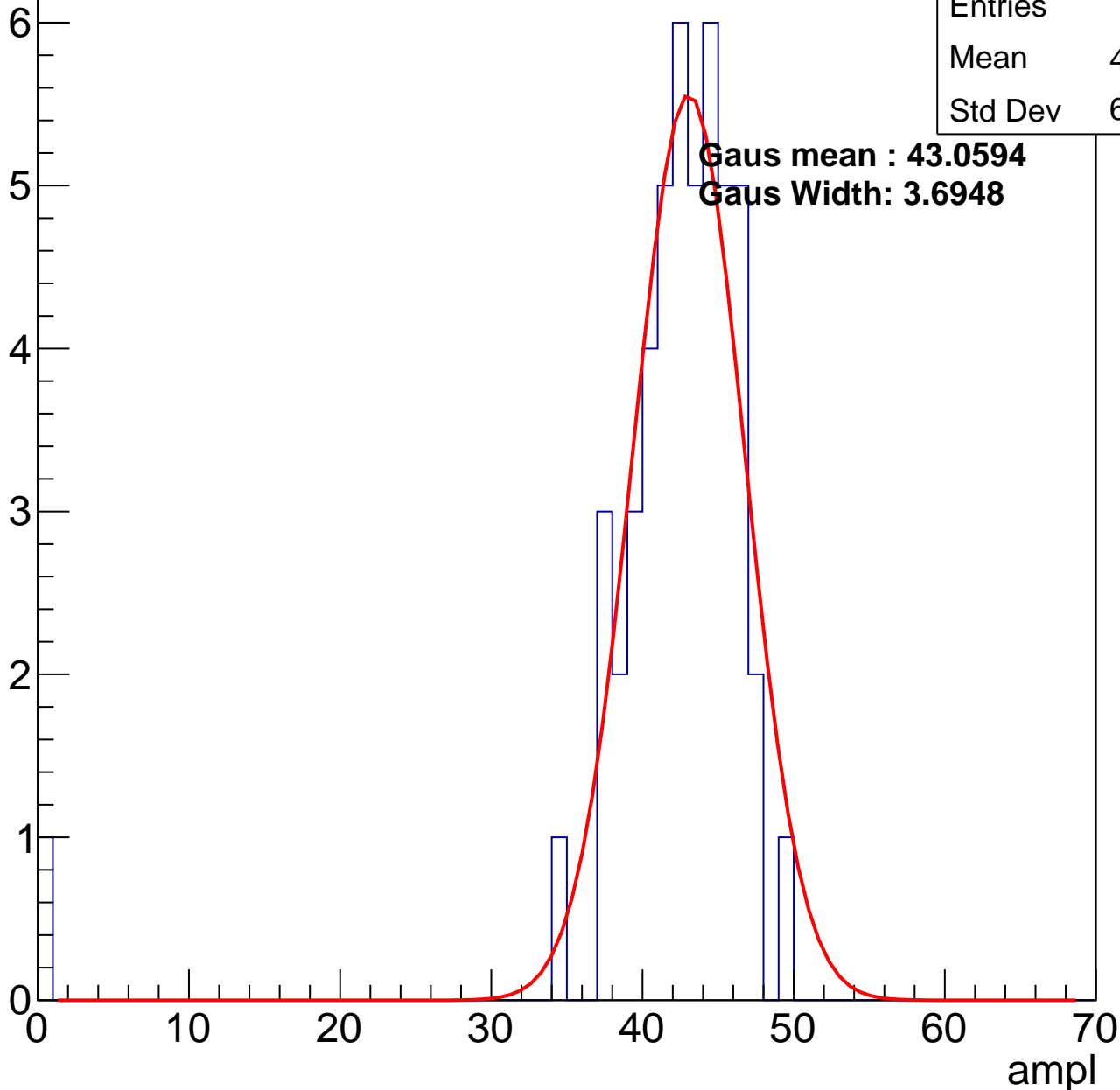
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	41.47
Std Dev	6.734

Gaus mean : 43.0594

Gaus Width: 3.6948

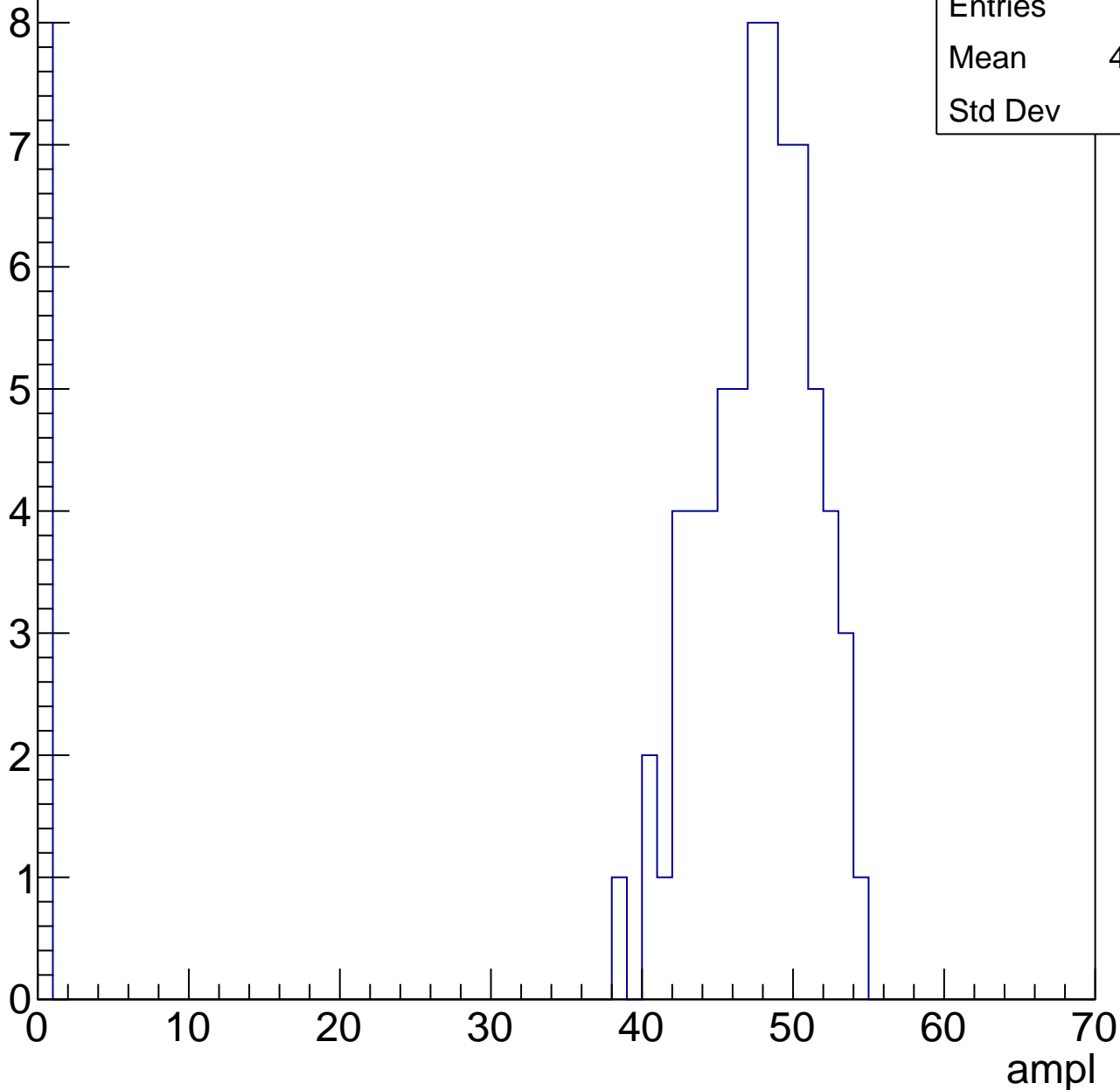


B1L103S, U24-ch42, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

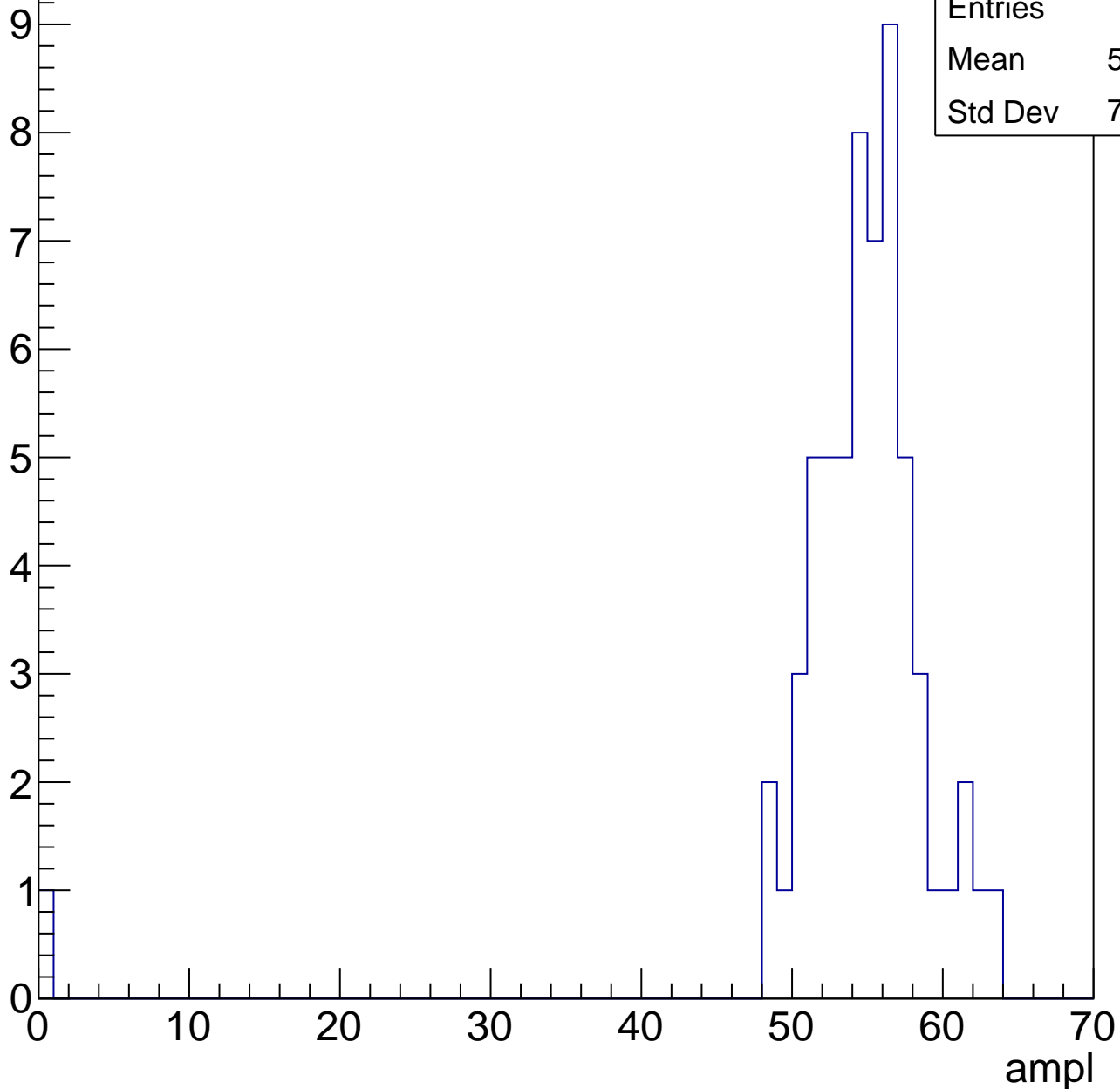
Entries	77
Mean	42.32
Std Dev	14.8



B1L103S, U24-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

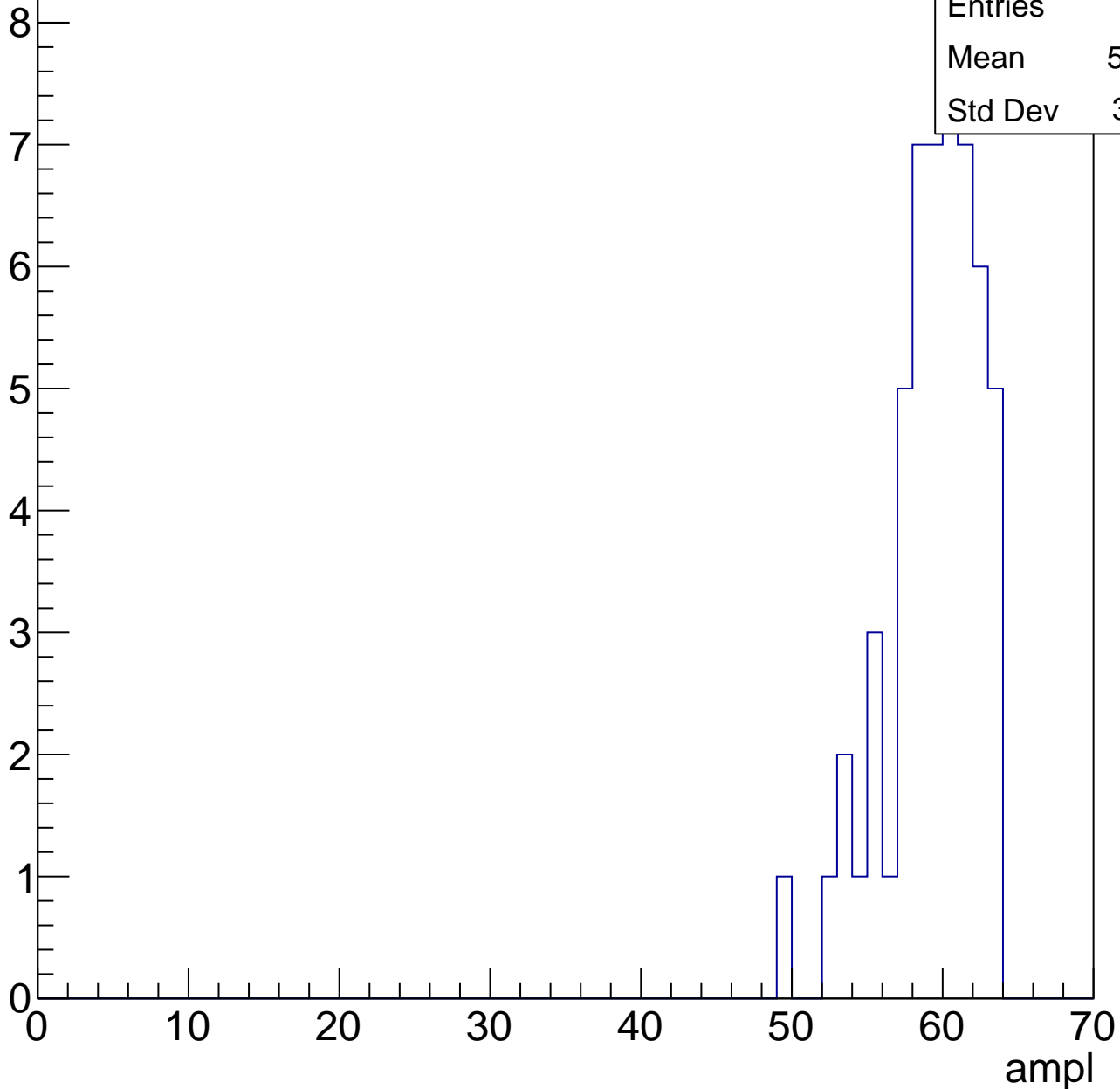


B1L103S, U24-ch42, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

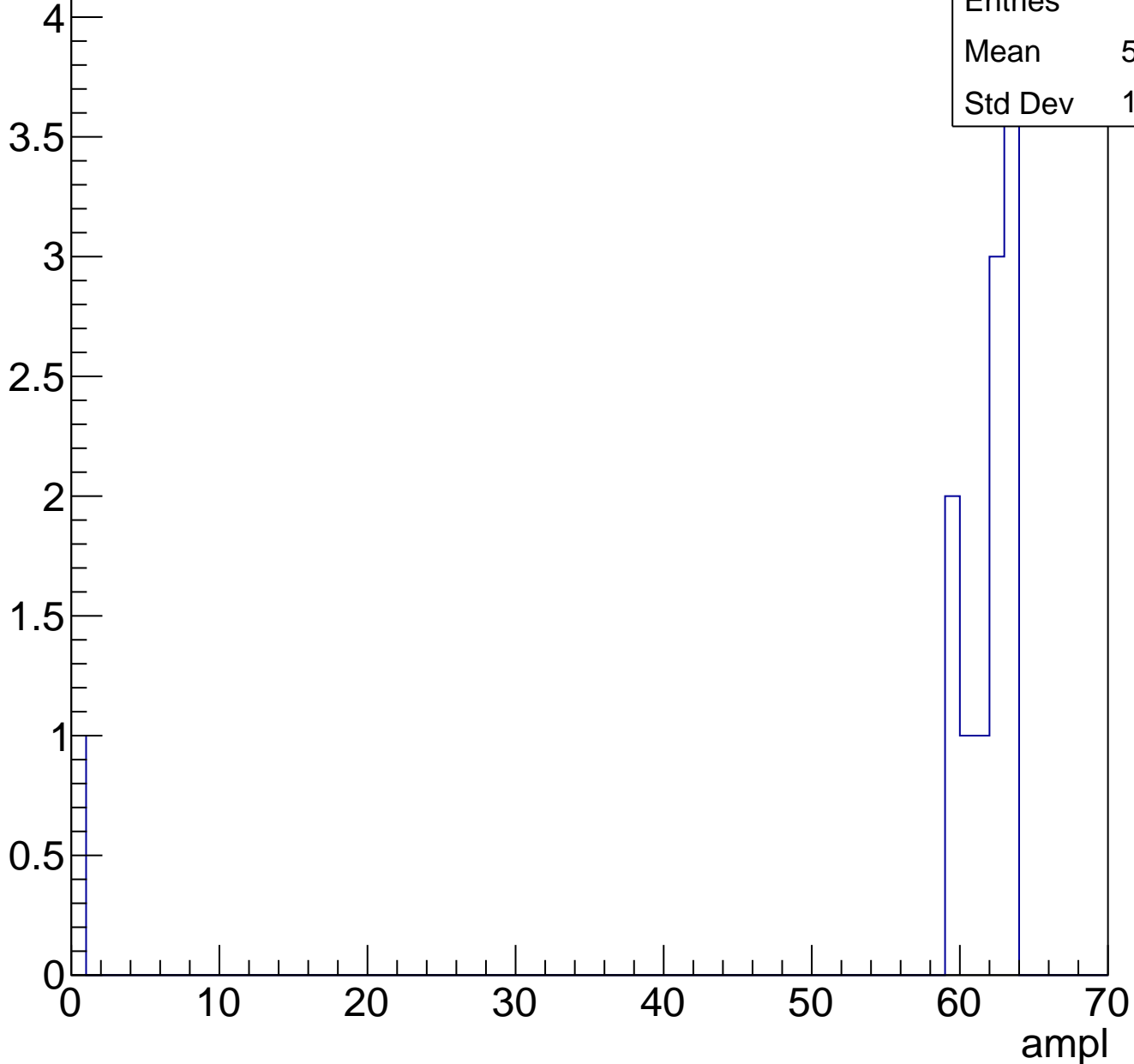
Entries	54
Mean	58.89
Std Dev	3.041



B1L103S, U24-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch43, adc0

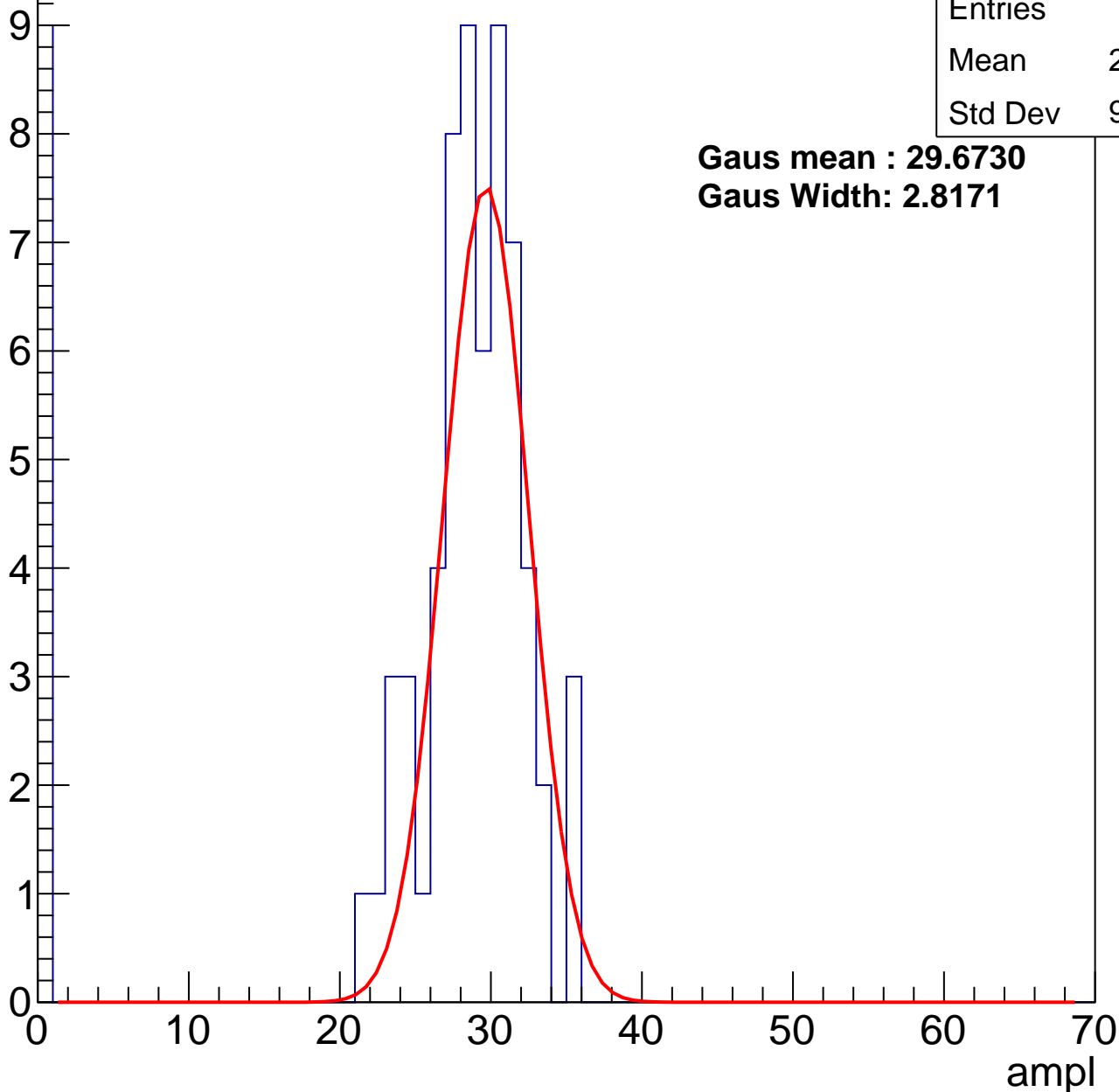
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	24.87
Std Dev	9.984

Gaus mean : 29.6730

Gaus Width: 2.8171



B1L103S, U24-ch43, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	32.85
Std Dev	9.32

Gaus mean : 35.9193

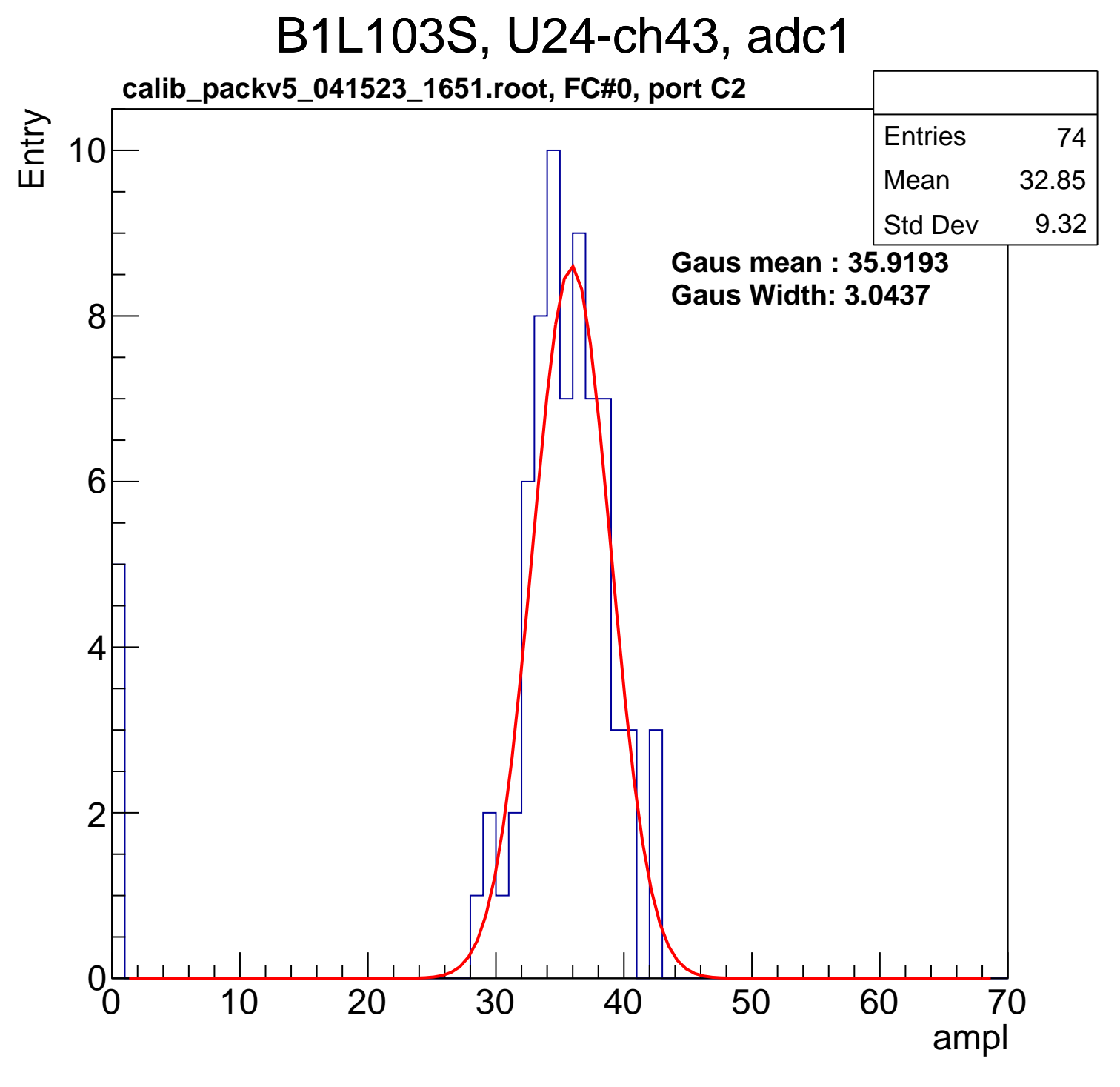
Gaus Width: 3.0437

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch43, adc2

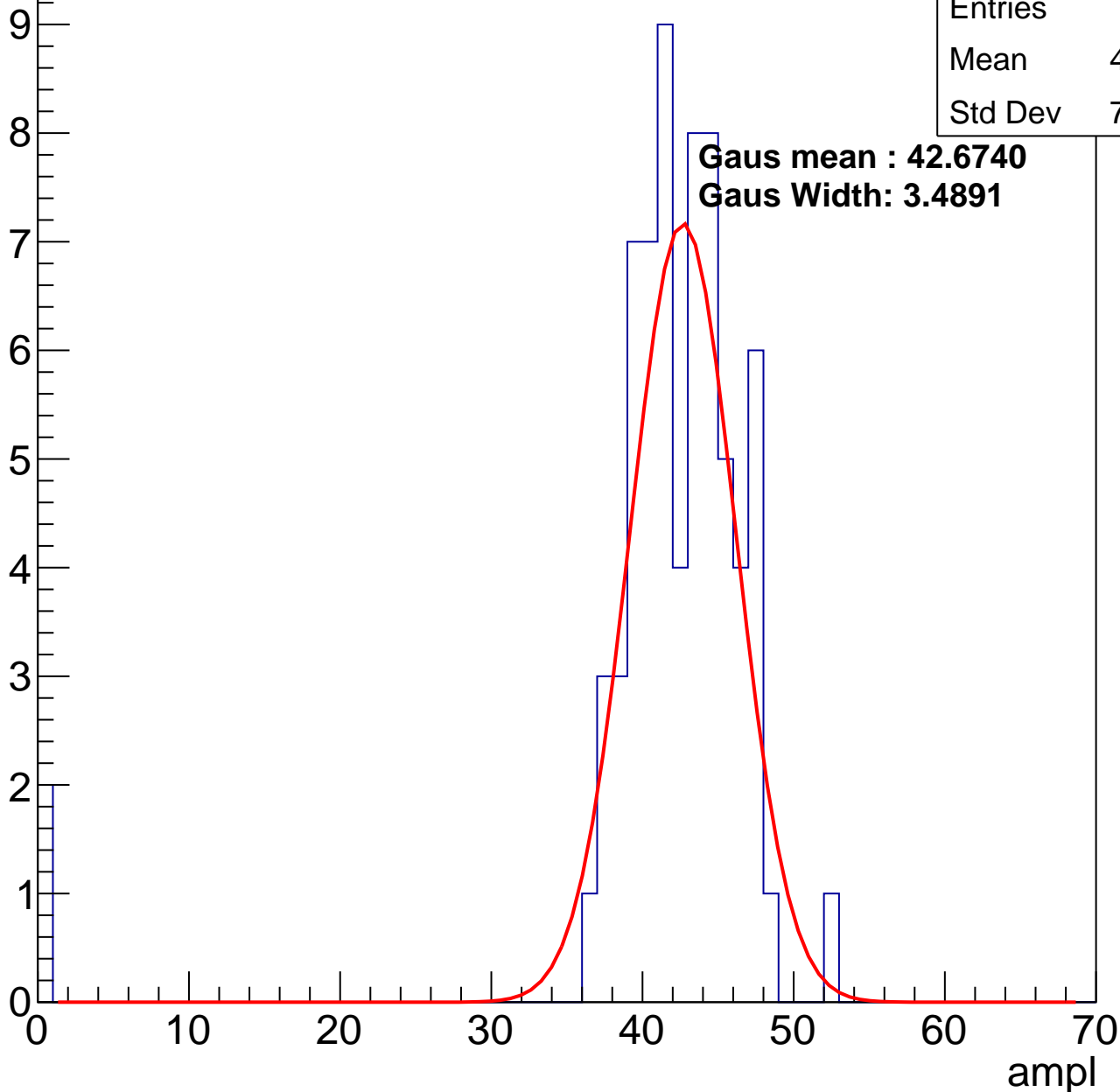
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	41.13
Std Dev	7.774

Gaus mean : 42.6740

Gaus Width: 3.4891

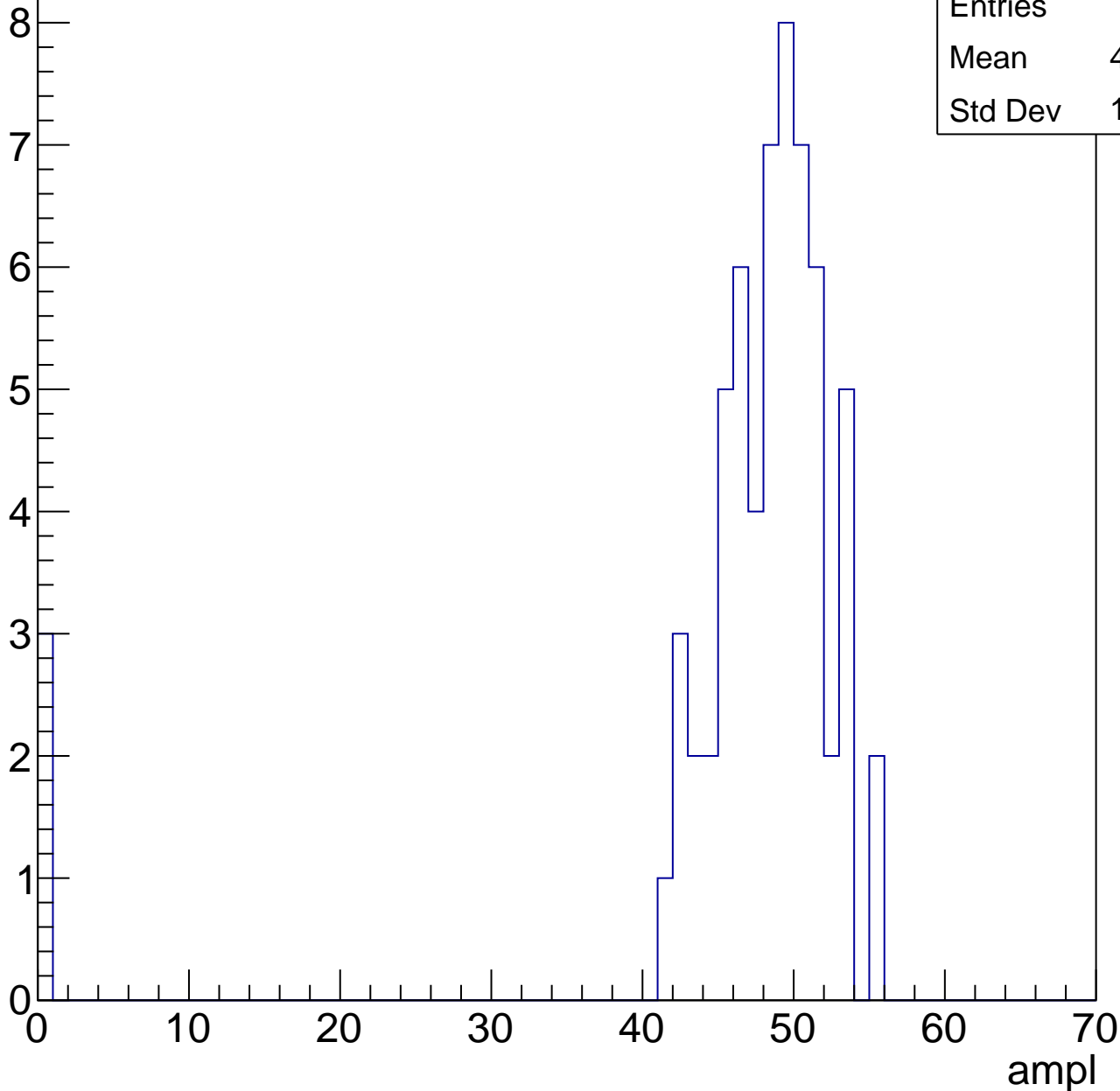


B1L103S, U24-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

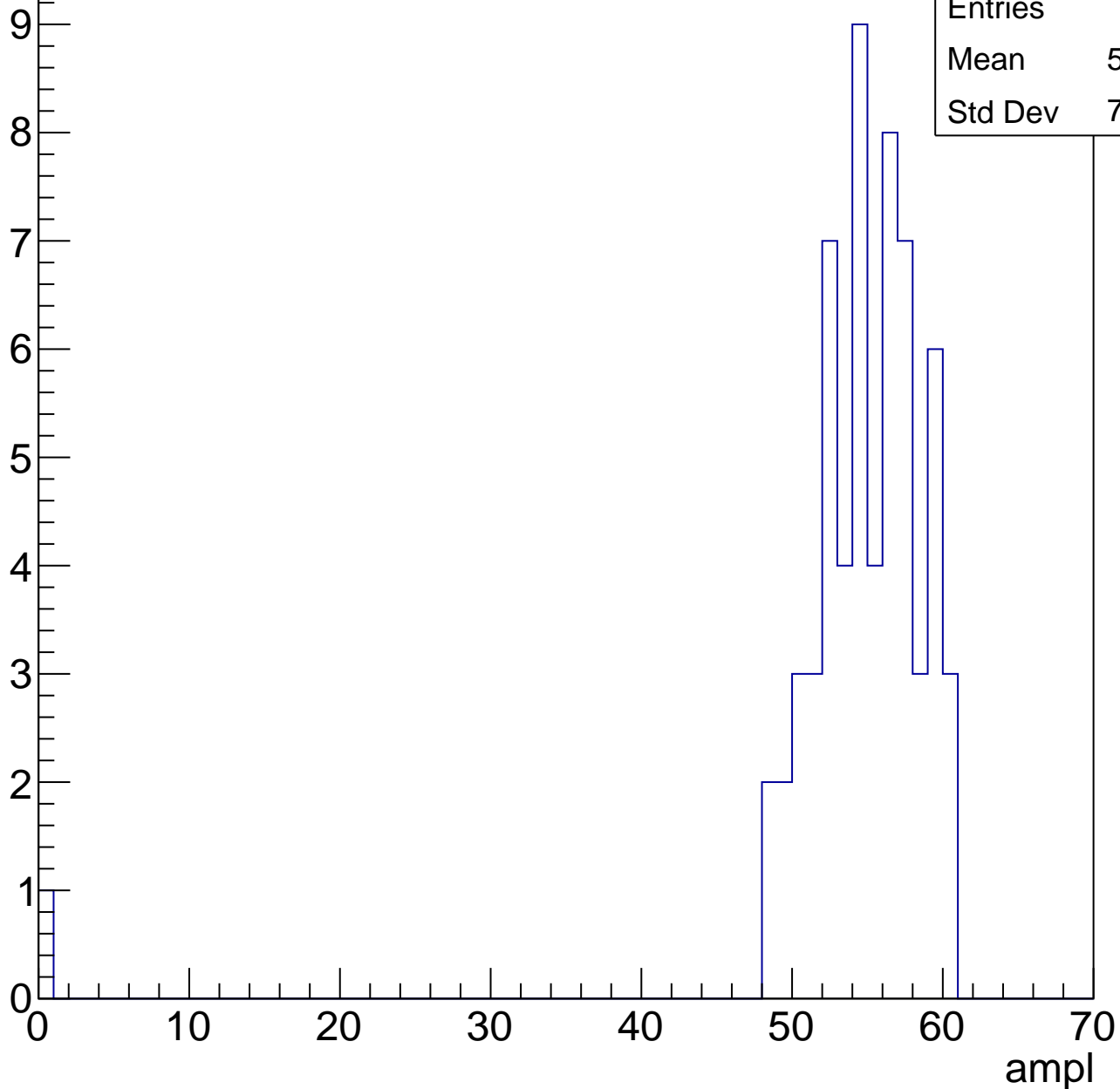
Entries	63
Mean	45.92
Std Dev	10.76



B1L103S, U24-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

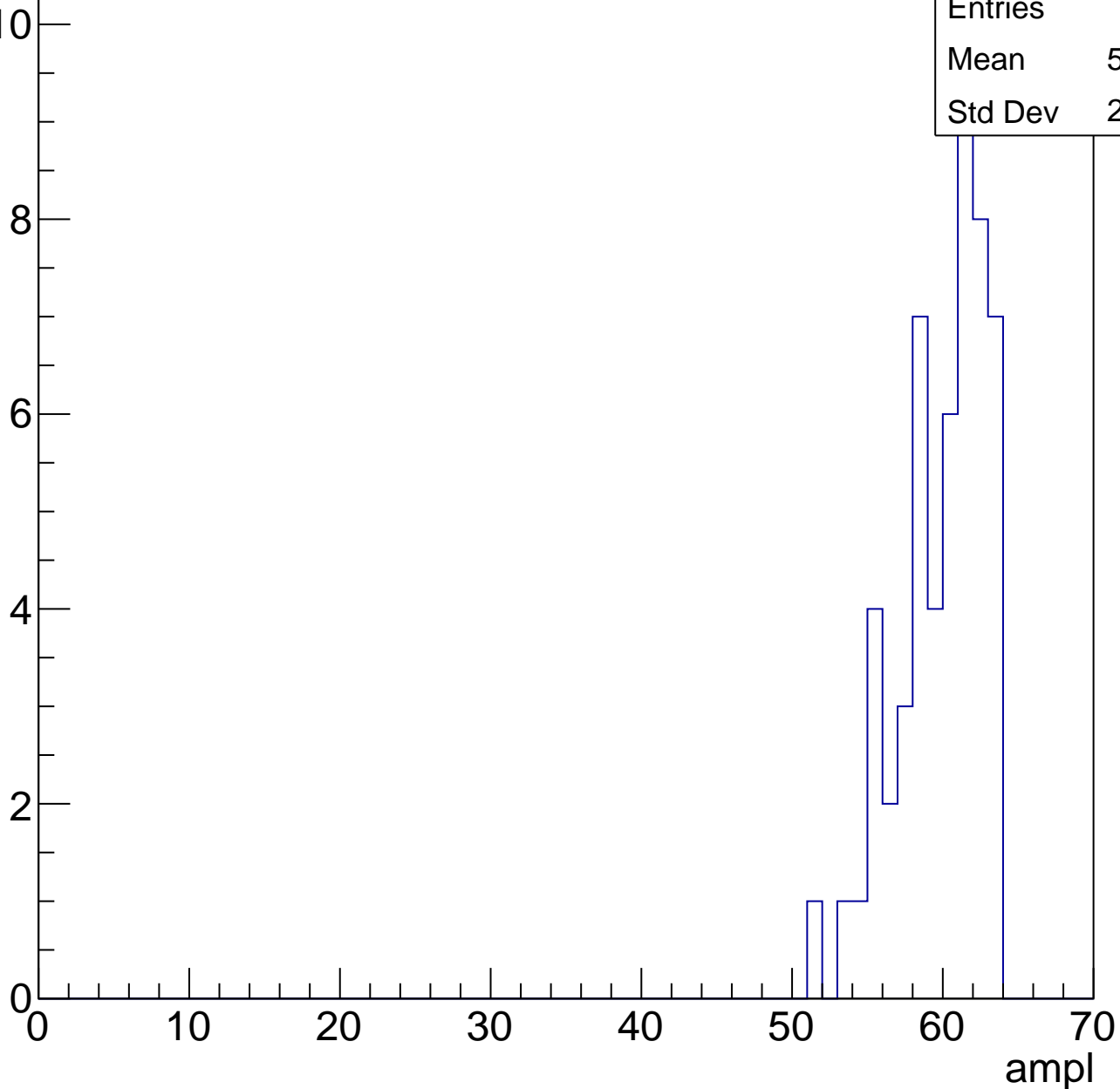


B1L103S, U24-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

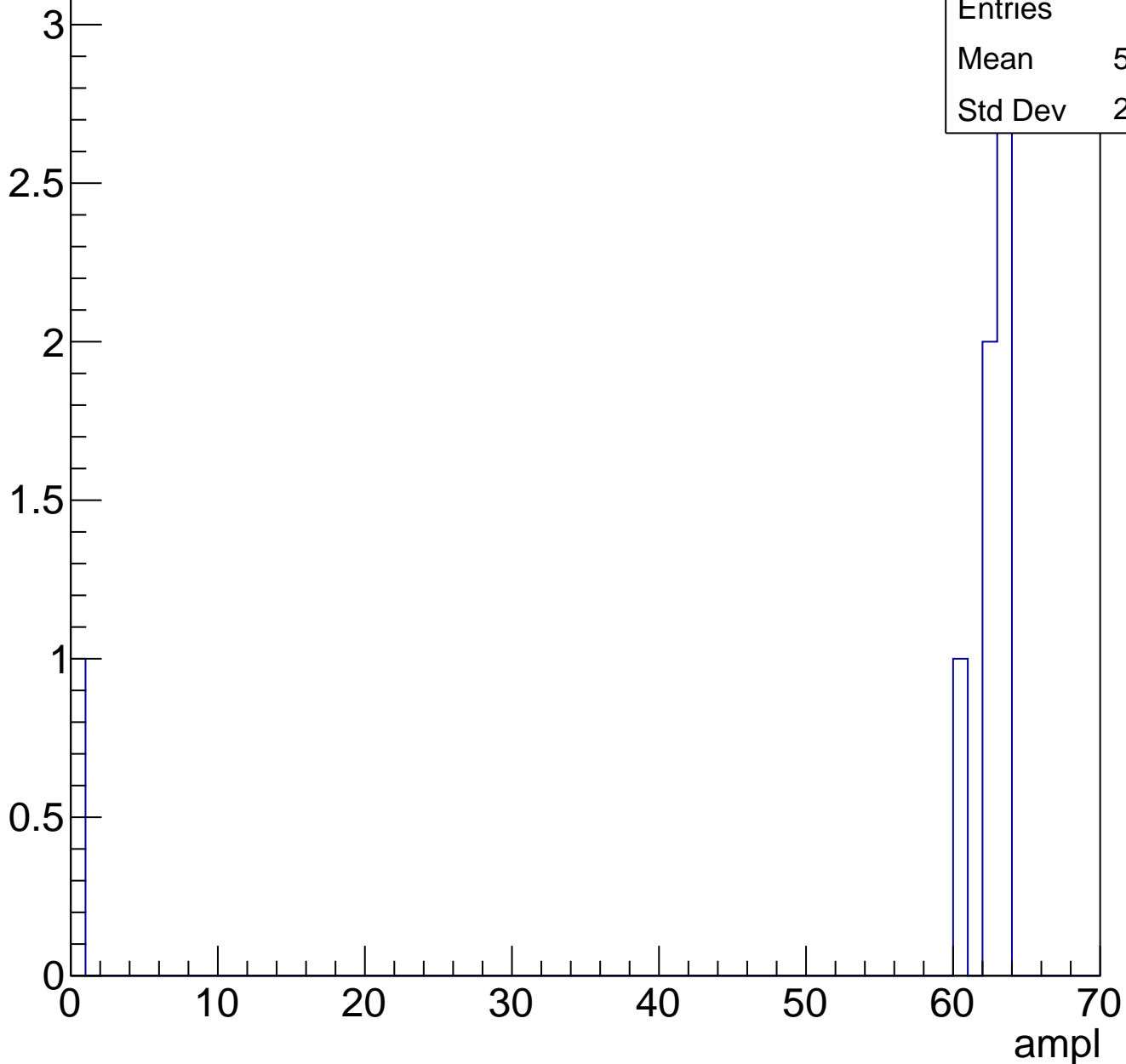
Entries	54
Mean	59.44
Std Dev	2.872



B1L103S, U24-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U24-ch44, adc0

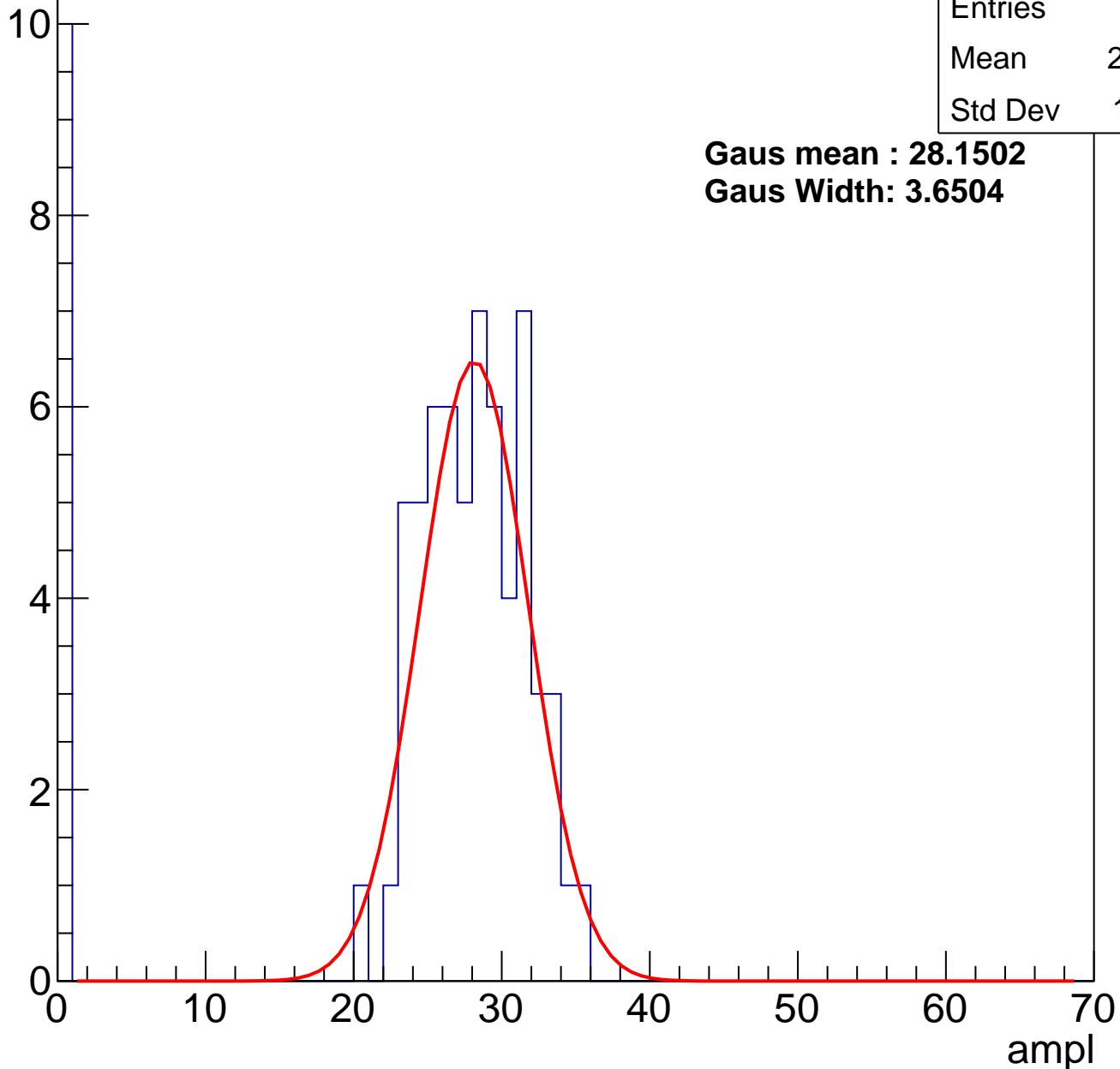
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	23.79
Std Dev	10.11

Gaus mean : 28.1502

Gaus Width: 3.6504

Entry



B1L103S, U24-ch44, adc1

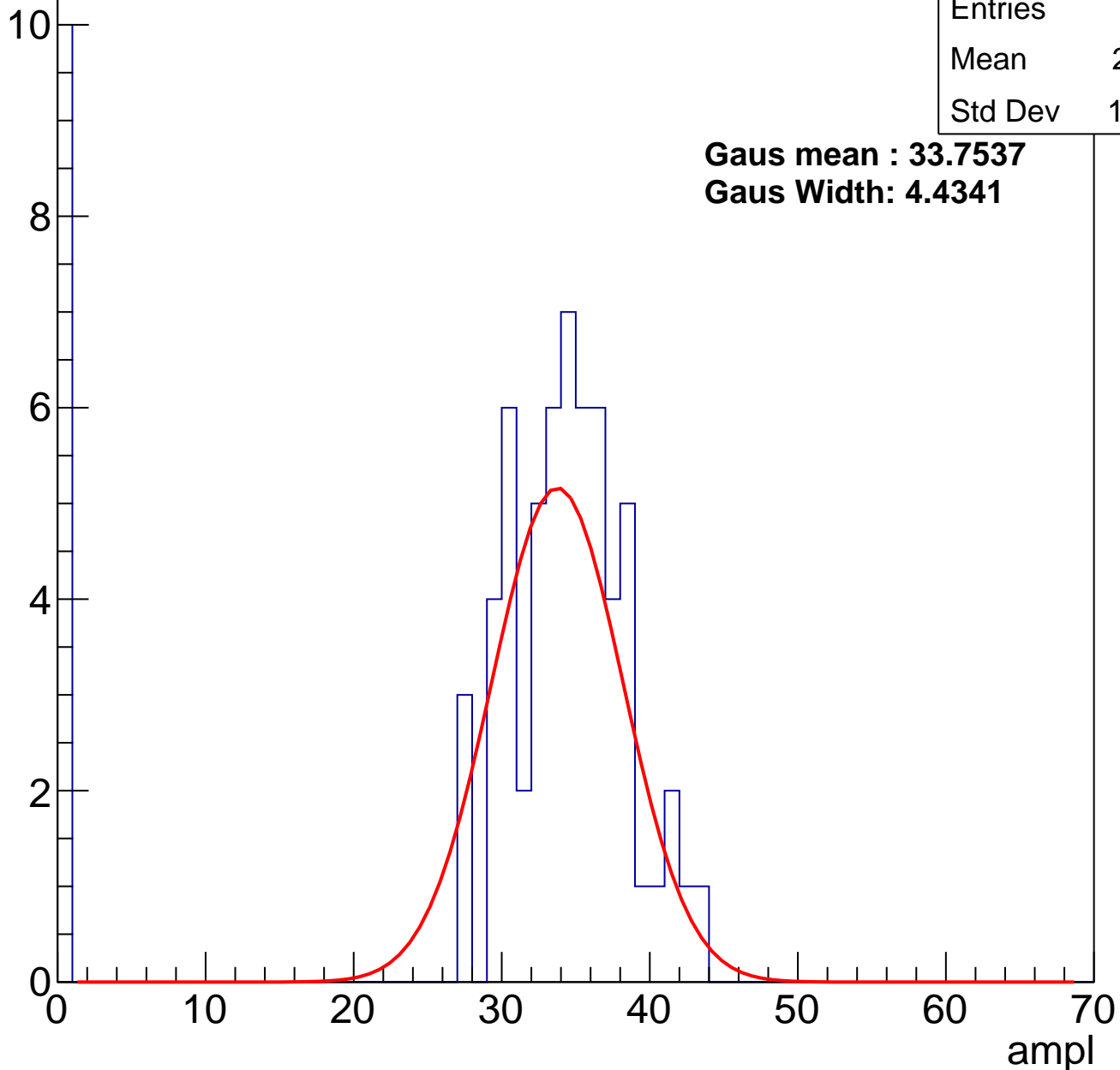
calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	29.21
Std Dev	12.42

Gaus mean : 33.7537

Gaus Width: 4.4341

Entry



B1L103S, U24-ch44, adc2

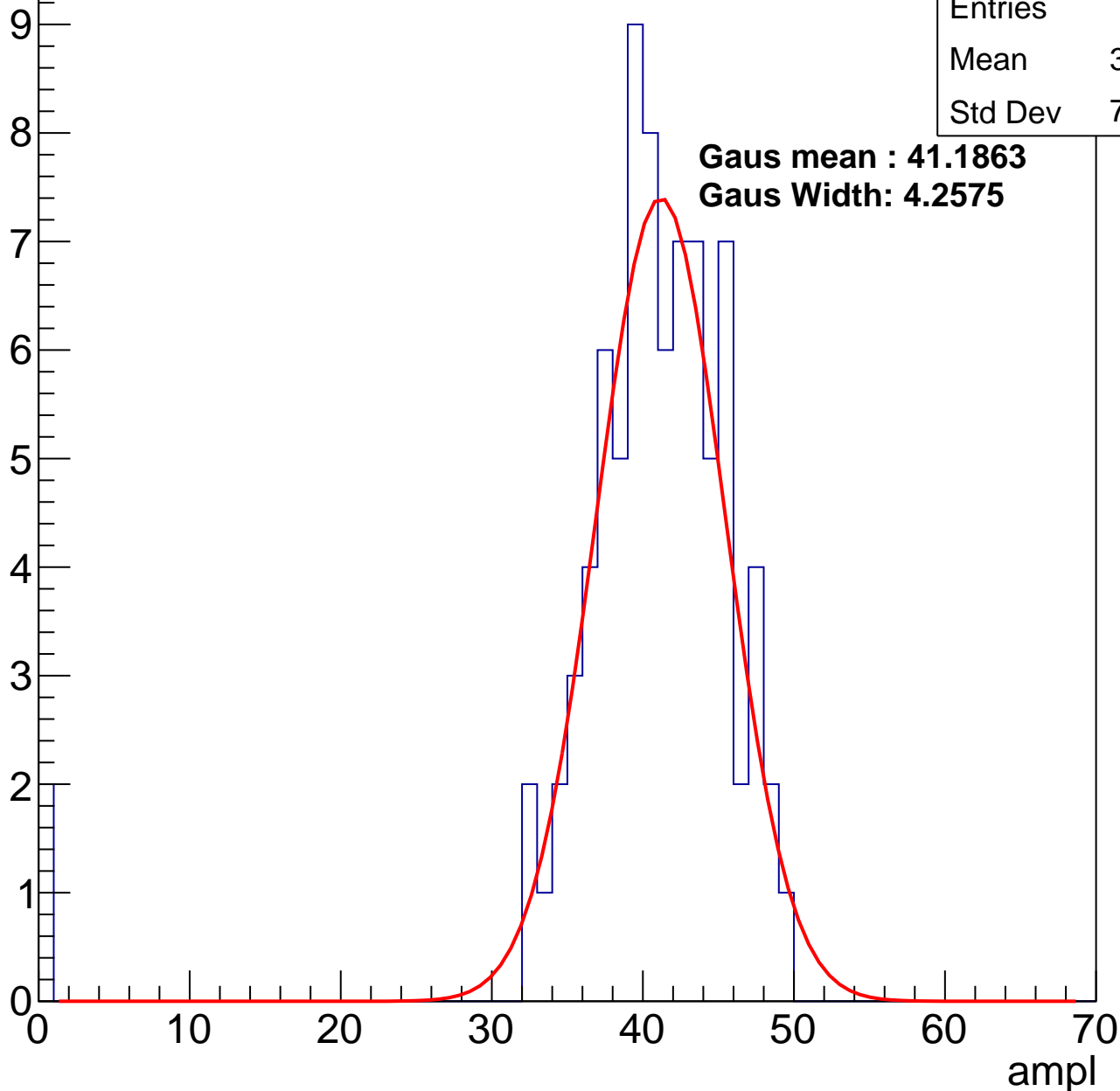
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	39.73
Std Dev	7.358

Gaus mean : 41.1863

Gaus Width: 4.2575

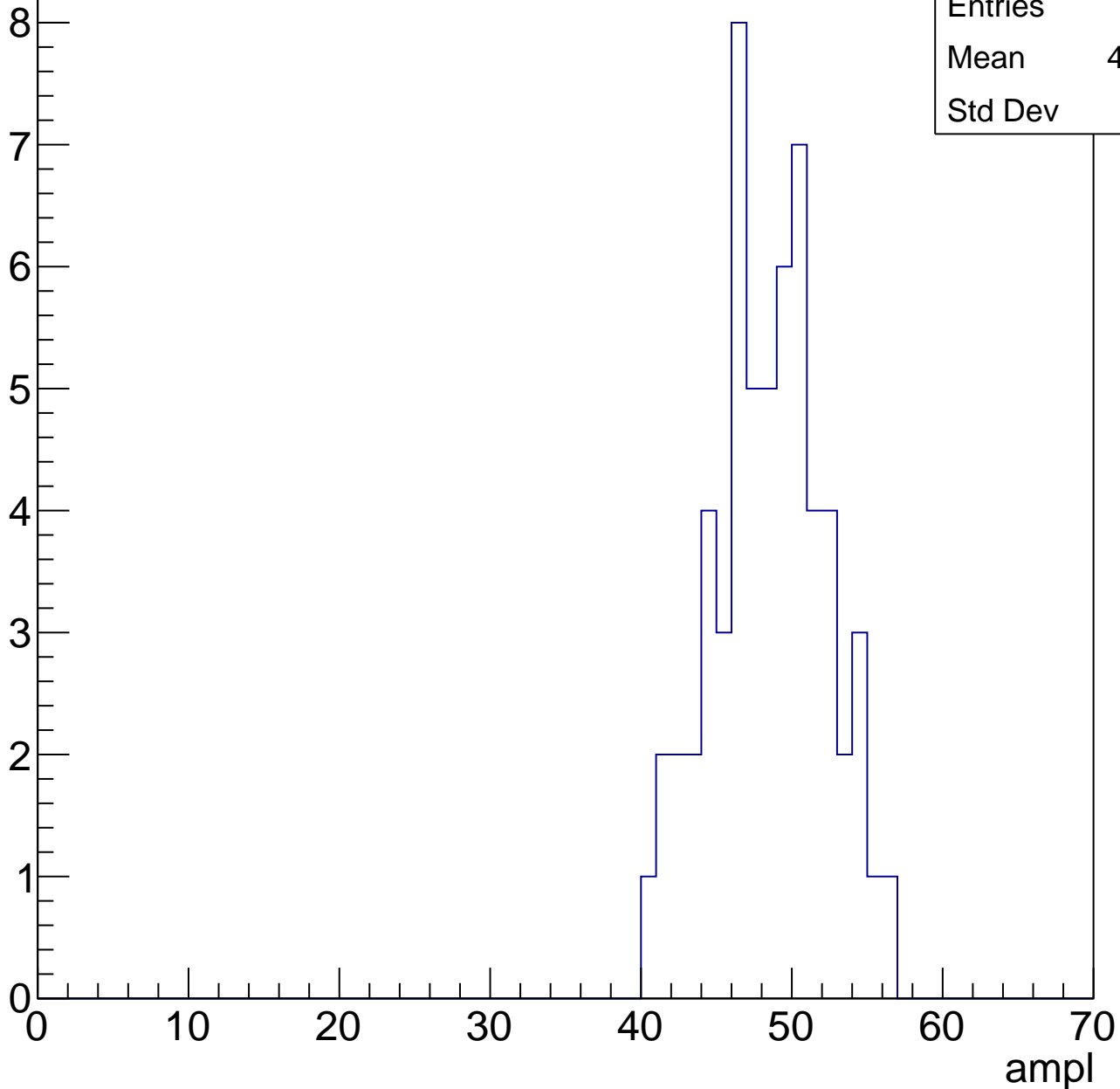


B1L103S, U24-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	48.02
Std Dev	3.69

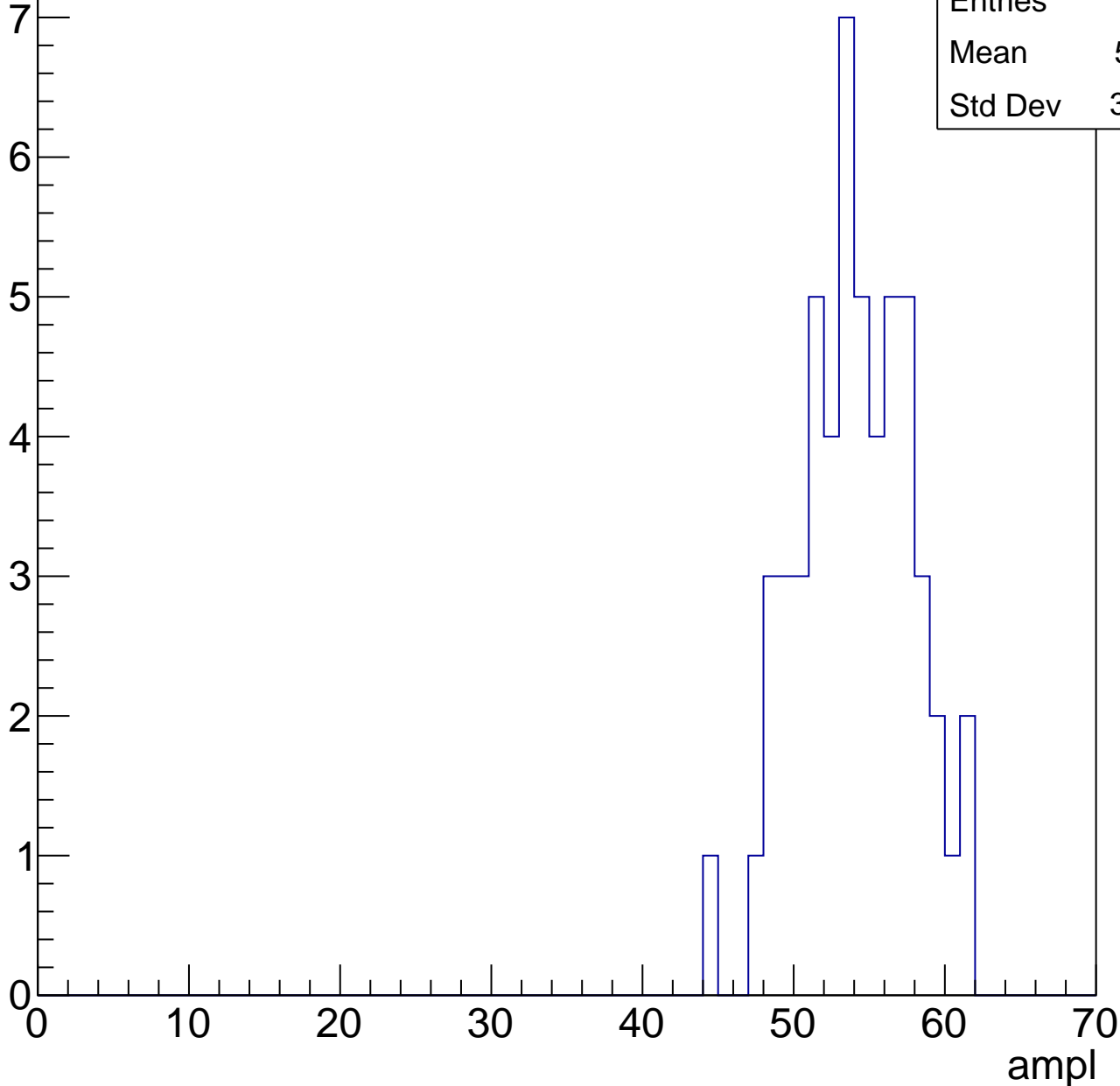


B1L103S, U24-ch44, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	53.61
Std Dev	3.704

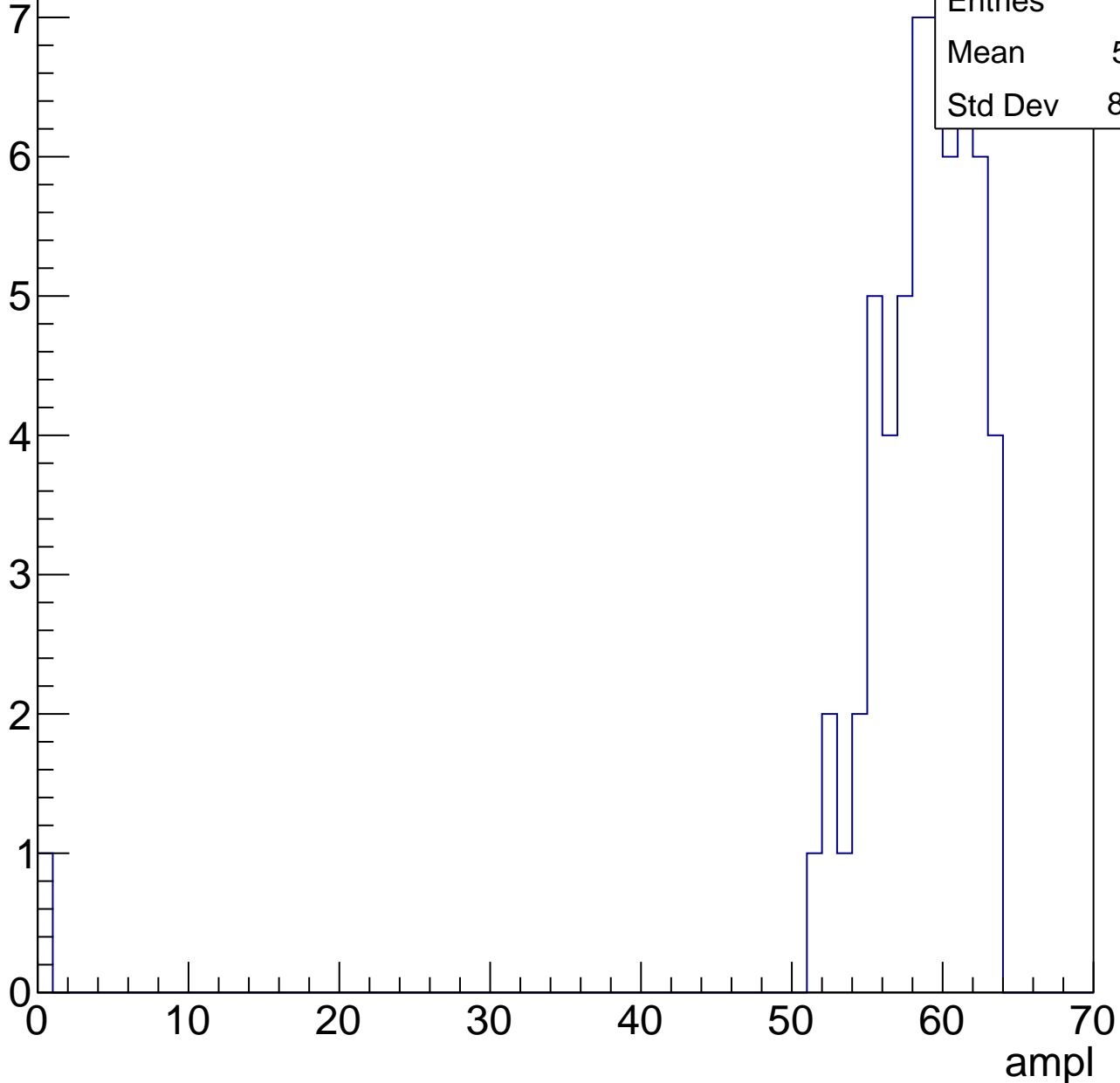


B1L103S, U24-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.41
Std Dev	8.177

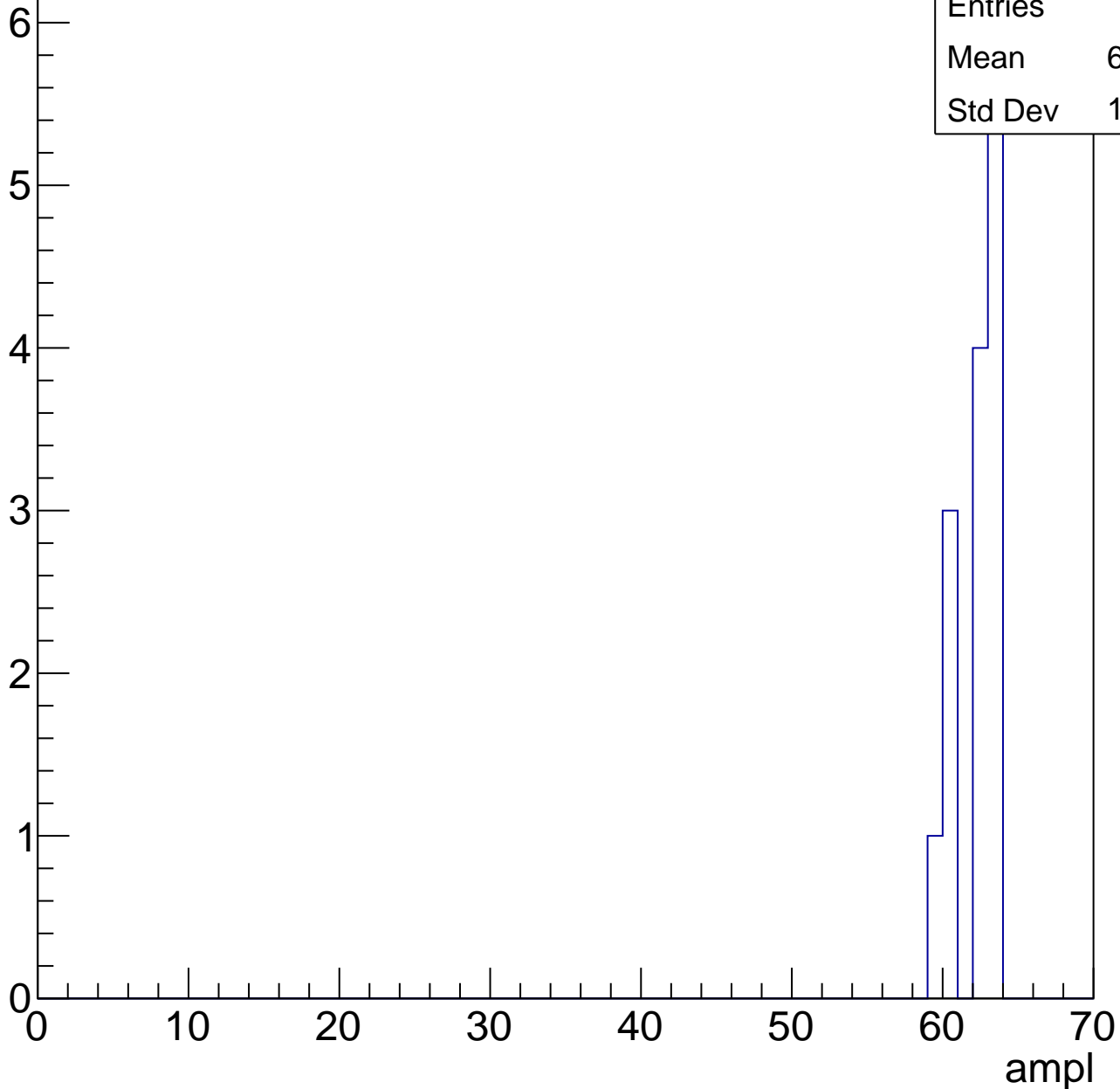


B1L103S, U24-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.79
Std Dev	1.372



B1L103S, U24-ch44, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U24-ch45, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	25.46
Std Dev	10.91

Gaus mean : 30.2352

Gaus Width: 3.2267

Entry

12

10

8

6

4

2

0

0

10

20

30

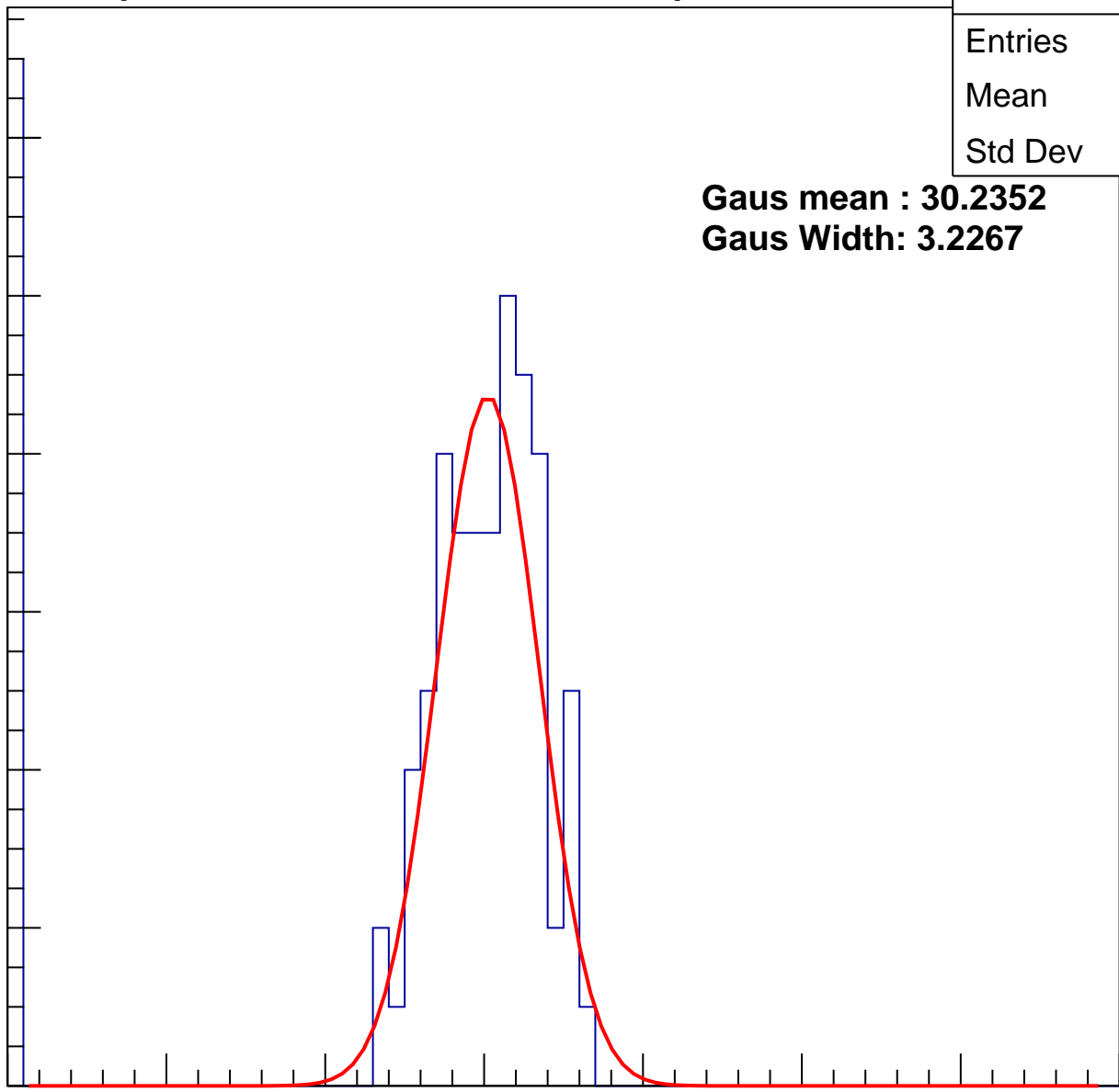
40

50

60

70

ampl



B1L103S, U24-ch45, adc1

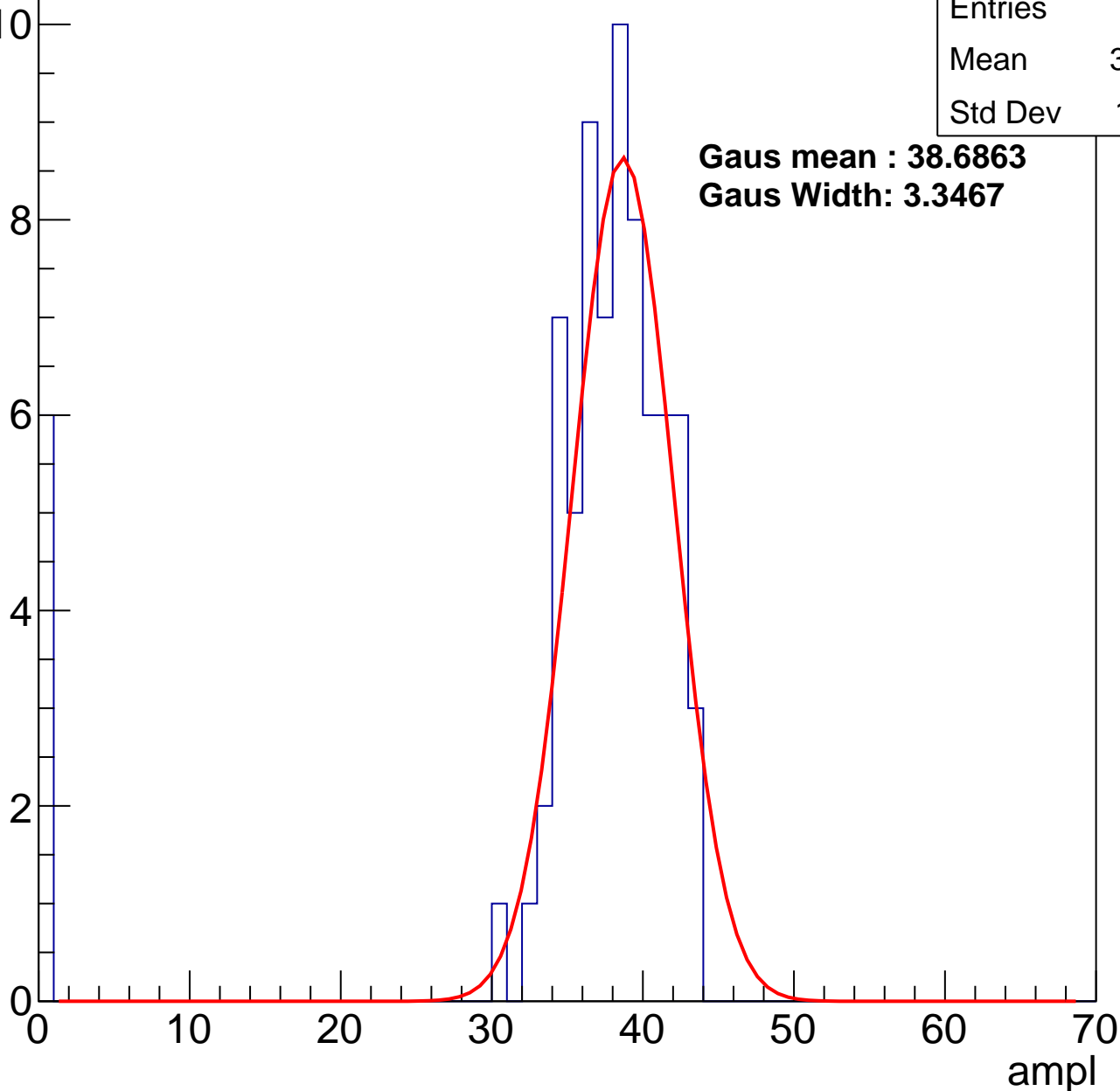
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	34.84
Std Dev	10.51

Gaus mean : 38.6863

Gaus Width: 3.3467



B1L103S, U24-ch45, adc2

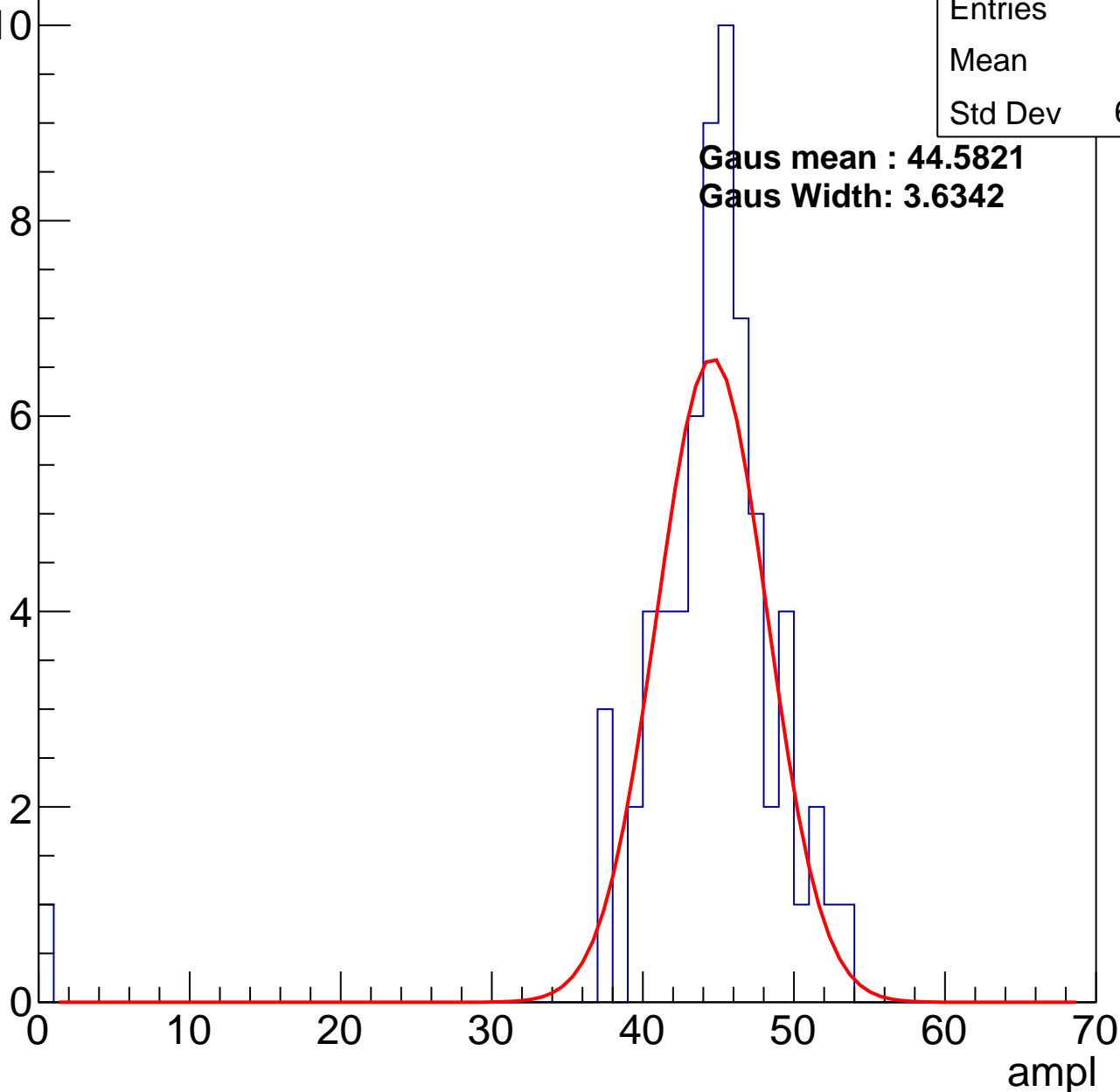
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	43.8
Std Dev	6.451

Gaus mean : 44.5821

Gaus Width: 3.6342

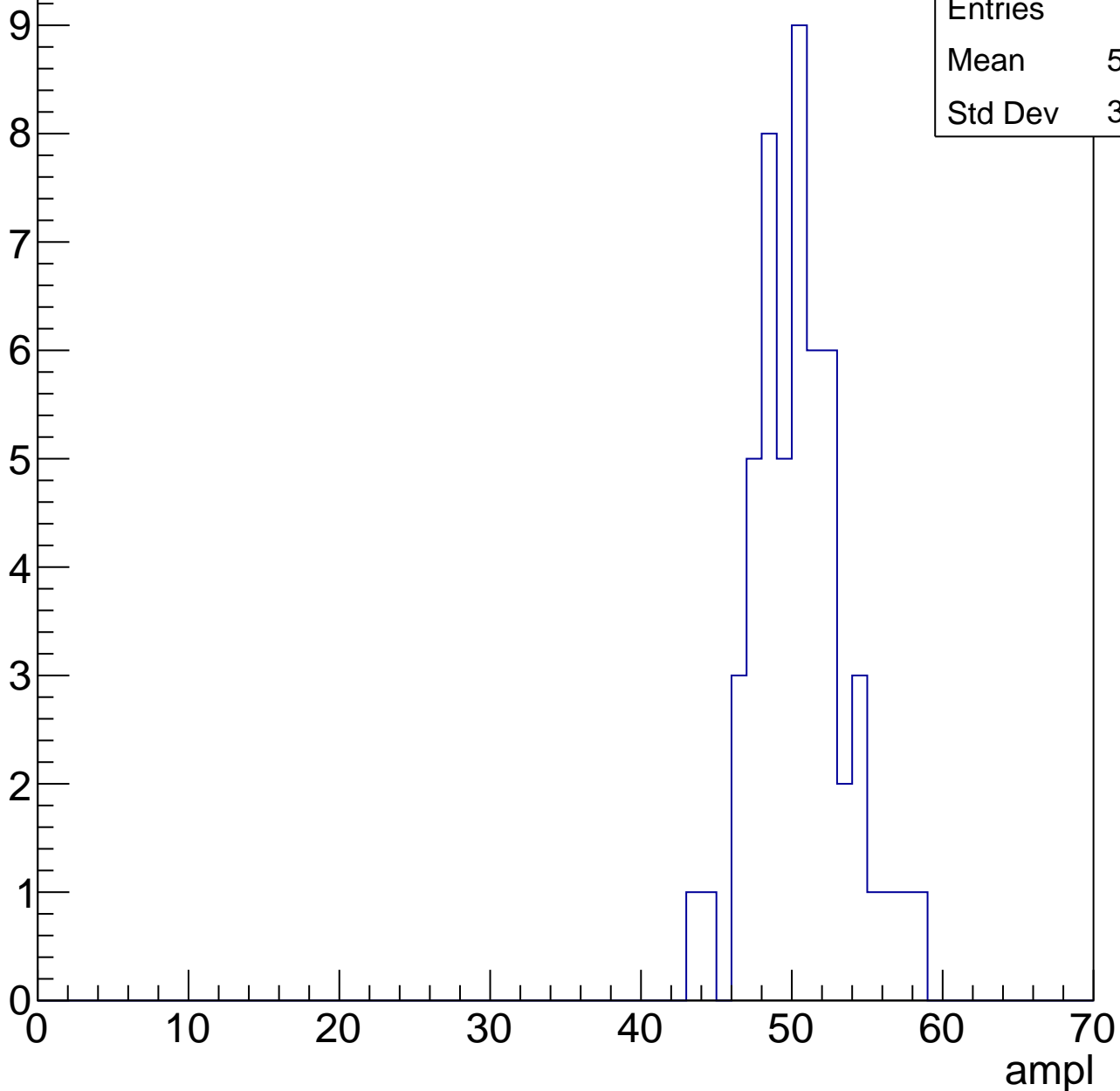


B1L103S, U24-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

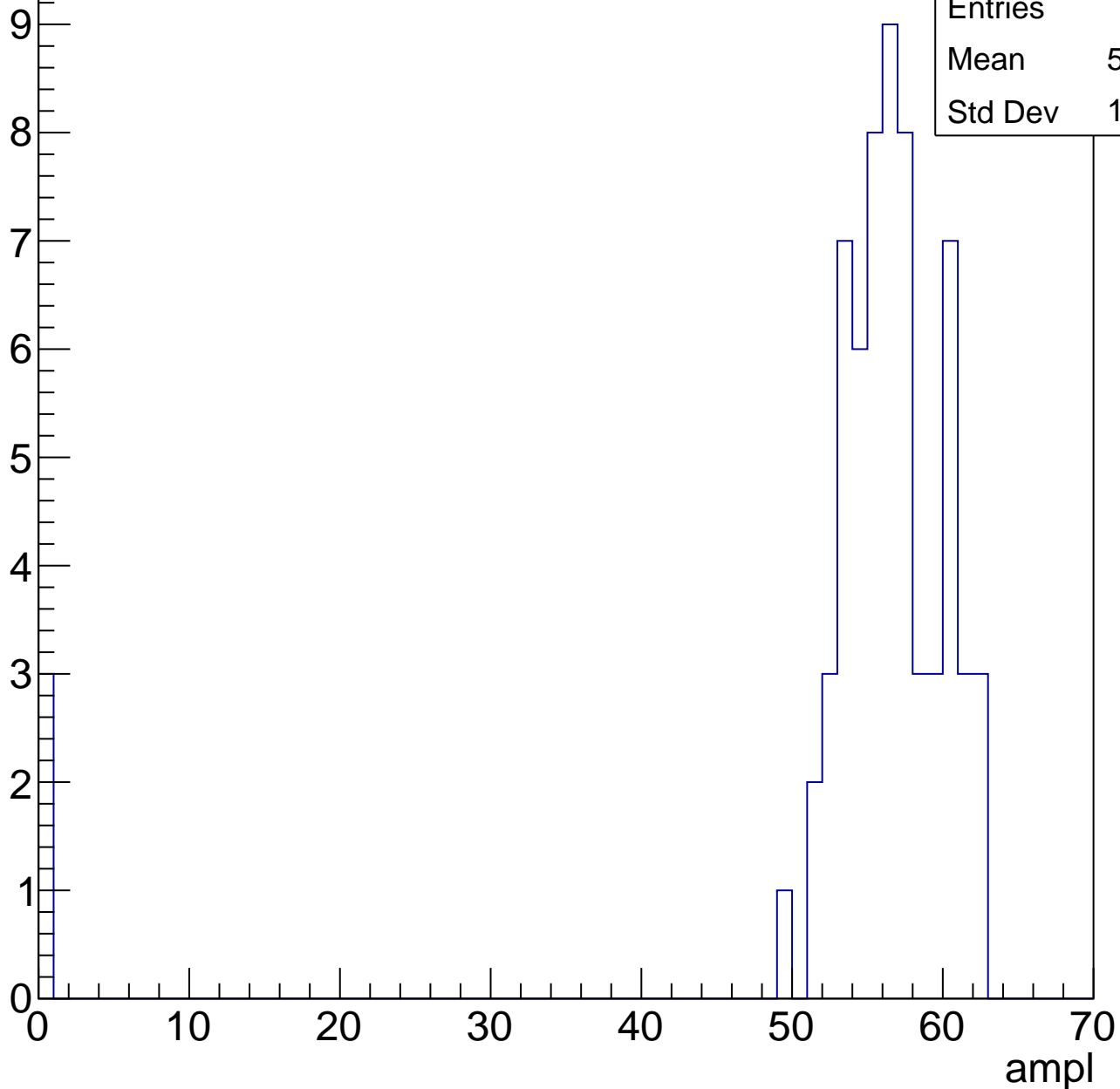
Entries	53
Mean	50.02
Std Dev	3.025



B1L103S, U24-ch45, adc4

calib_packv5_041523_1651.root, FC#0, port C2

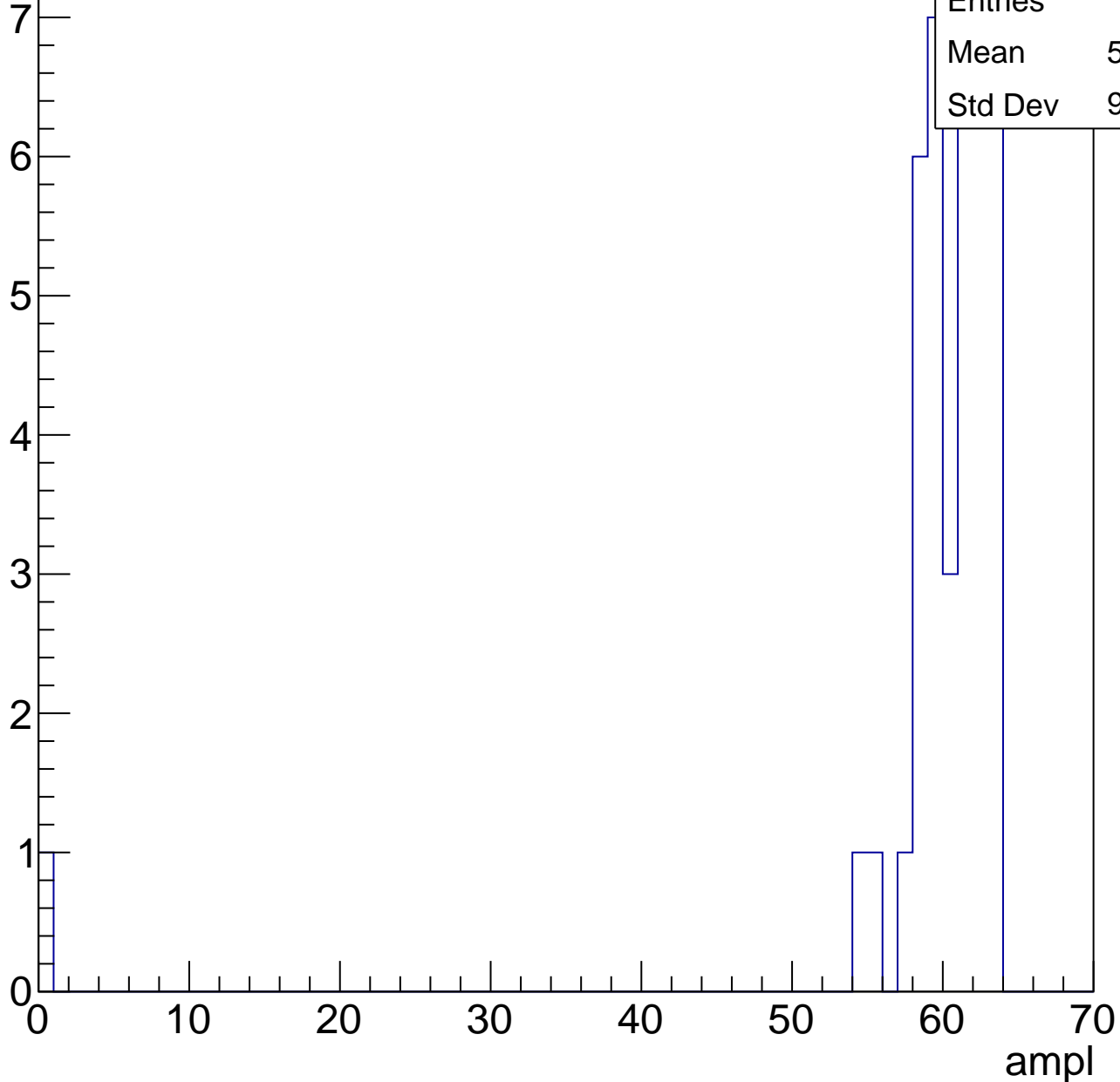
Entry



B1L103S, U24-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch46, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	19.87
Std Dev	12.12

Gaus mean : 27.5915

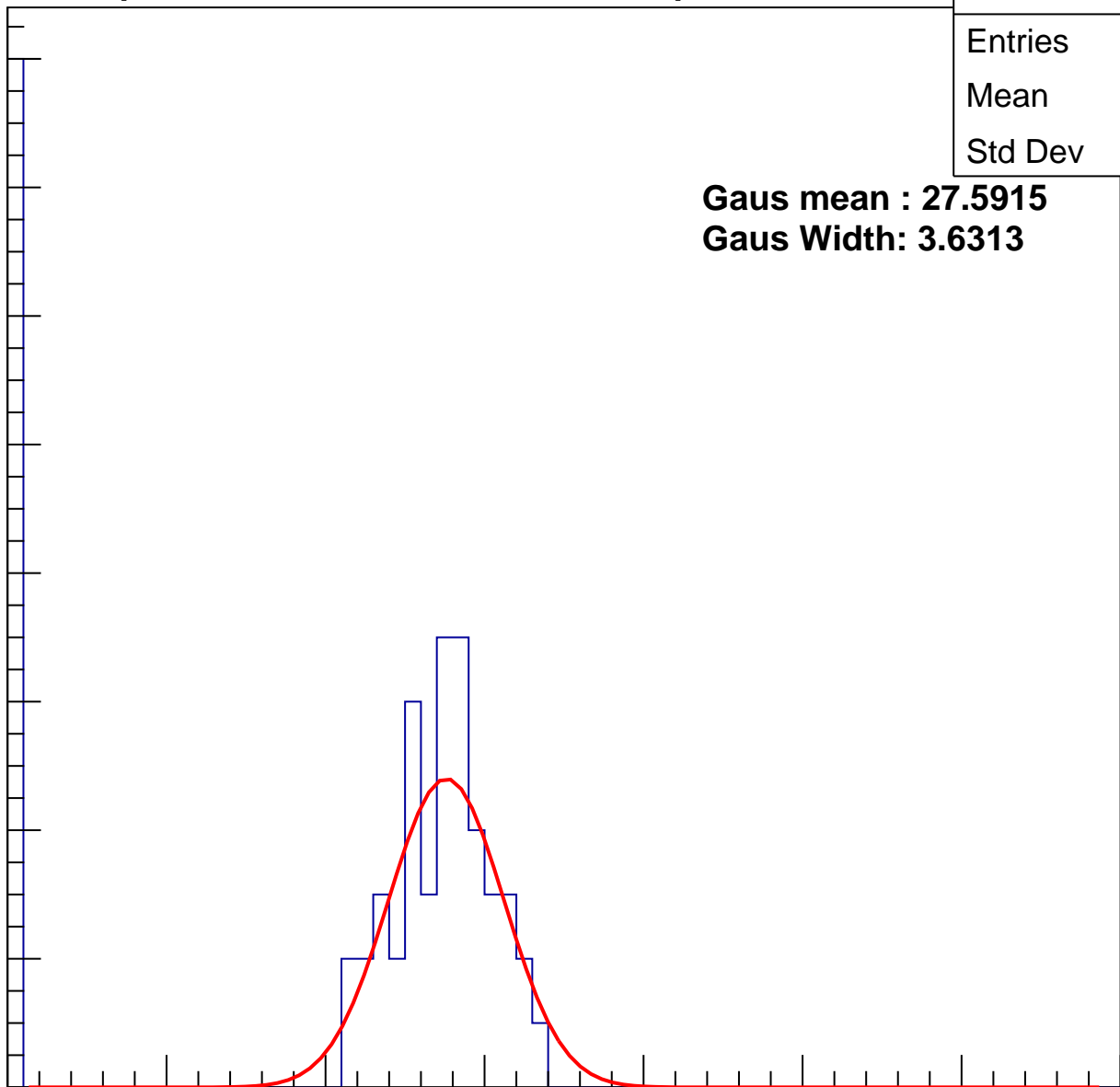
Gaus Width: 3.6313

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



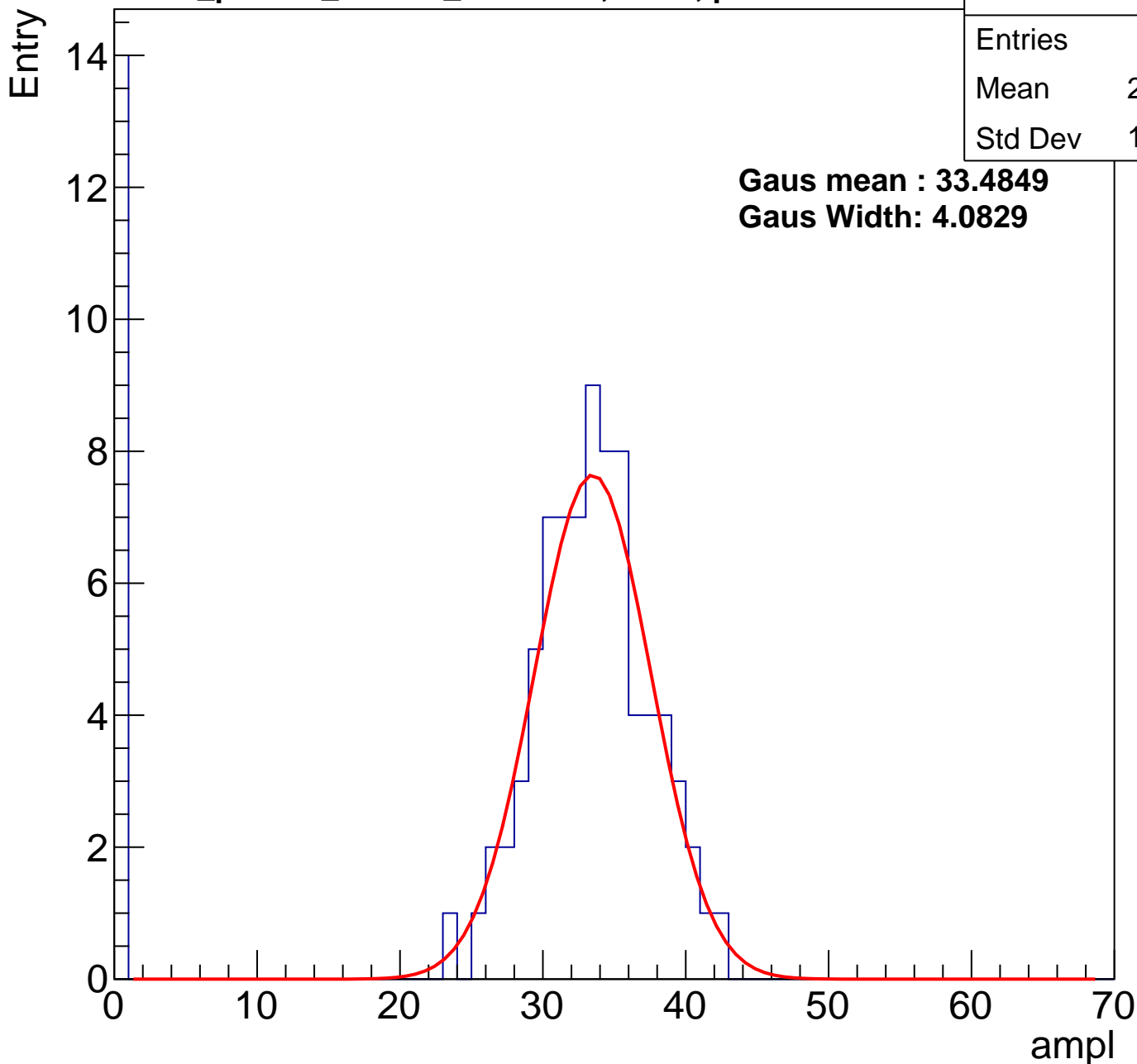
B1L103S, U24-ch46, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	28.03
Std Dev	12.33

Gaus mean : 33.4849

Gaus Width: 4.0829



B1L103S, U24-ch46, adc2

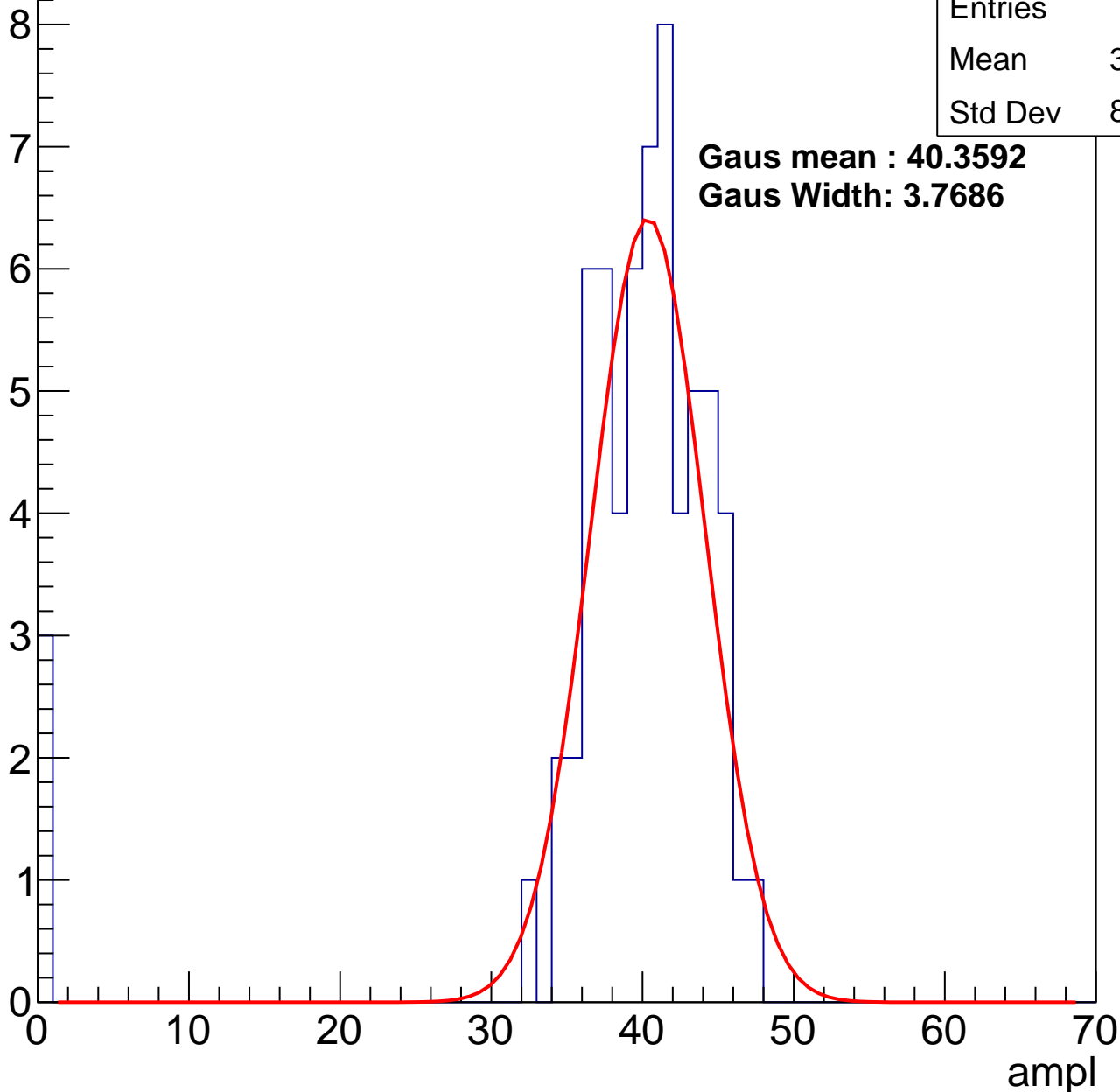
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.12
Std Dev	8.998

Gaus mean : 40.3592

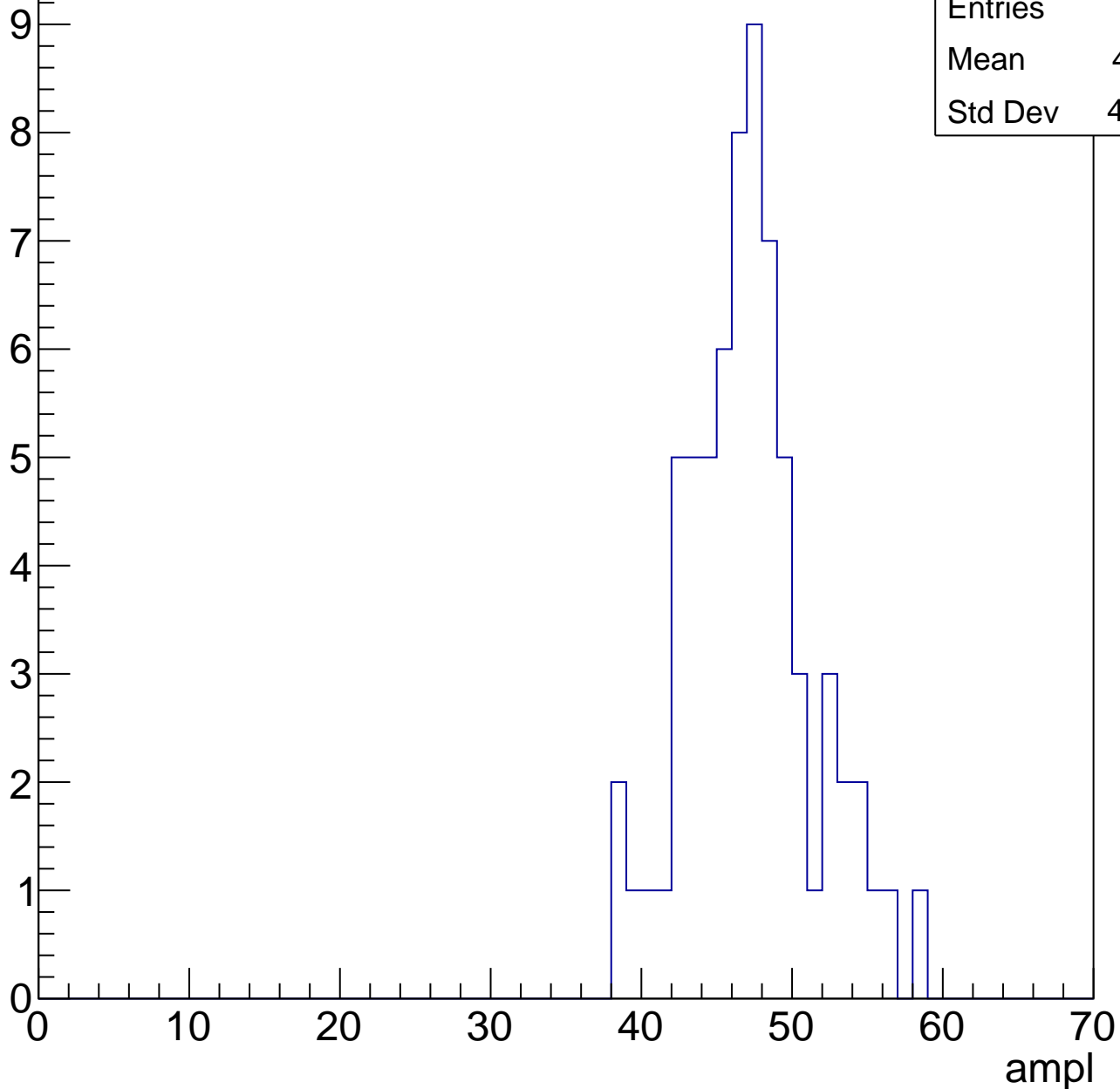
Gaus Width: 3.7686



B1L103S, U24-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry



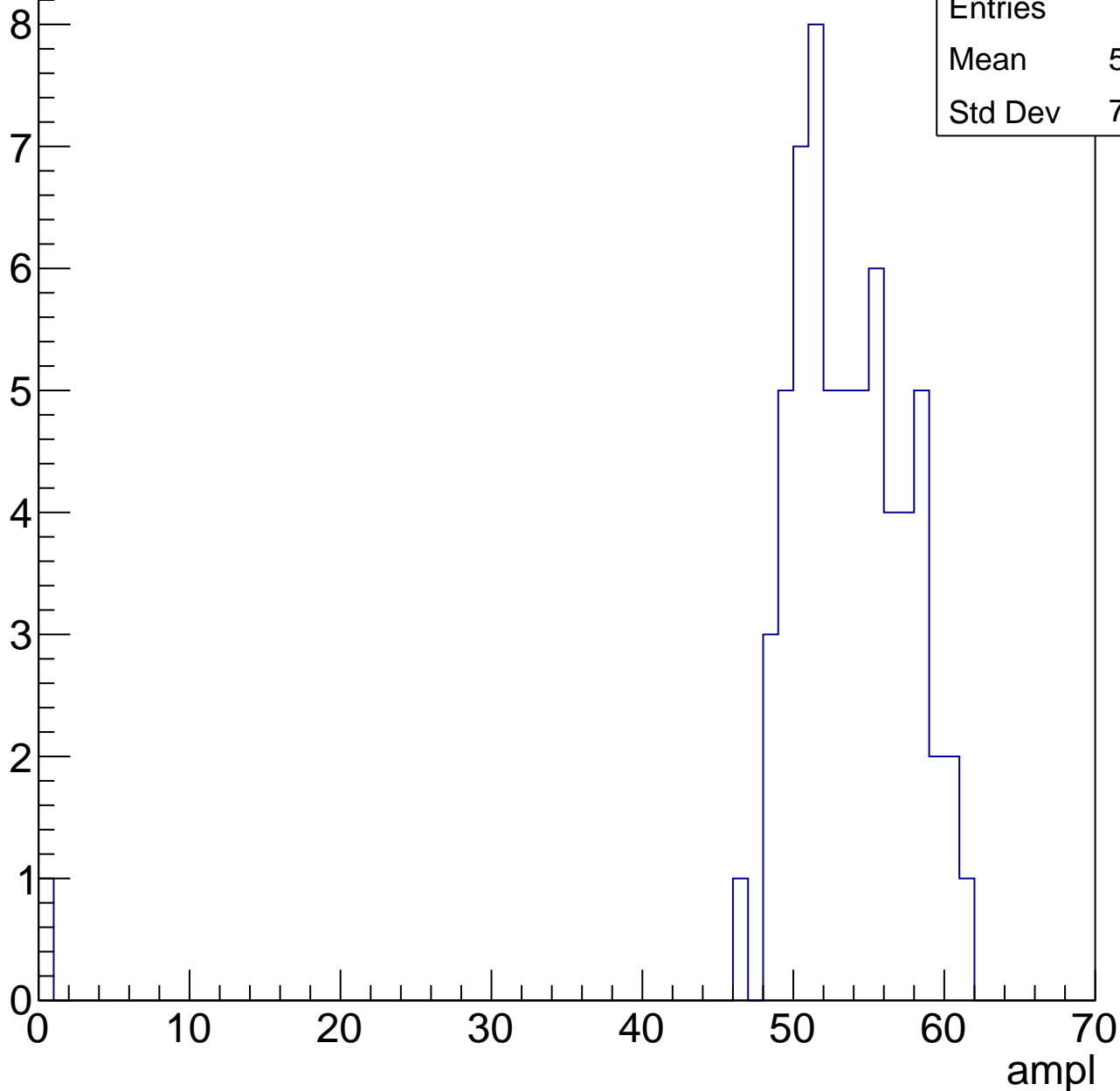
Entries	69
Mean	46.71
Std Dev	4.115

B1L103S, U24-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	52.48
Std Dev	7.483

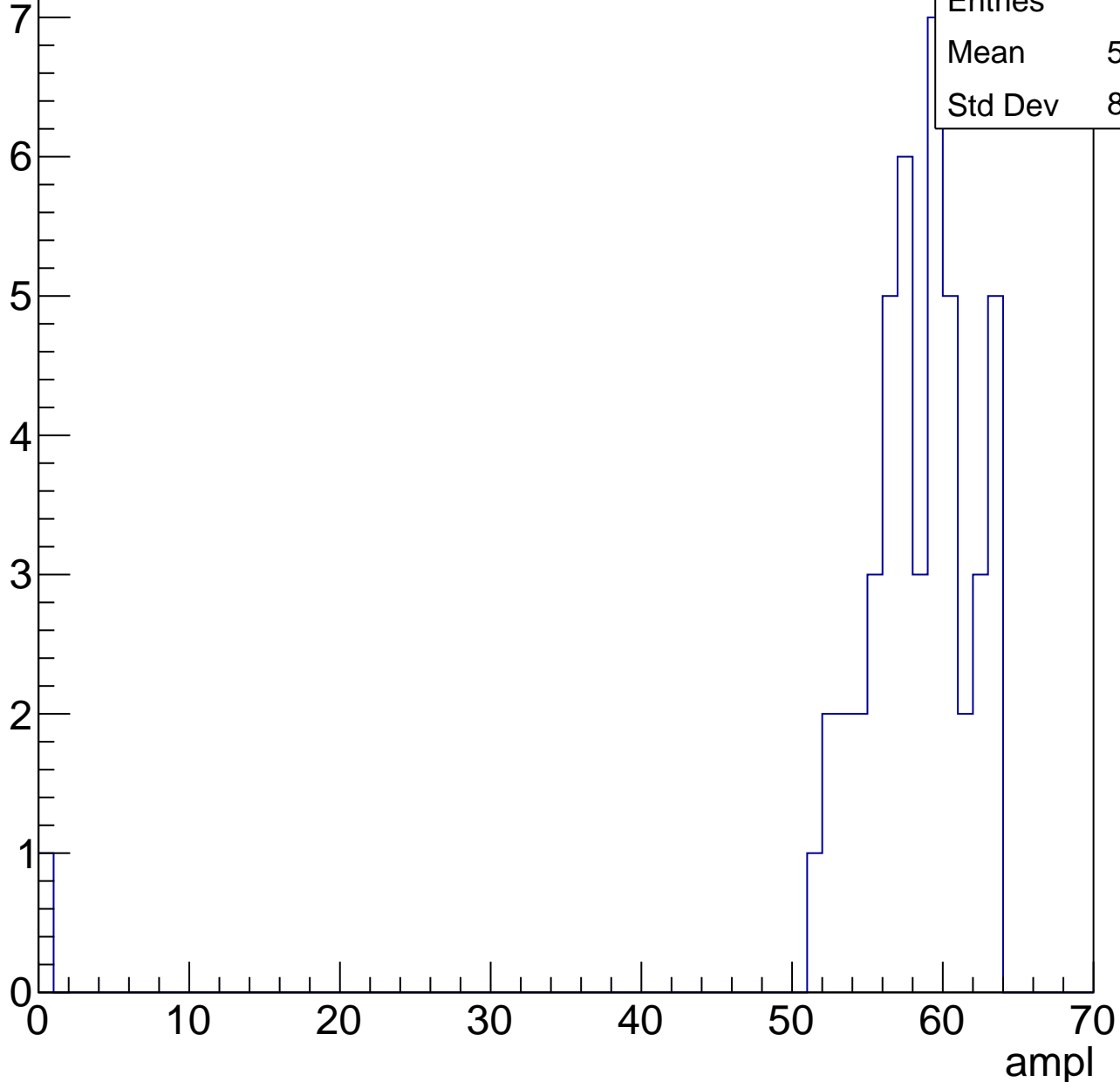


B1L103S, U24-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	56.72
Std Dev	8.946

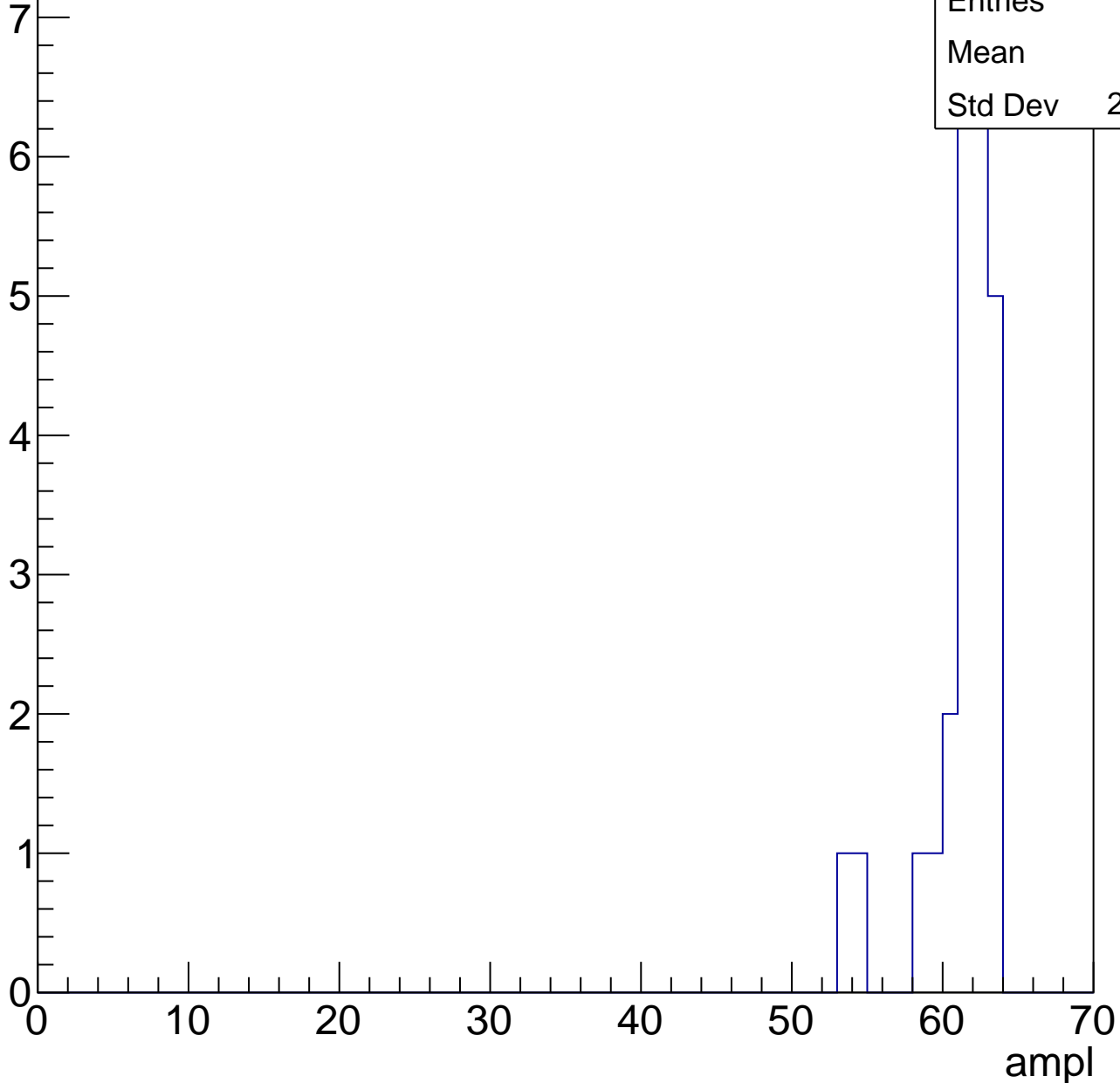


B1L103S, U24-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	60.8
Std Dev	2.482



B1L103S, U24-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



B1L103S, U24-ch47, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	20.69
Std Dev	12.89

Gaus mean : 28.8812

Gaus Width: 3.7991

Entry

25

20

15

10

5

0

0

10

20

30

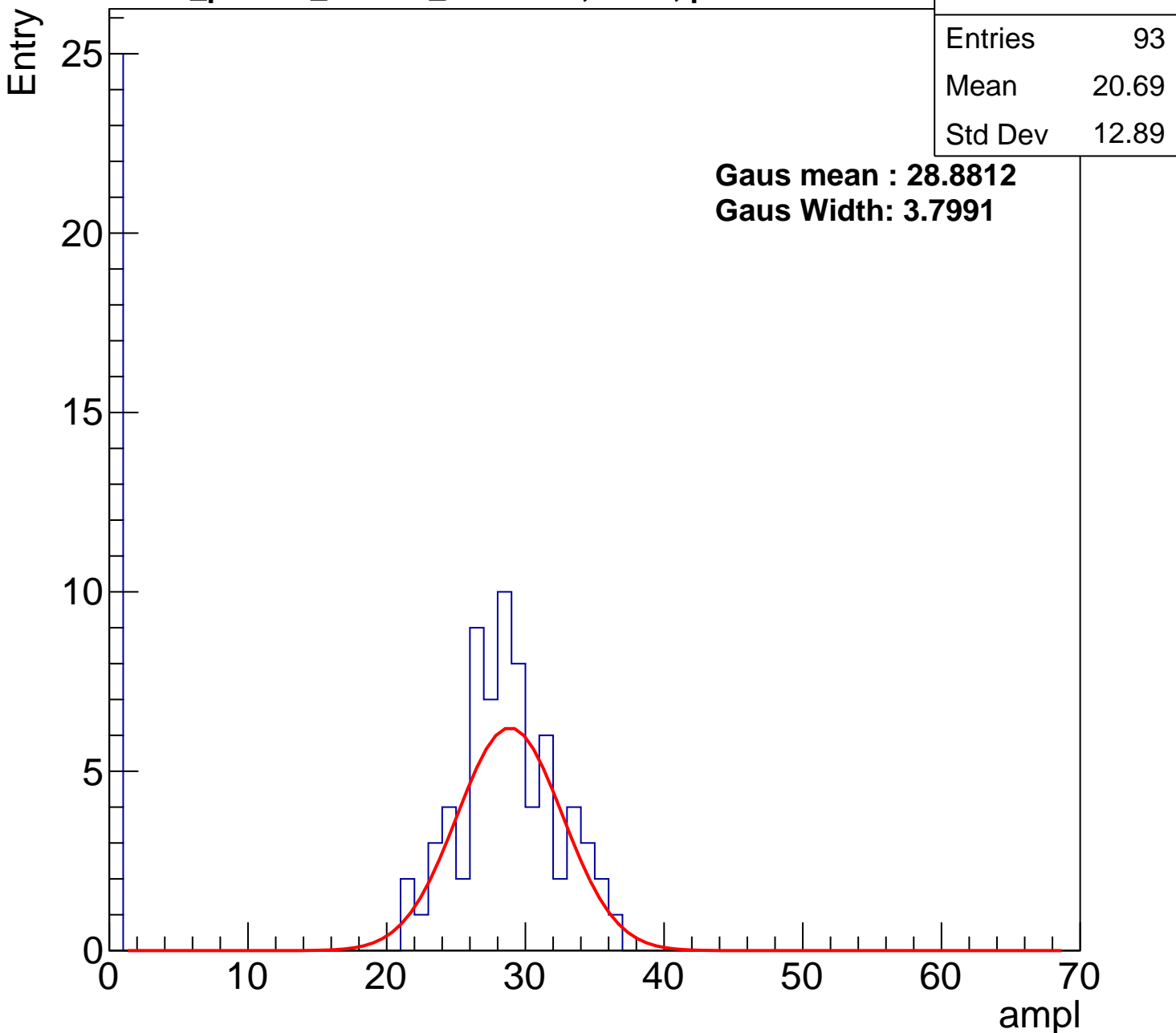
40

50

60

70

ampl



B1L103S, U24-ch47, adc1

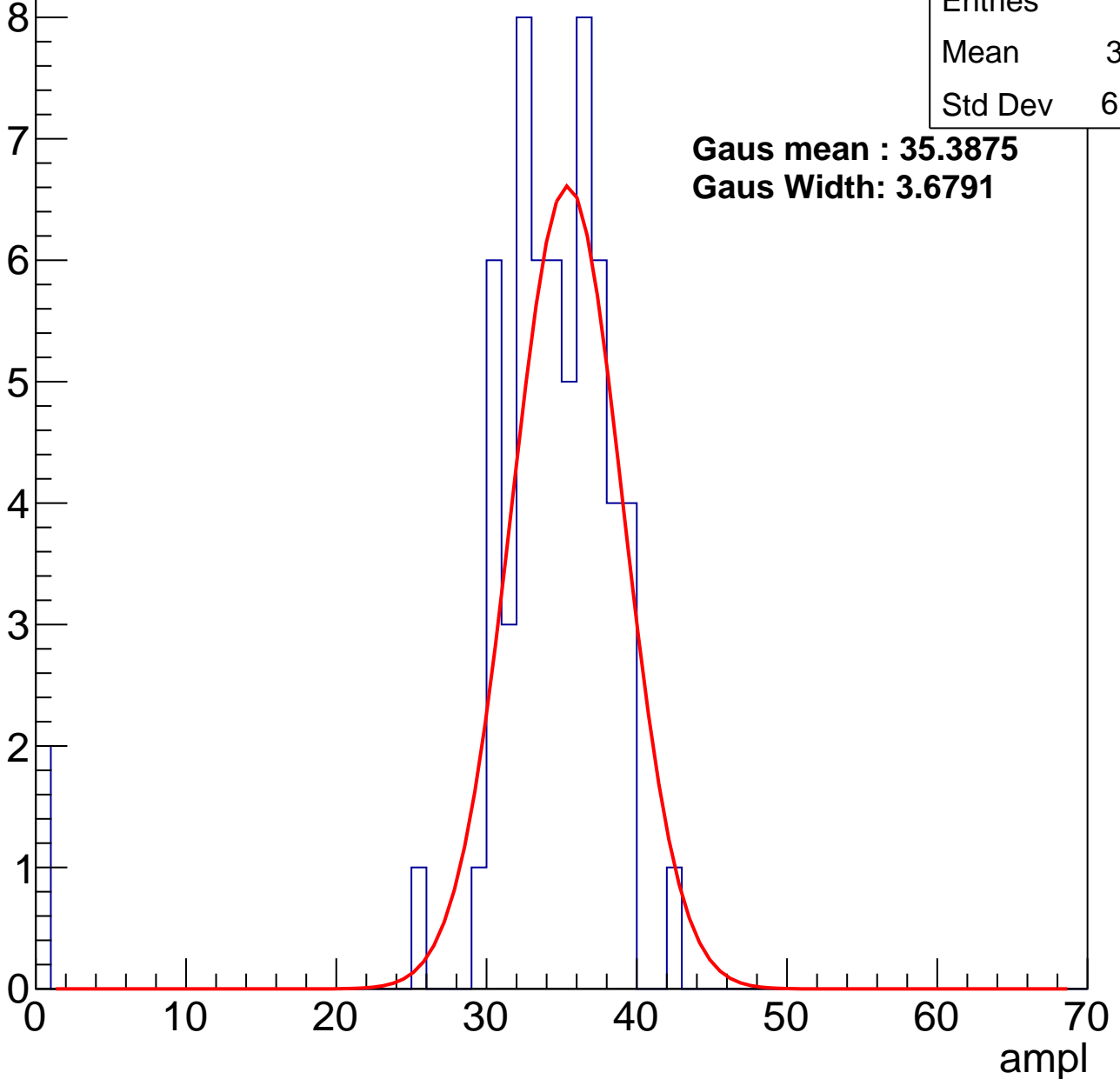
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	33.11
Std Dev	6.836

Gaus mean : 35.3875

Gaus Width: 3.6791



B1L103S, U24-ch47, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	36.51
Std Dev	14.41

Gaus mean : 42.1764

Gaus Width: 3.1419

Entry

12

10

8

6

4

2

0

0

10

20

30

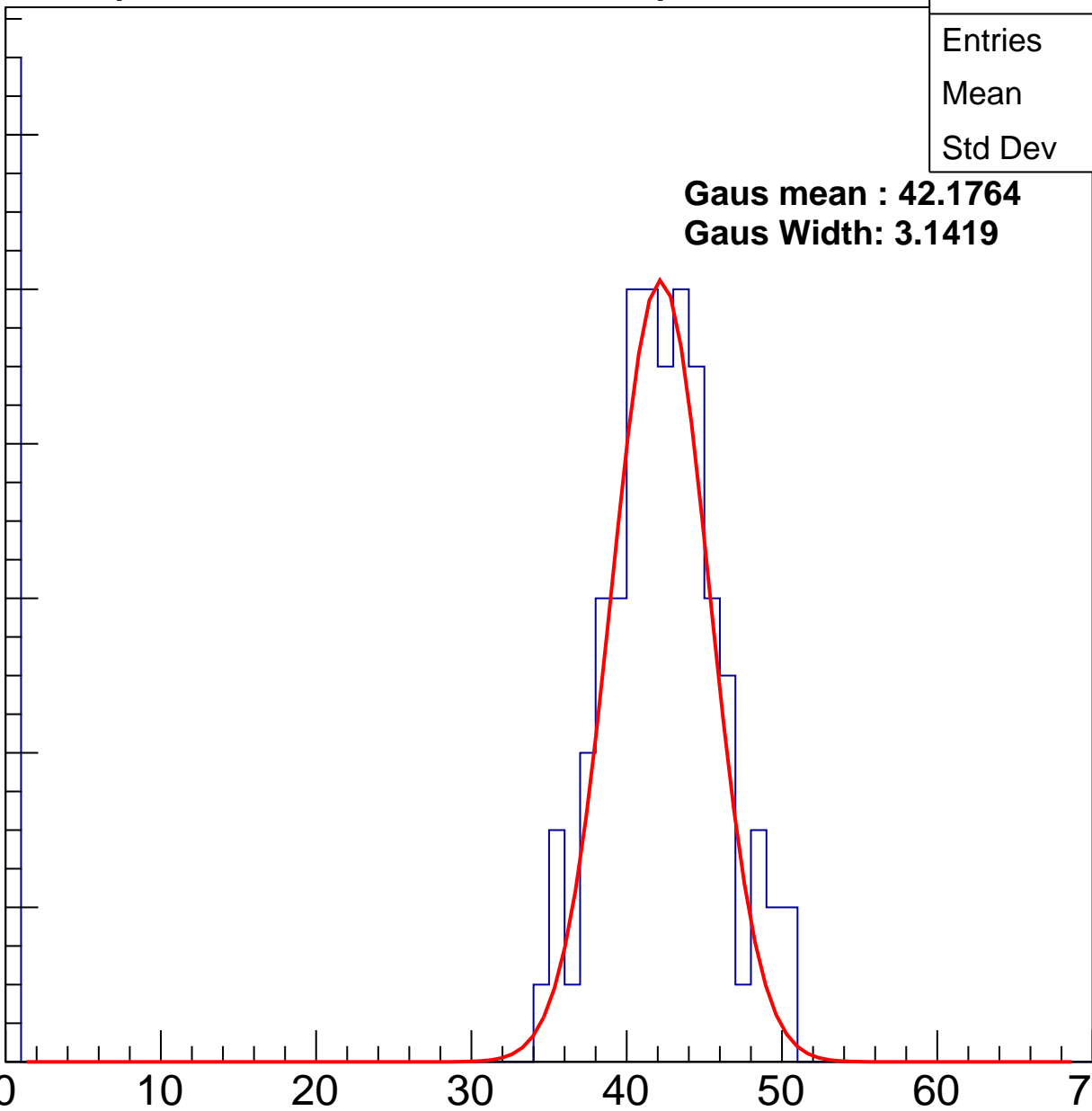
40

50

60

70

ampl

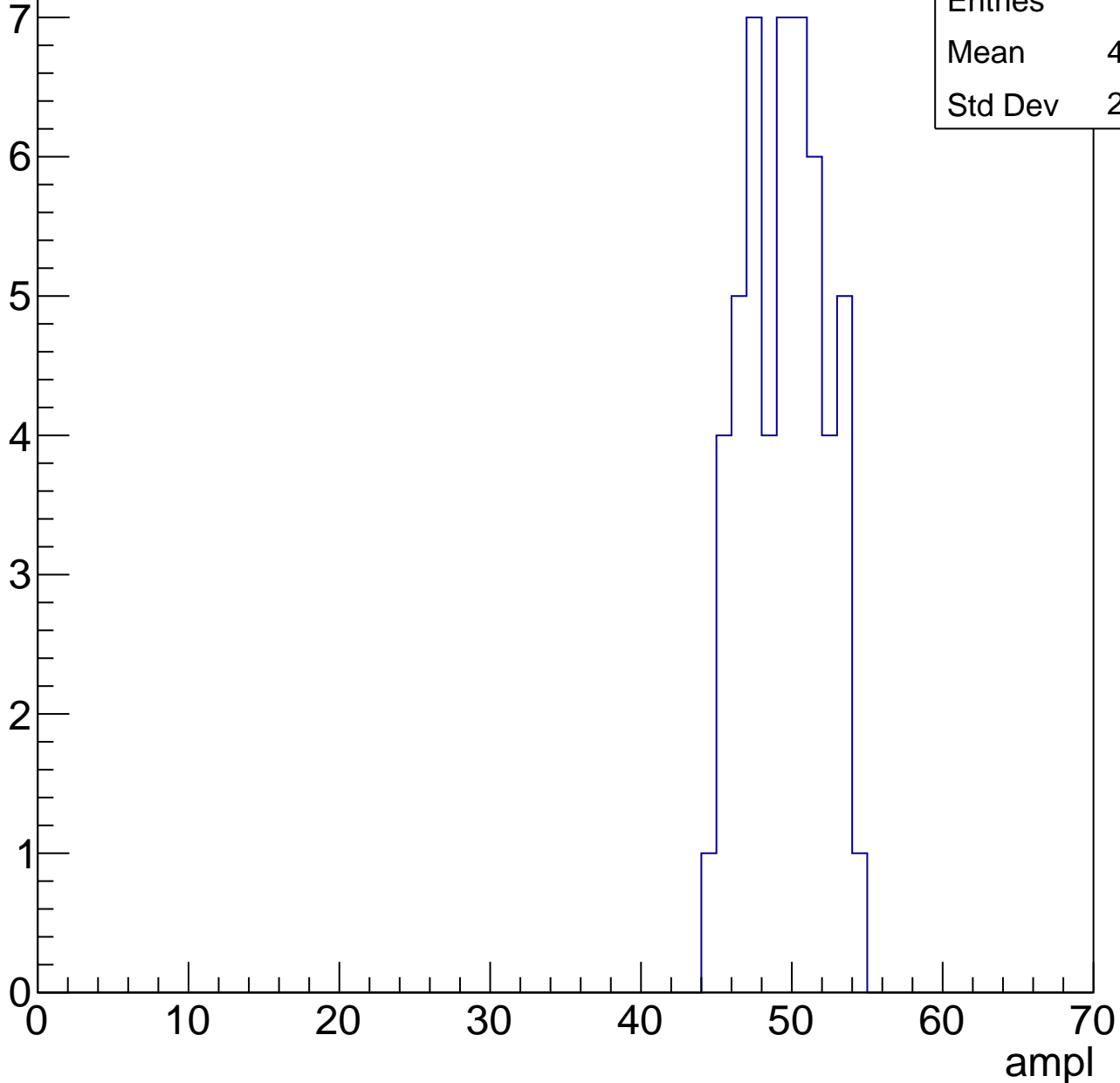


B1L103S, U24-ch47, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

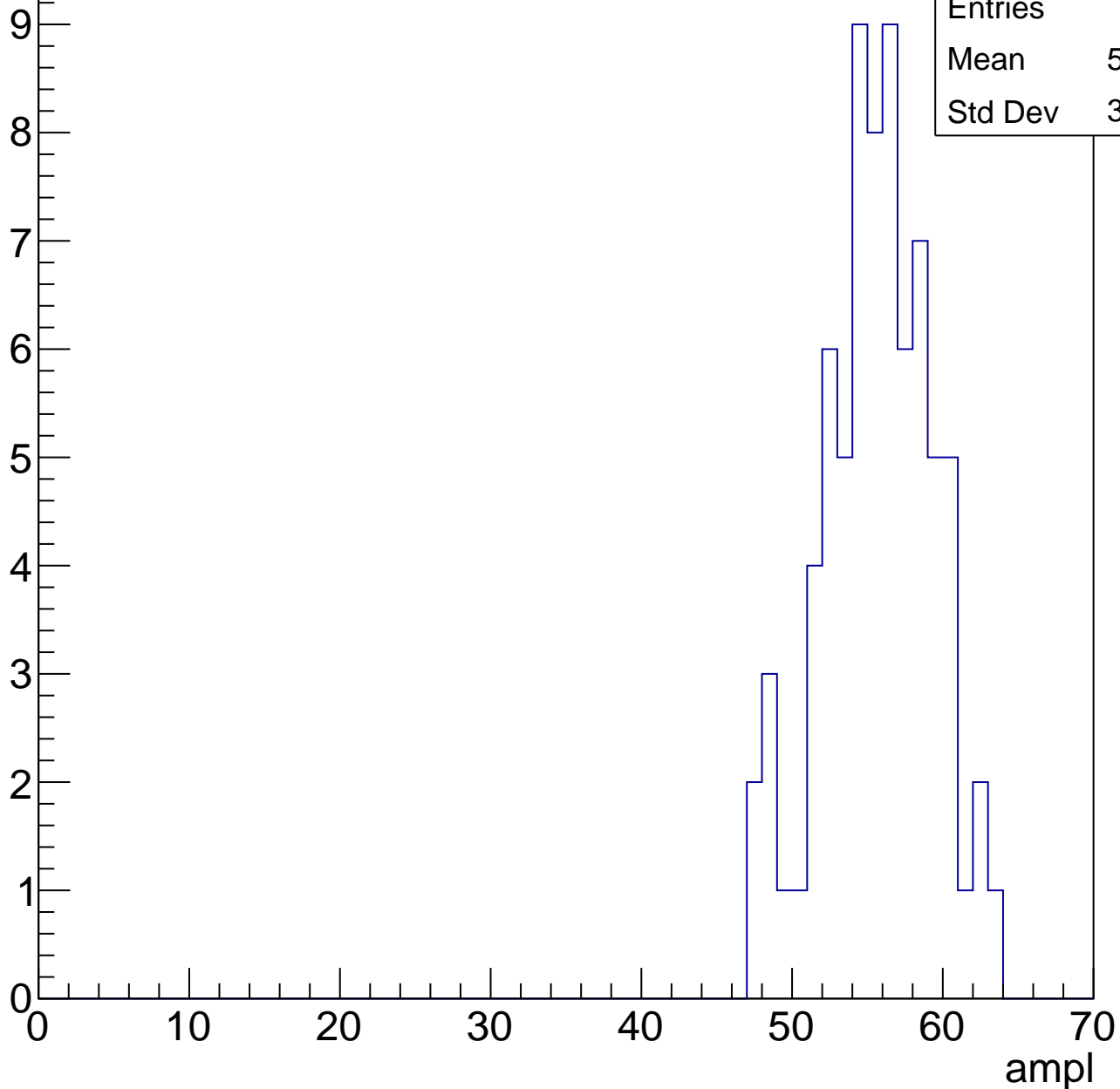
Entries	51
Mean	49.04
Std Dev	2.574



B1L103S, U24-ch47, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

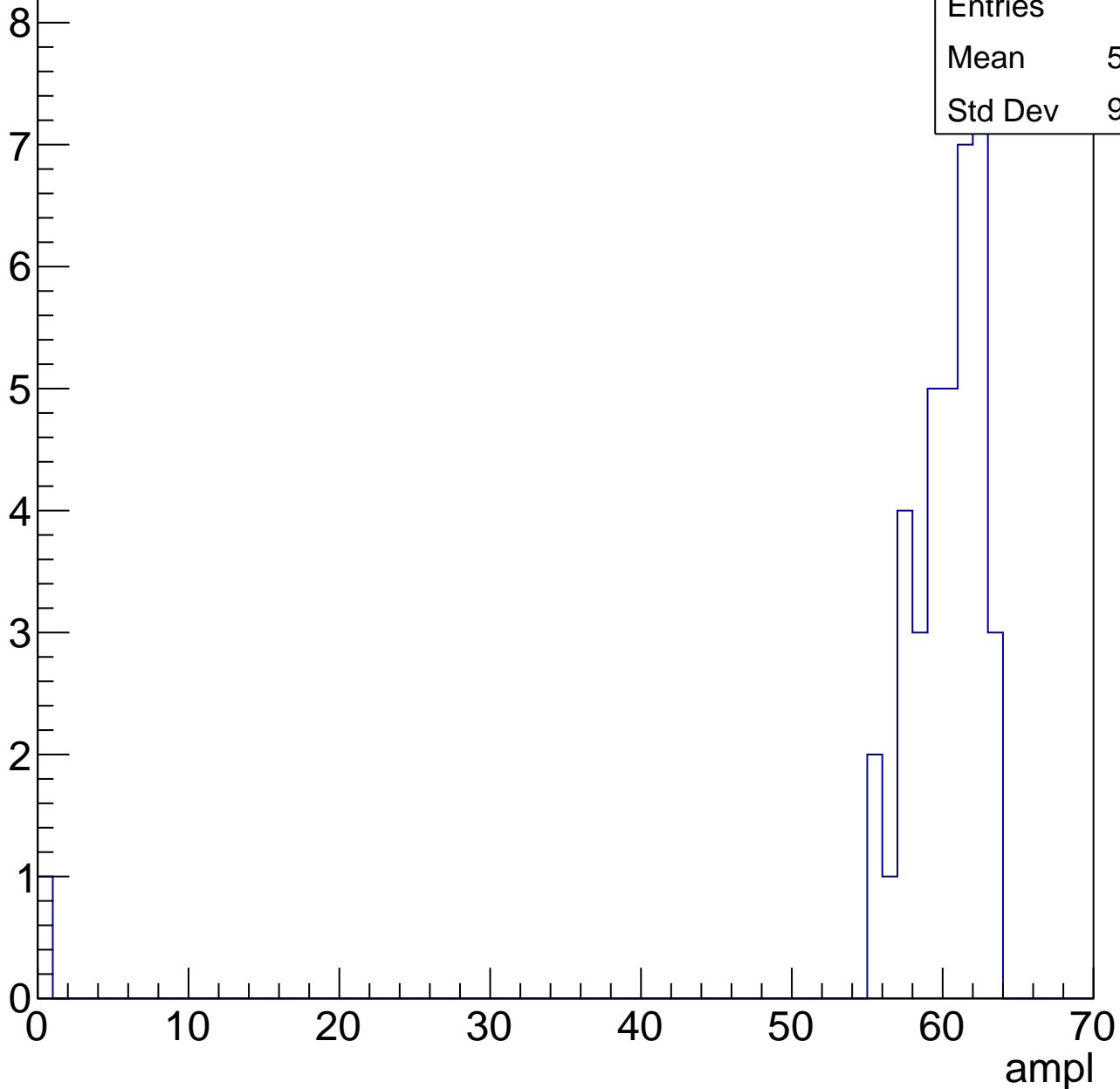


B1L103S, U24-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	58.33
Std Dev	9.709

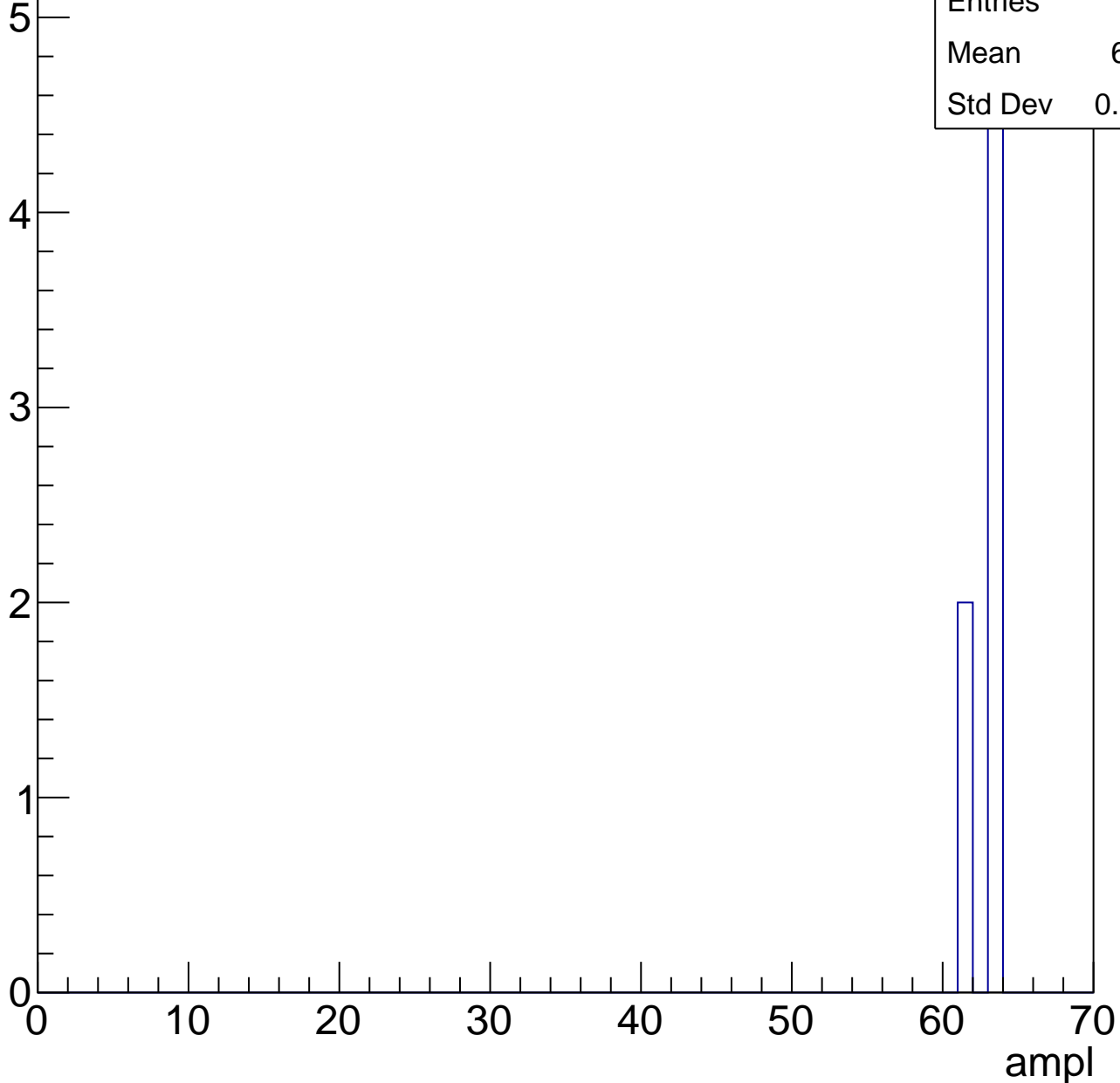


B1L103S, U24-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	7
Mean	62.43
Std Dev	0.9035



B1L103S, U24-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U24-ch48, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	23.17
Std Dev	10.12

Gaus mean : 27.7054

Gaus Width: 4.1769

Entry

12

10

8

6

4

2

0

0

10

20

30

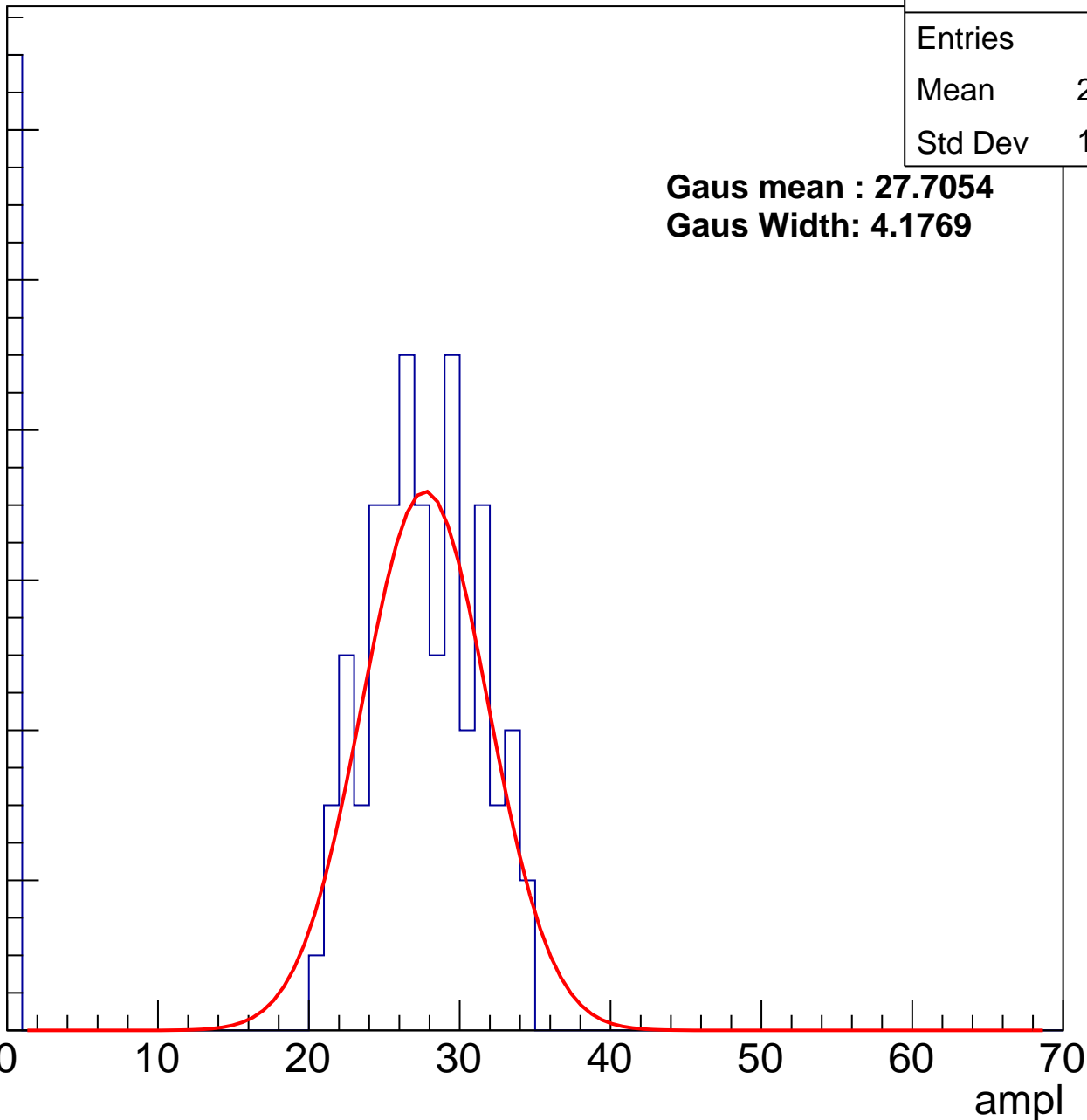
40

50

60

70

ampl



B1L103S, U24-ch48, adc1

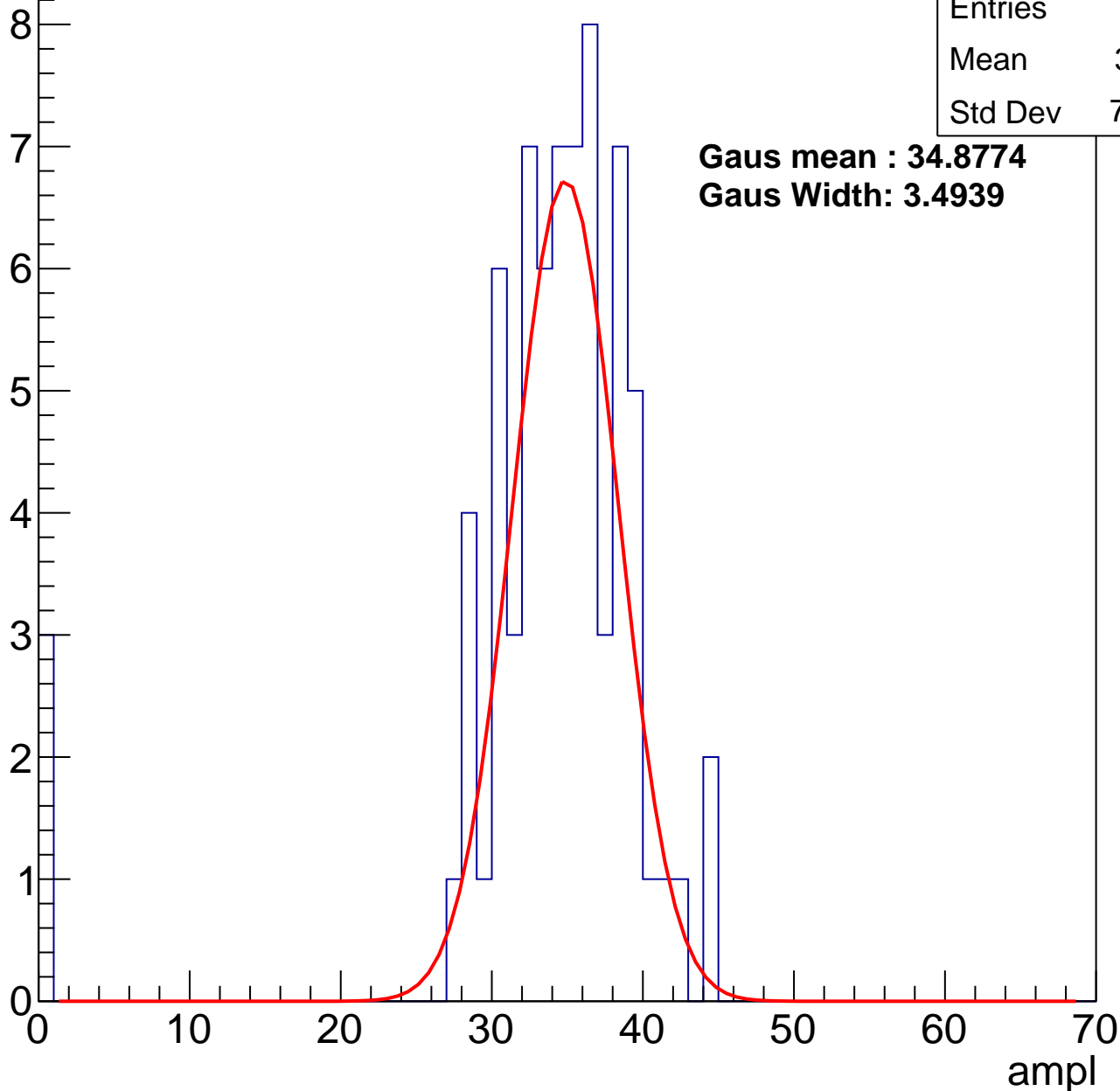
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	33.11
Std Dev	7.796

Gaus mean : 34.8774

Gaus Width: 3.4939



B1L103S, U24-ch48, adc2

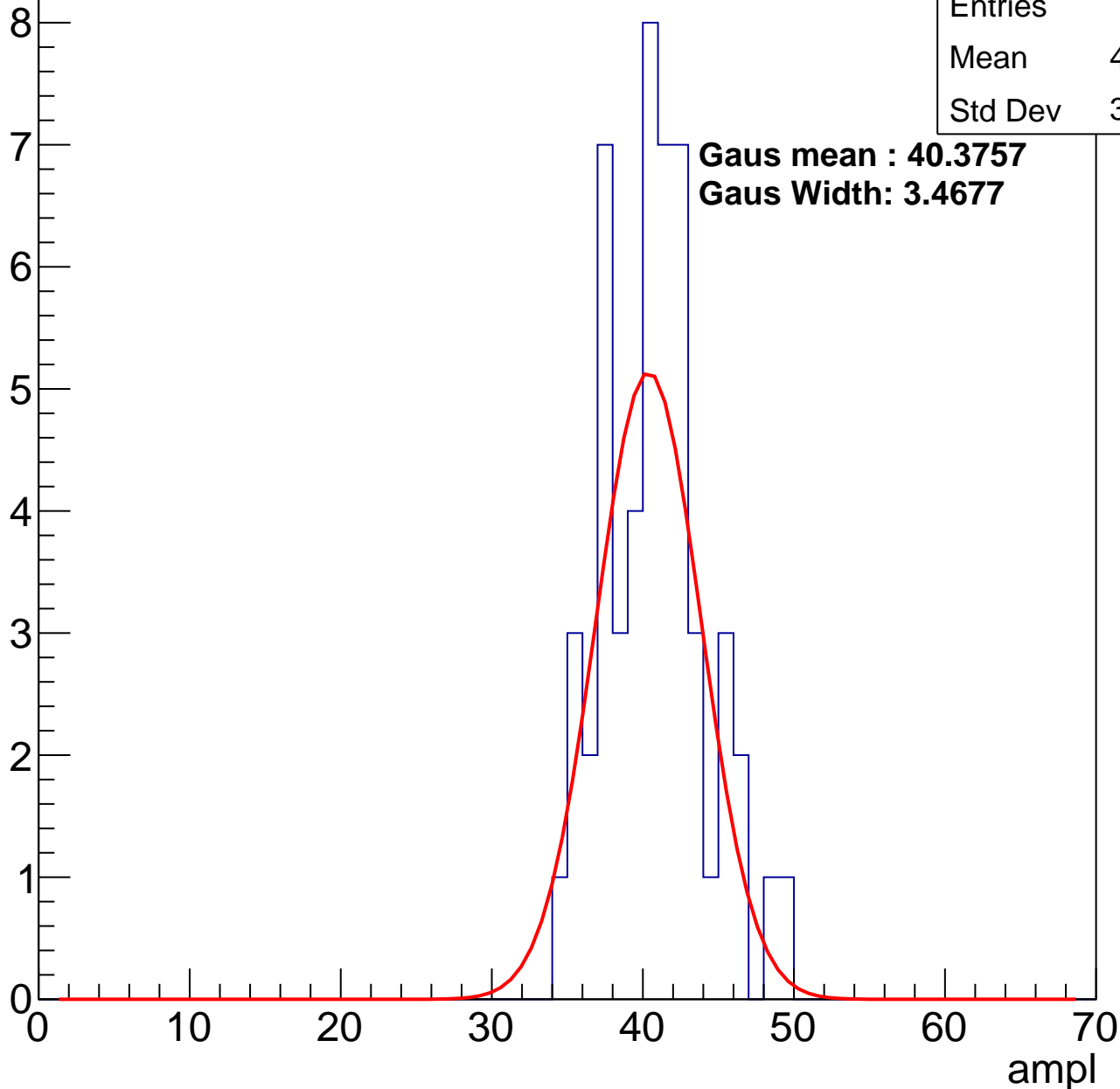
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	40.34
Std Dev	3.325

Gaus mean : 40.3757

Gaus Width: 3.4677

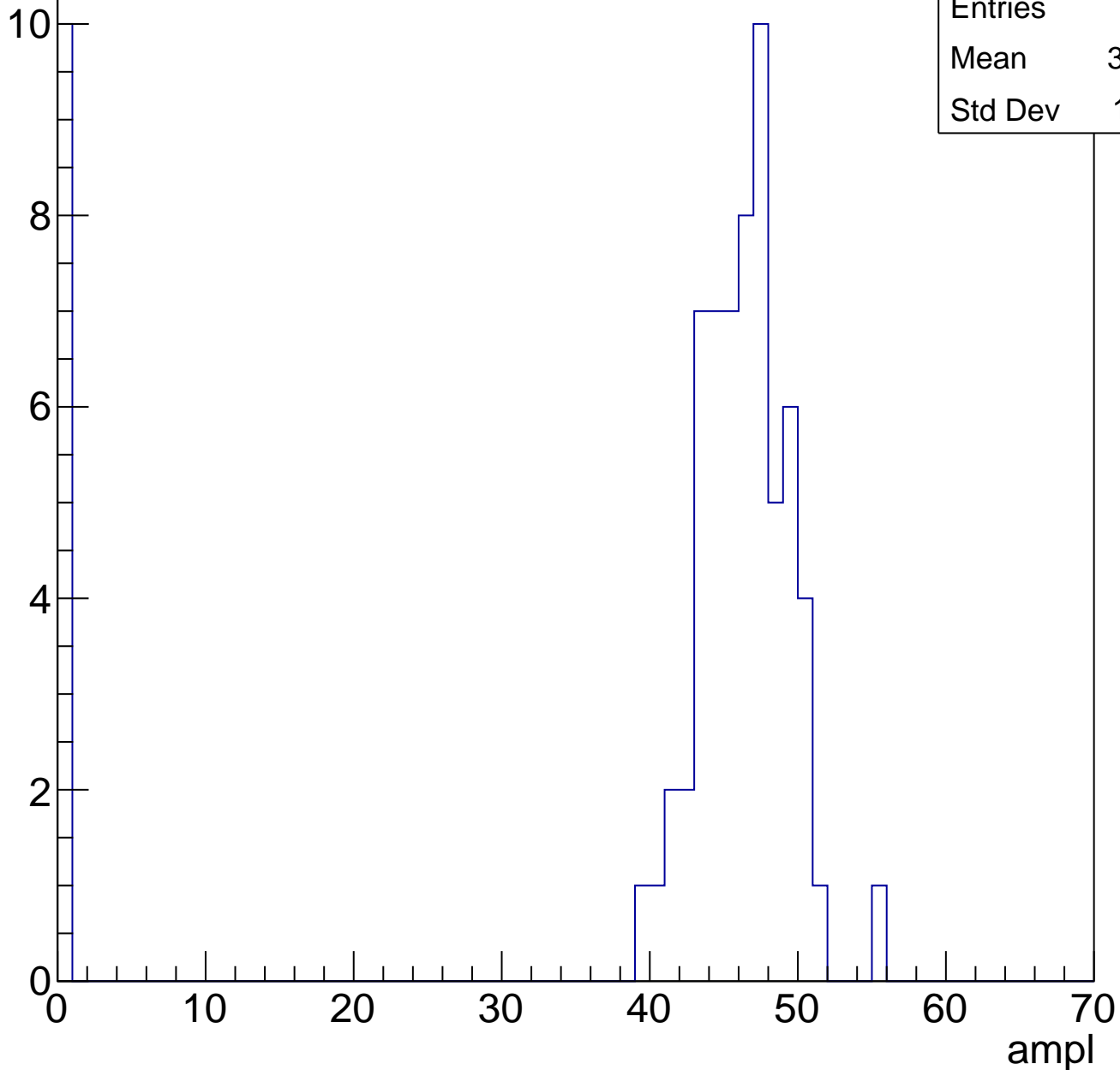


B1L103S, U24-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	39.54
Std Dev	16.11

Entry

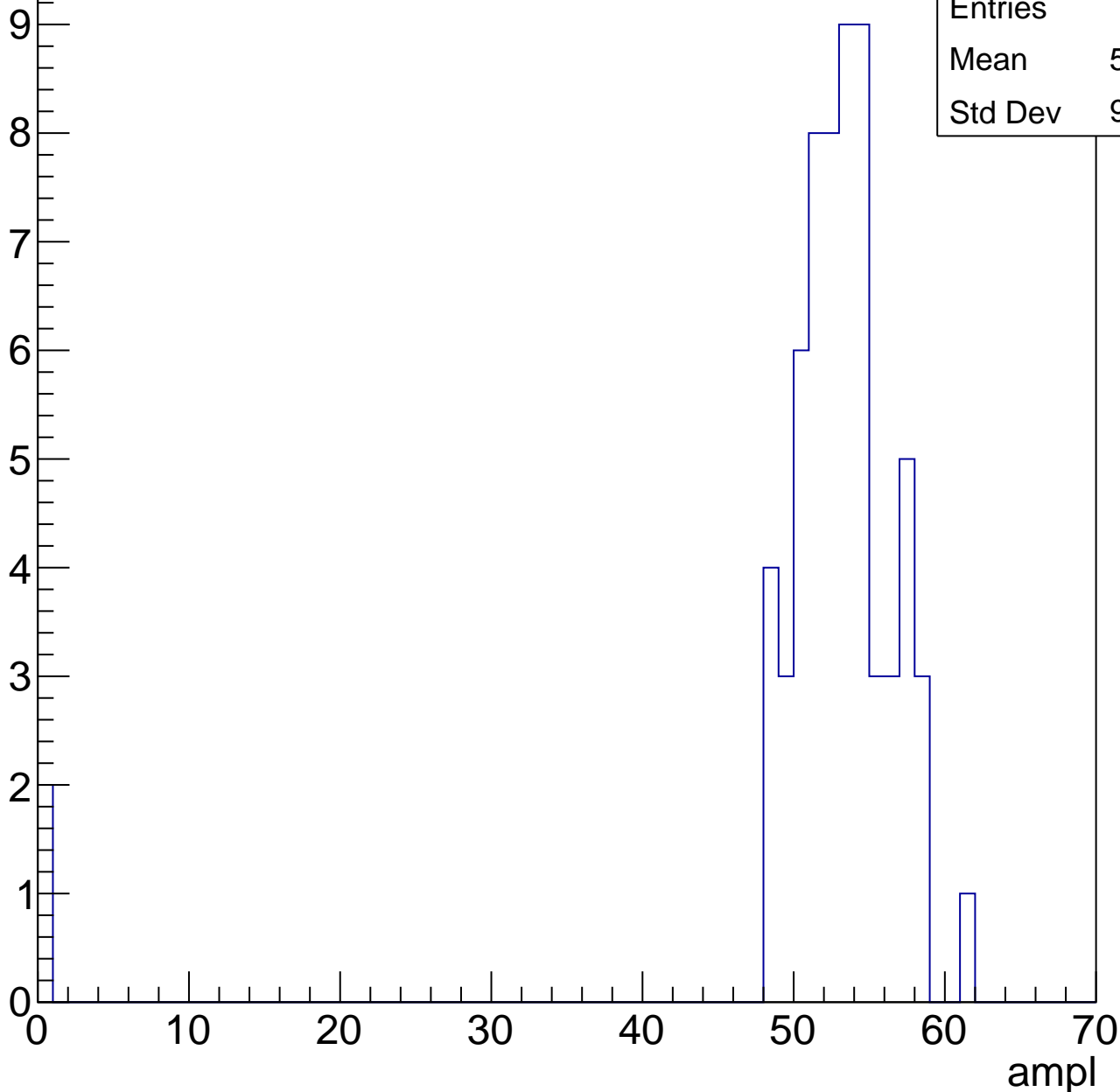


B1L103S, U24-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	51.23
Std Dev	9.624

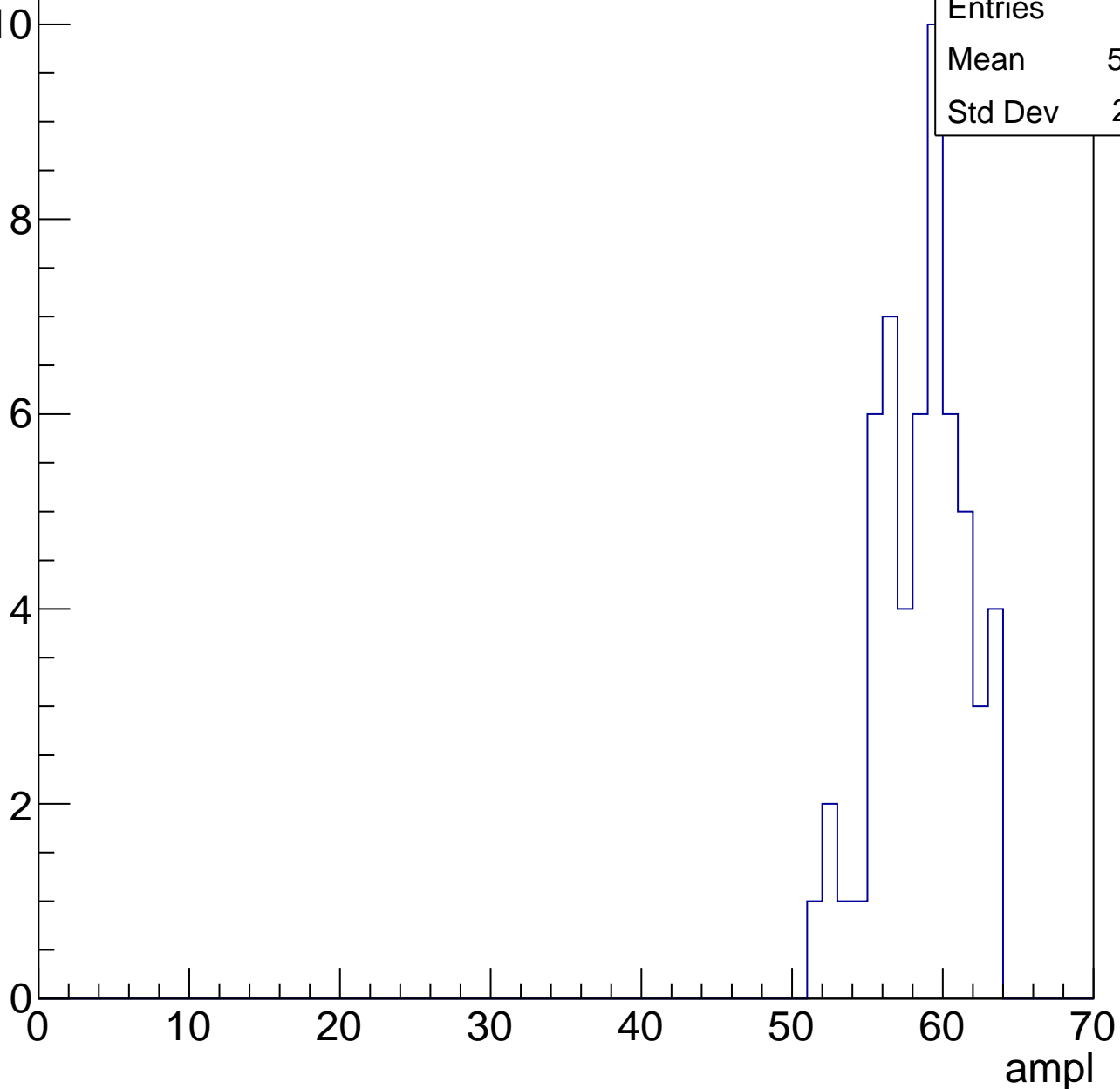


B1L103S, U24-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.09
Std Dev	2.911

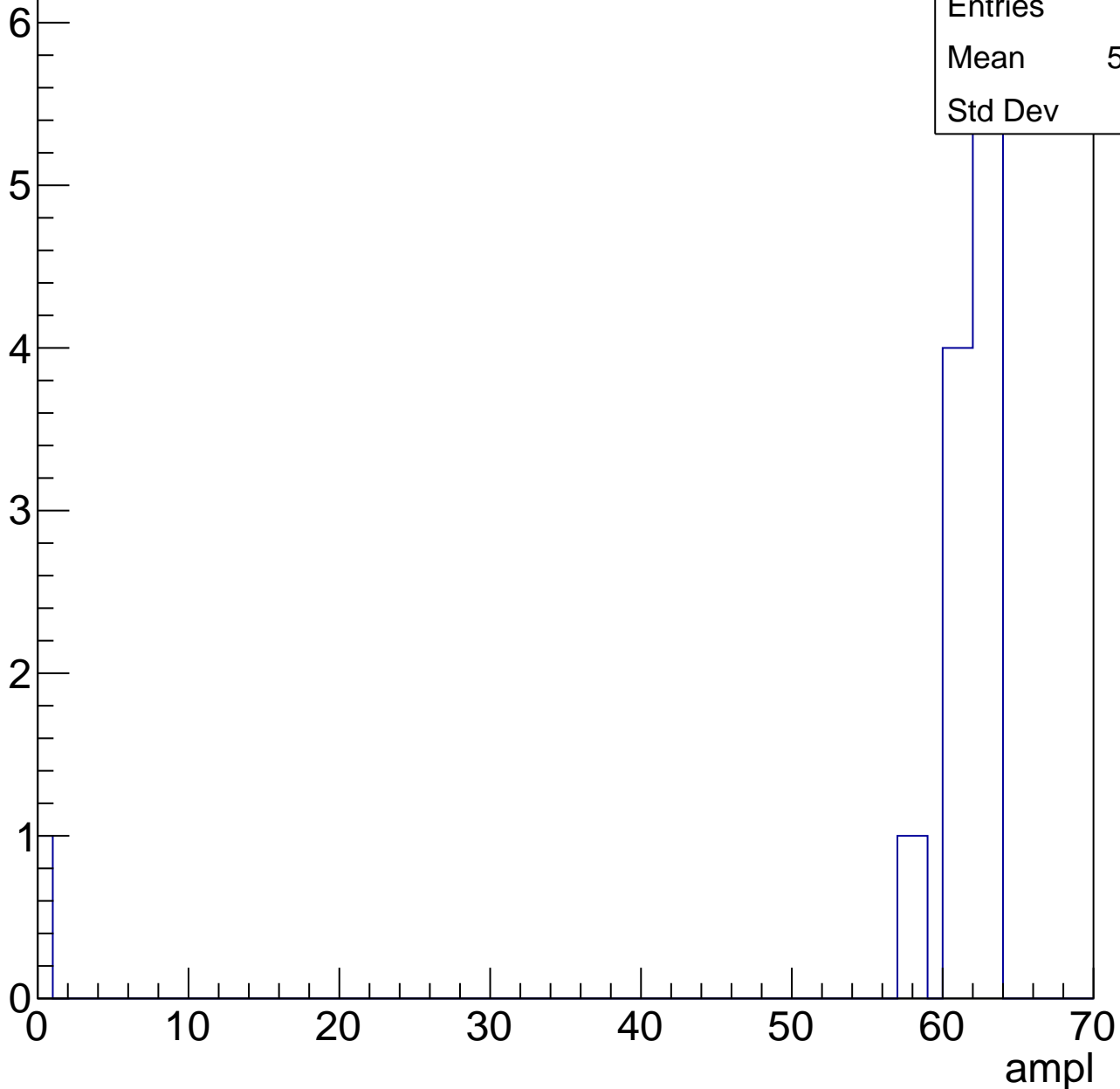


B1L103S, U24-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.65
Std Dev	12.6



B1L103S, U24-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



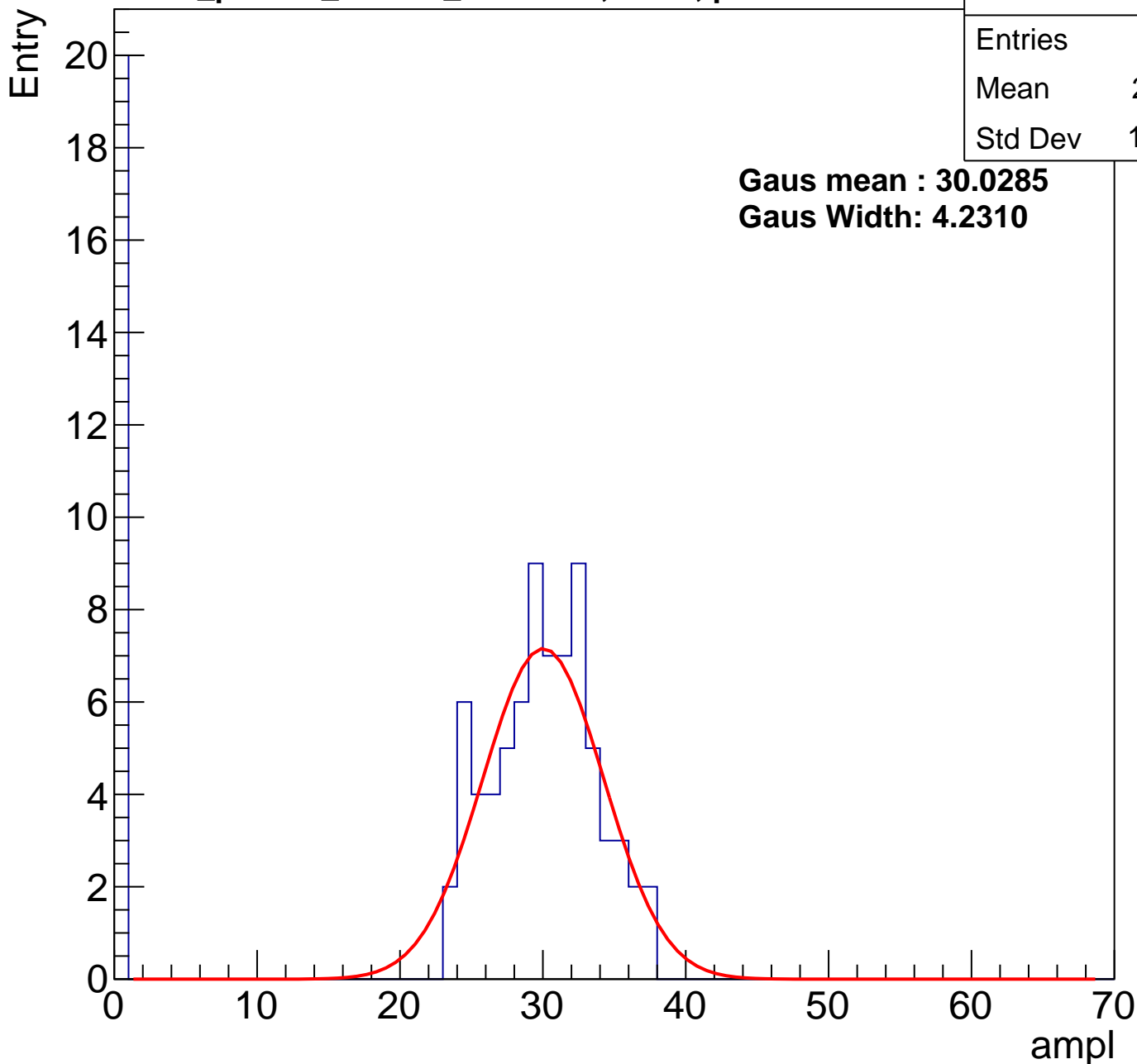
B1L103S, U24-ch49, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	23.31
Std Dev	12.52

Gaus mean : 30.0285

Gaus Width: 4.2310



B1L103S, U24-ch49, adc1

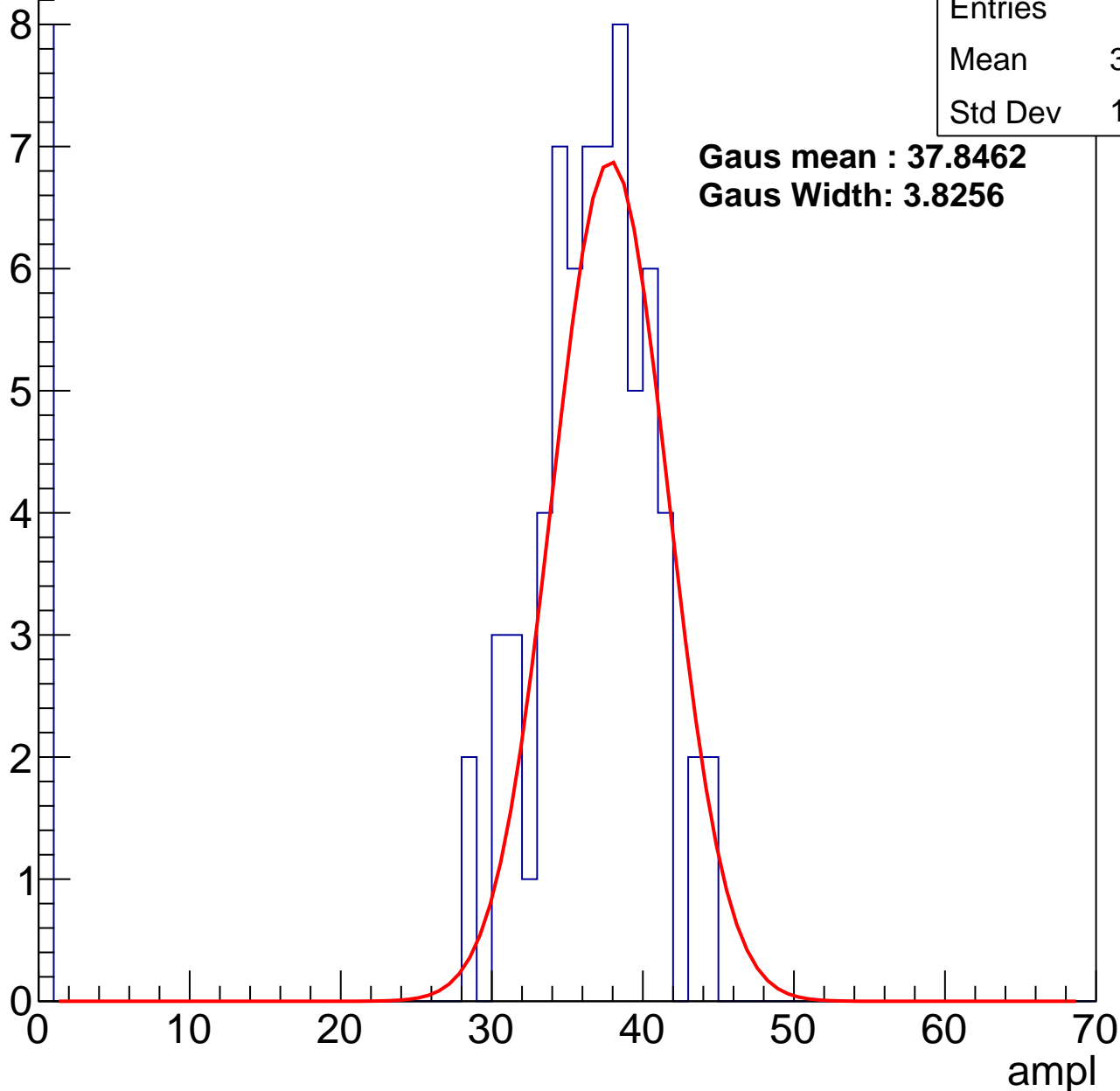
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	32.52
Std Dev	11.75

Gaus mean : 37.8462

Gaus Width: 3.8256



B1L103S, U24-ch49, adc2

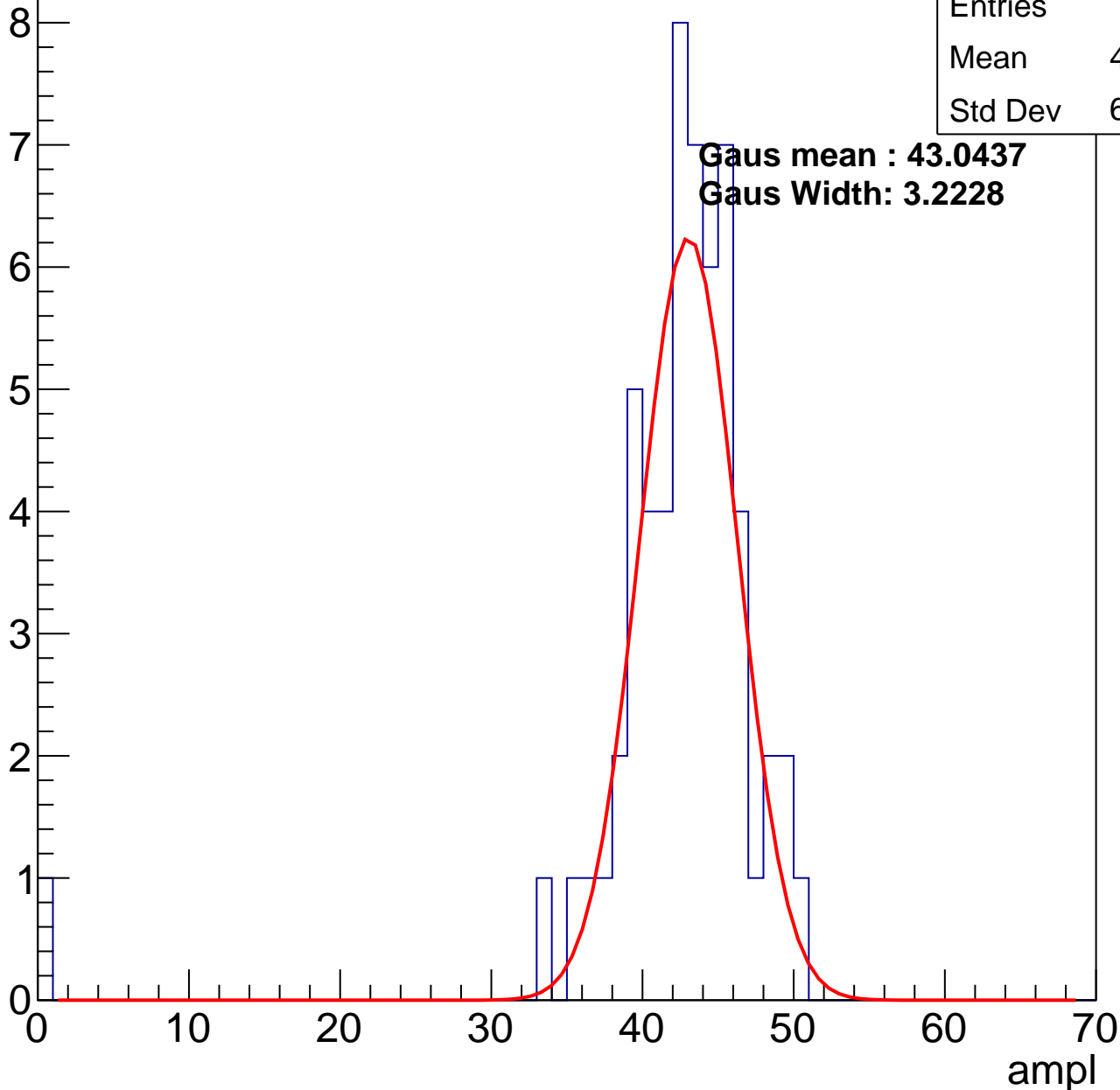
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	41.84
Std Dev	6.523

Gaus mean : 43.0437

Gaus Width: 3.2228

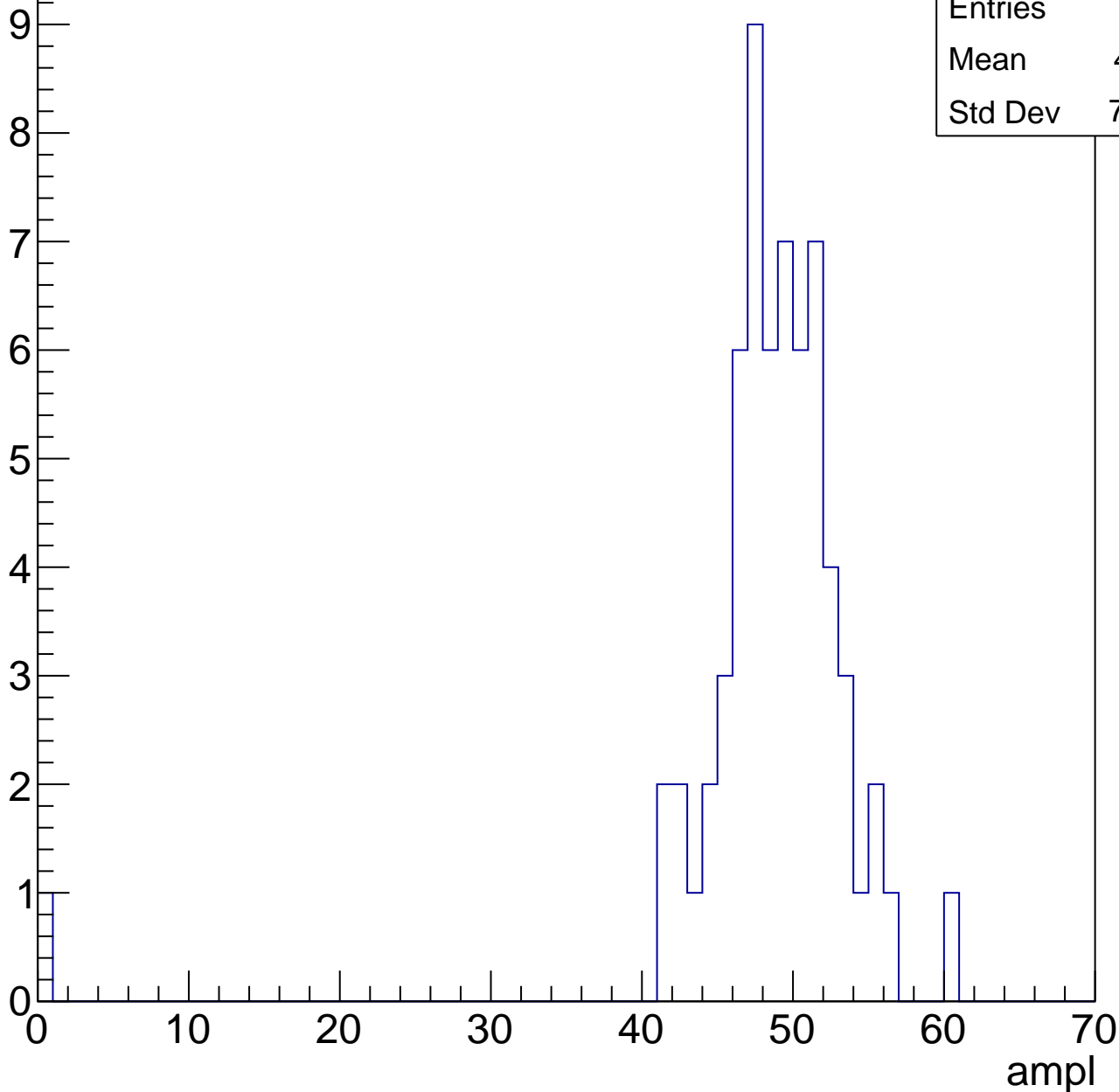


B1L103S, U24-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	47.91
Std Dev	7.033

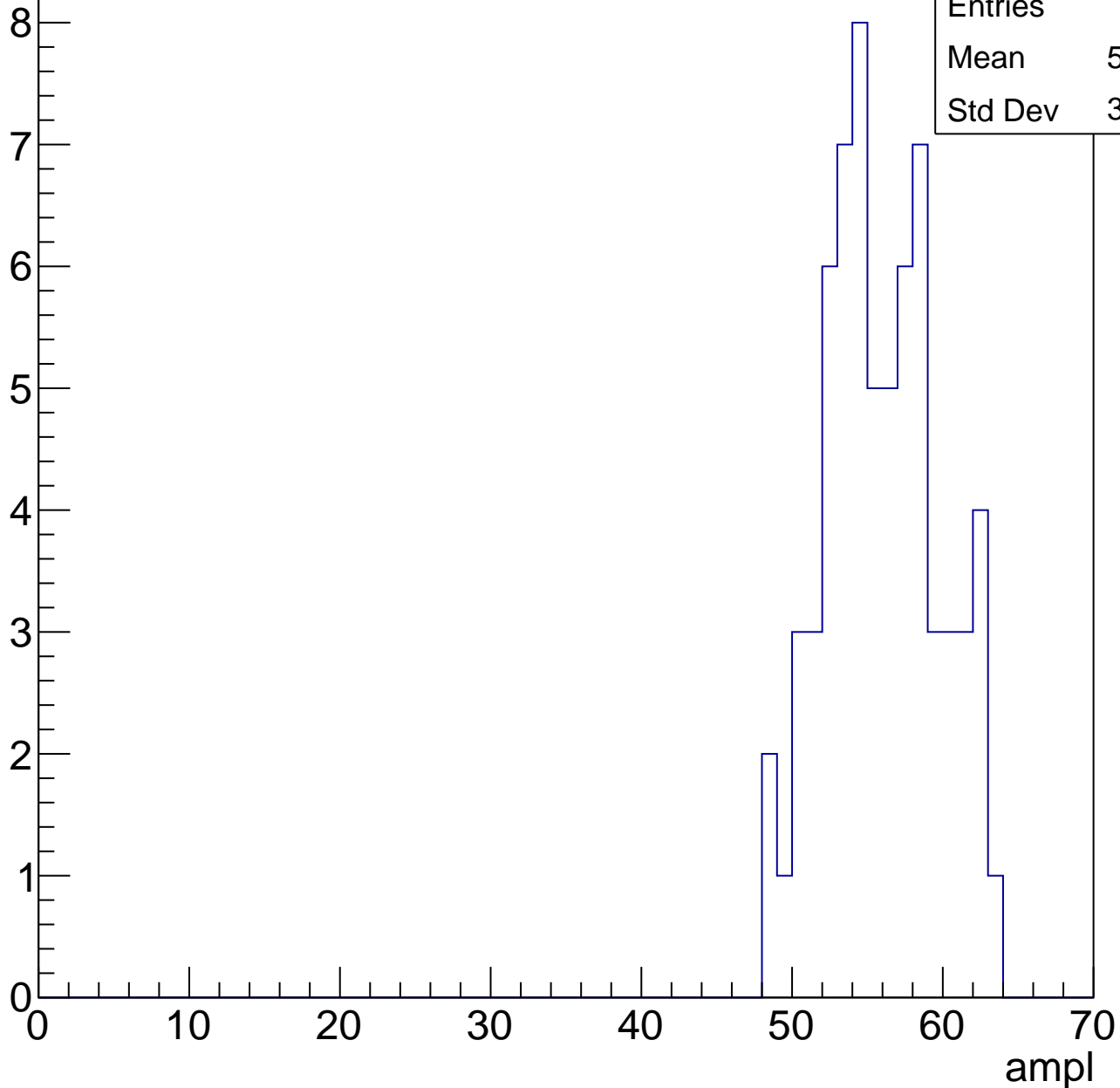


B1L103S, U24-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	55.48
Std Dev	3.695

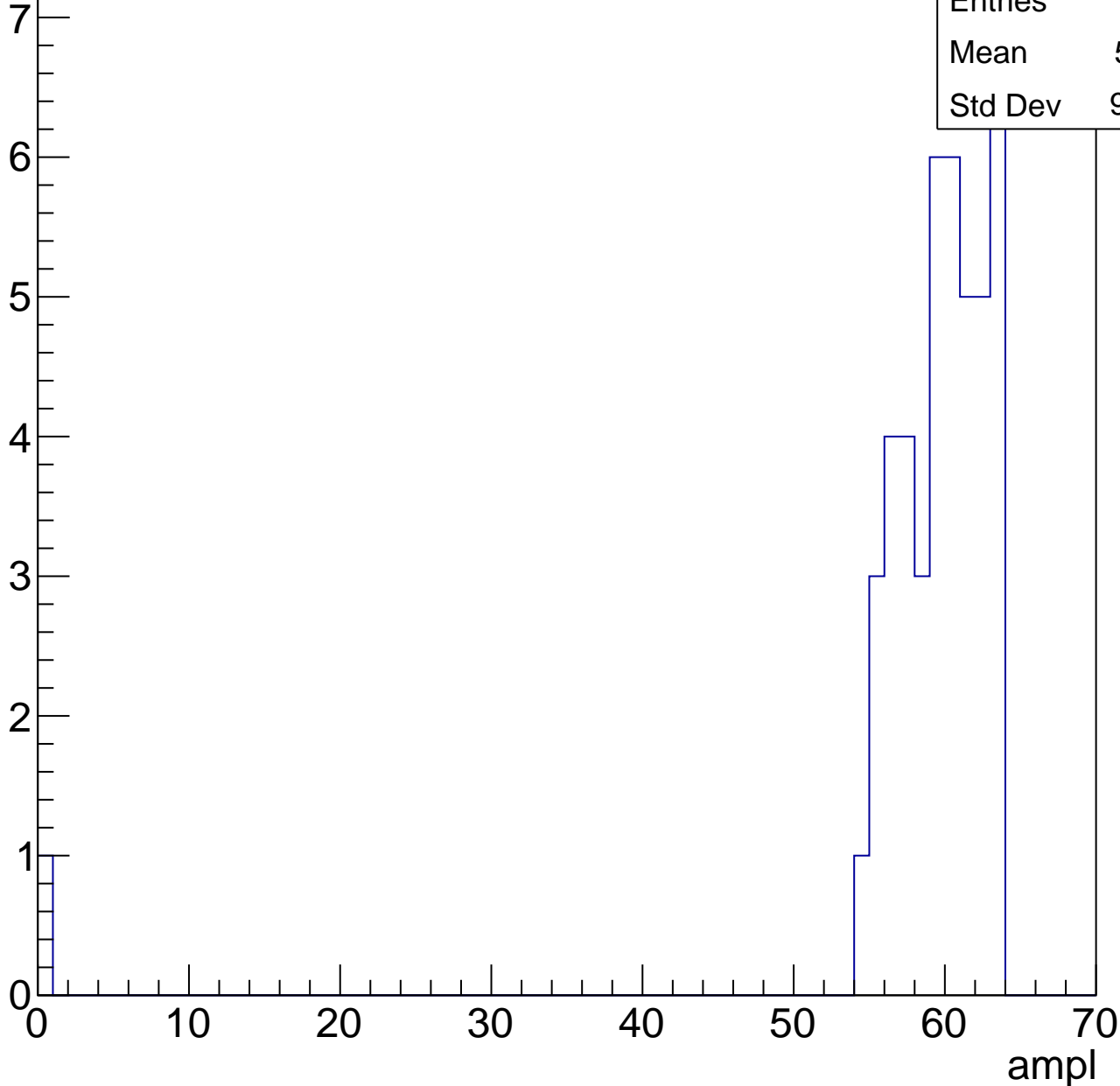


B1L103S, U24-ch49, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

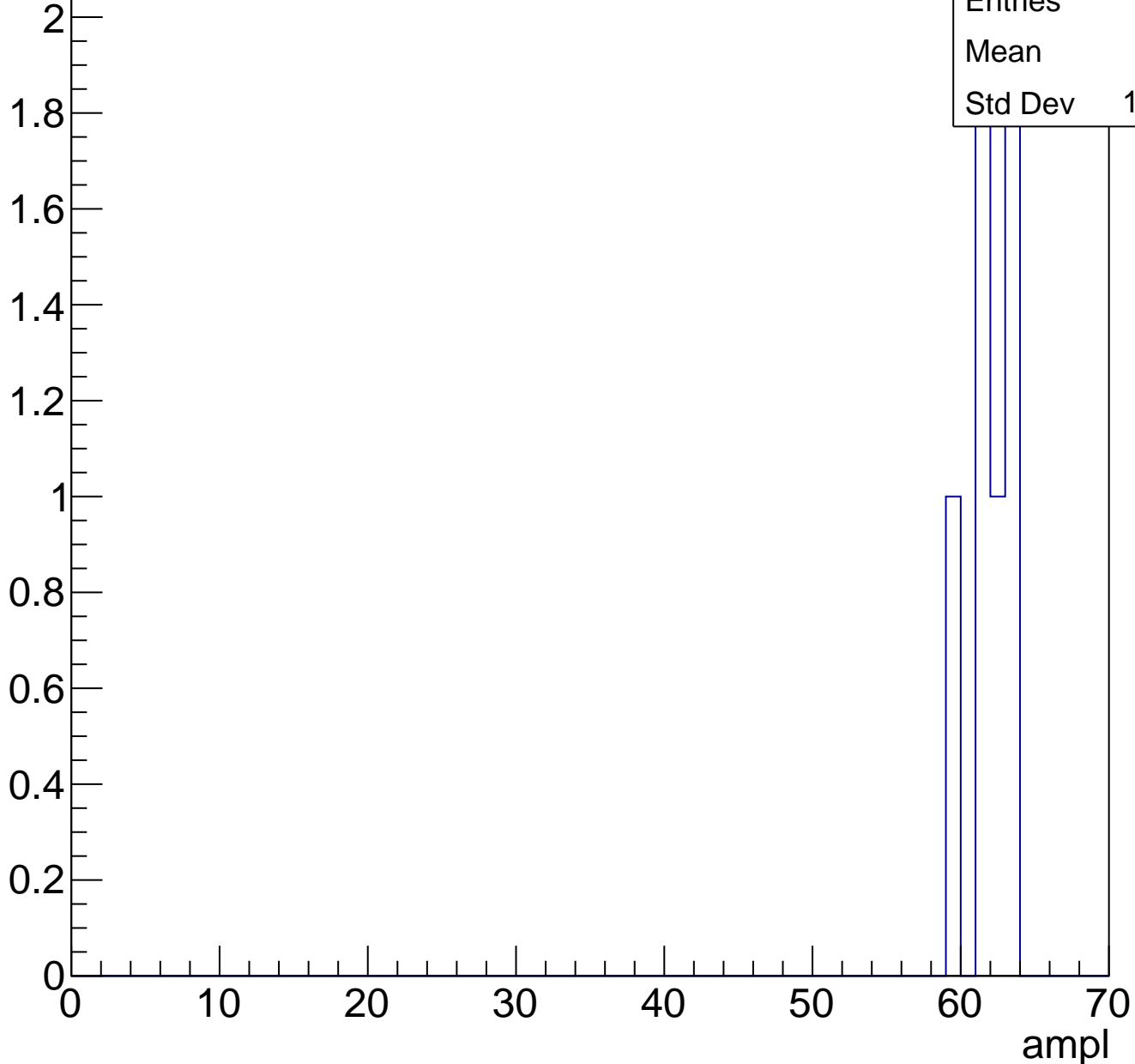
Entries	45
Mean	58.11
Std Dev	9.137



B1L103S, U24-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch50, adc0

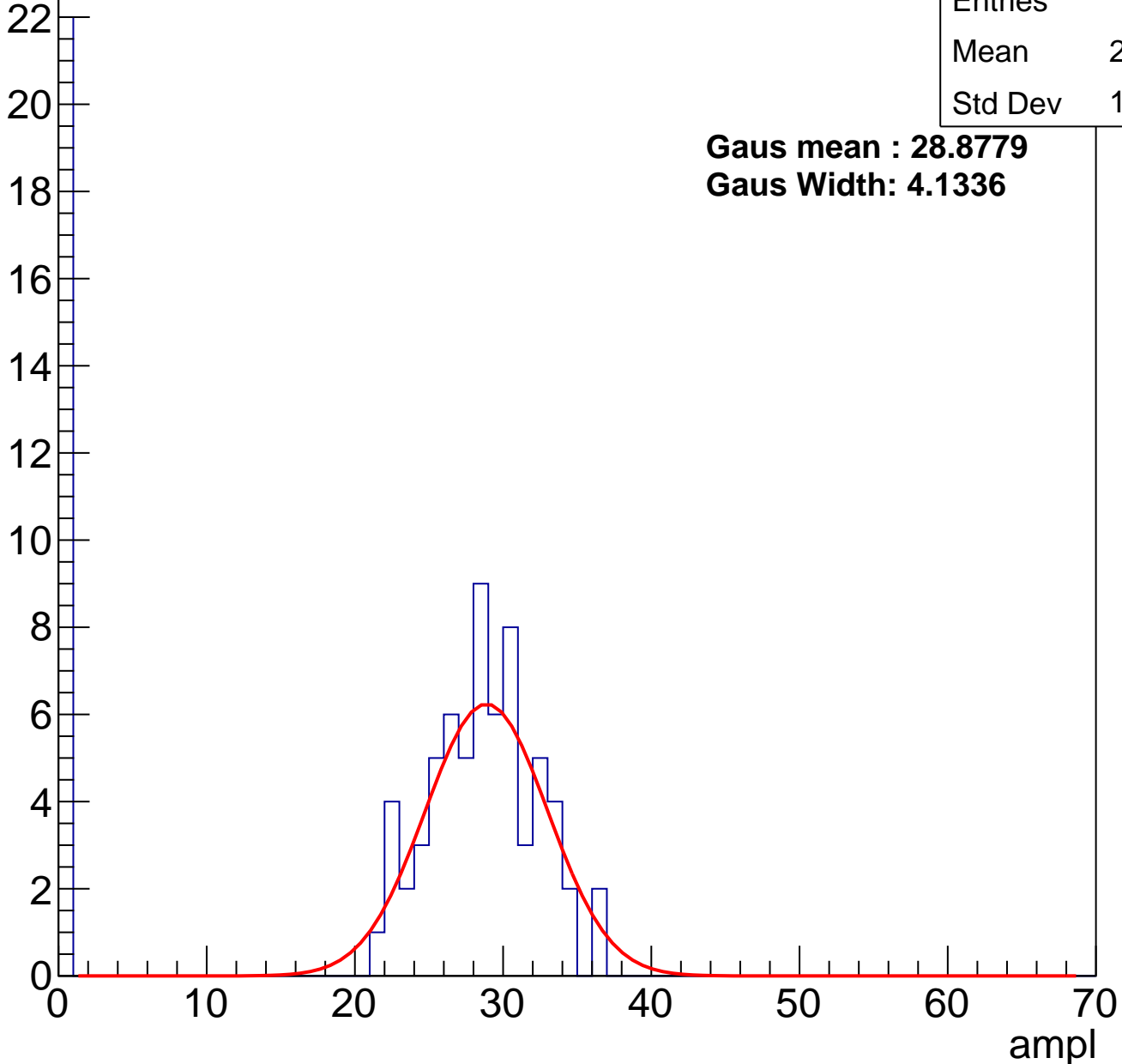
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	21.08
Std Dev	12.64

Gaus mean : 28.8779

Gaus Width: 4.1336

Entry



B1L103S, U24-ch50, adc1

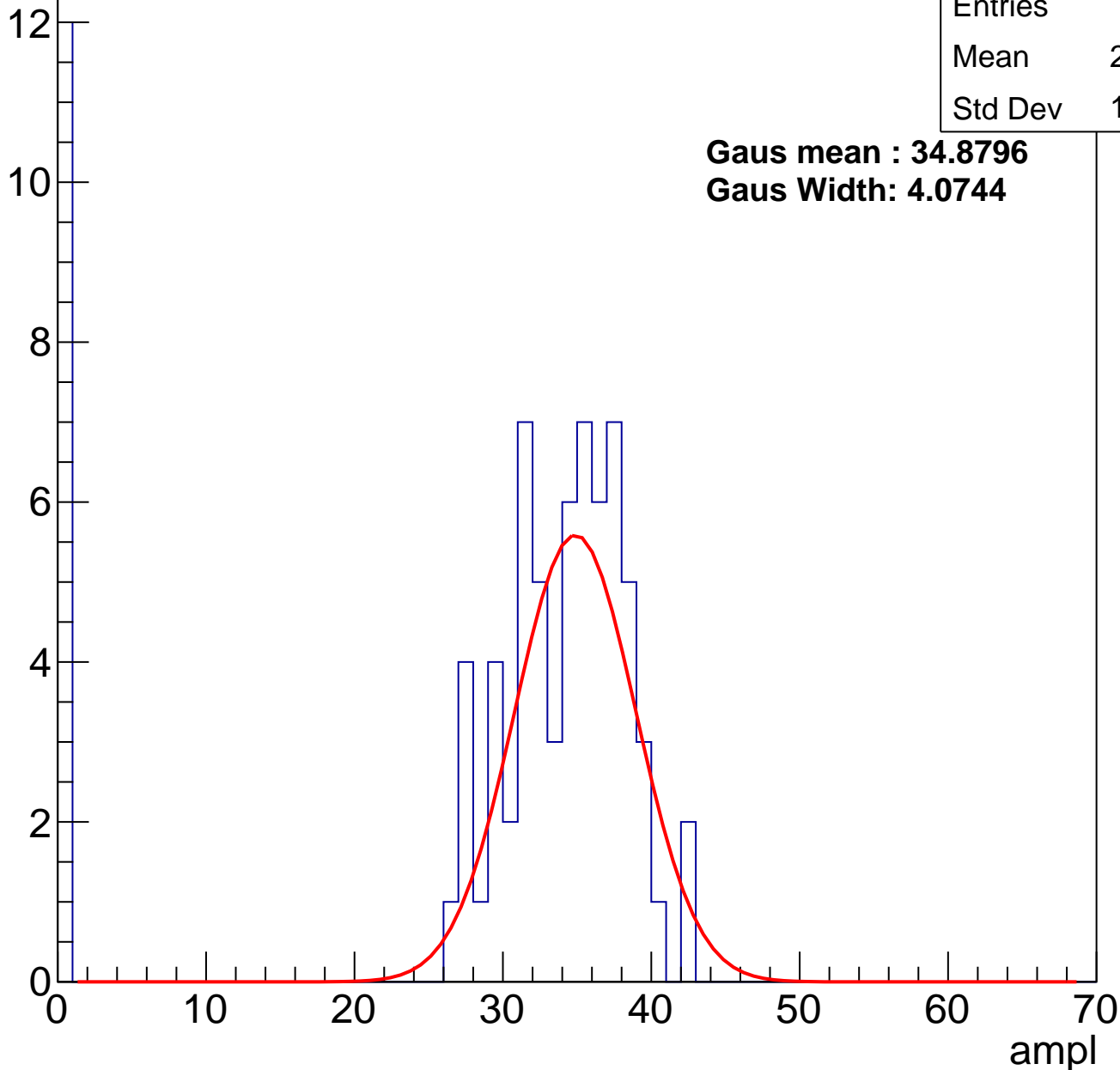
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	28.54
Std Dev	12.84

Gaus mean : 34.8796

Gaus Width: 4.0744

Entry



B1L103S, U24-ch50, adc2

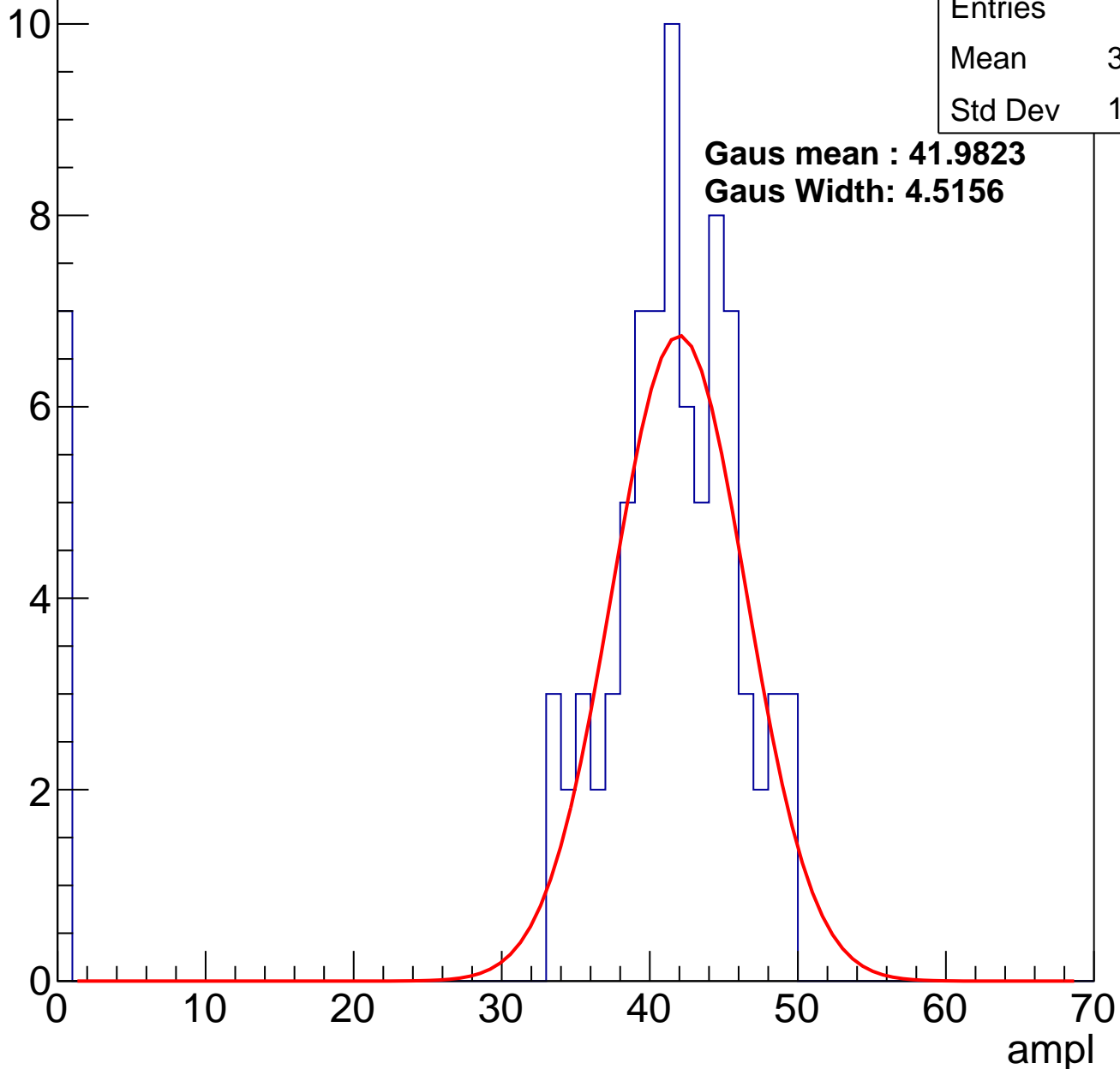
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	37.97
Std Dev	11.93

Gaus mean : 41.9823

Gaus Width: 4.5156

Entry

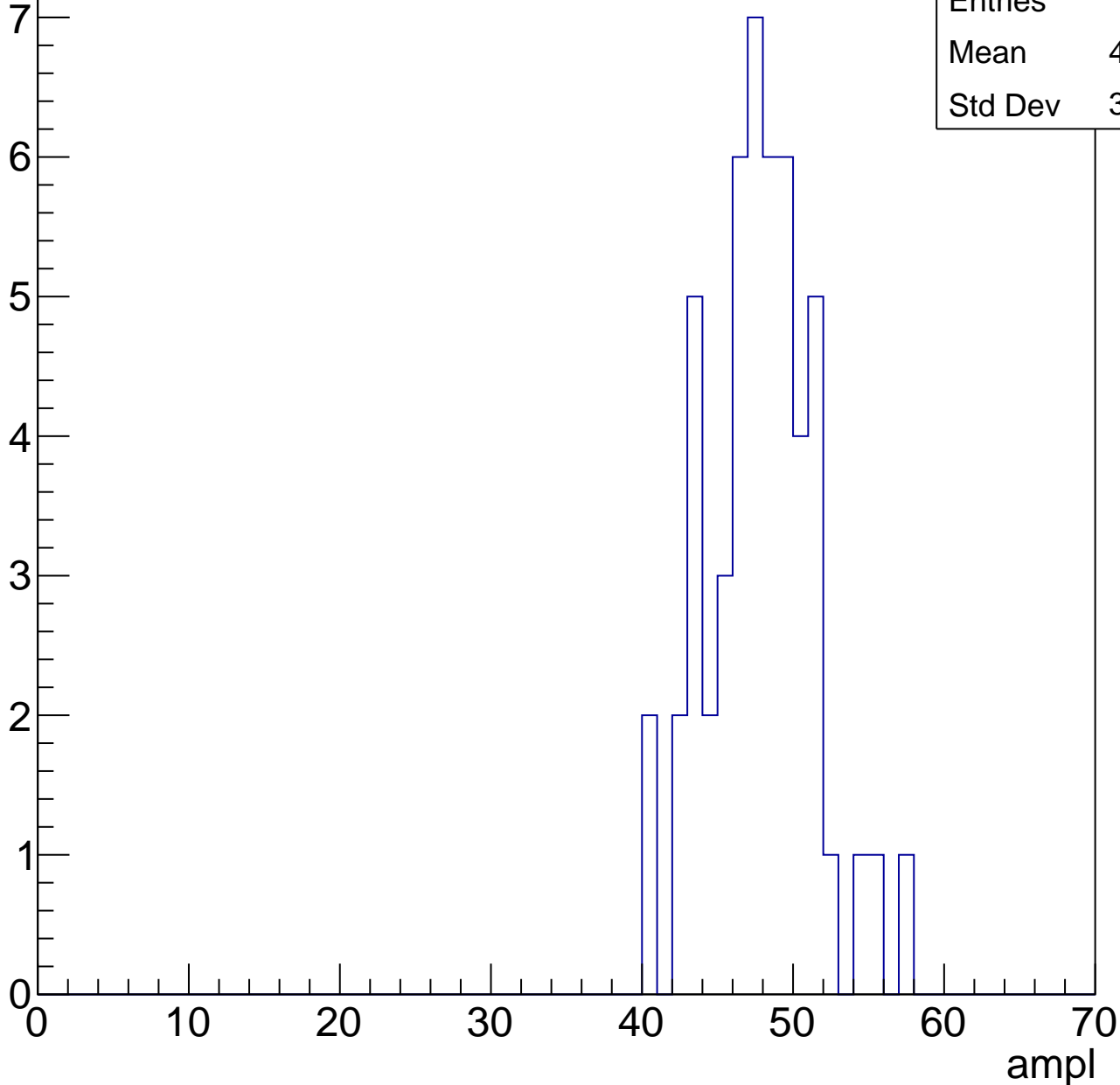


B1L103S, U24-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	47.35
Std Dev	3.513

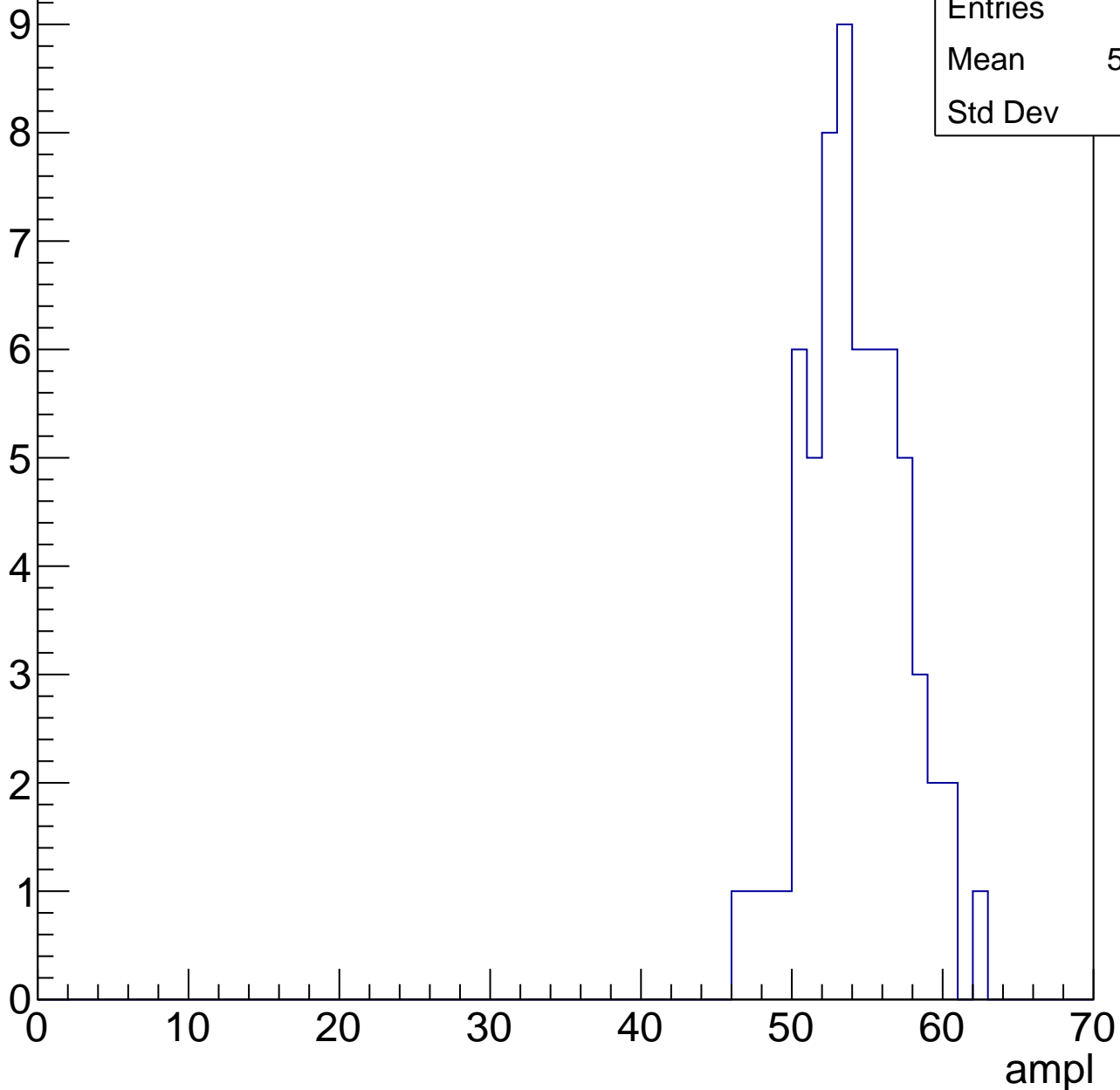


B1L103S, U24-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	53.76
Std Dev	3.24

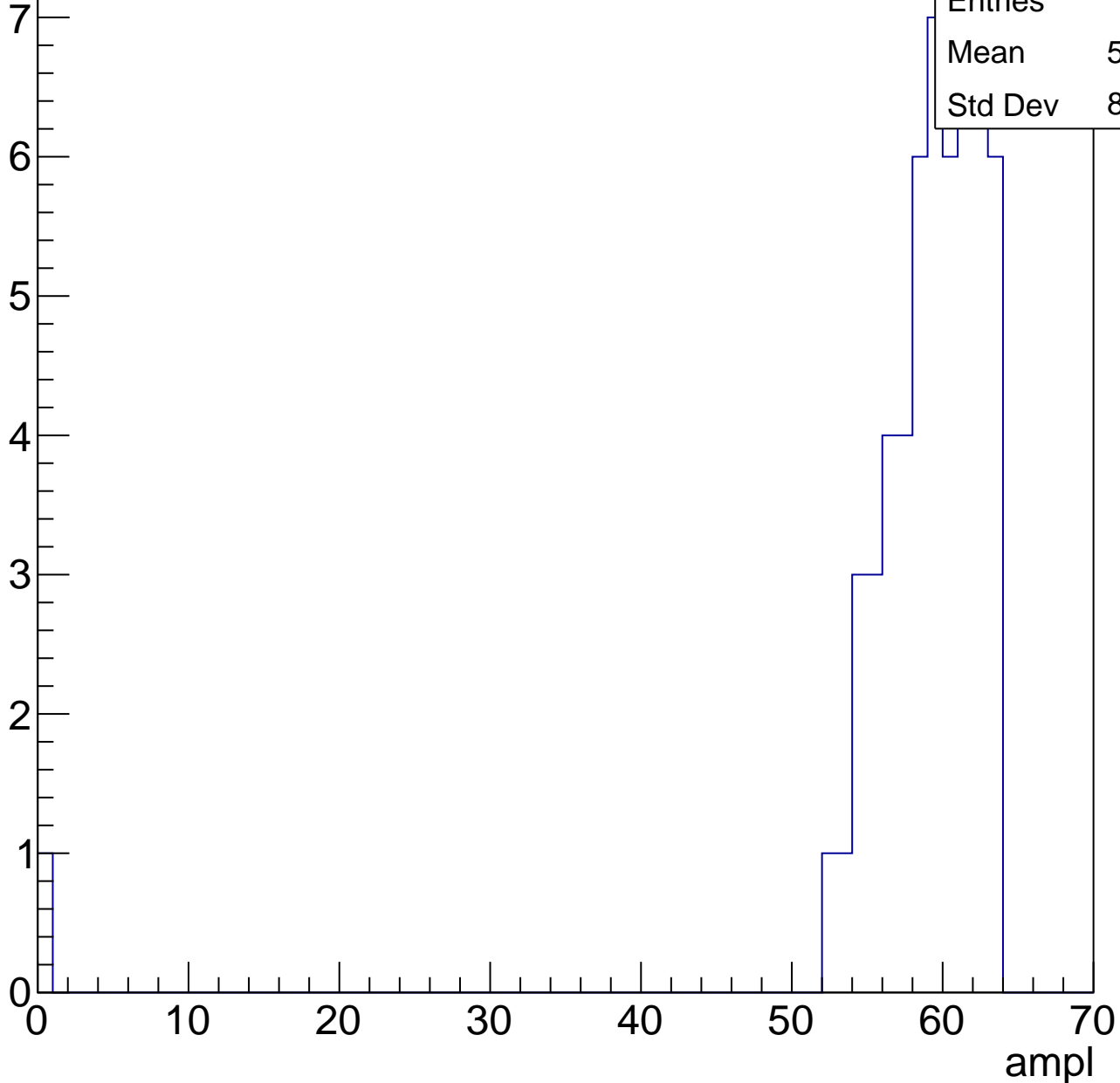


B1L103S, U24-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

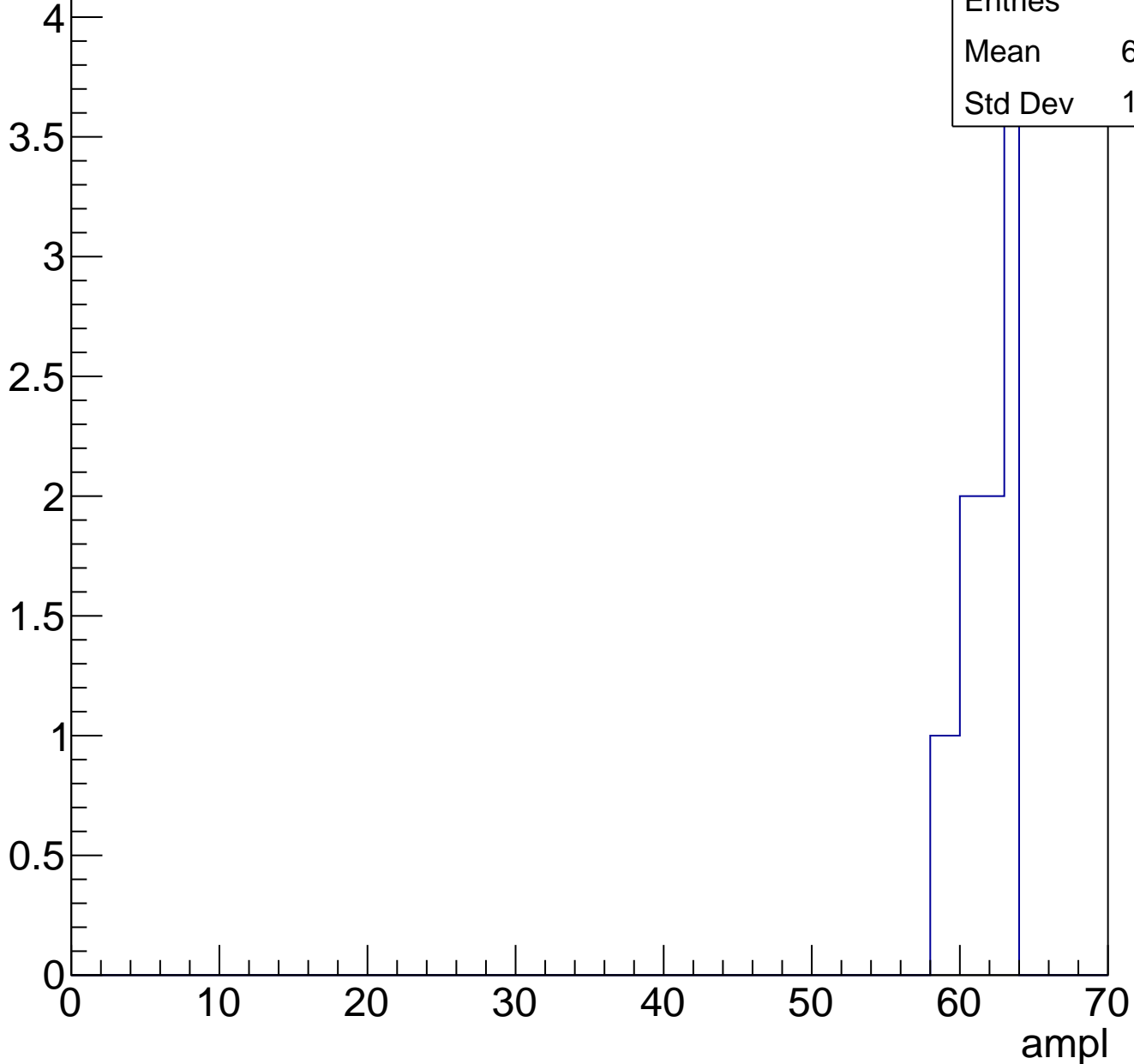
Entries	56
Mean	57.93
Std Dev	8.319



B1L103S, U24-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	12
Mean	61.25
Std Dev	1.639

B1L103S, U24-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U24-ch51, adc0

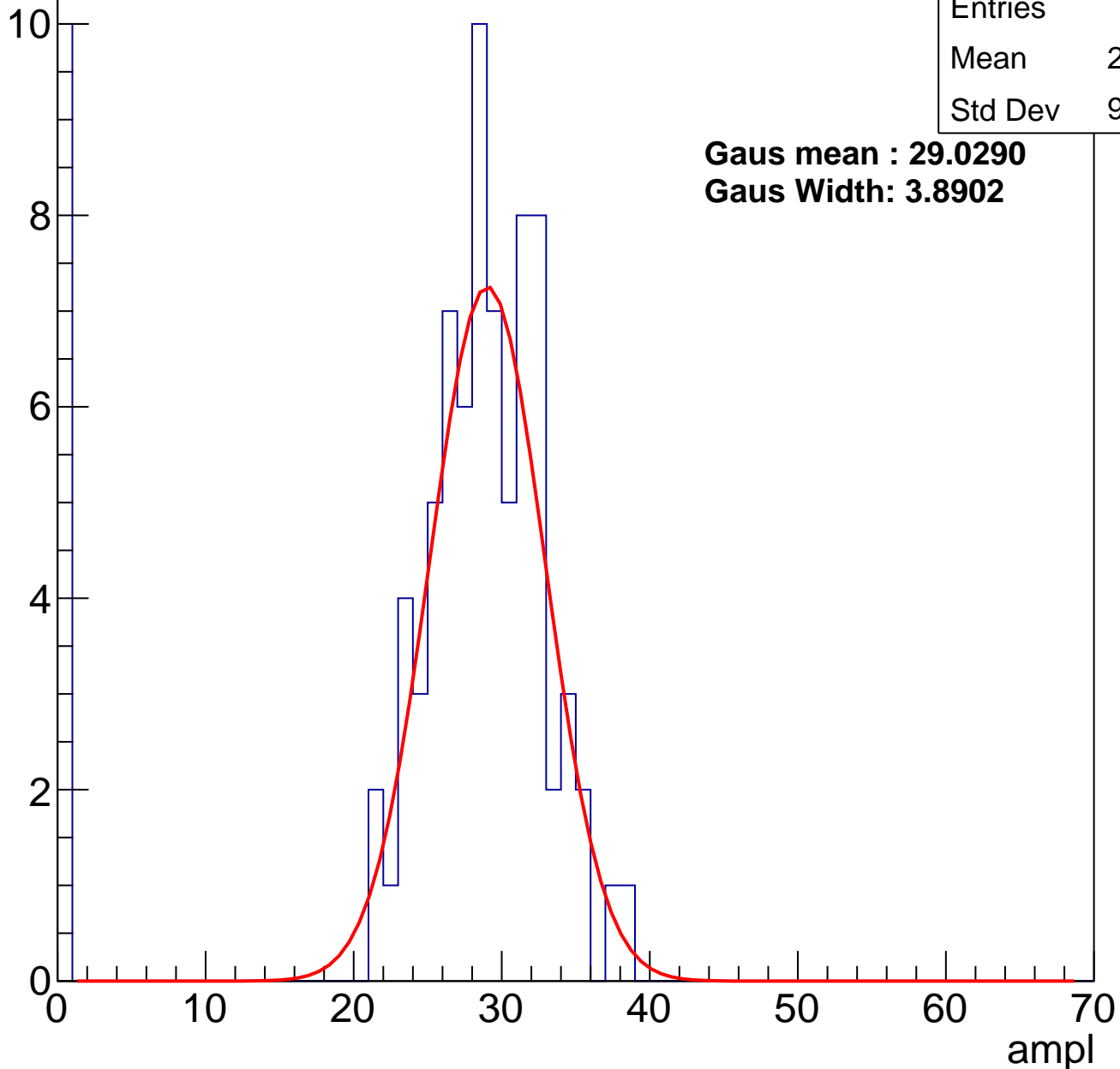
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	25.26
Std Dev	9.838

Gaus mean : 29.0290

Gaus Width: 3.8902

Entry



B1L103S, U24-ch51, adc1

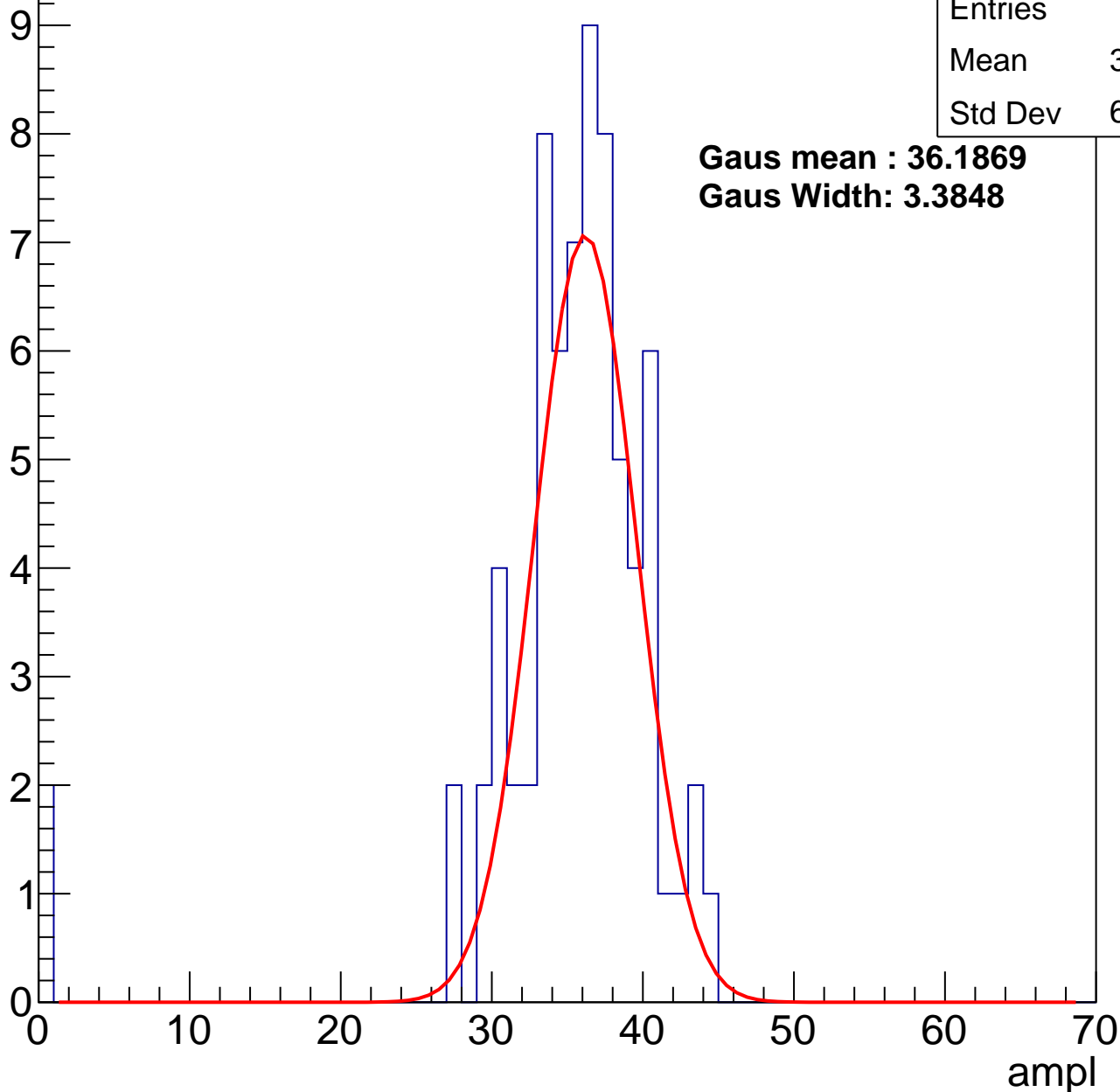
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	34.58
Std Dev	6.892

Gaus mean : 36.1869

Gaus Width: 3.3848



B1L103S, U24-ch51, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	37.02
Std Dev	14.97

Gaus mean : 42.8622

Gaus Width: 3.0900

Entry

10

8

6

4

2

0

0

10

20

30

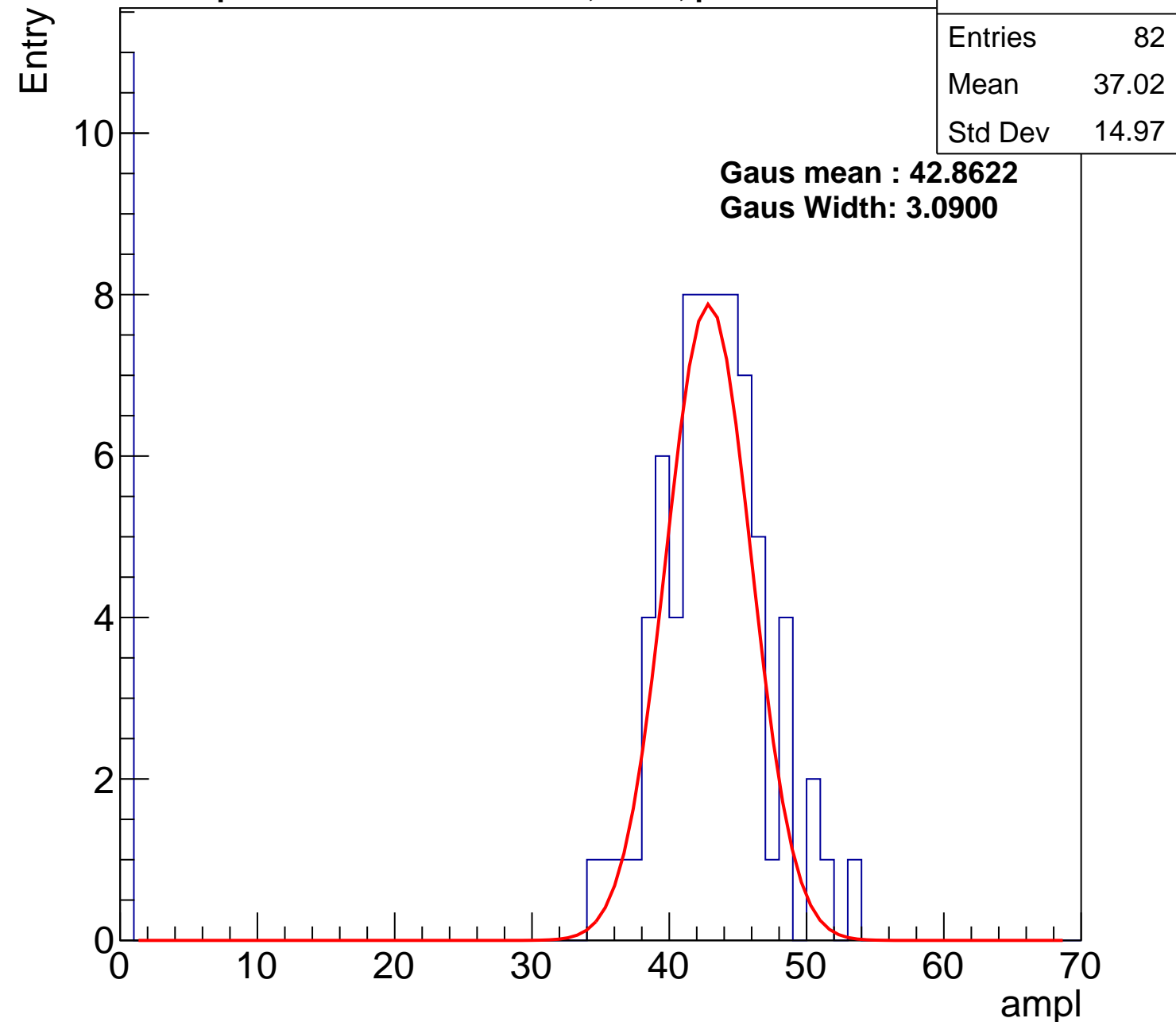
40

50

60

70

ampl

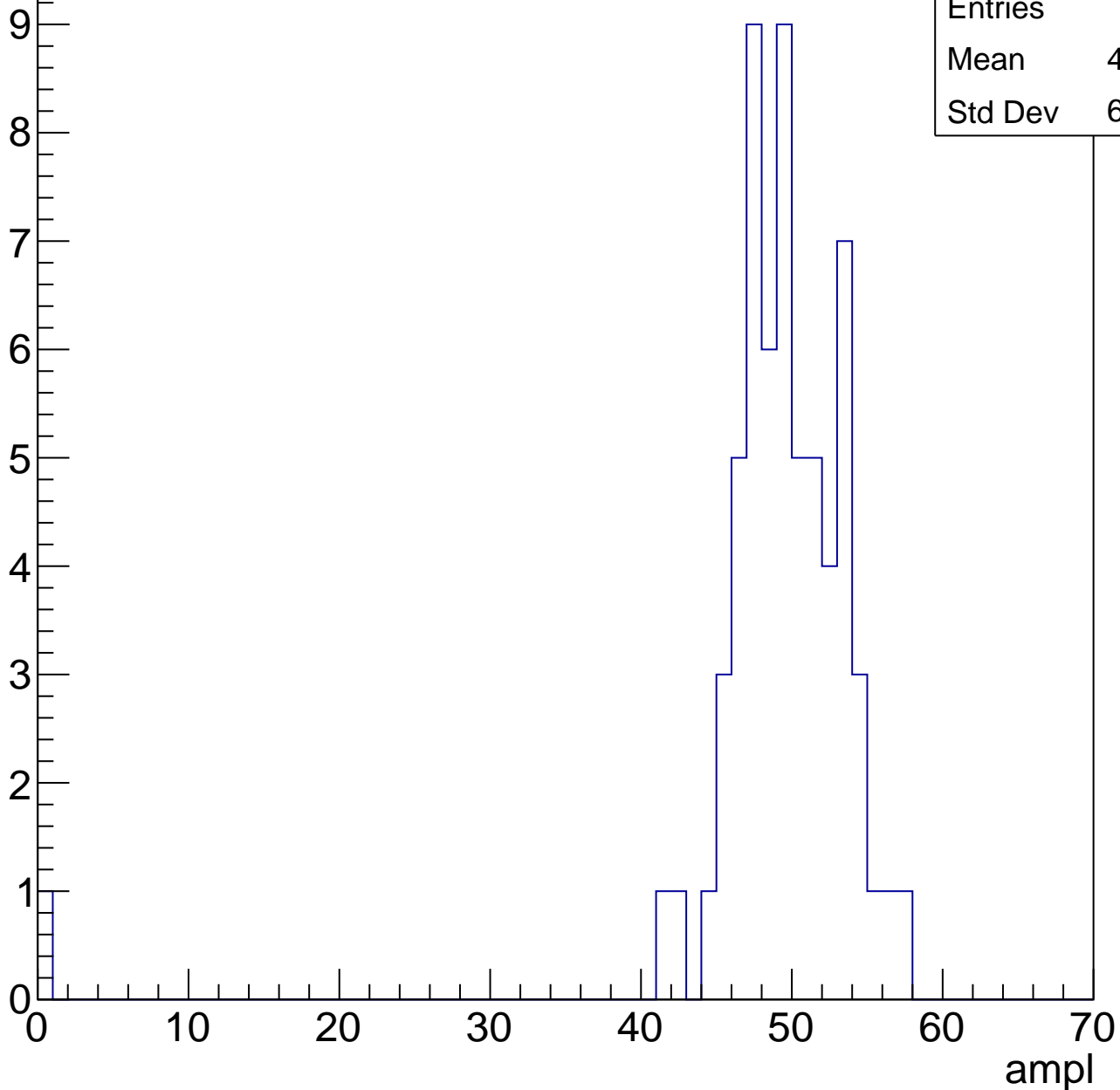


B1L103S, U24-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	48.54
Std Dev	6.967

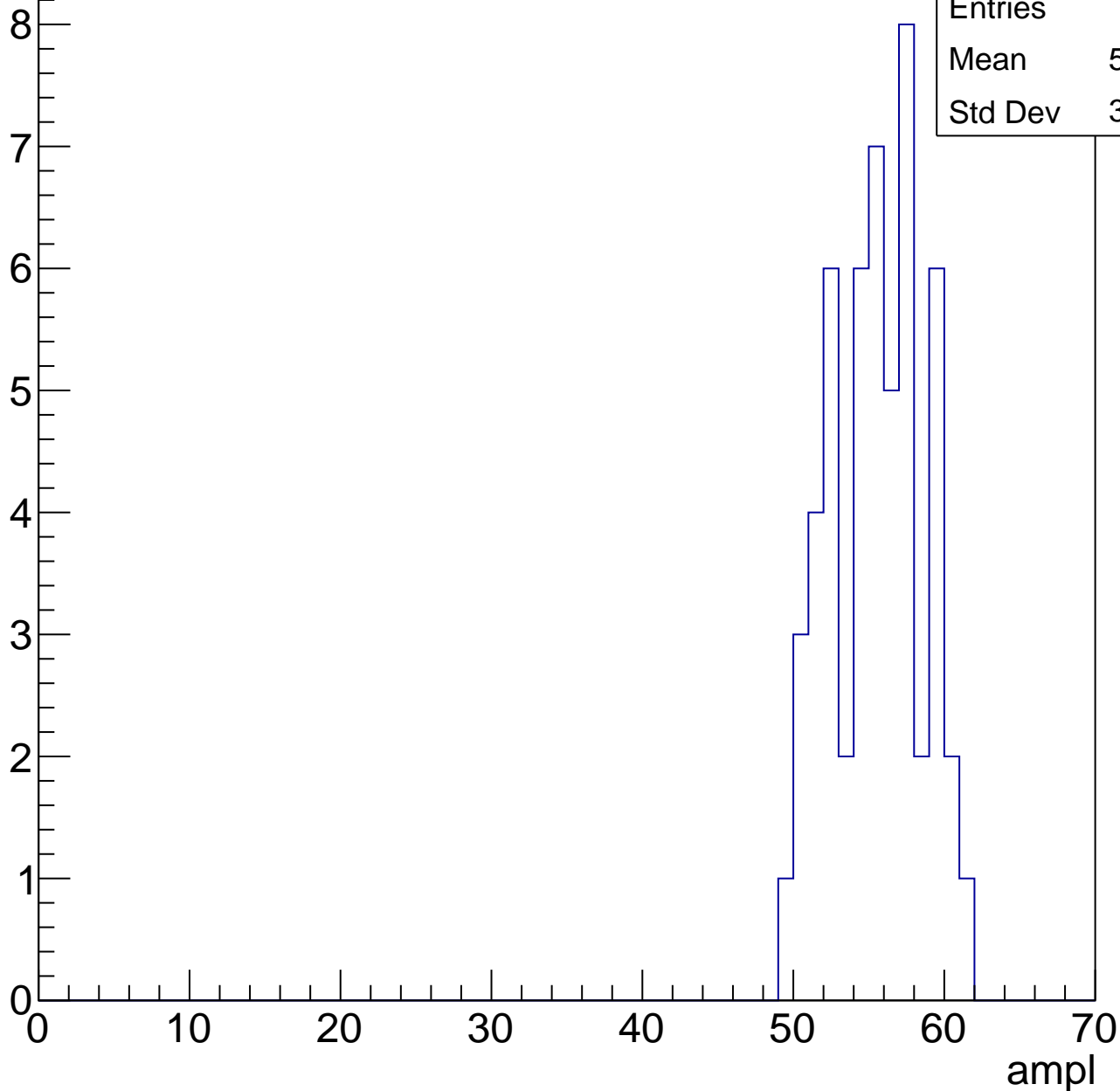


B1L103S, U24-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

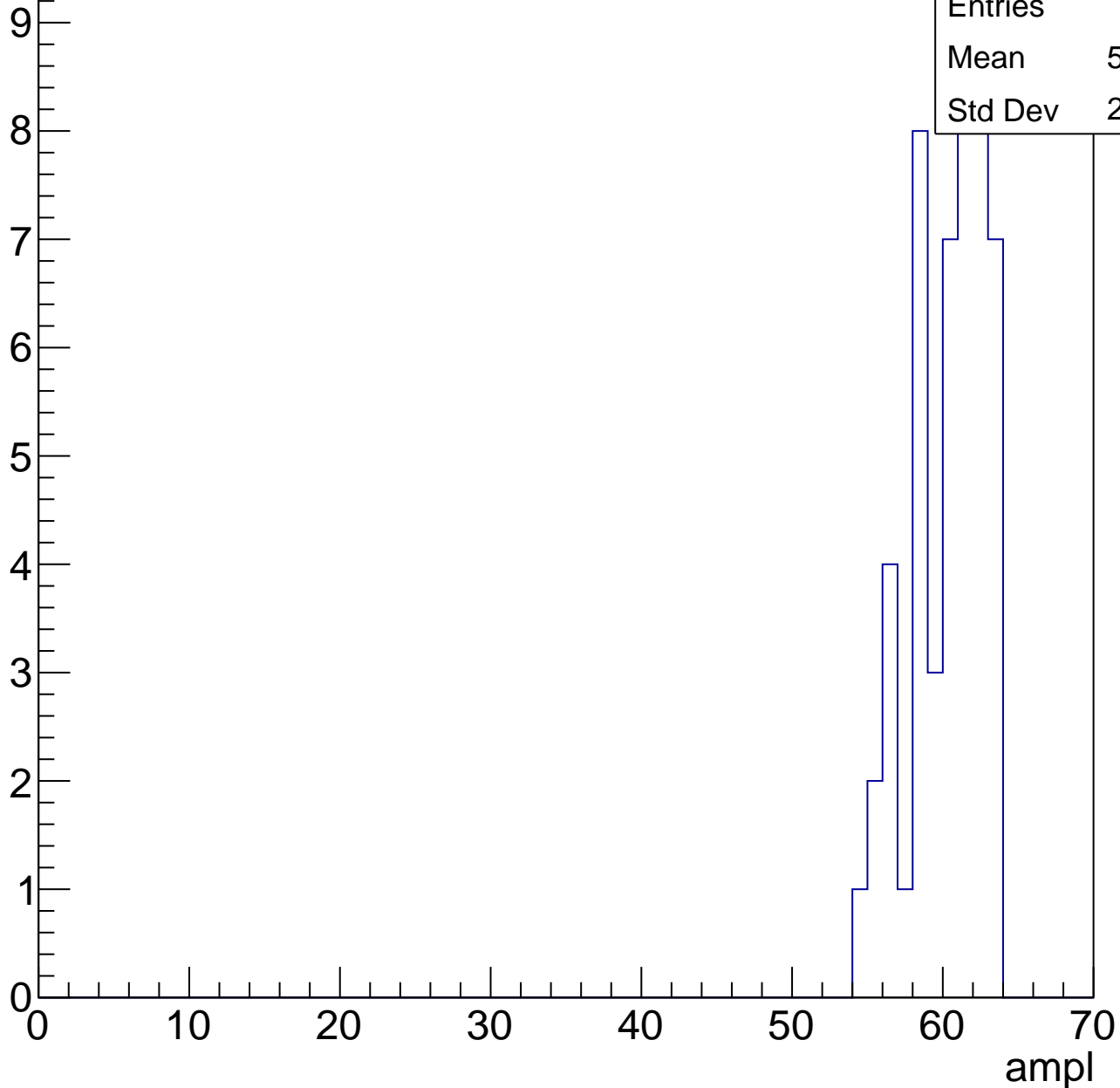
Entries	53
Mean	55.04
Std Dev	3.009



B1L103S, U24-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

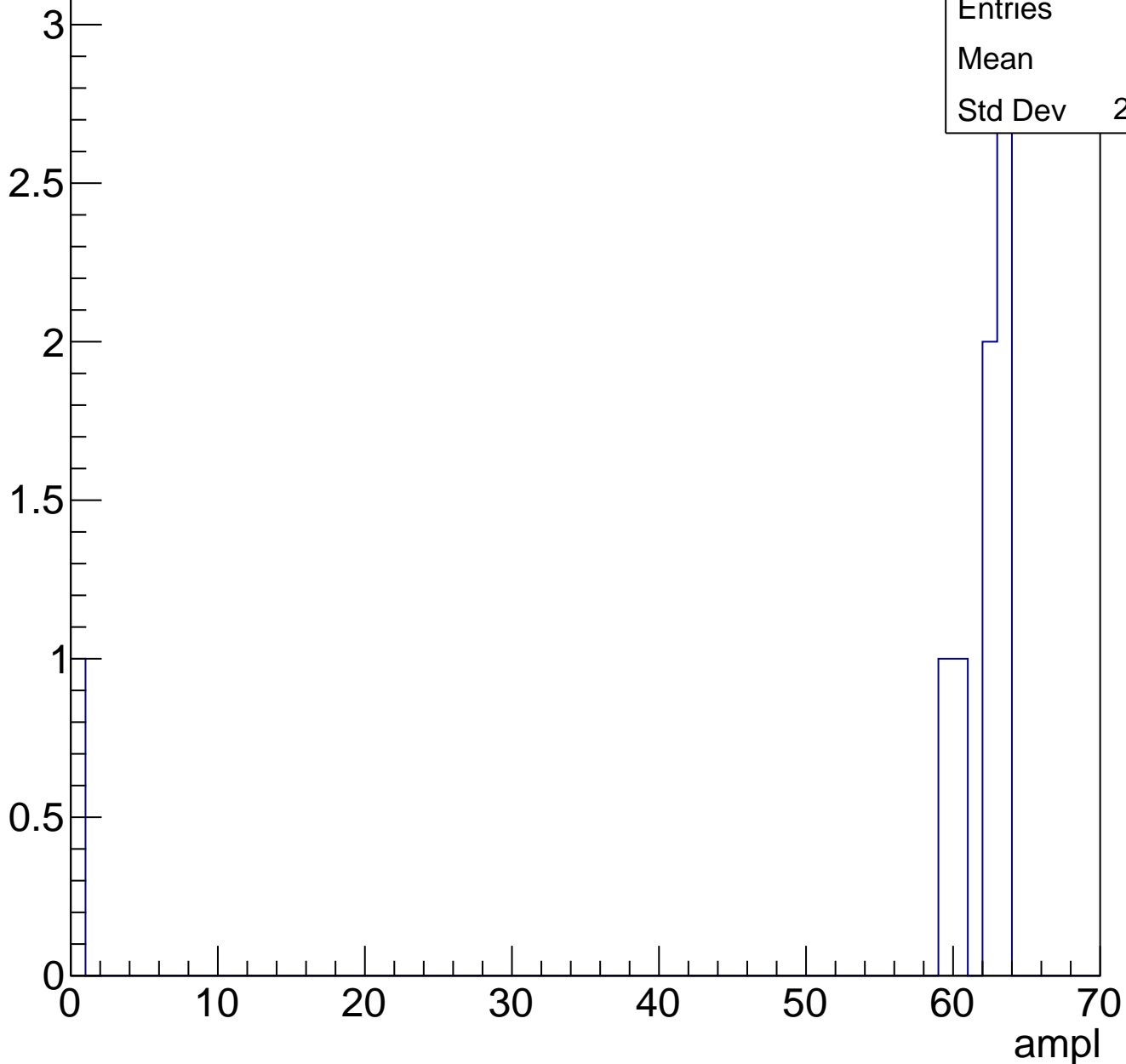
Entry



B1L103S, U24-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch52, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	24.99
Std Dev	9.87

Gaus mean : 29.2126

Gaus Width: 4.3396

Entry

10

8

6

4

2

0

0

10

20

30

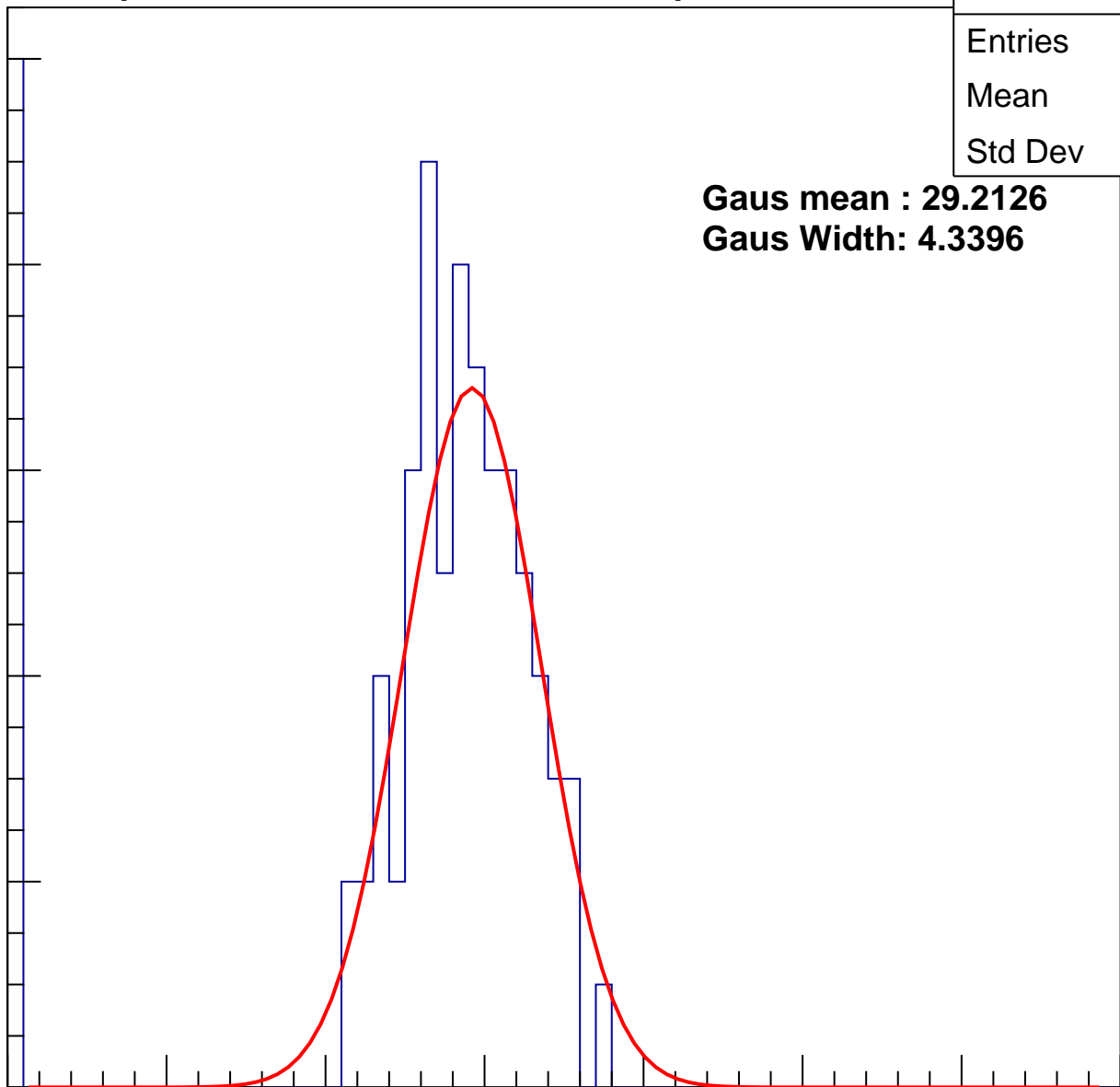
40

50

60

70

ampl



B1L103S, U24-ch52, adc1

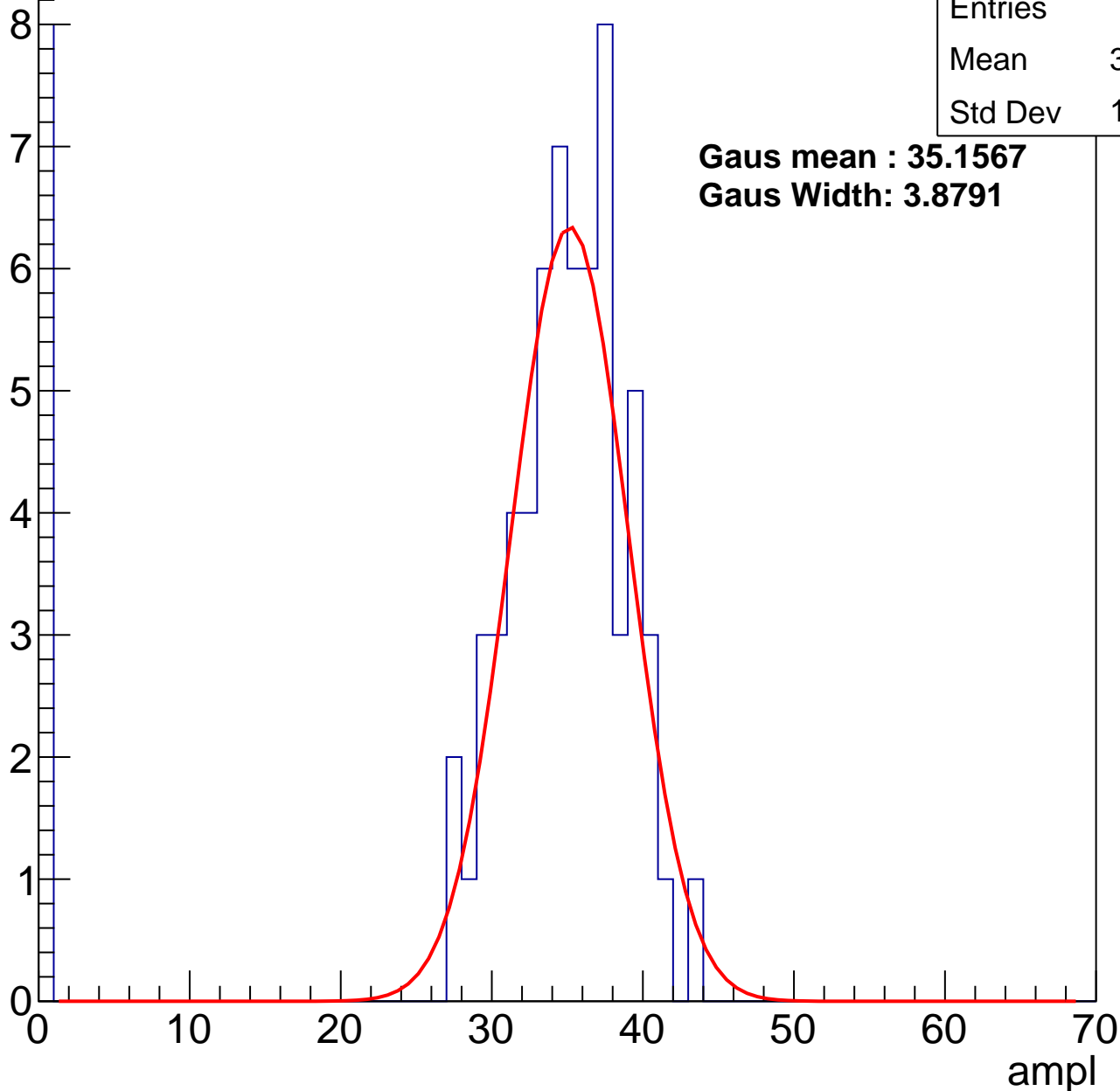
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	30.73
Std Dev	11.46

Gaus mean : 35.1567

Gaus Width: 3.8791



B1L103S, U24-ch52, adc2

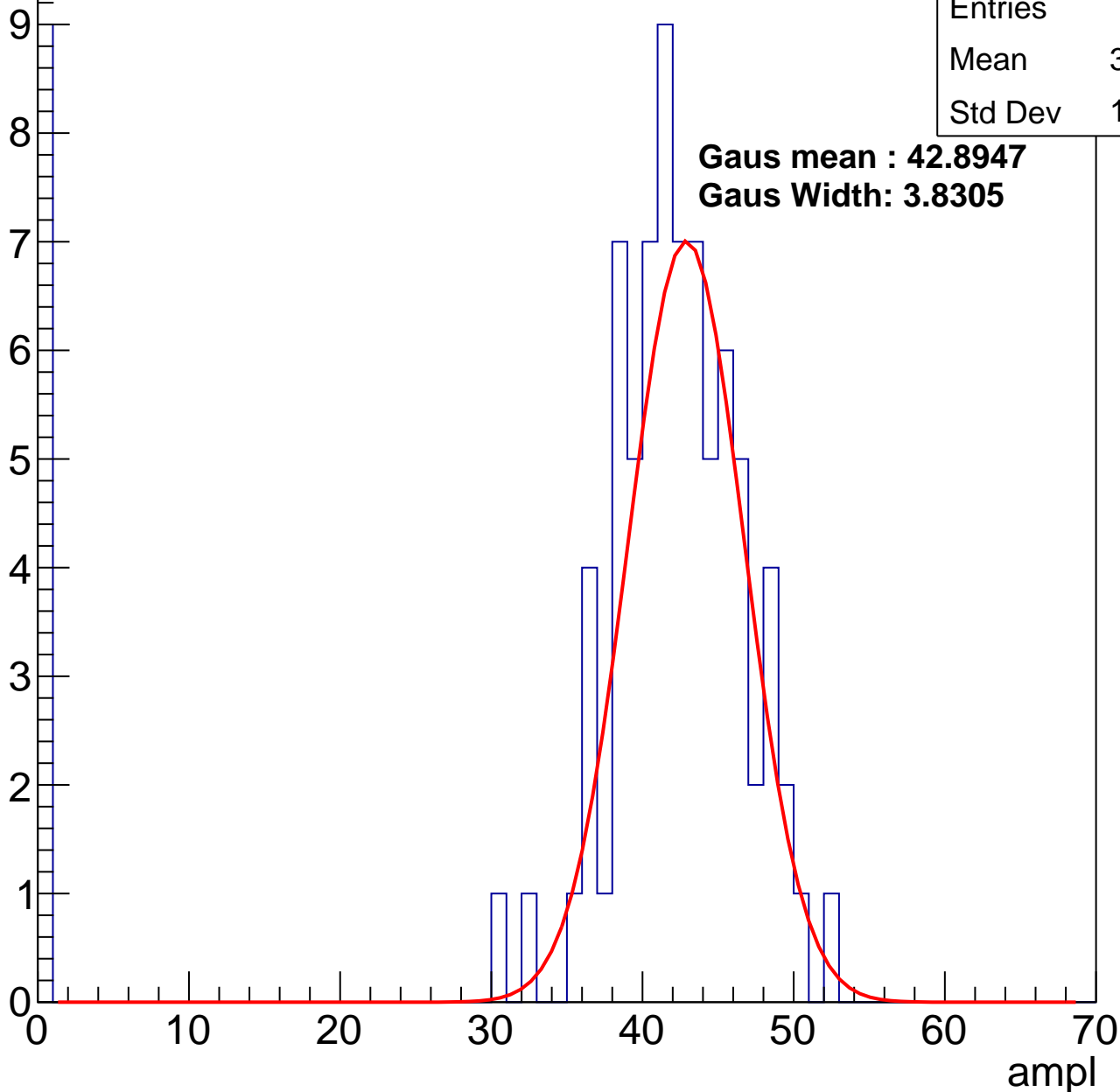
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	37.52
Std Dev	13.48

Gaus mean : 42.8947

Gaus Width: 3.8305

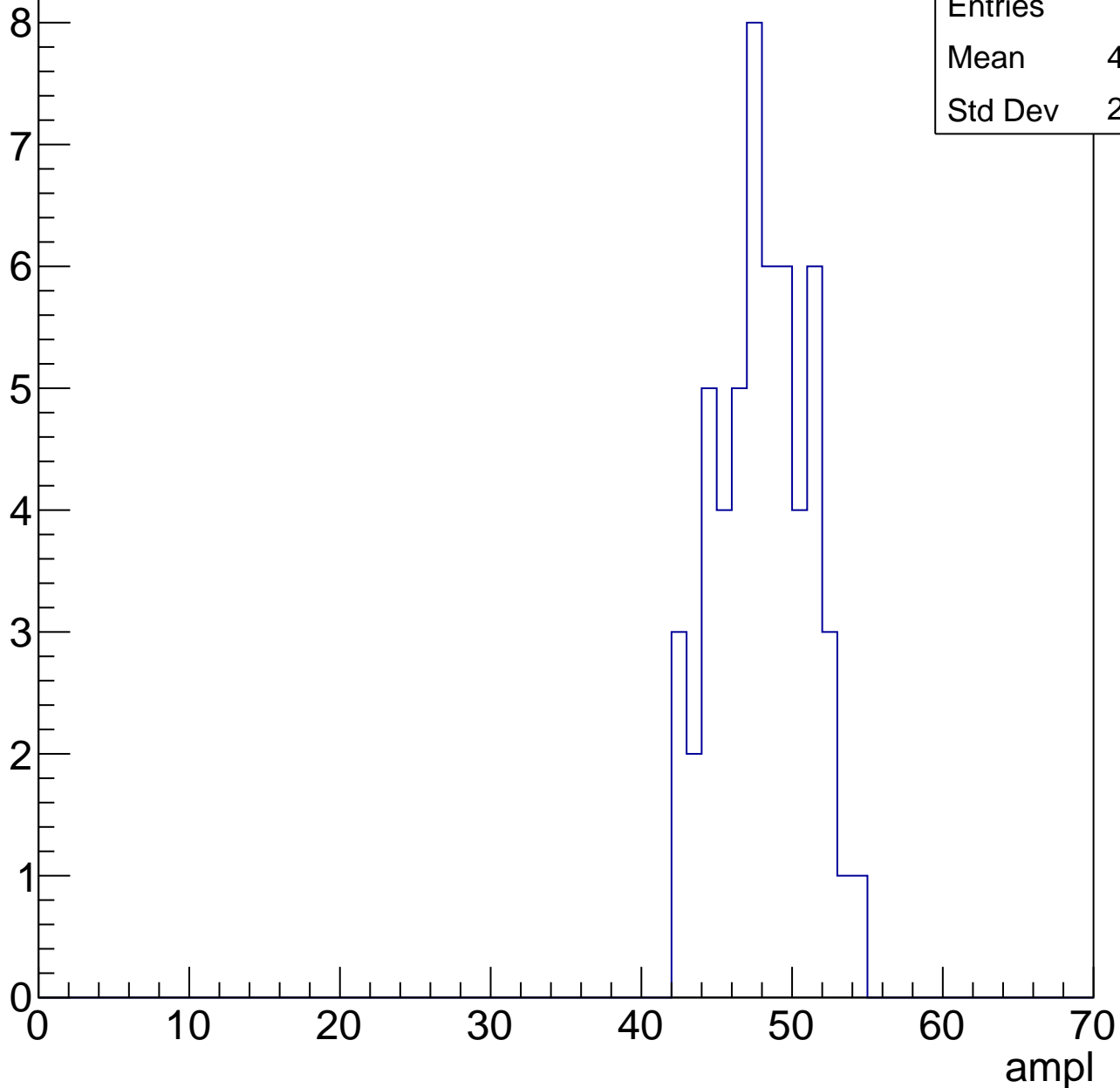


B1L103S, U24-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	47.57
Std Dev	2.973

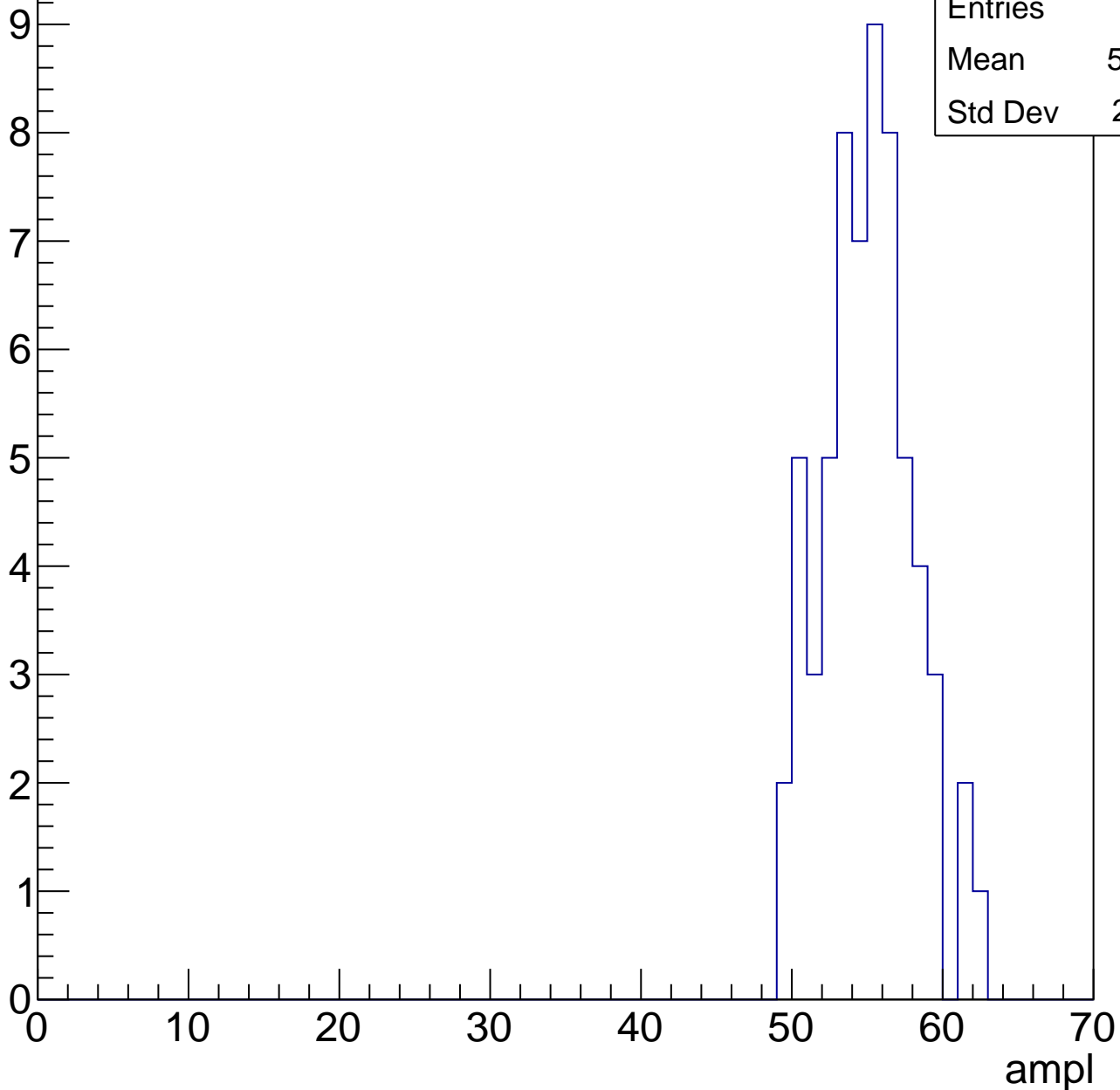


B1L103S, U24-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	54.58
Std Dev	2.981

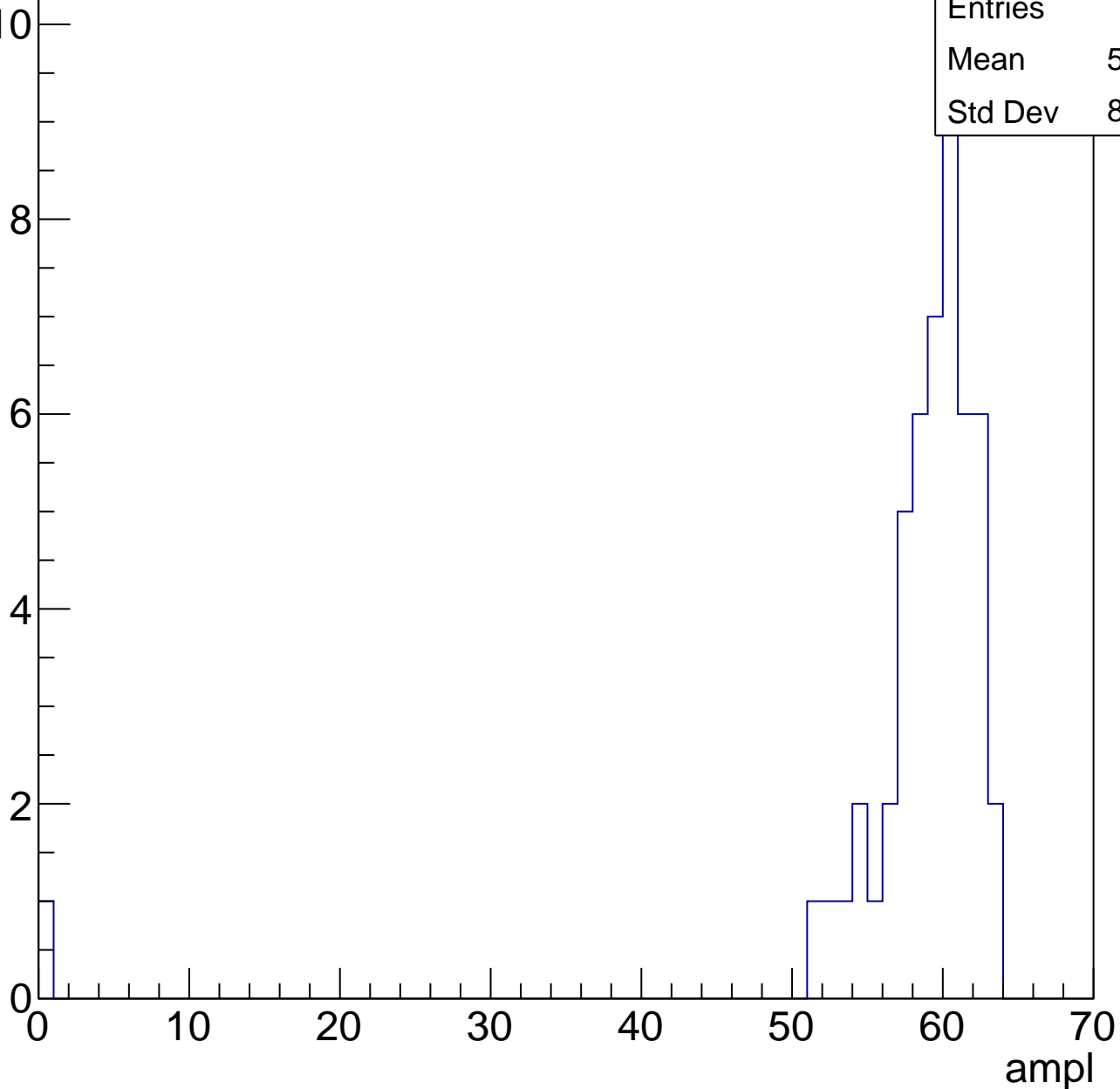


B1L103S, U24-ch52, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.67
Std Dev	8.597

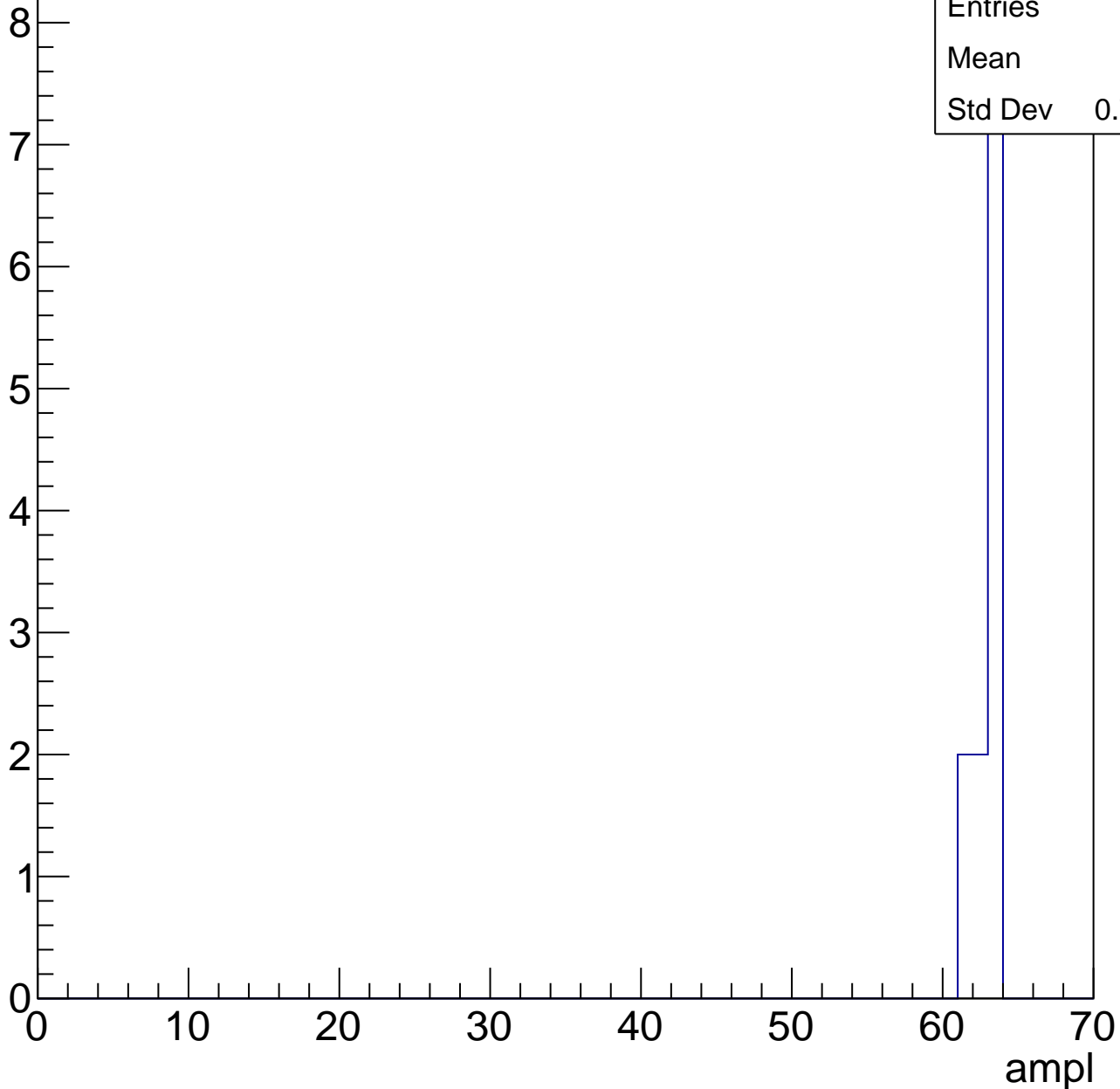


B1L103S, U24-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62.5
Std Dev	0.7638

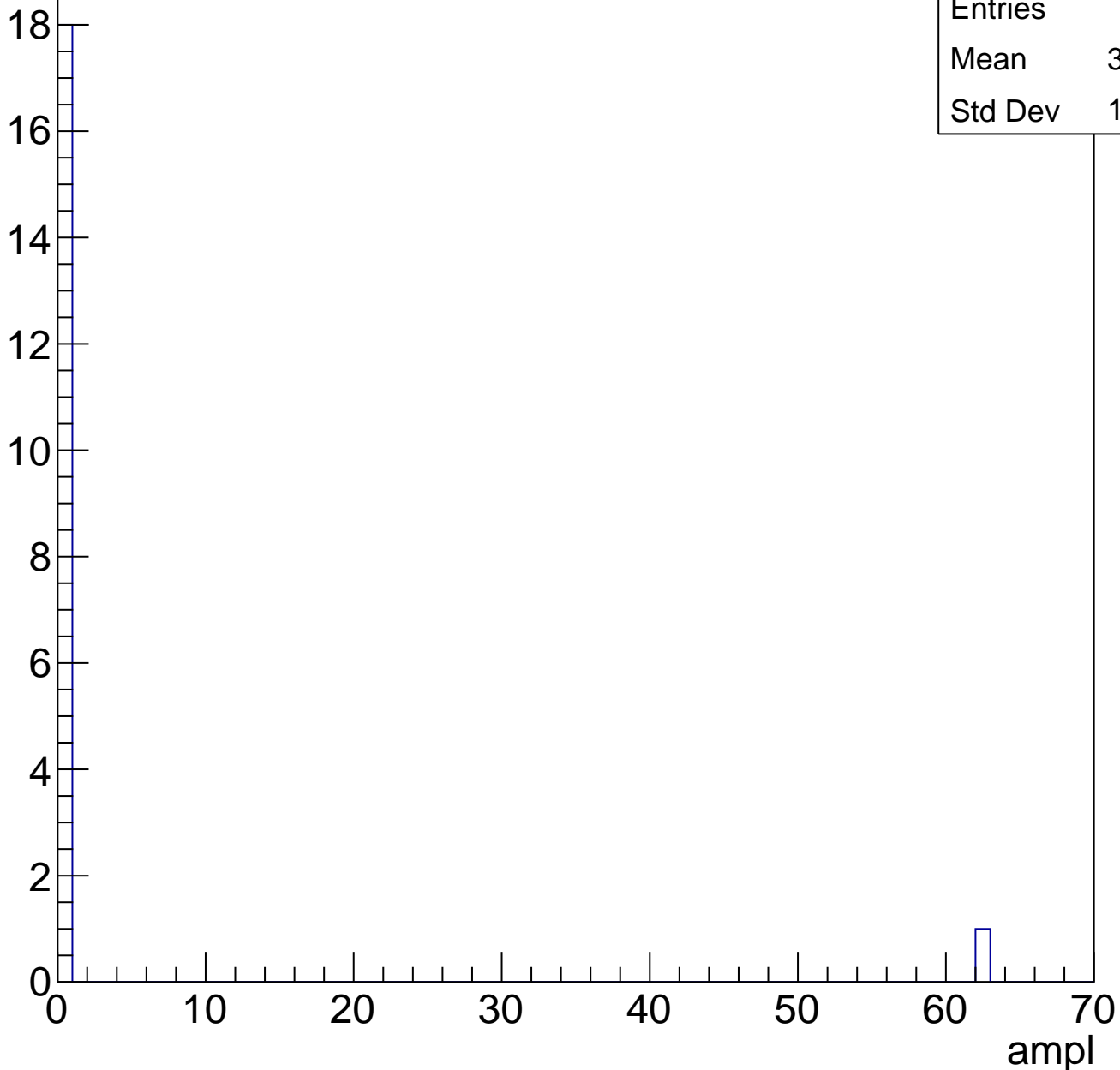


B1L103S, U24-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U24-ch53, adc0

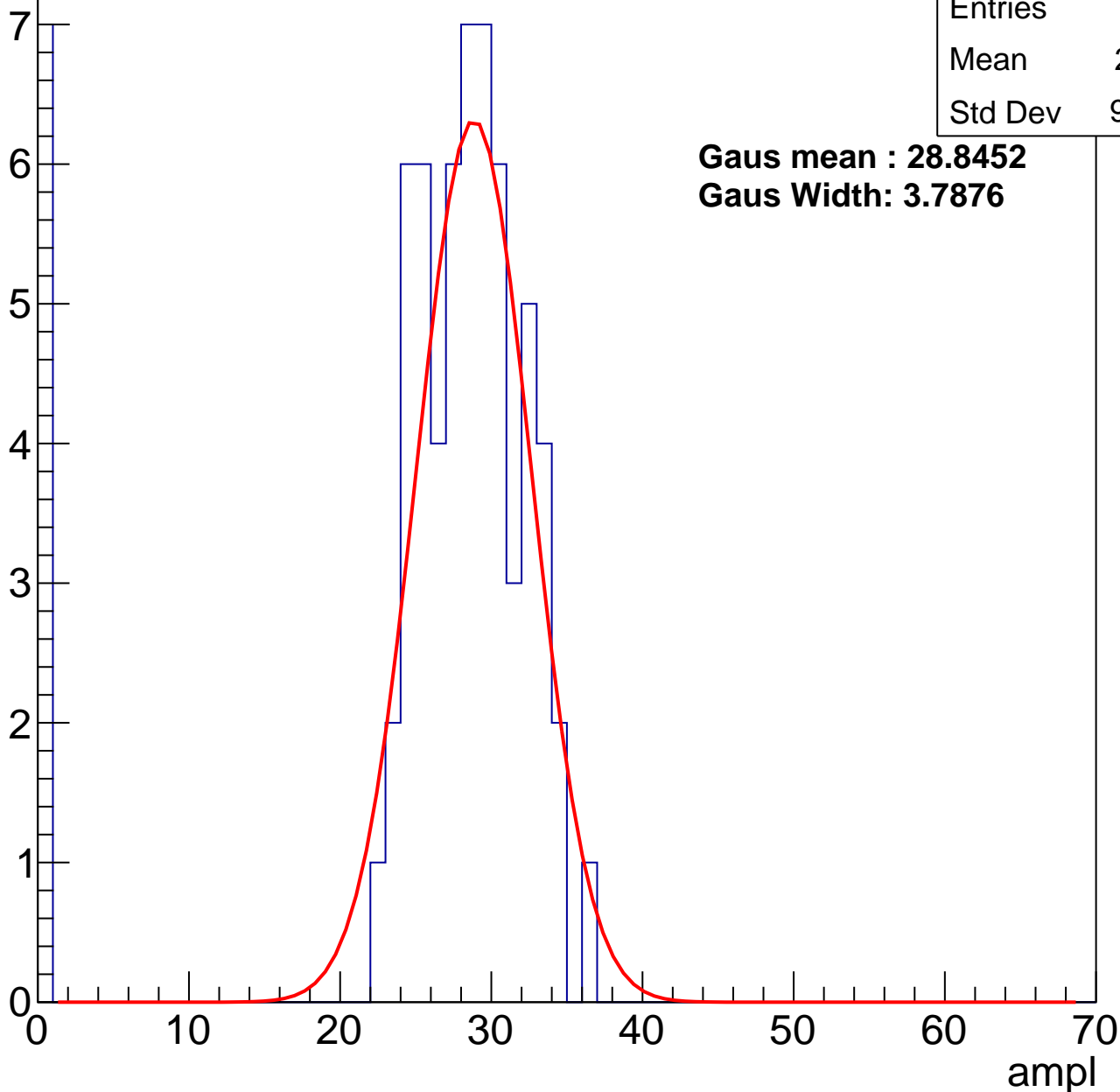
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	25.31
Std Dev	9.172

Gaus mean : 28.8452

Gaus Width: 3.7876



B1L103S, U24-ch53, adc1

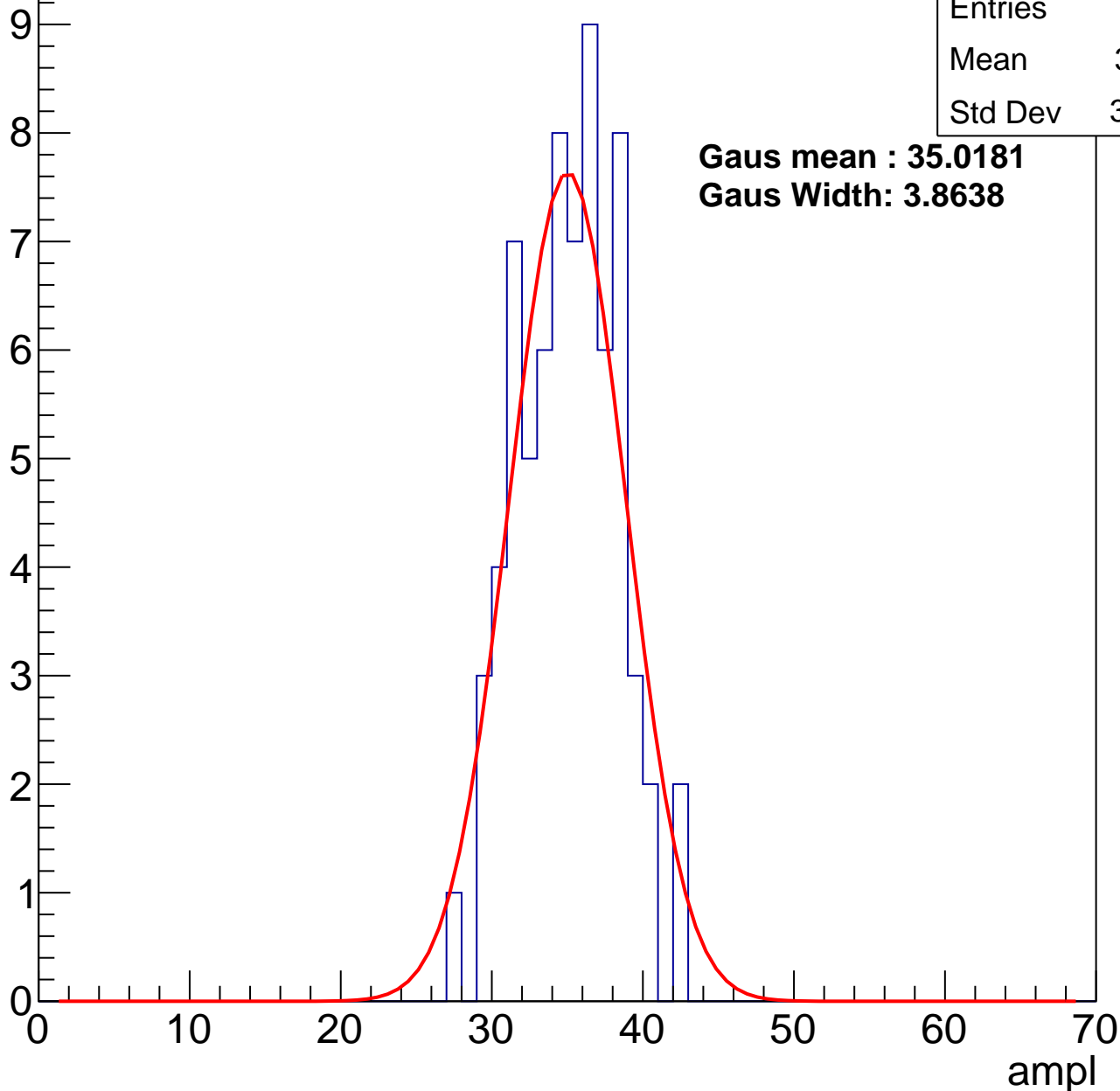
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	34.61
Std Dev	3.252

Gaus mean : 35.0181

Gaus Width: 3.8638

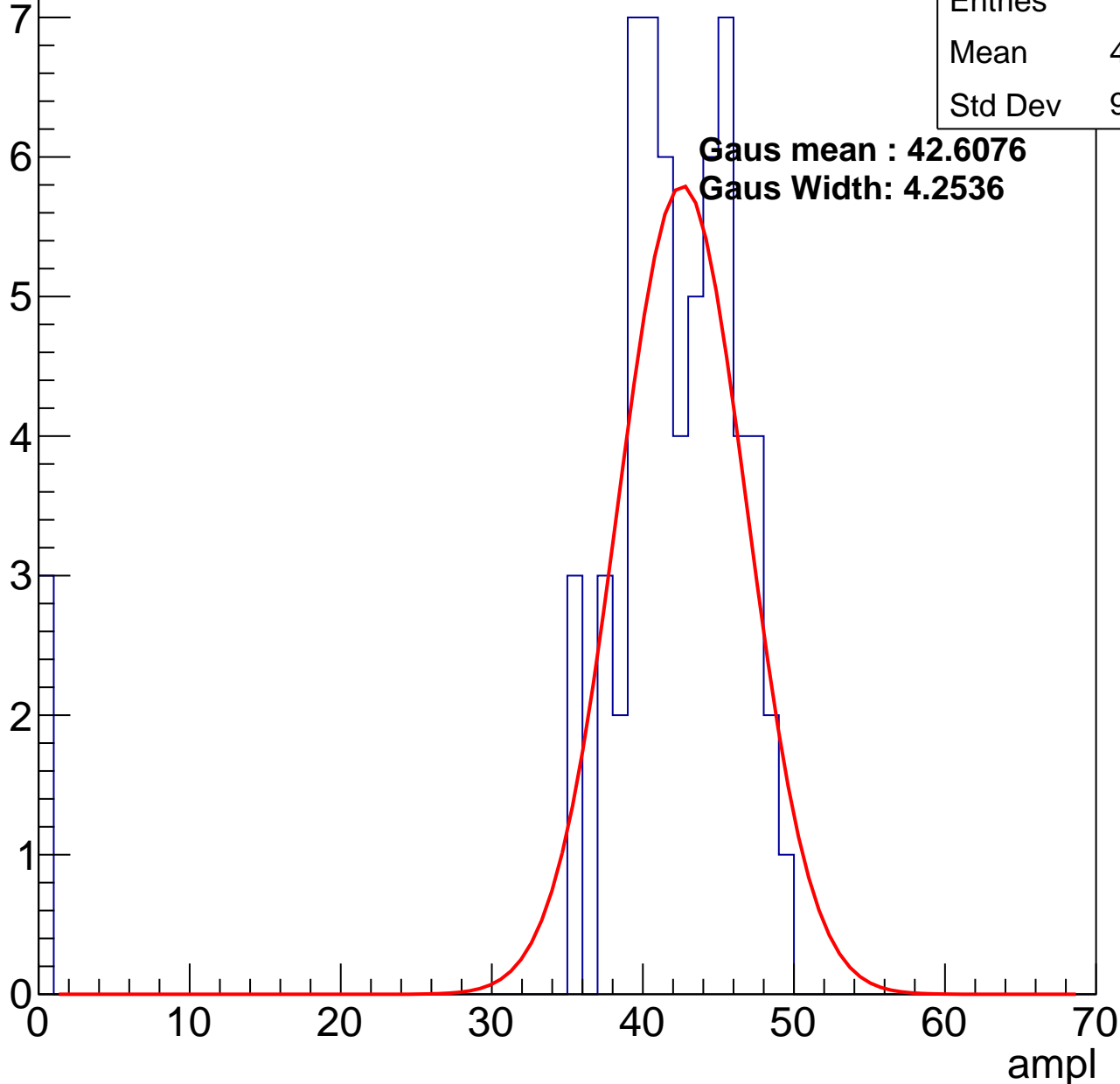


B1L103S, U24-ch53, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	40.16
Std Dev	9.522

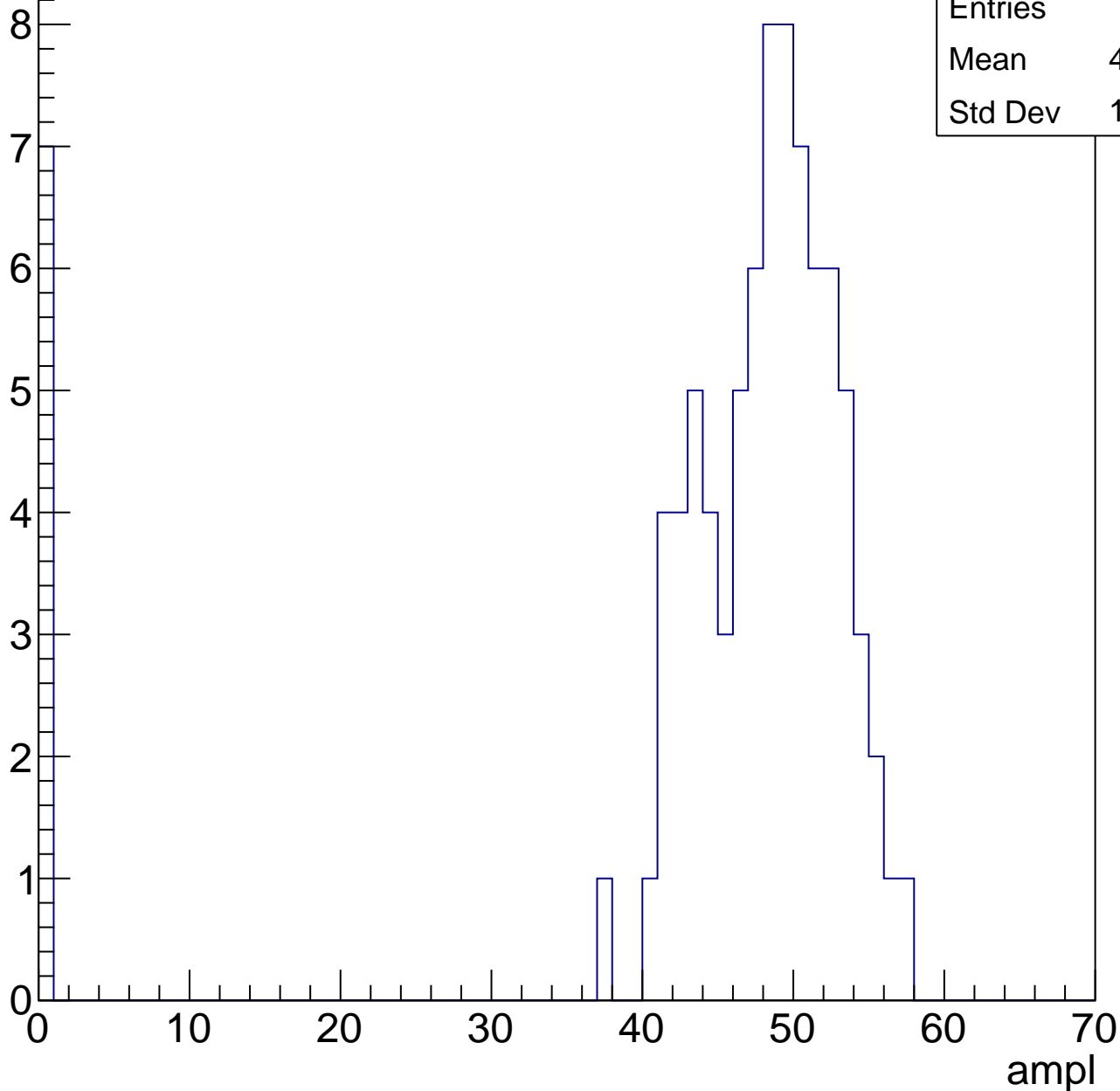


B1L103S, U24-ch53, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	44.15
Std Dev	13.67

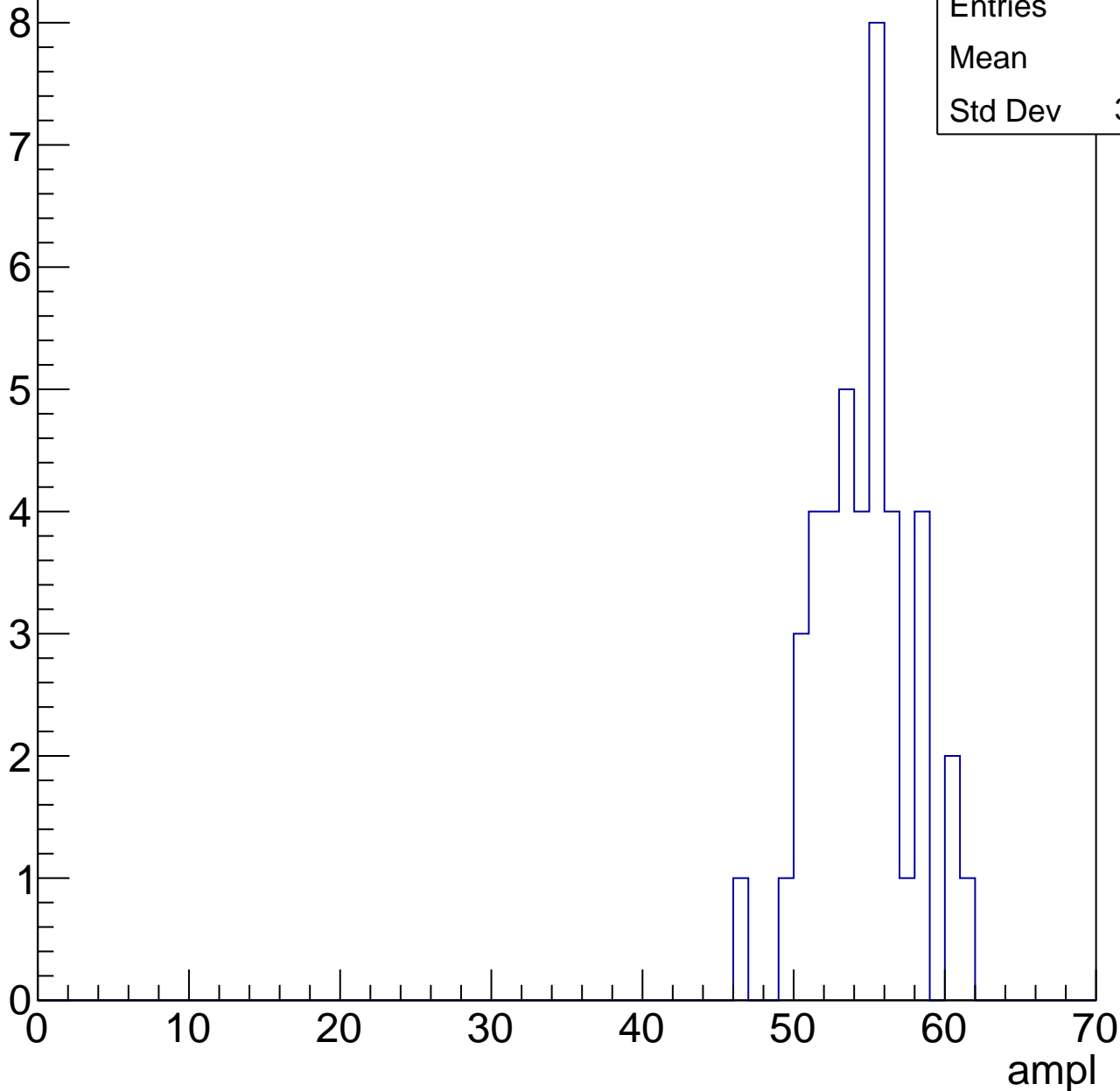


B1L103S, U24-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	54.1
Std Dev	3.131



B1L103S, U24-ch53, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 65

Mean 58.52

Std Dev 7.71

8

6

4

2

0

0

10

20

30

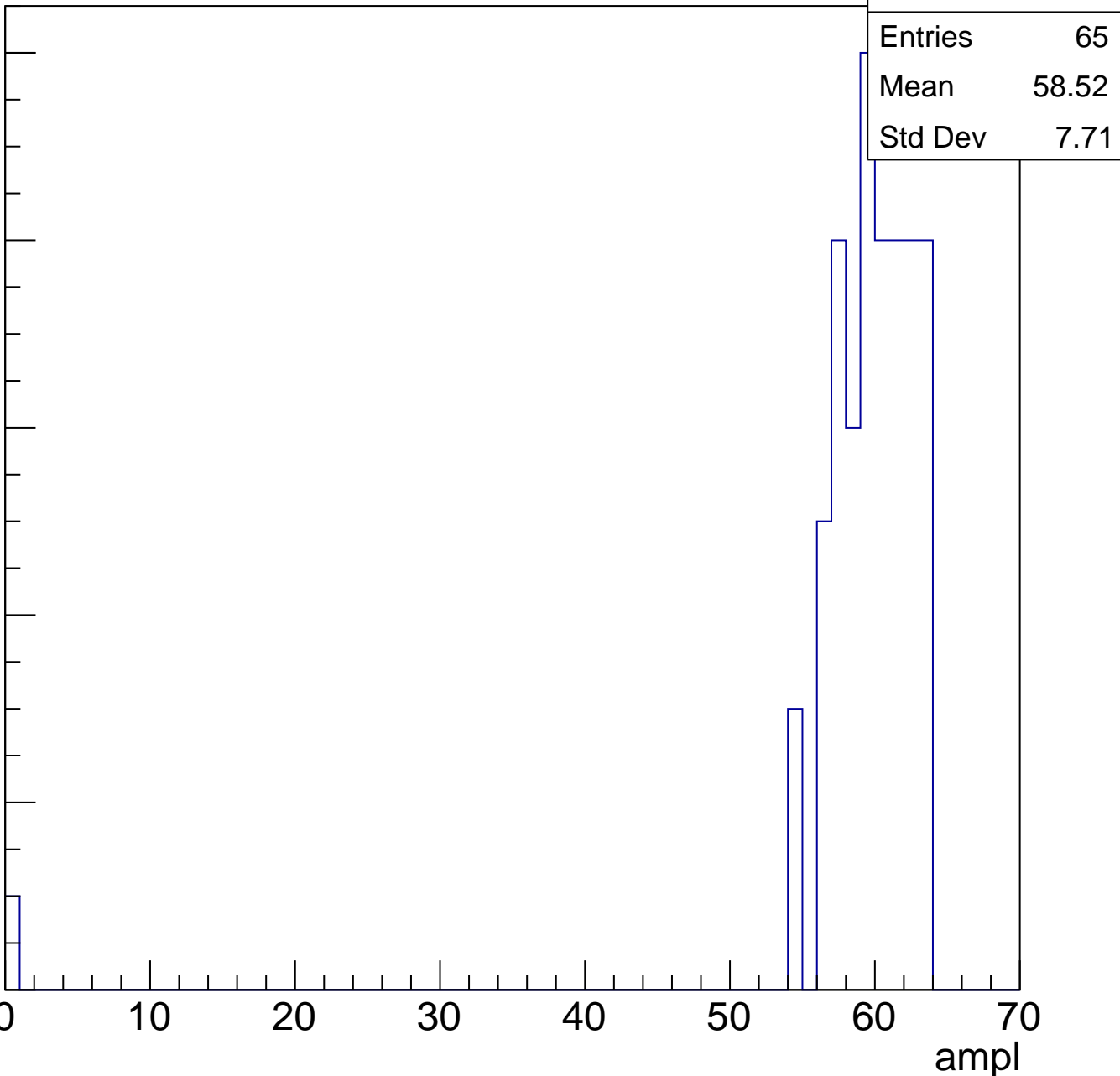
40

50

60

70

ampl



B1L103S, U24-ch53, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch54, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	23.82
Std Dev	11.82

Gaus mean : 30.3983

Gaus Width: 3.0528

Entry

16

14

12

10

8

6

4

2

0

0

10

20

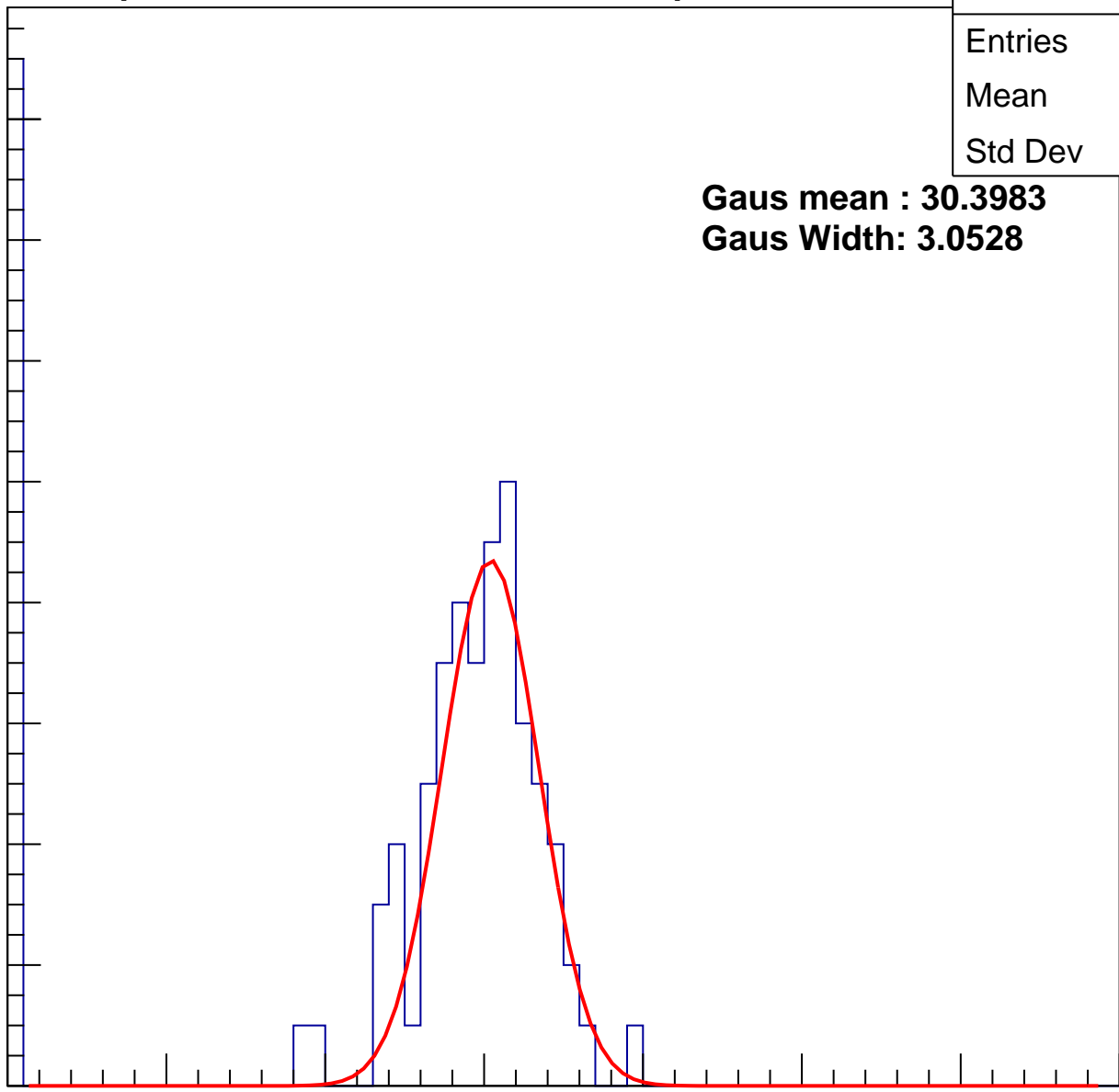
30

50

60

70

ampl



B1L103S, U24-ch54, adc1

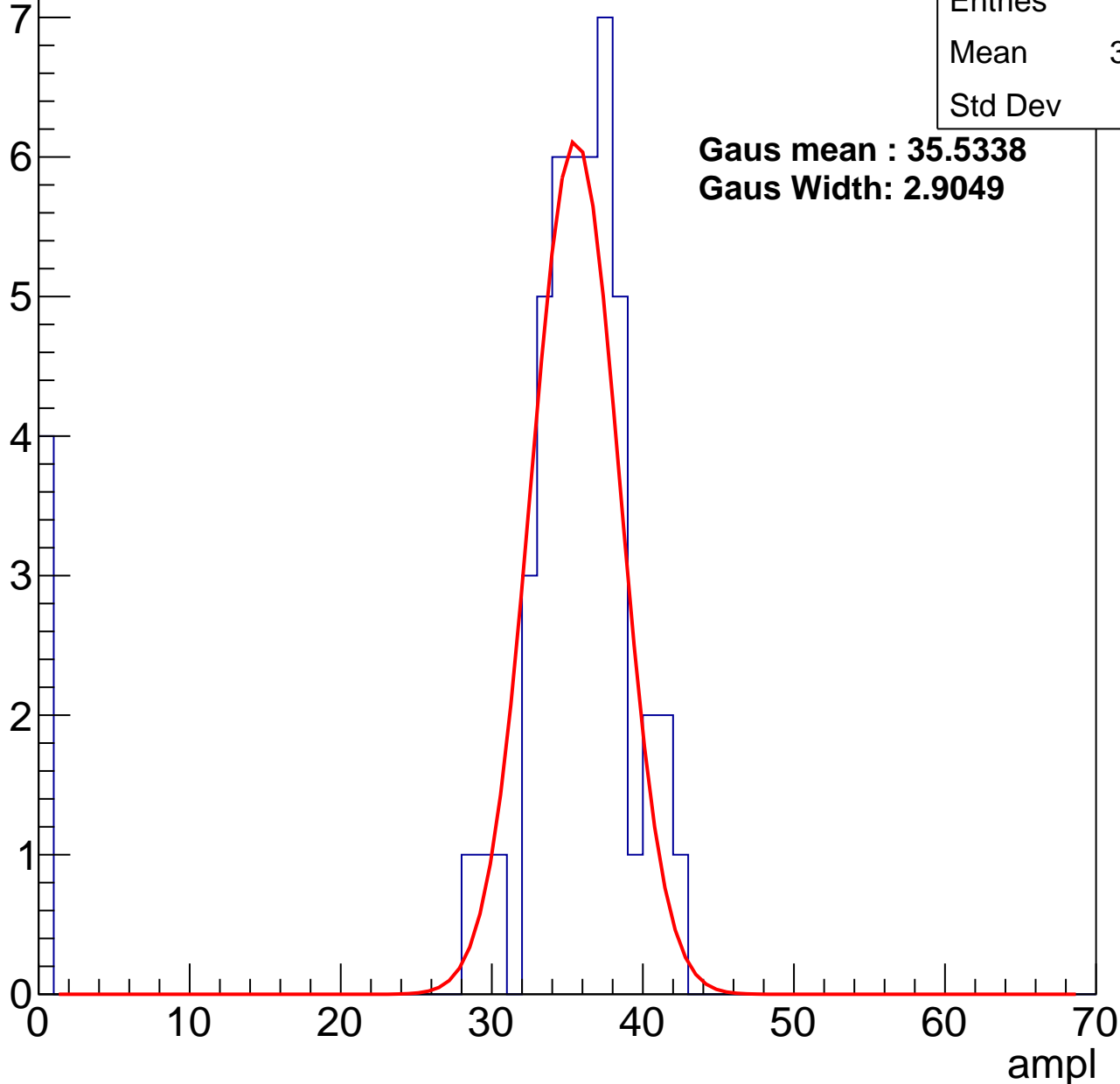
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	32.75
Std Dev	9.97

Gaus mean : 35.5338

Gaus Width: 2.9049



B1L103S, U24-ch54, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	35.94
Std Dev	14.78

Gaus mean : 42.0391

Gaus Width: 3.6878

Entry

10

8

6

4

2

0

0

10

20

30

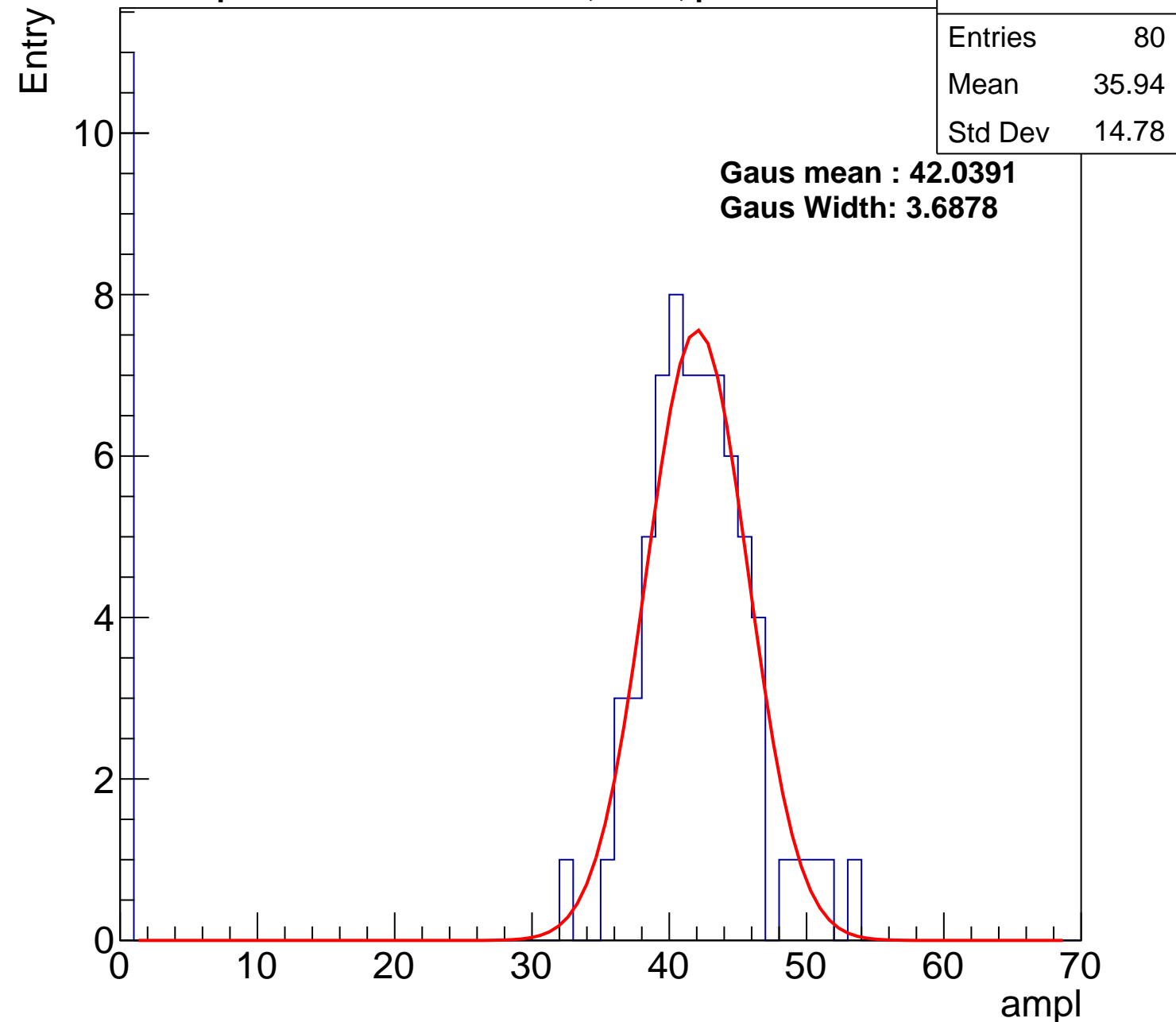
40

50

60

70

ampl



B1L103S, U24-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	47.89
Std Dev	6.763

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

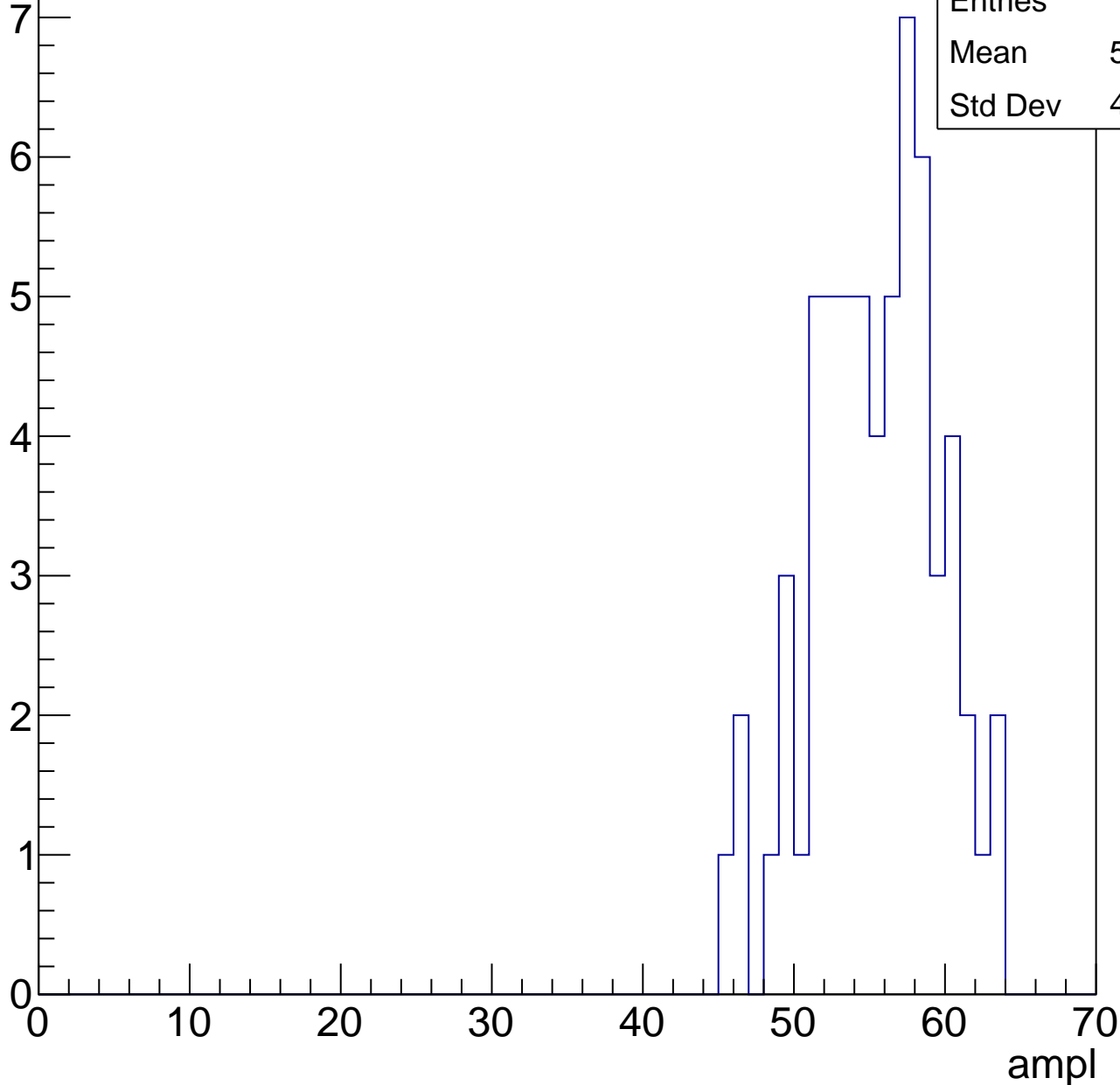
70

B1L103S, U24-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	54.94
Std Dev	4.169



B1L103S, U24-ch54, adc5

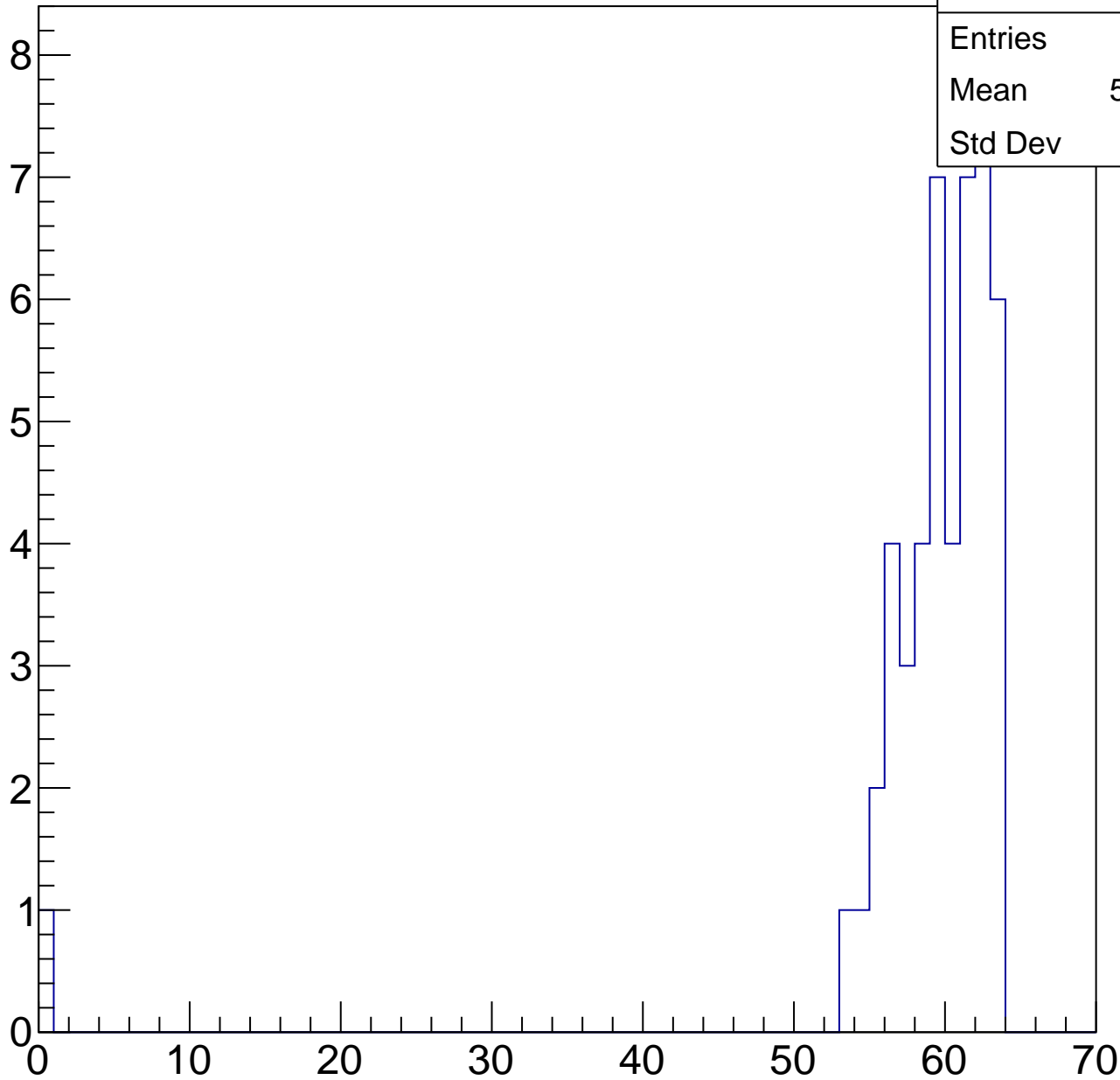
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.29
Std Dev	8.9

ampl



B1L103S, U24-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.258

ampl

0 10 20 30 40 50 60 70

B1L103S, U24-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch55, adc0

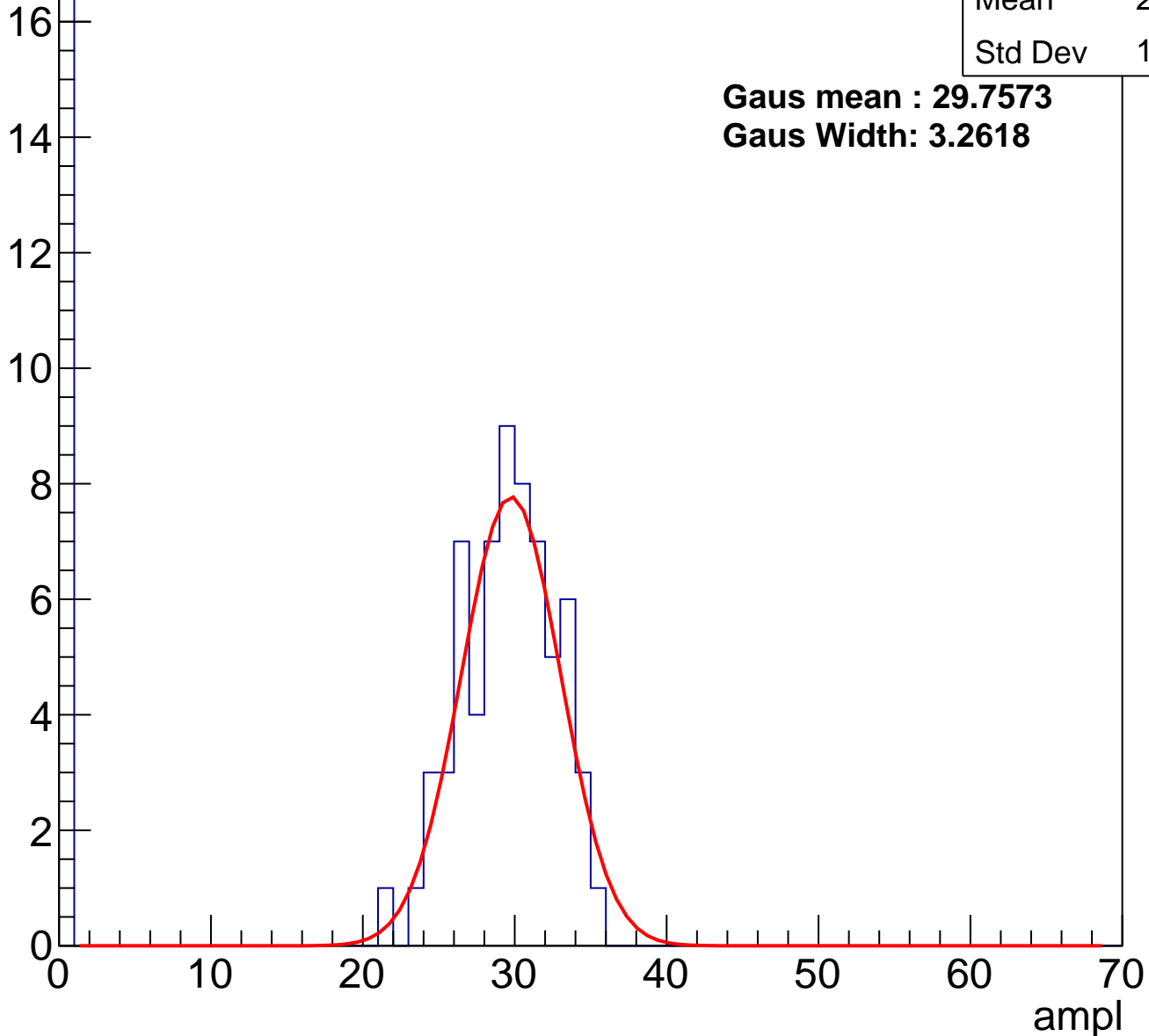
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.05
Std Dev	12.09

Gaus mean : 29.7573

Gaus Width: 3.2618

Entry



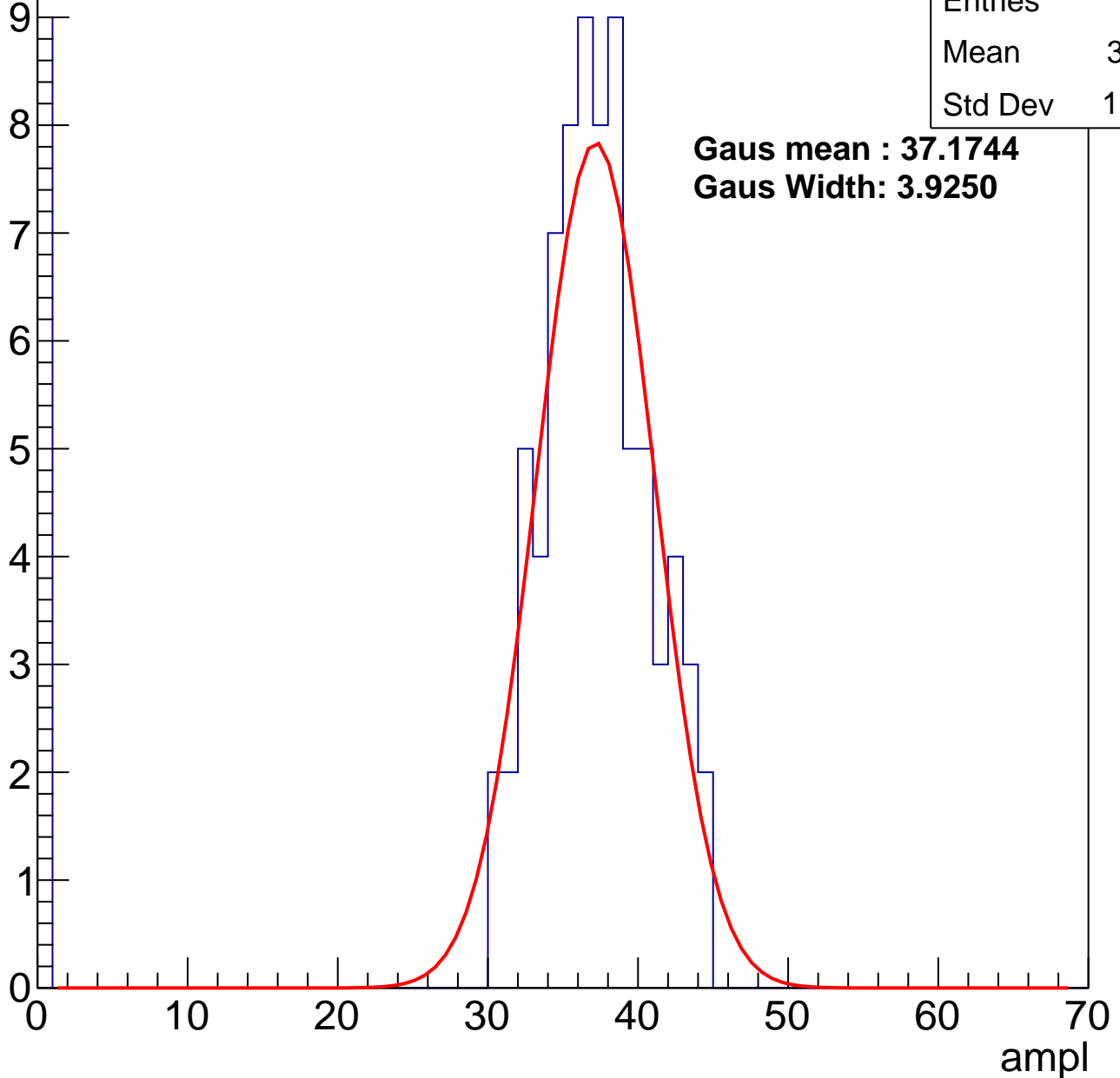
B1L103S, U24-ch55, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	32.91
Std Dev	11.78

Gaus mean : 37.1744
Gaus Width: 3.9250



B1L103S, U24-ch55, adc2

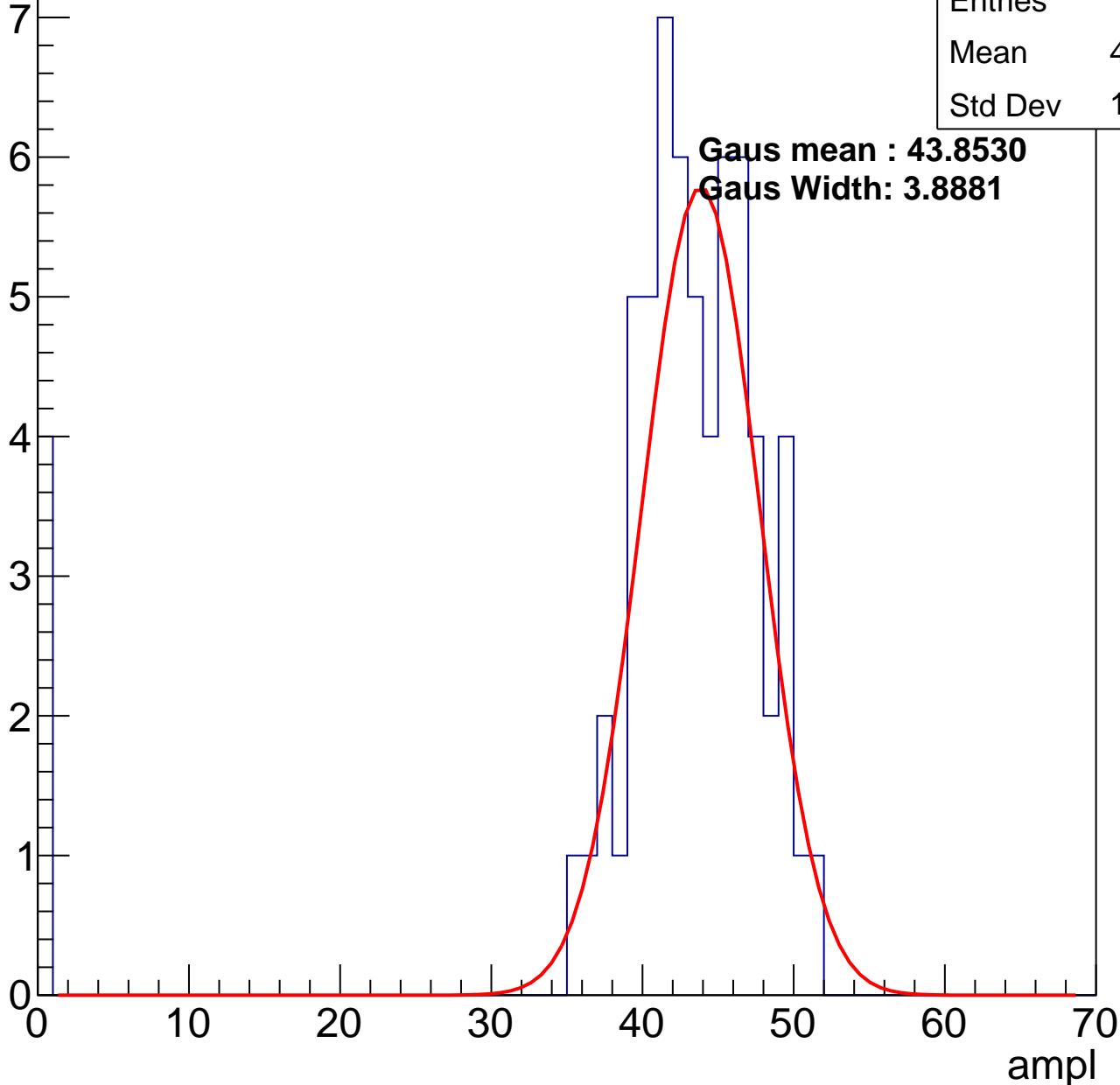
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	40.54
Std Dev	10.97

Gaus mean : 43.8530

Gaus Width: 3.8881

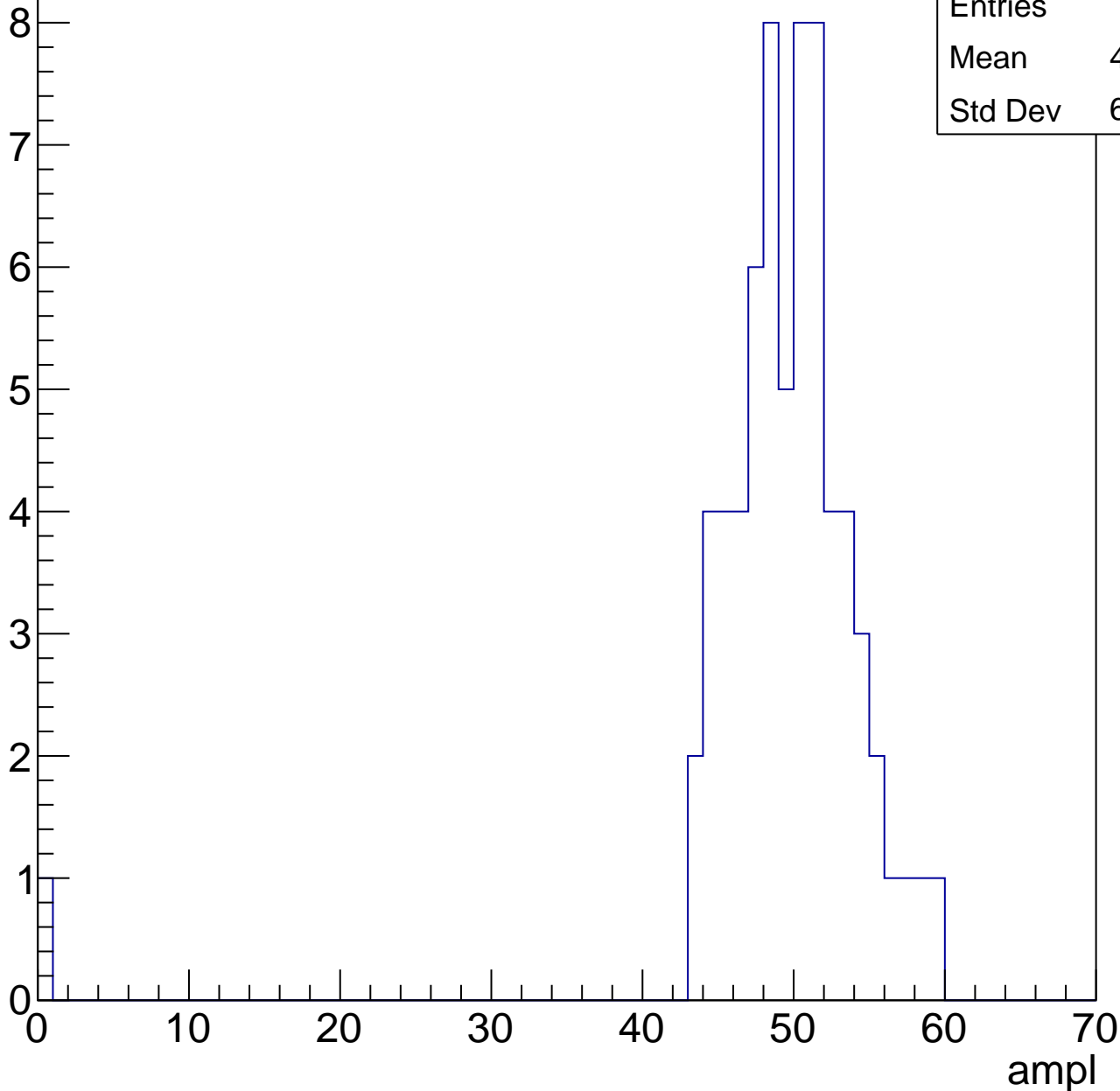


B1L103S, U24-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	48.76
Std Dev	6.999

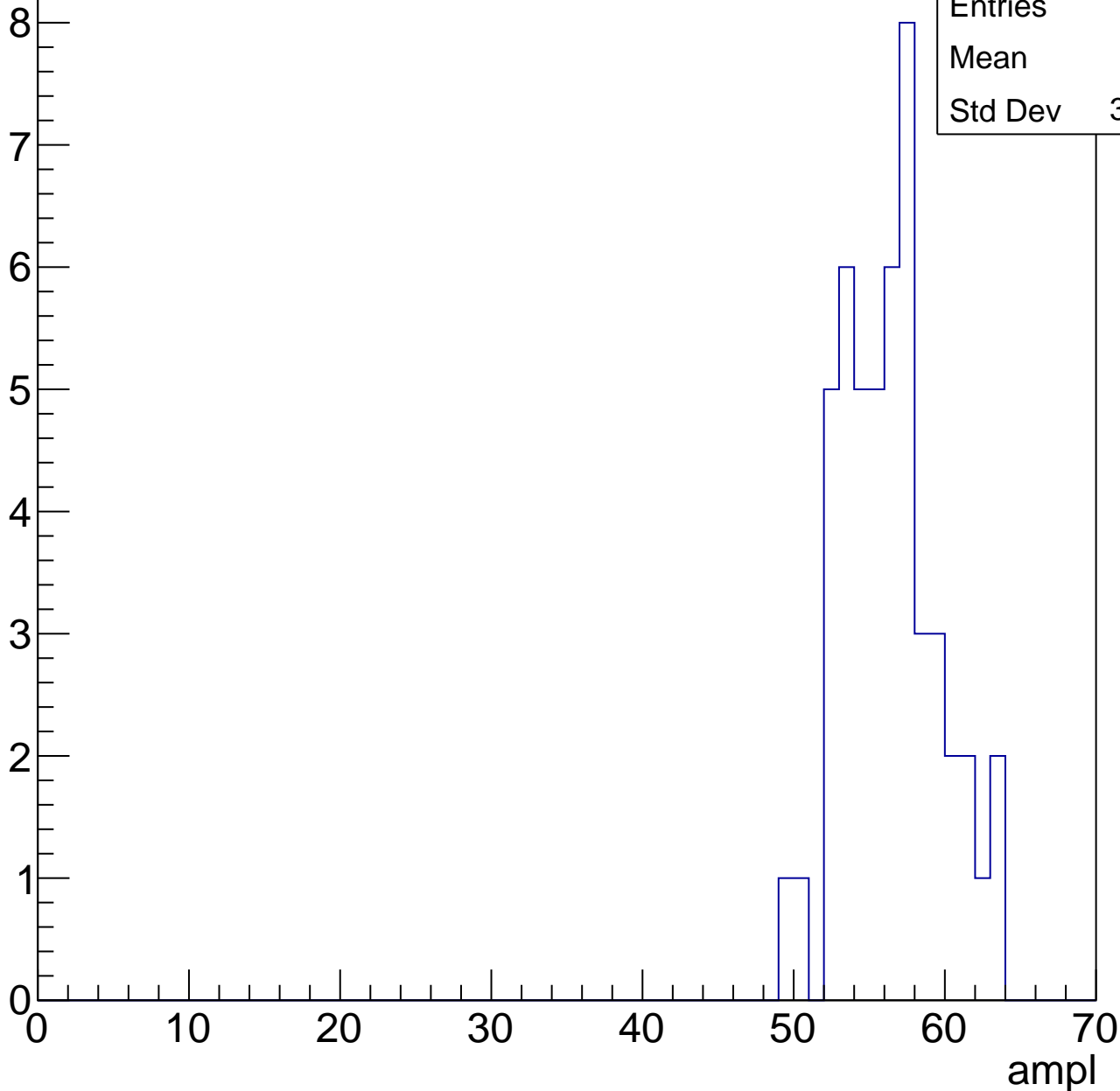


B1L103S, U24-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.9
Std Dev	3.183

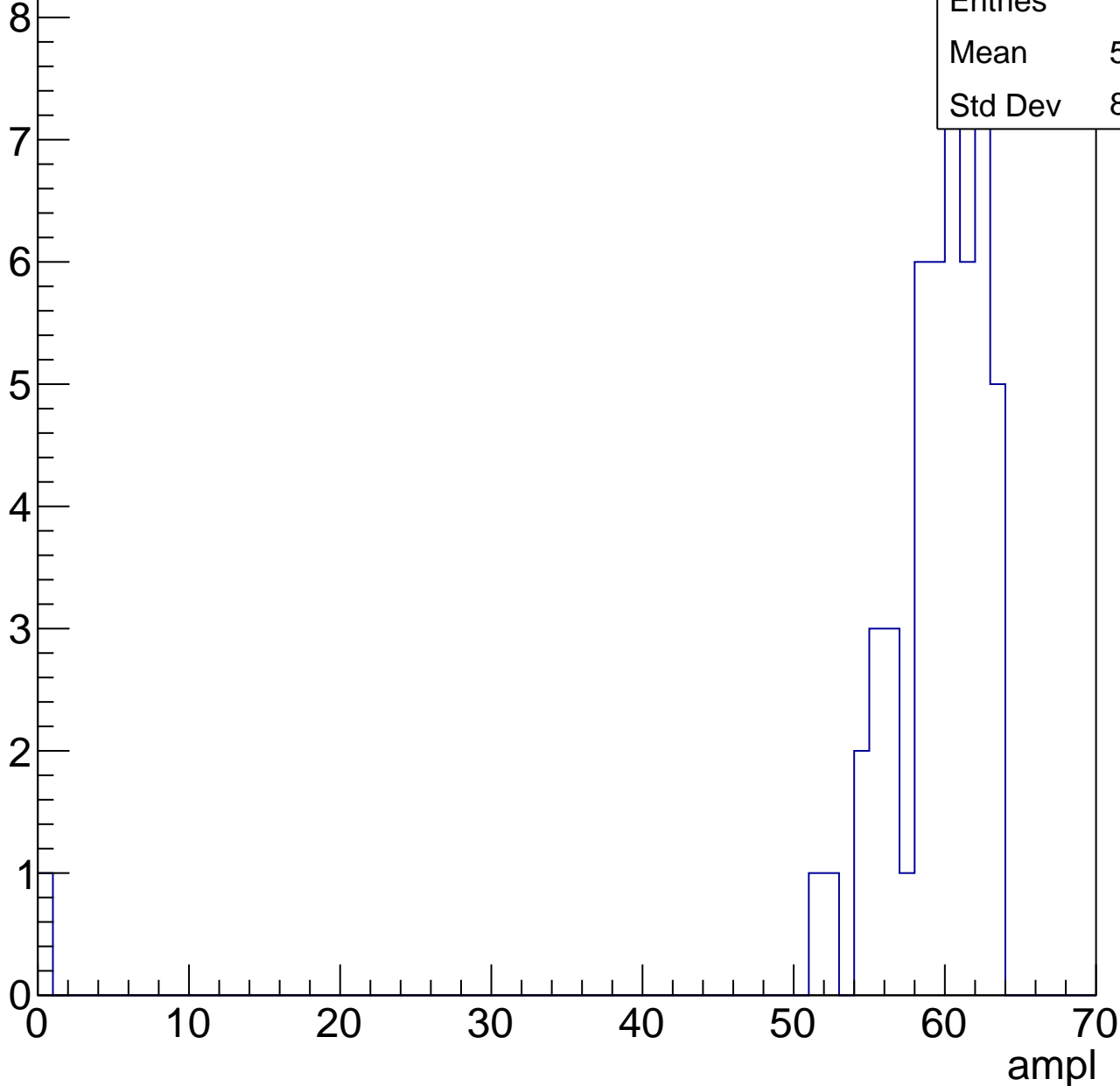


B1L103S, U24-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.04
Std Dev	8.706



B1L103S, U24-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch56, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	23.96
Std Dev	10.56

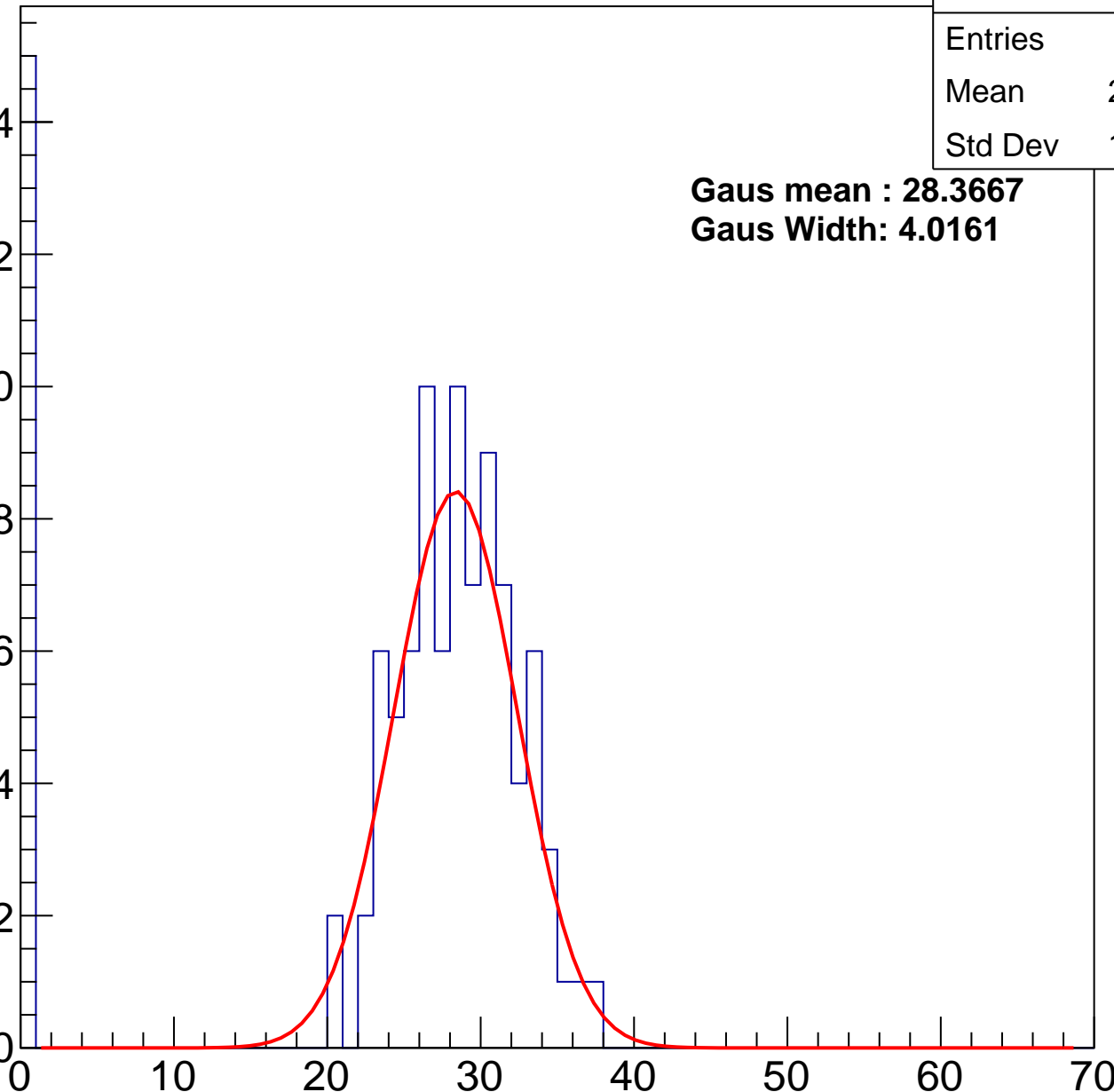
Gaus mean : 28.3667

Gaus Width: 4.0161

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch56, adc1

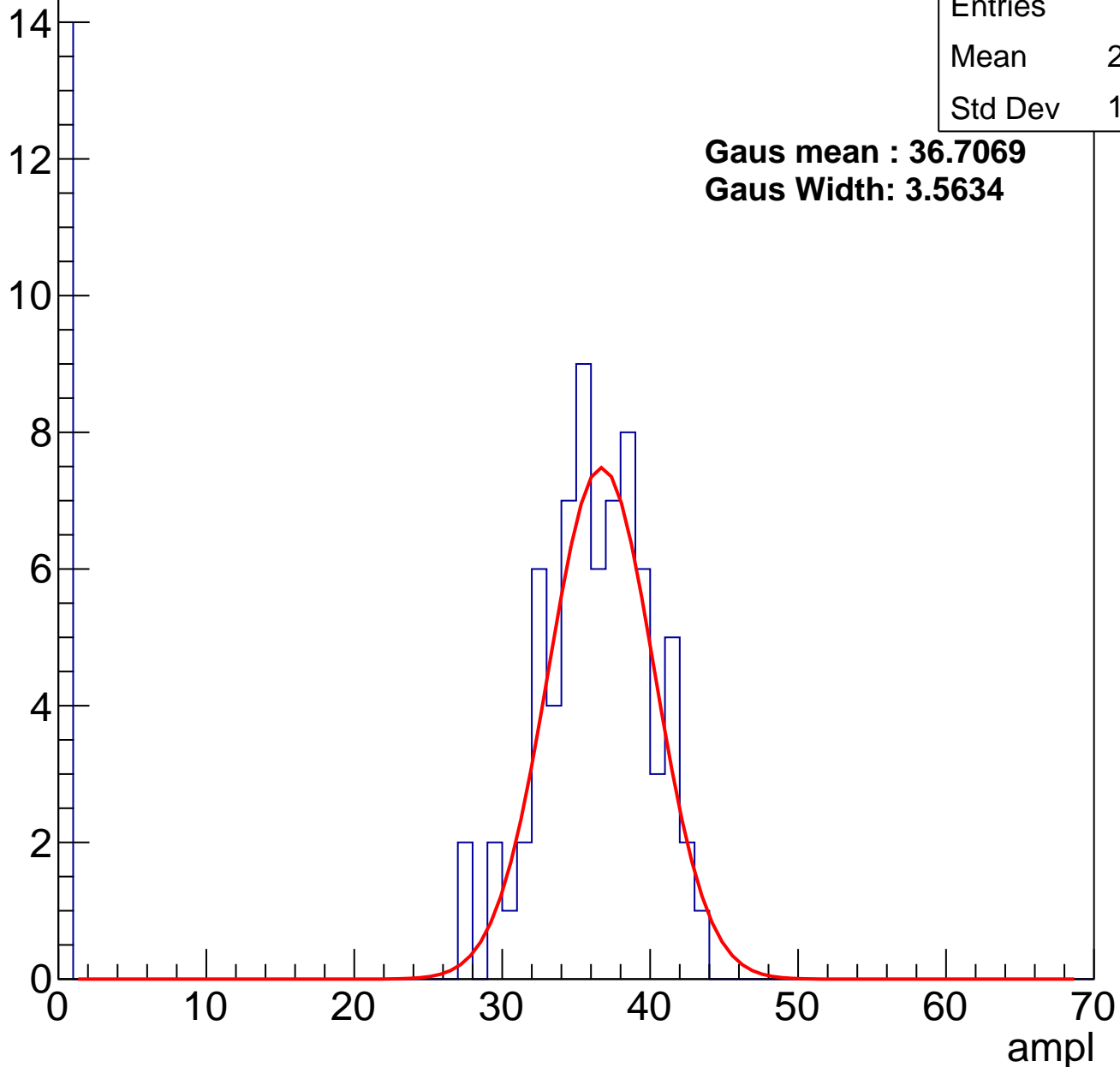
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	29.95
Std Dev	13.69

Gaus mean : 36.7069

Gaus Width: 3.5634

Entry



B1L103S, U24-ch56, adc2

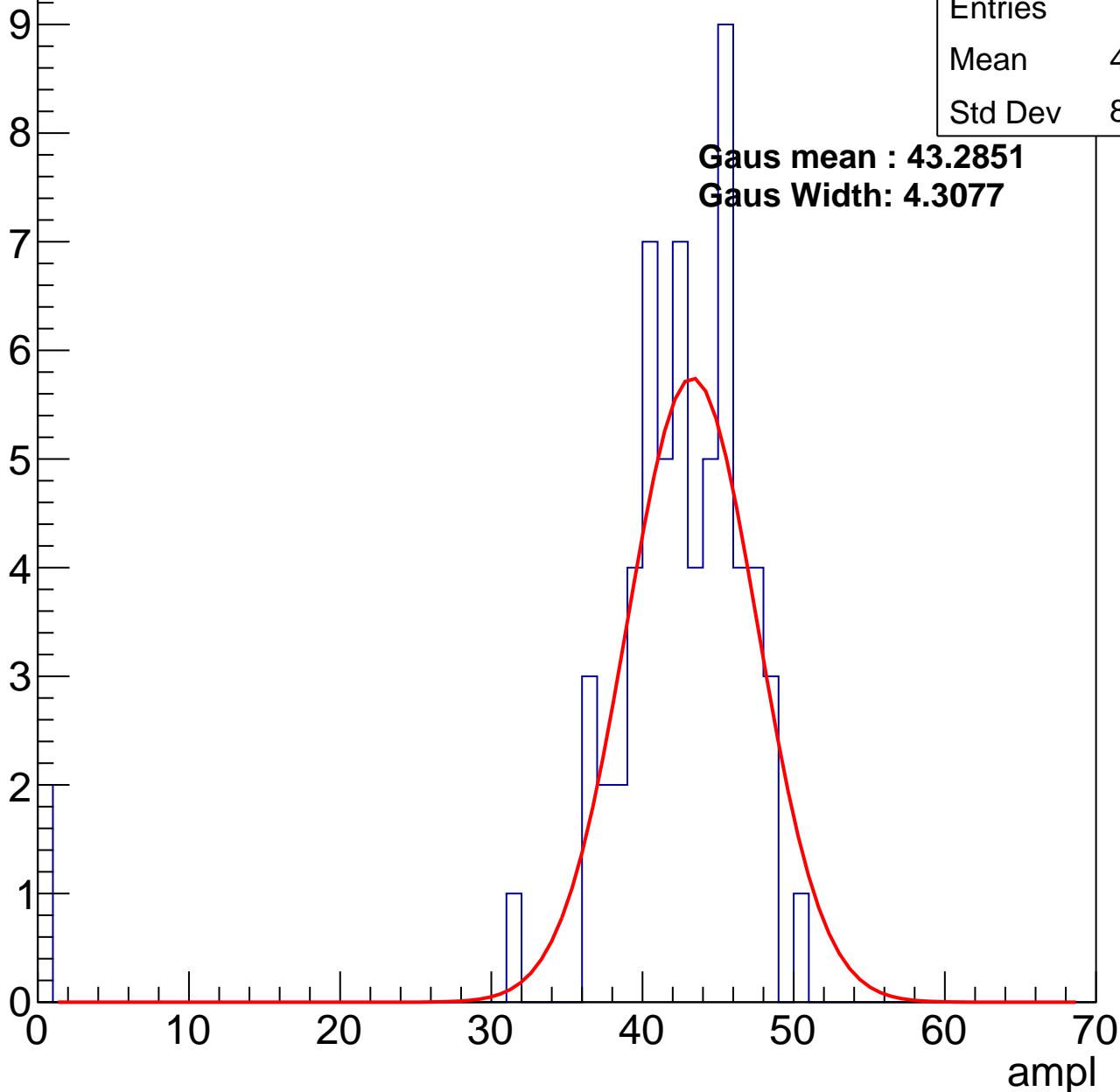
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	41.06
Std Dev	8.256

Gaus mean : 43.2851

Gaus Width: 4.3077

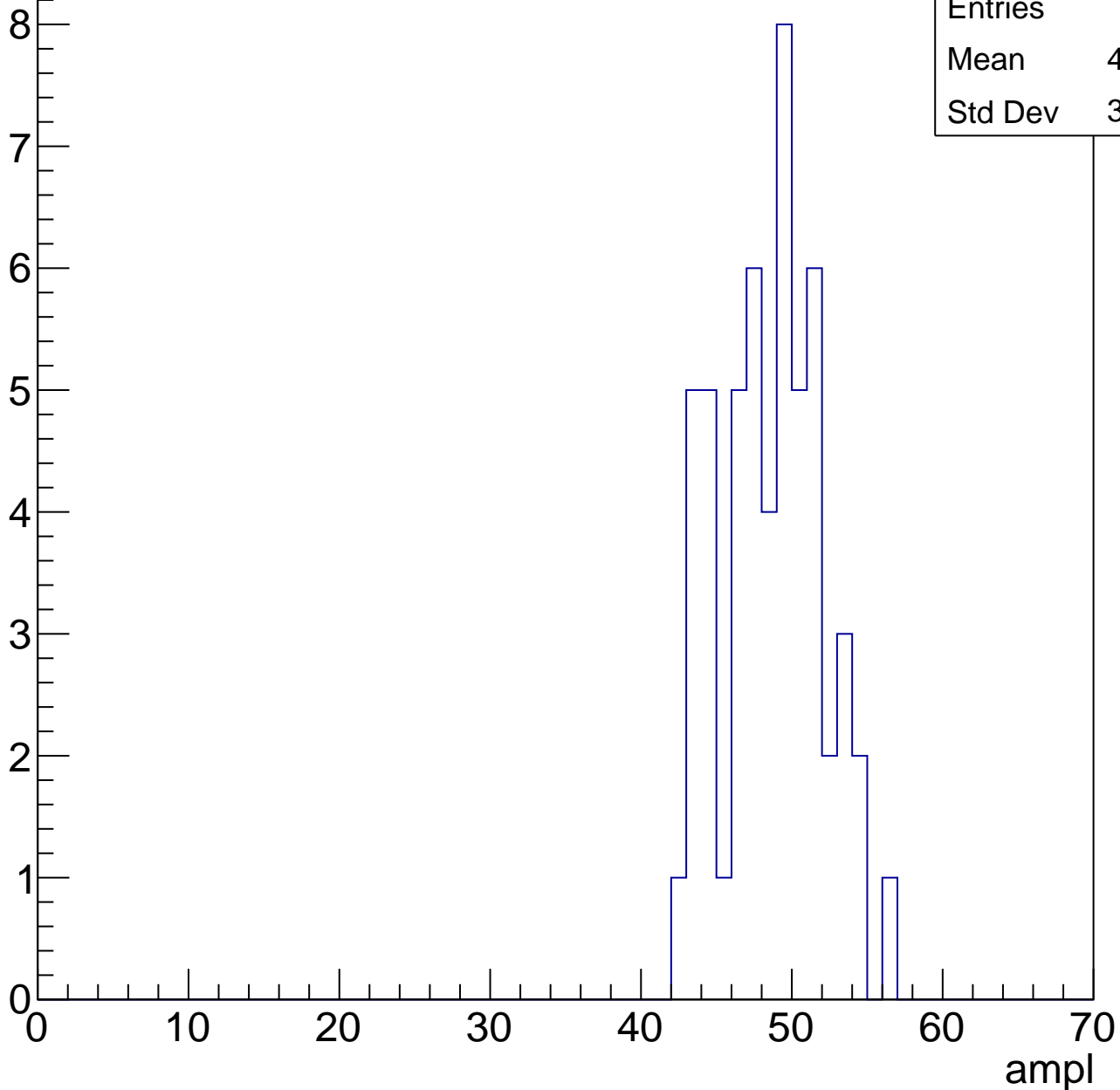


B1L103S, U24-ch56, adc3

calib_packv5_041523_1651.root, FC#0, port C2

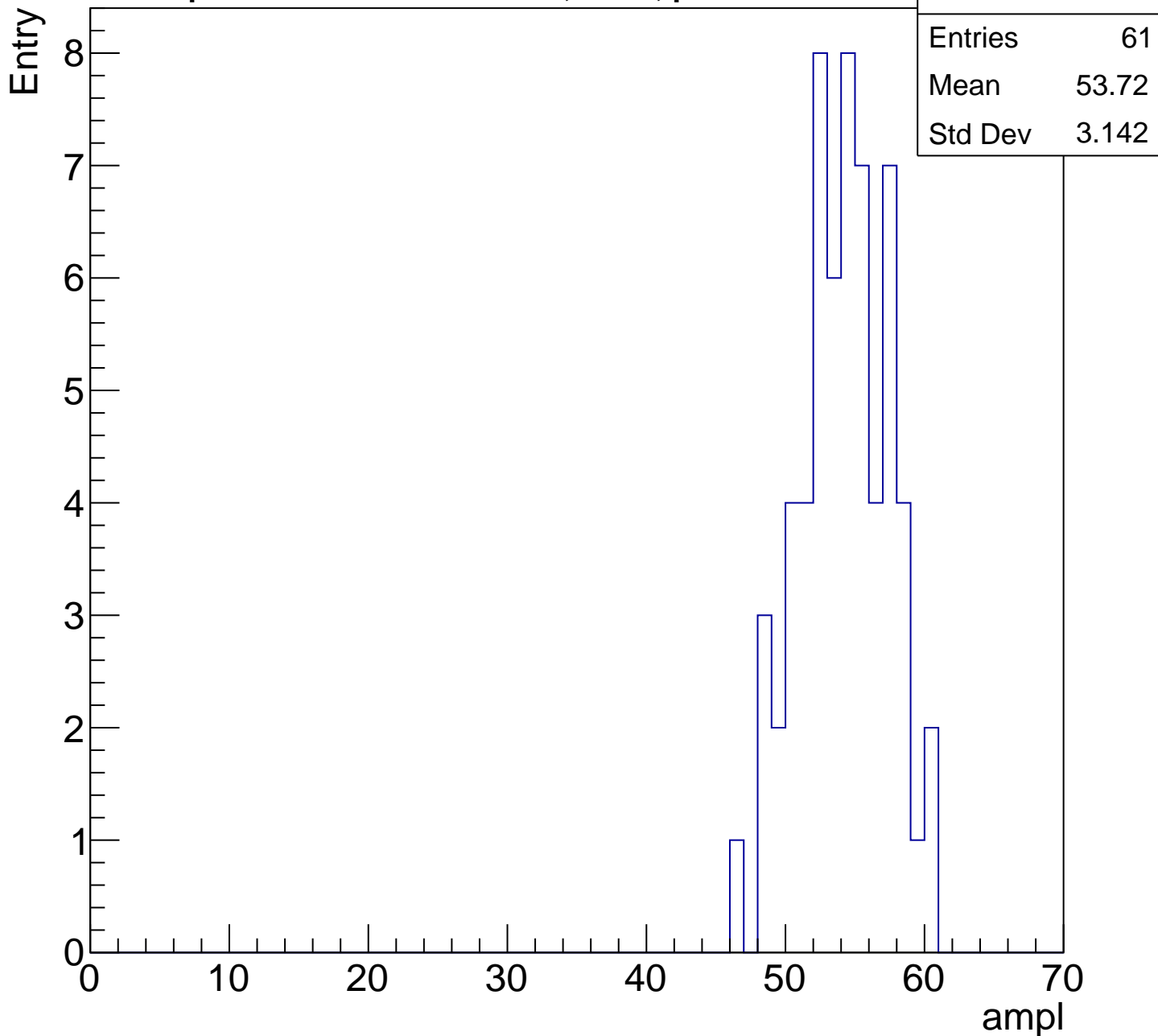
Entry

Entries	54
Mean	48.17
Std Dev	3.332



B1L103S, U24-ch56, adc4

calib_packv5_041523_1651.root, FC#0, port C2

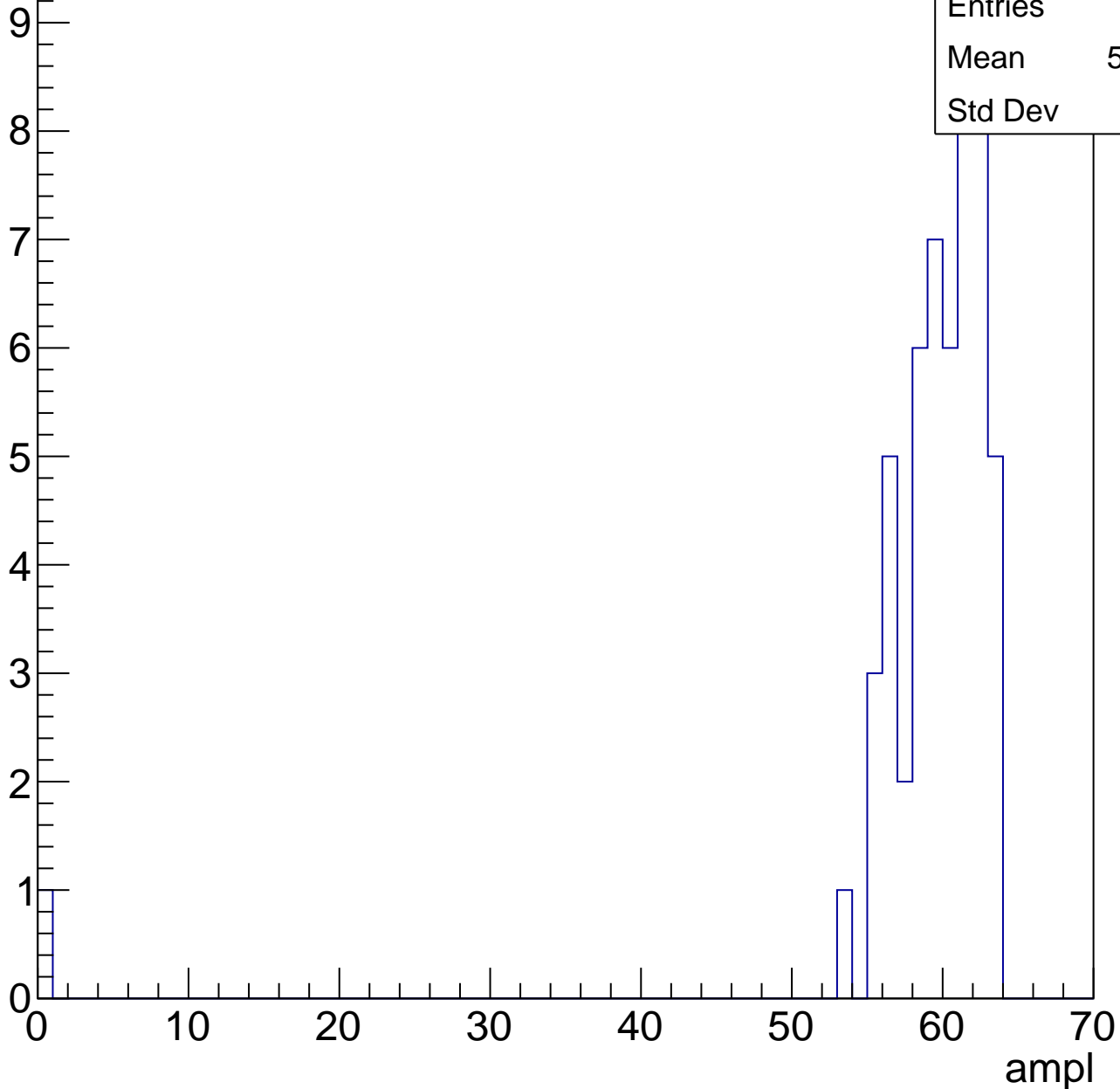


B1L103S, U24-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.38
Std Dev	8.47

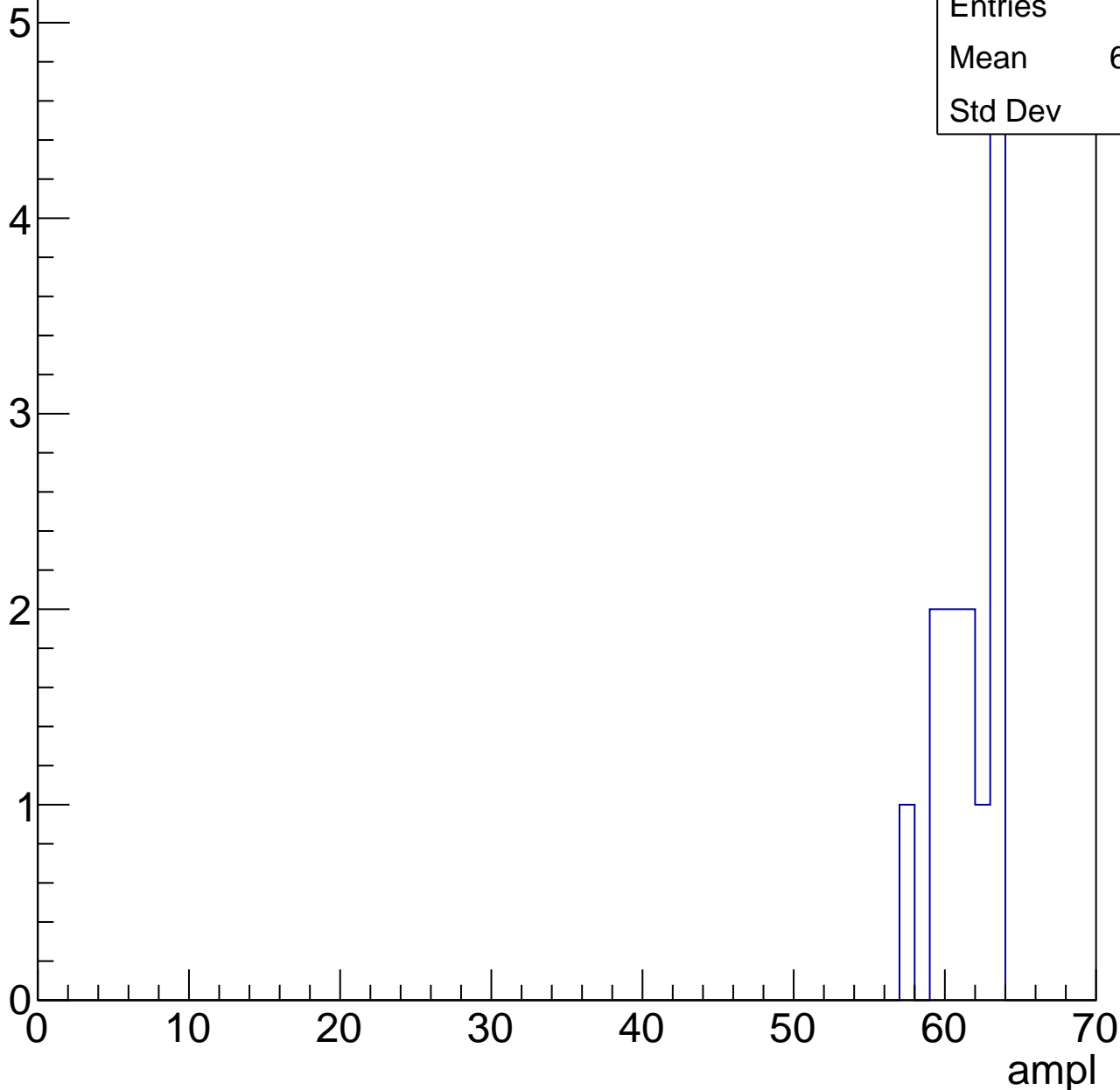


B1L103S, U24-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.08
Std Dev	1.9



B1L103S, U24-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch57, adc0

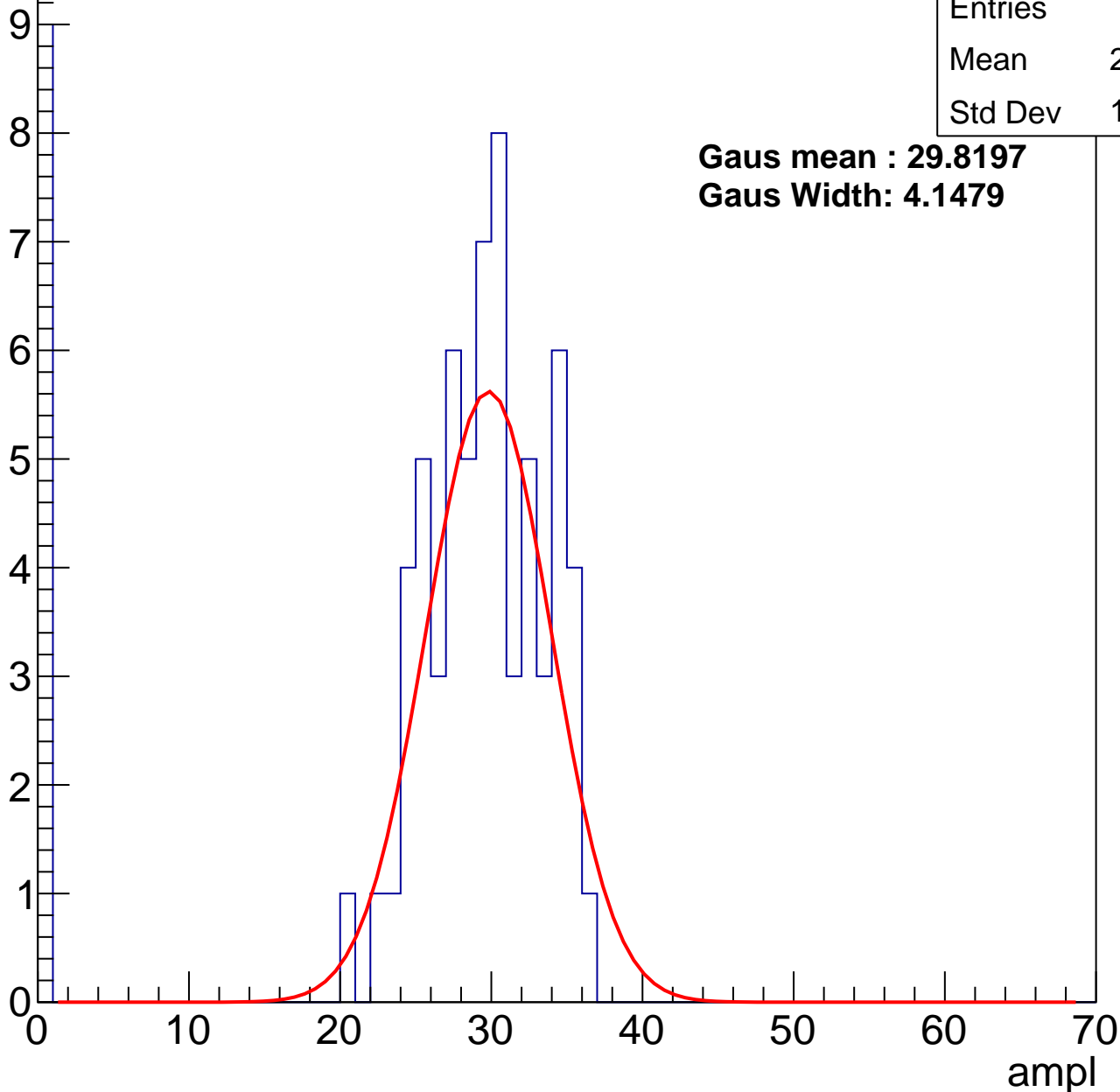
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	25.57
Std Dev	10.26

Gaus mean : 29.8197

Gaus Width: 4.1479



B1L103S, U24-ch57, adc1

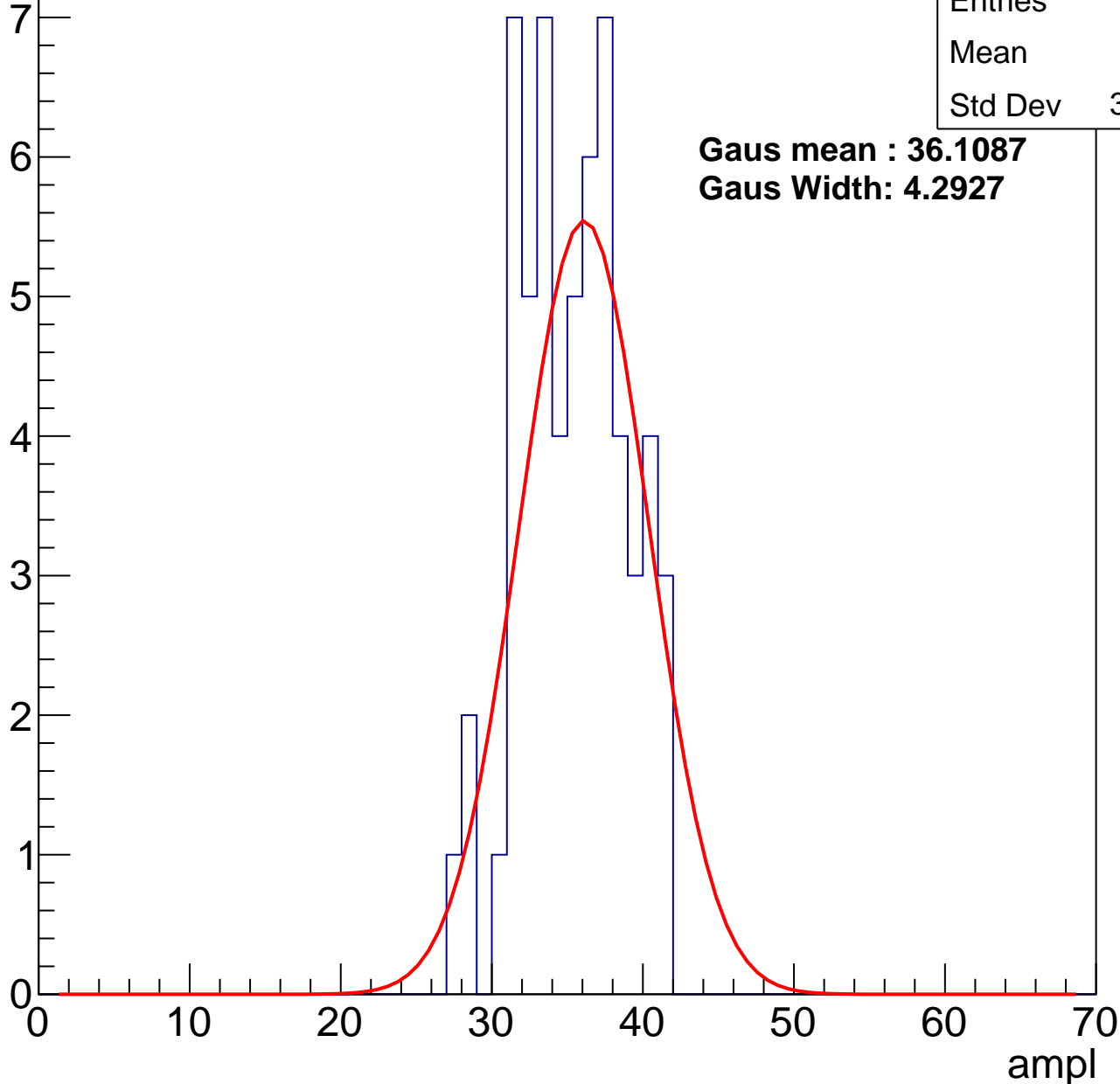
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	34.9
Std Dev	3.448

Gaus mean : 36.1087

Gaus Width: 4.2927



B1L103S, U24-ch57, adc2

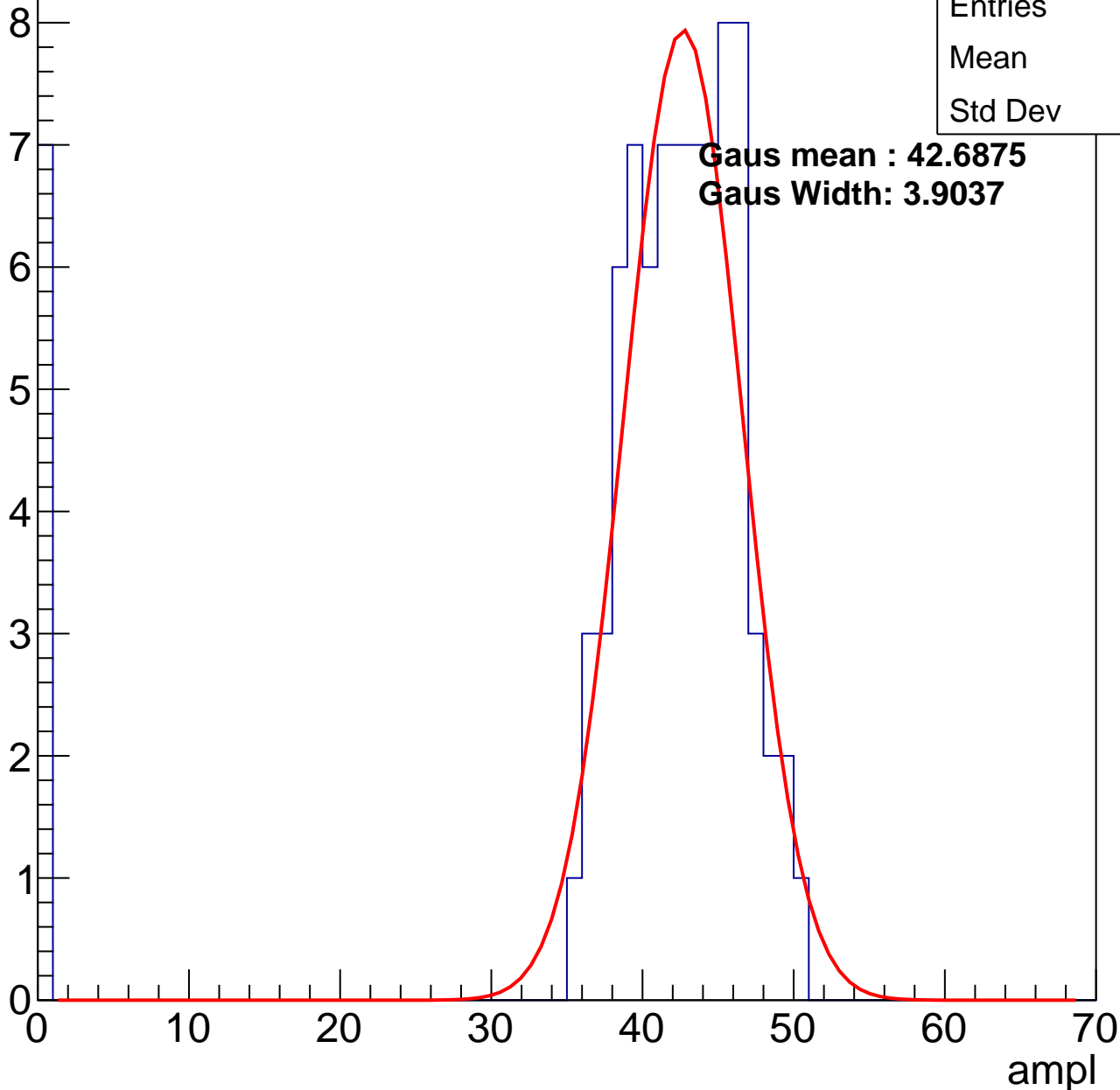
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	38.8
Std Dev	12.1

Gaus mean : 42.6875

Gaus Width: 3.9037

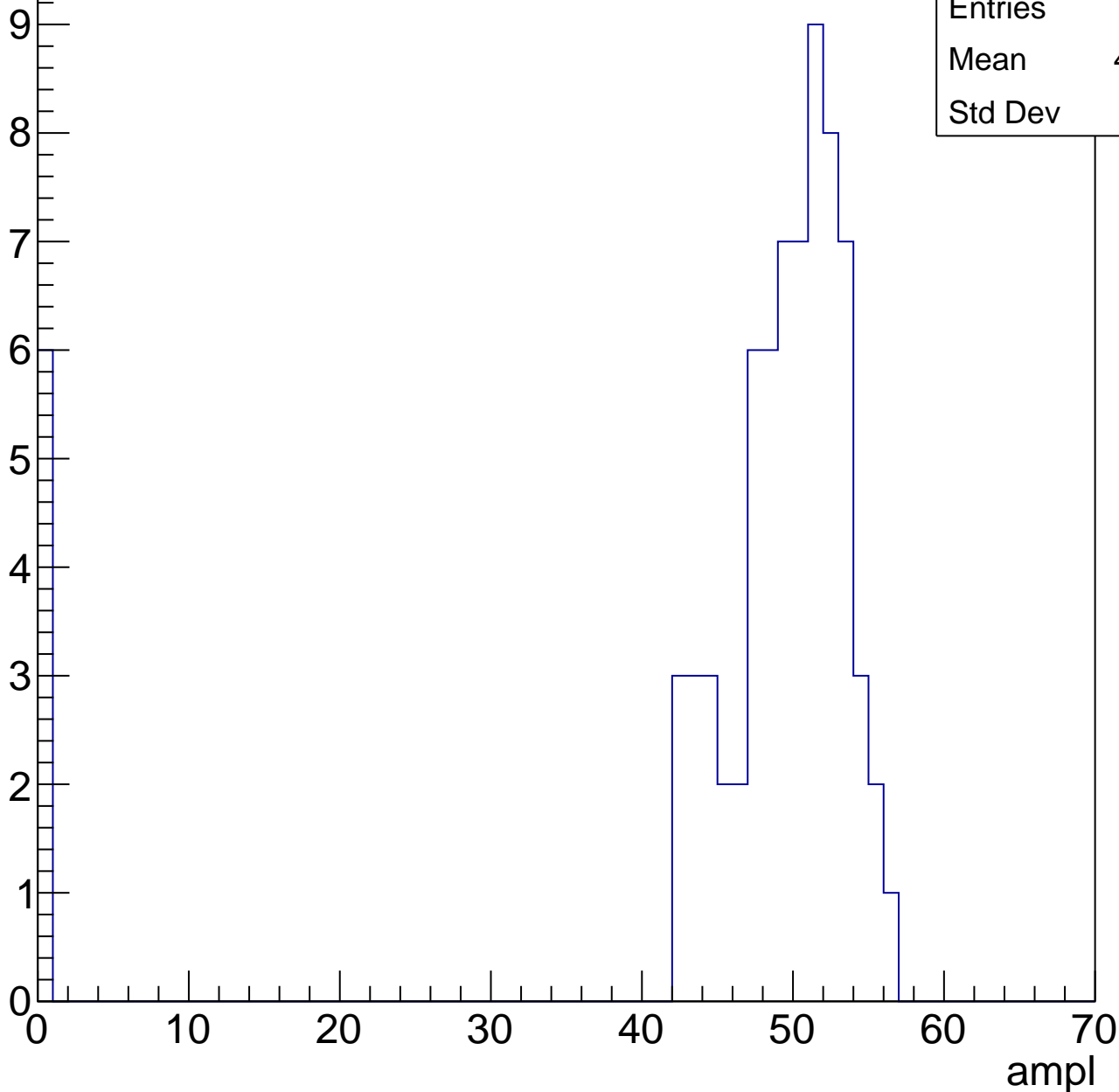


B1L103S, U24-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	45.41
Std Dev	13.8

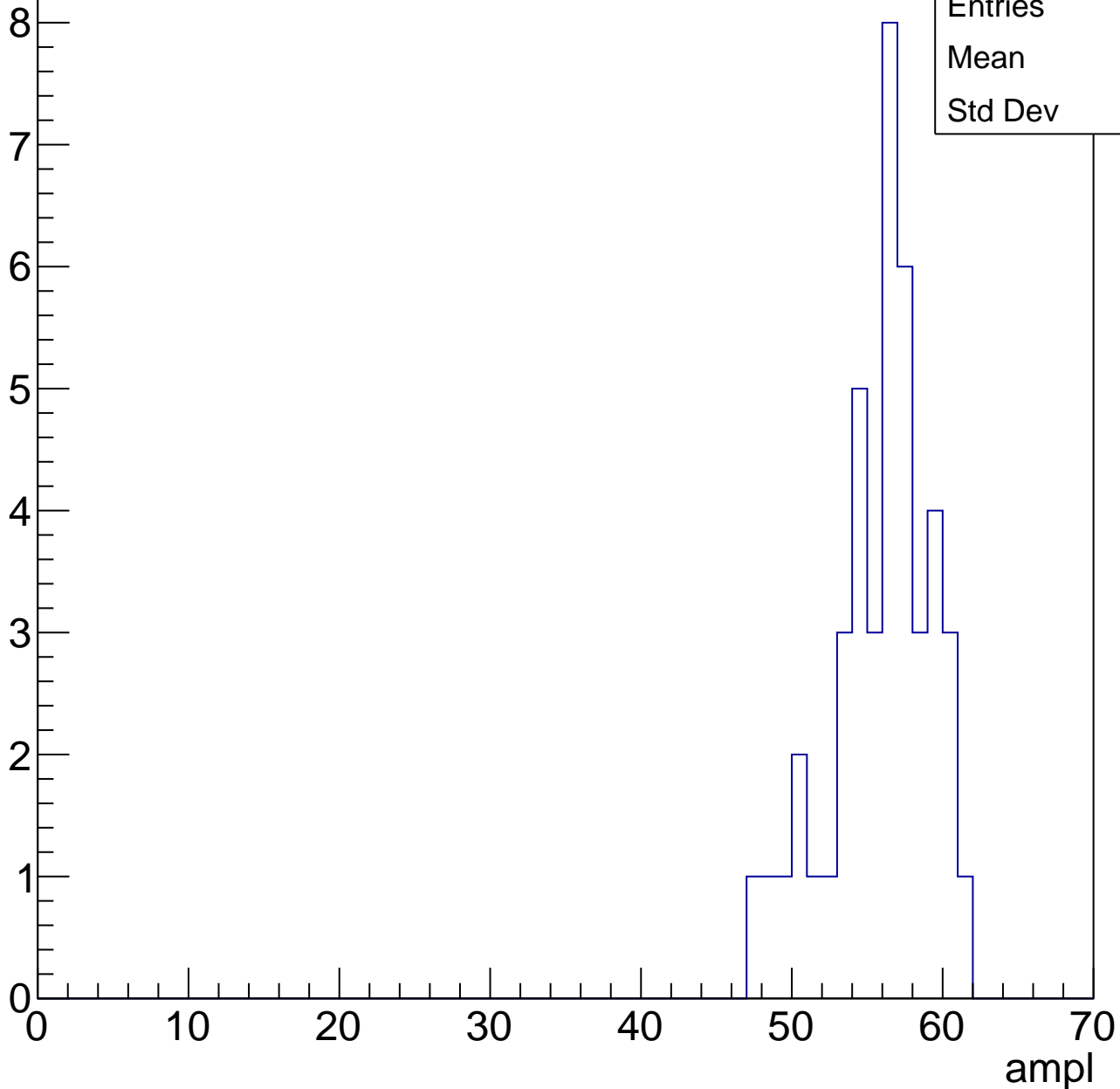


B1L103S, U24-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	55.4
Std Dev	3.3

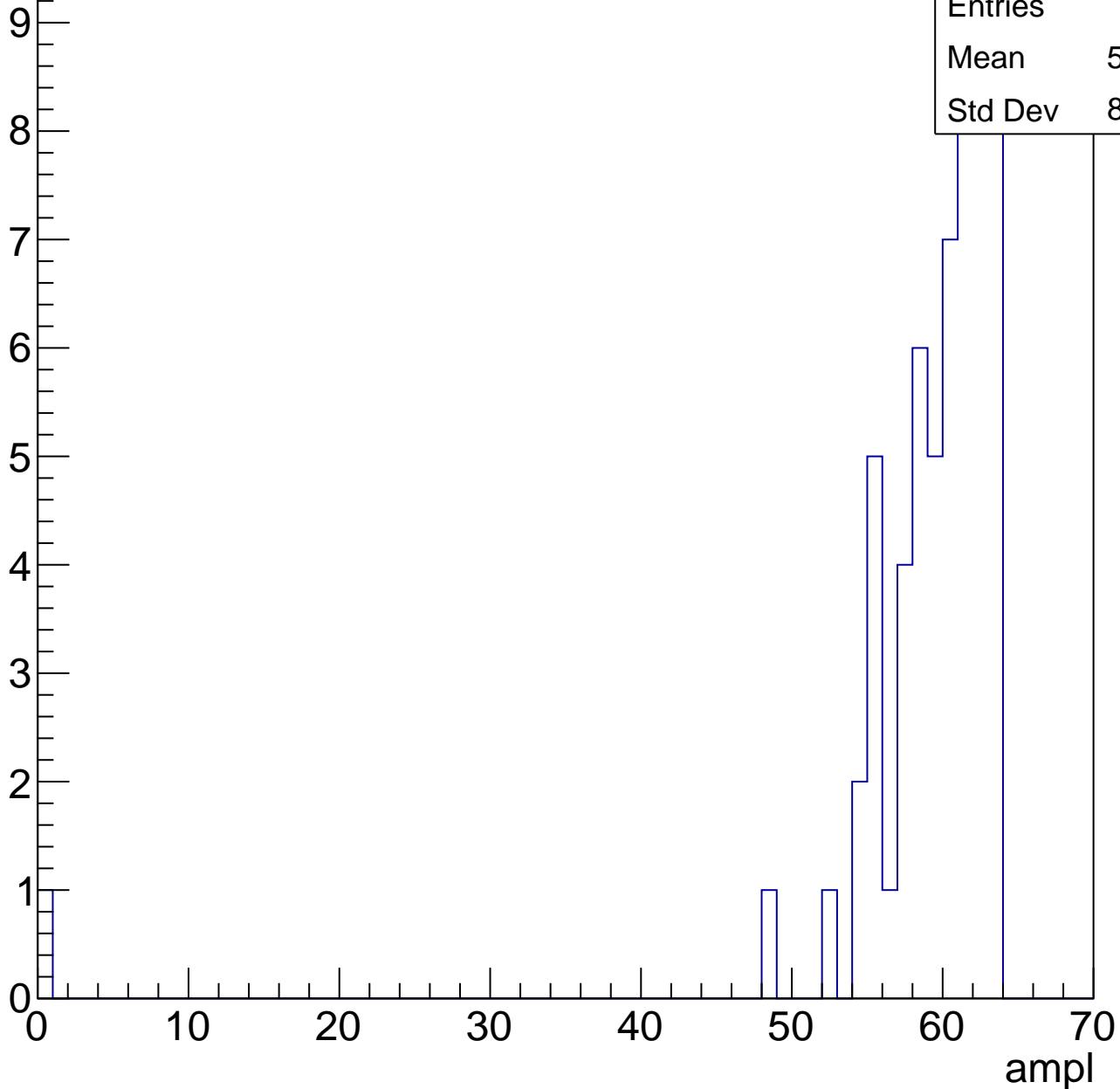


B1L103S, U24-ch57, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

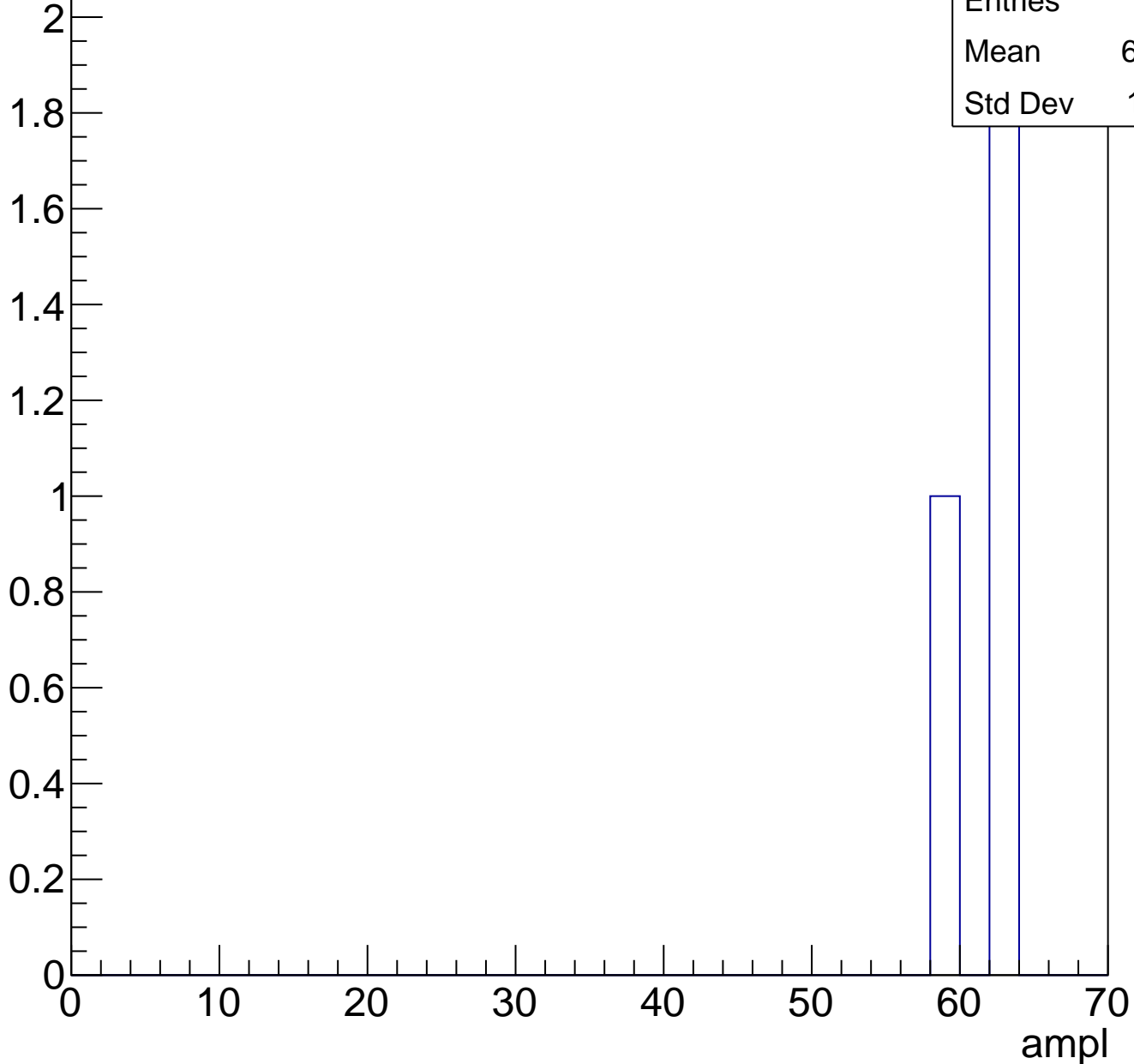
Entries	58
Mean	58.26
Std Dev	8.329



B1L103S, U24-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch58, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	23.09
Std Dev	11.09

Gaus mean : 28.0717

Gaus Width: 2.8312

Entry

12

10

8

6

4

2

0

0

10

20

30

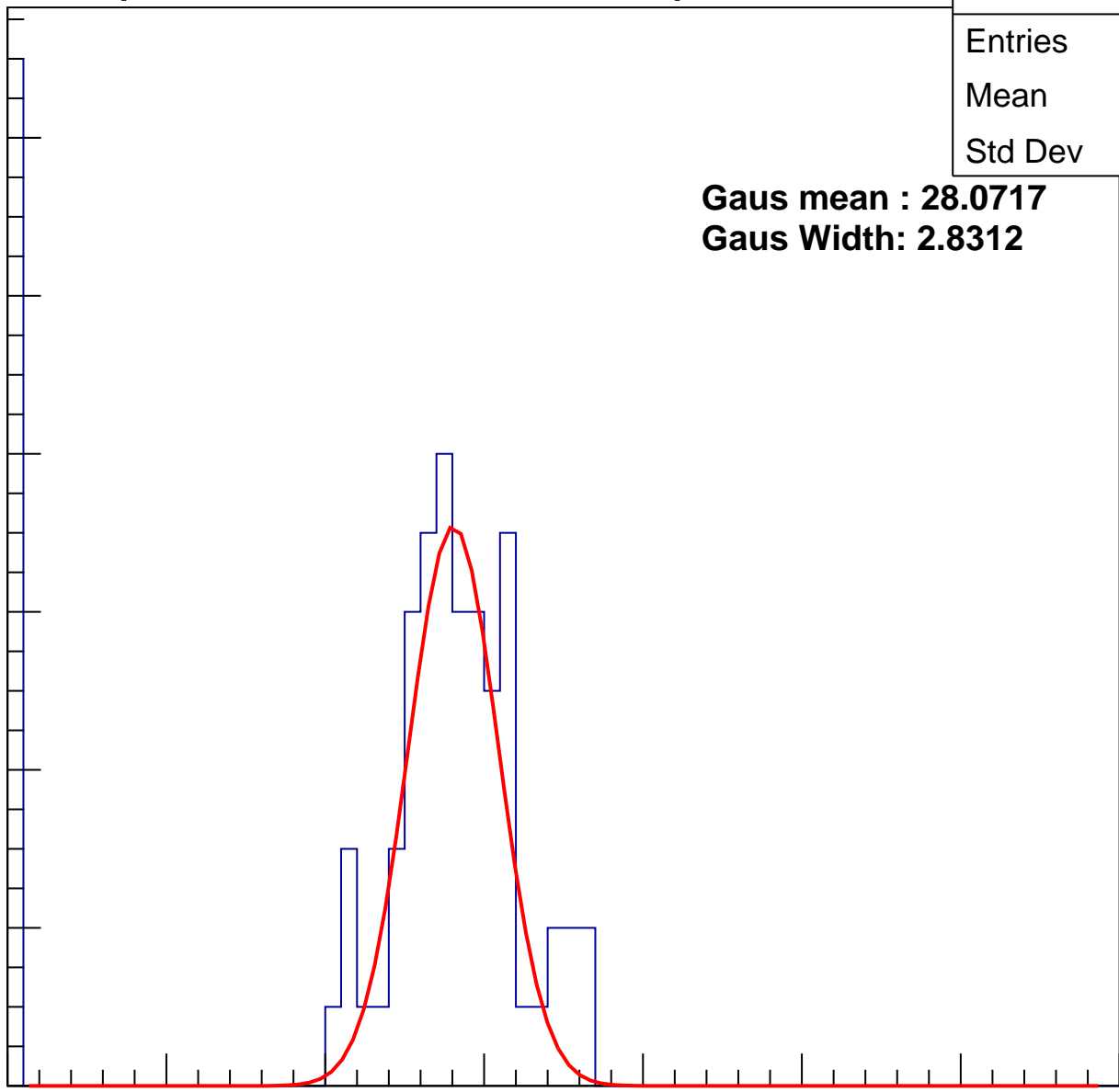
40

50

60

70

ampl



B1L103S, U24-ch58, adc1

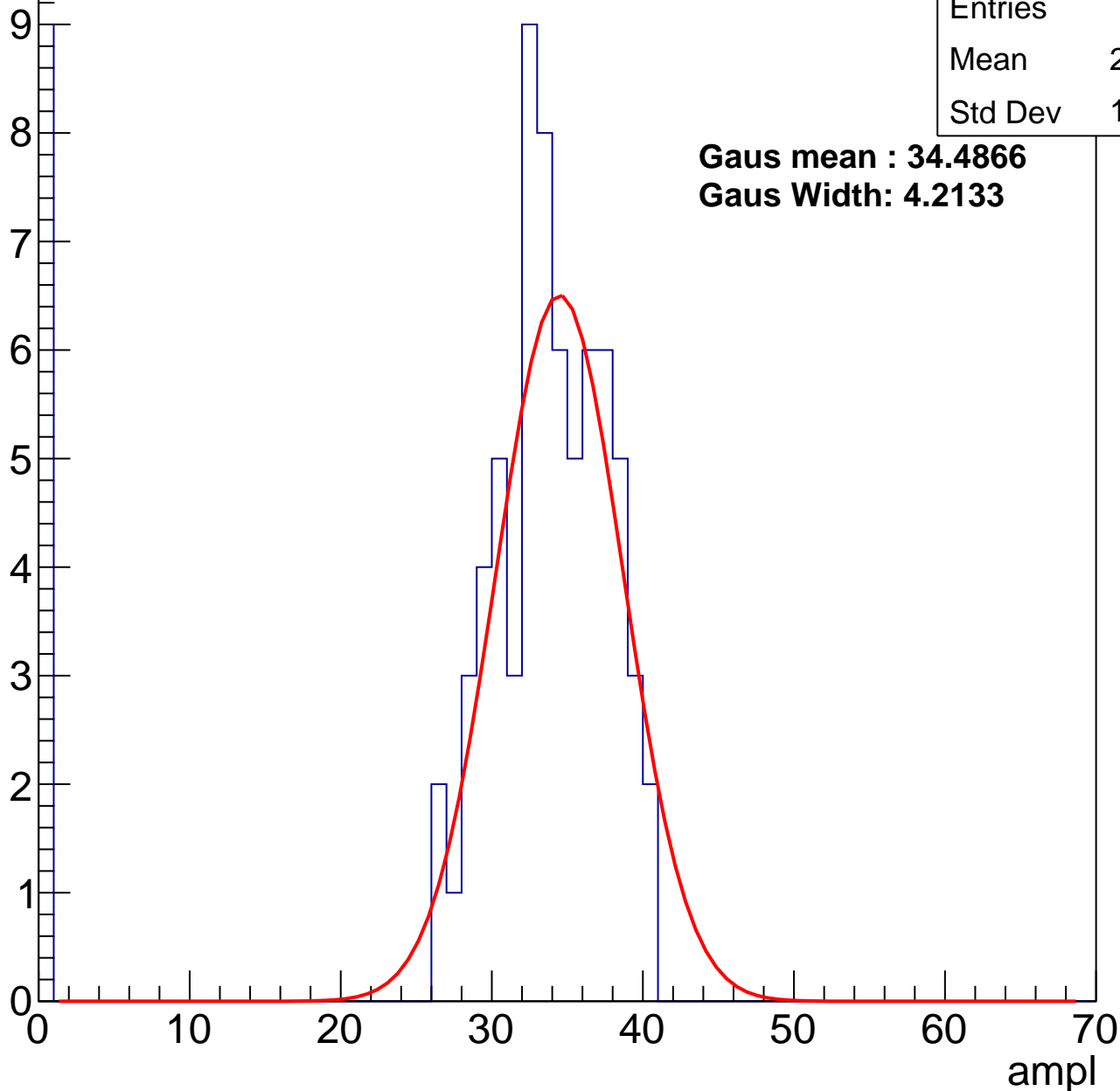
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	29.58
Std Dev	11.25

Gaus mean : 34.4866

Gaus Width: 4.2133



B1L103S, U24-ch58, adc2

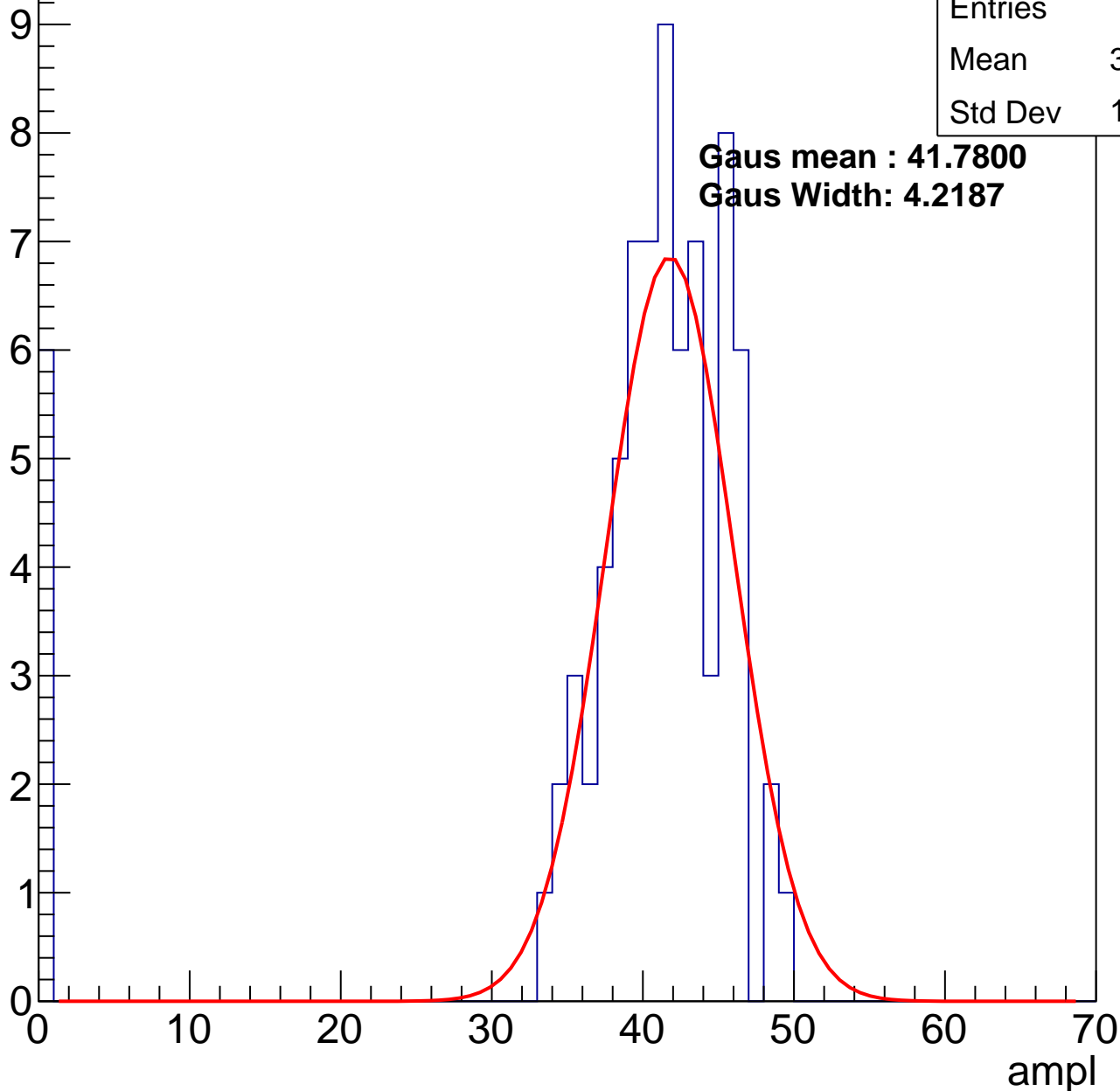
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	38.03
Std Dev	11.45

Gaus mean : 41.7800

Gaus Width: 4.2187

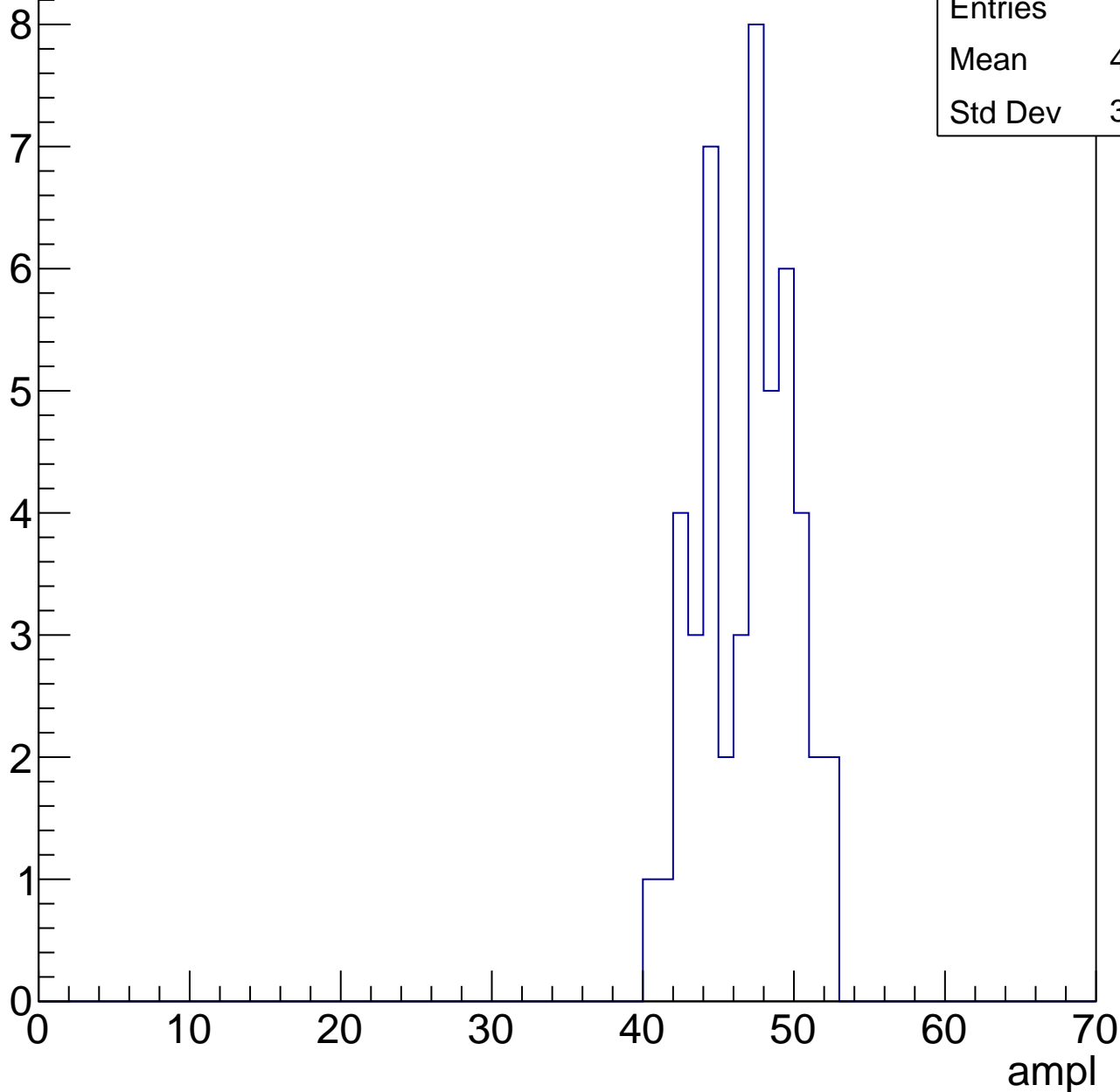


B1L103S, U24-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	46.46
Std Dev	3.027

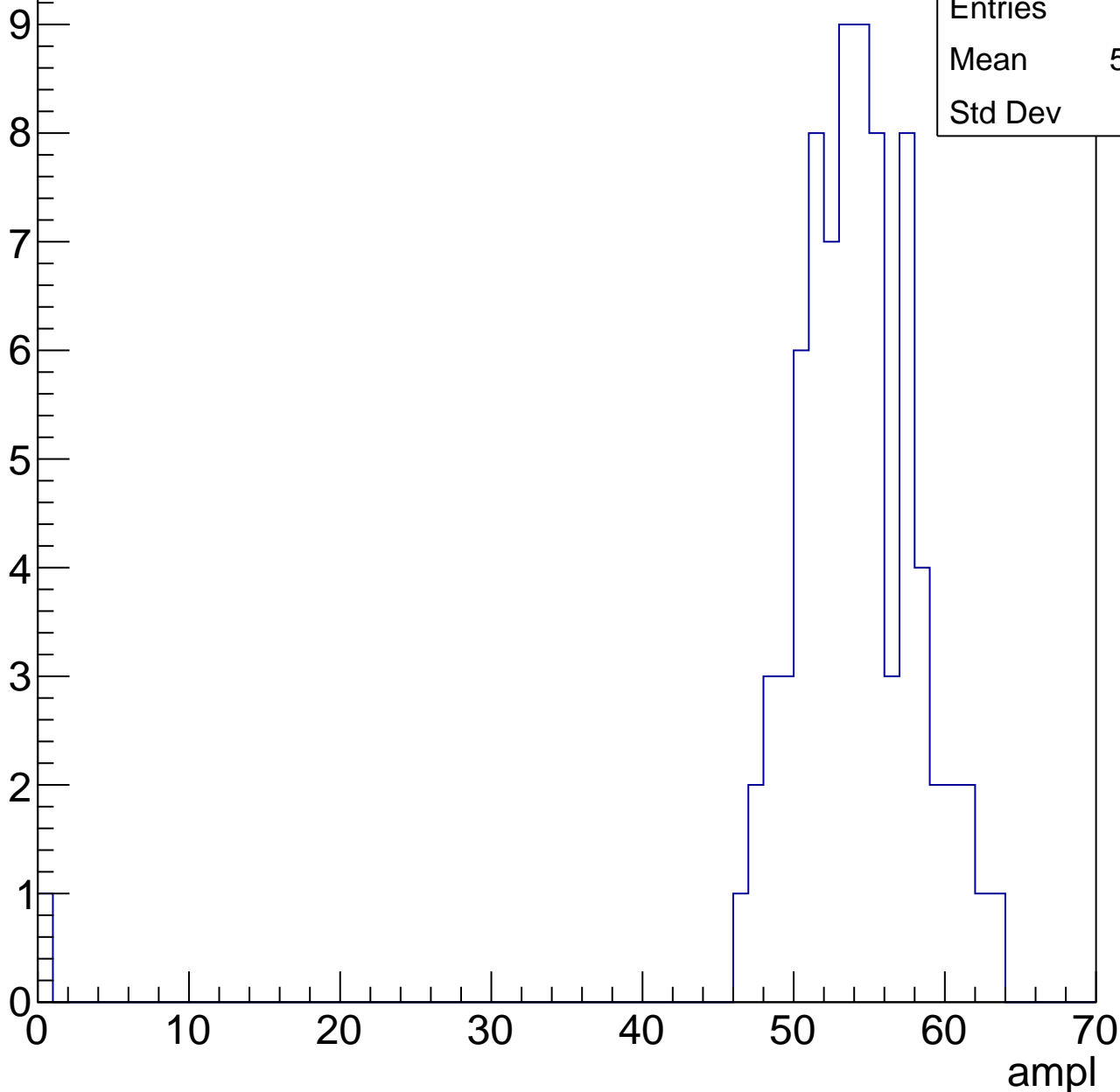


B1L103S, U24-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

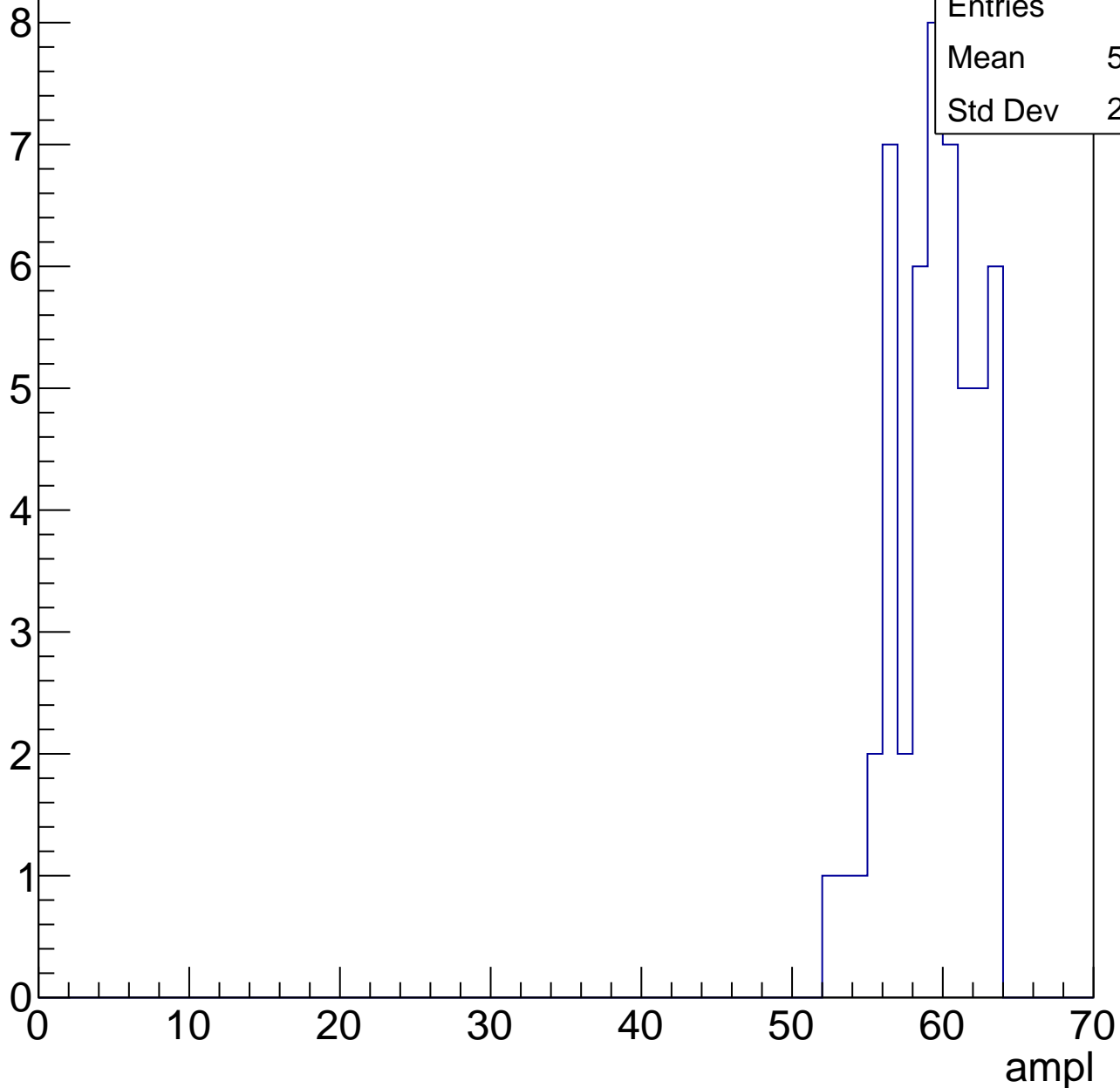
Entries	80
Mean	53.09
Std Dev	7



B1L103S, U24-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch58, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

9

Mean

61.67

Std Dev

0.9428

B1L103S, U24-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U24-ch59, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	21.36
Std Dev	13.14

Gaus mean : 29.5814

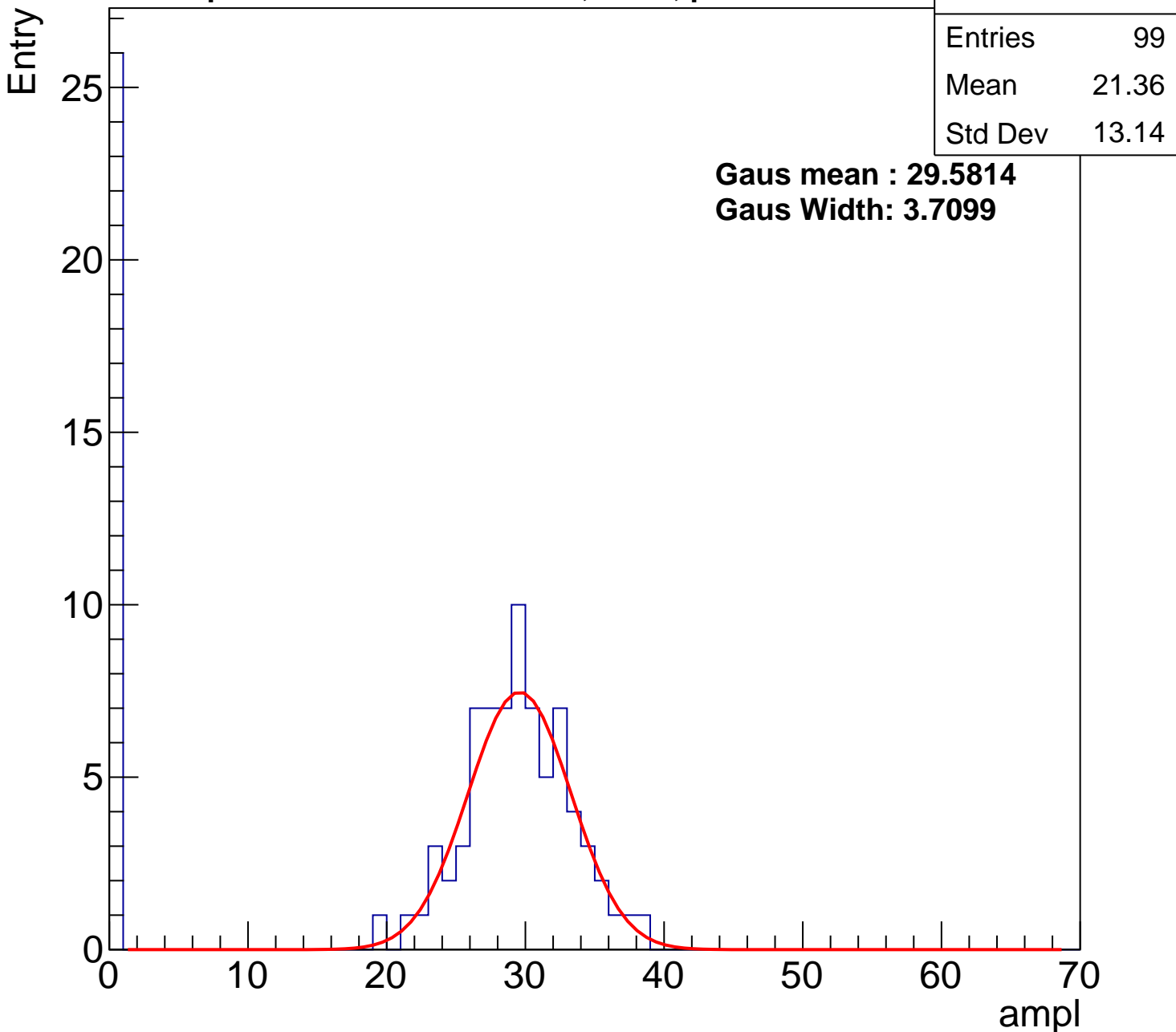
Gaus Width: 3.7099

Entry

25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch59, adc1

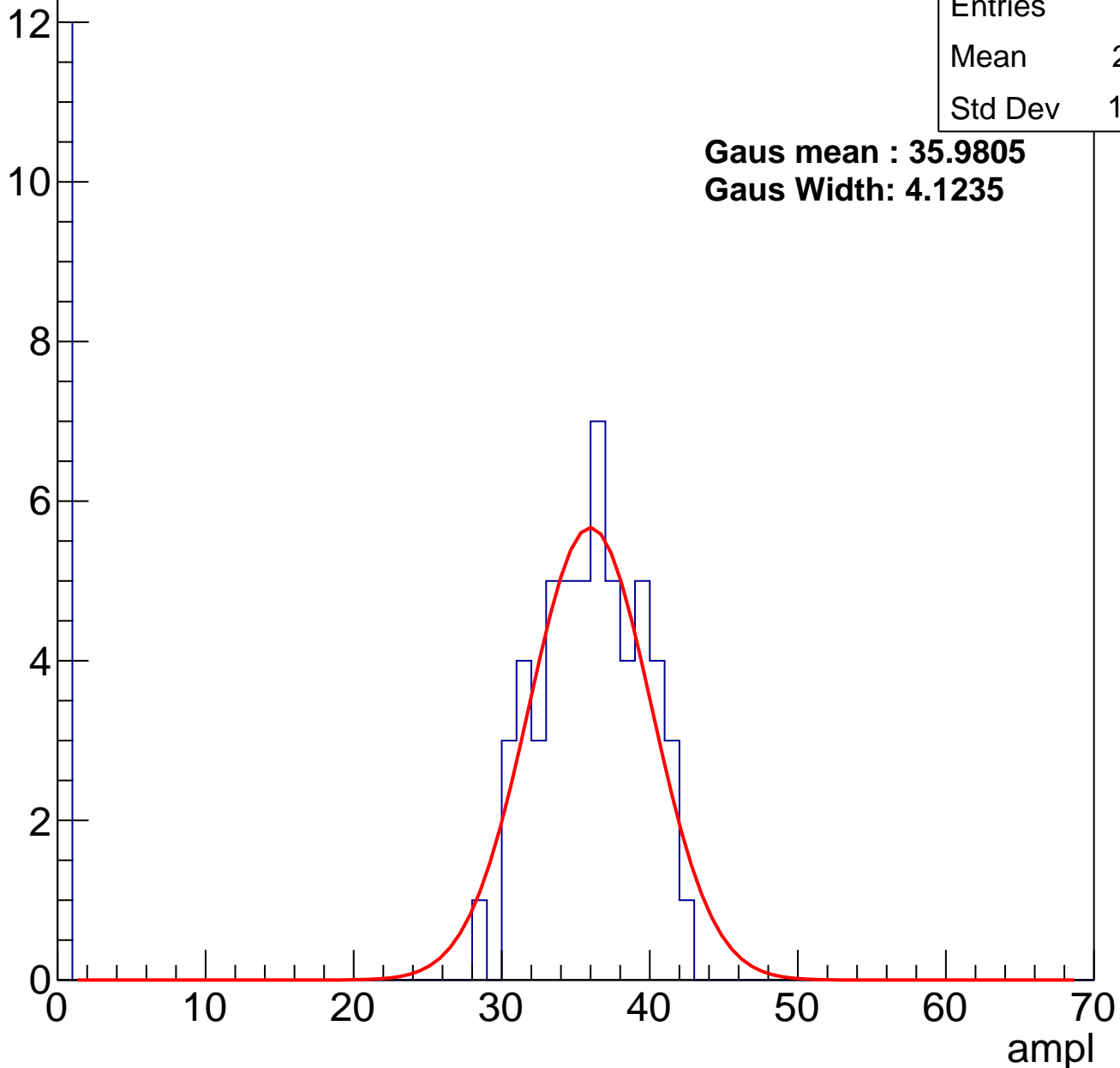
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	29.21
Std Dev	13.98

Gaus mean : 35.9805

Gaus Width: 4.1235

Entry



B1L103S, U24-ch59, adc2

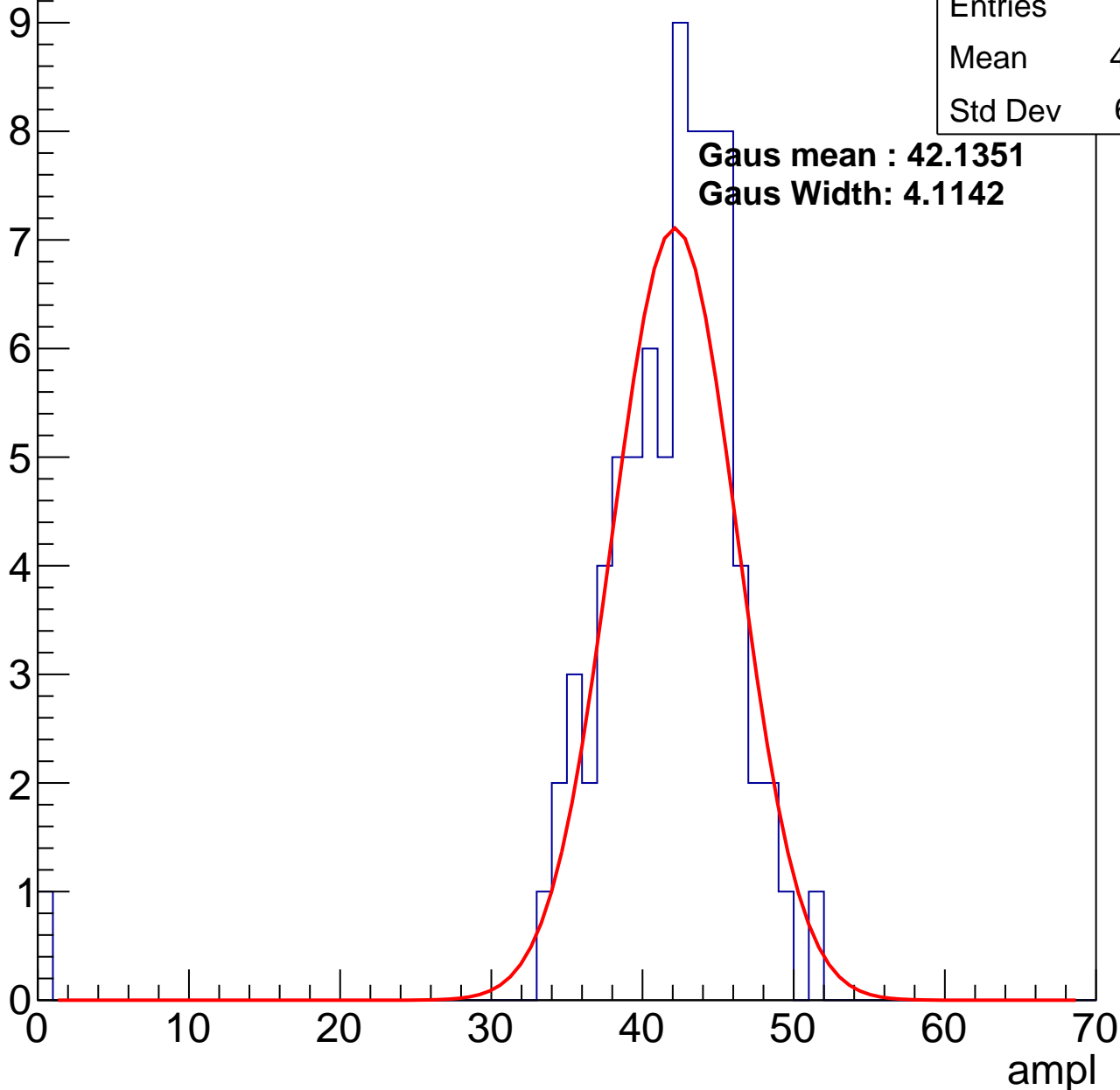
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	41.09
Std Dev	6.041

Gaus mean : 42.1351

Gaus Width: 4.1142

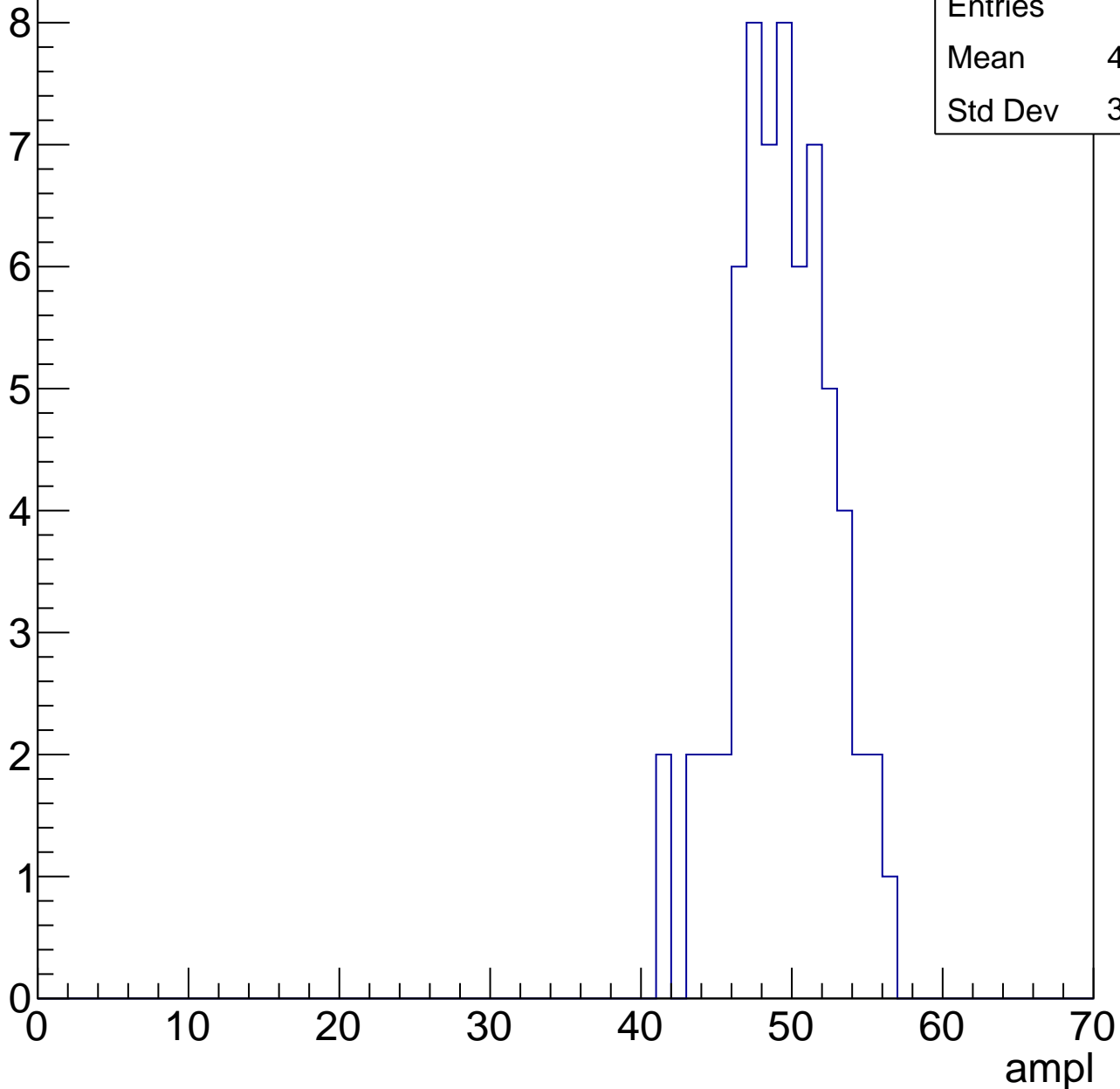


B1L103S, U24-ch59, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

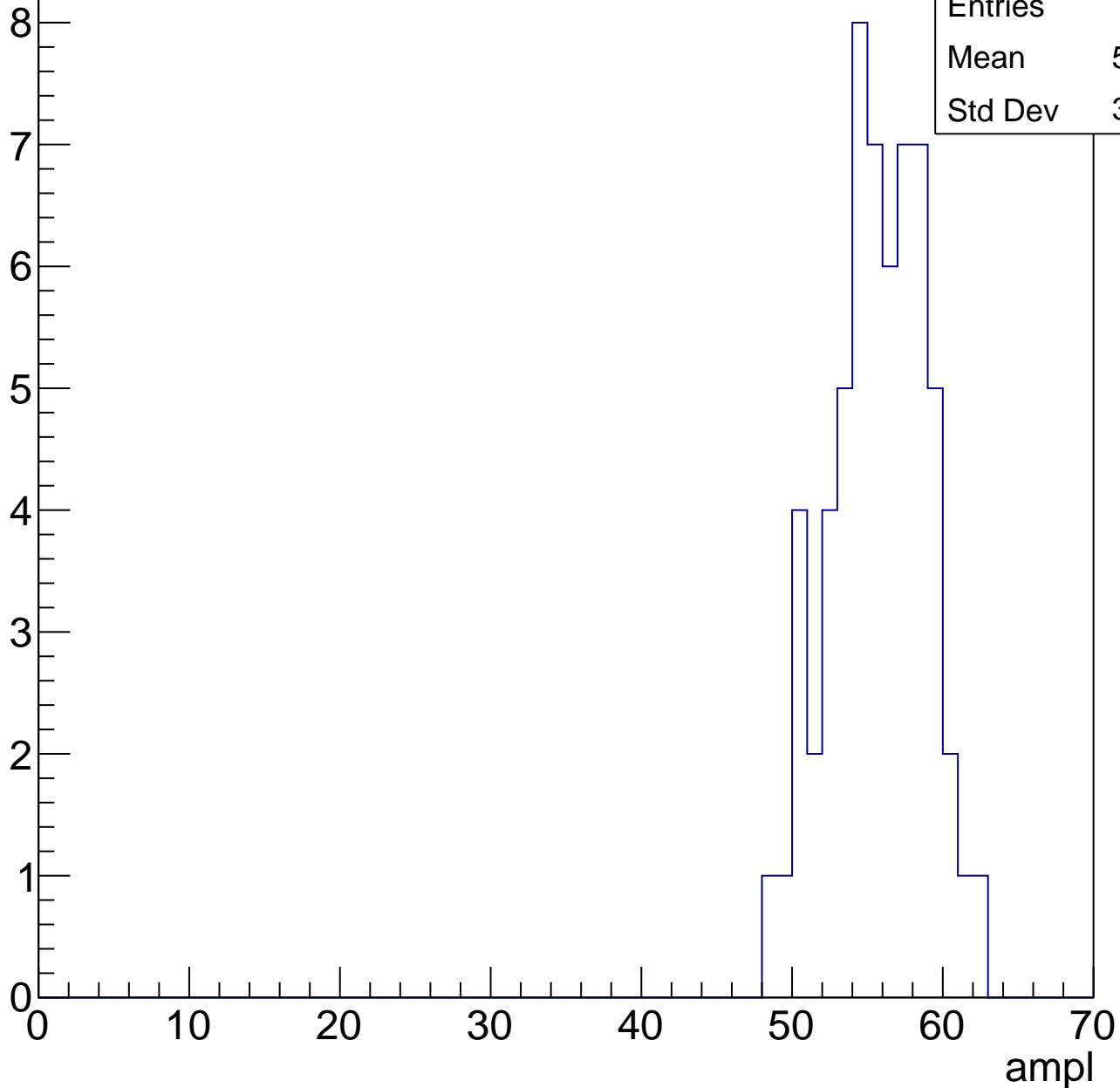
Entries	64
Mean	48.89
Std Dev	3.279



B1L103S, U24-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



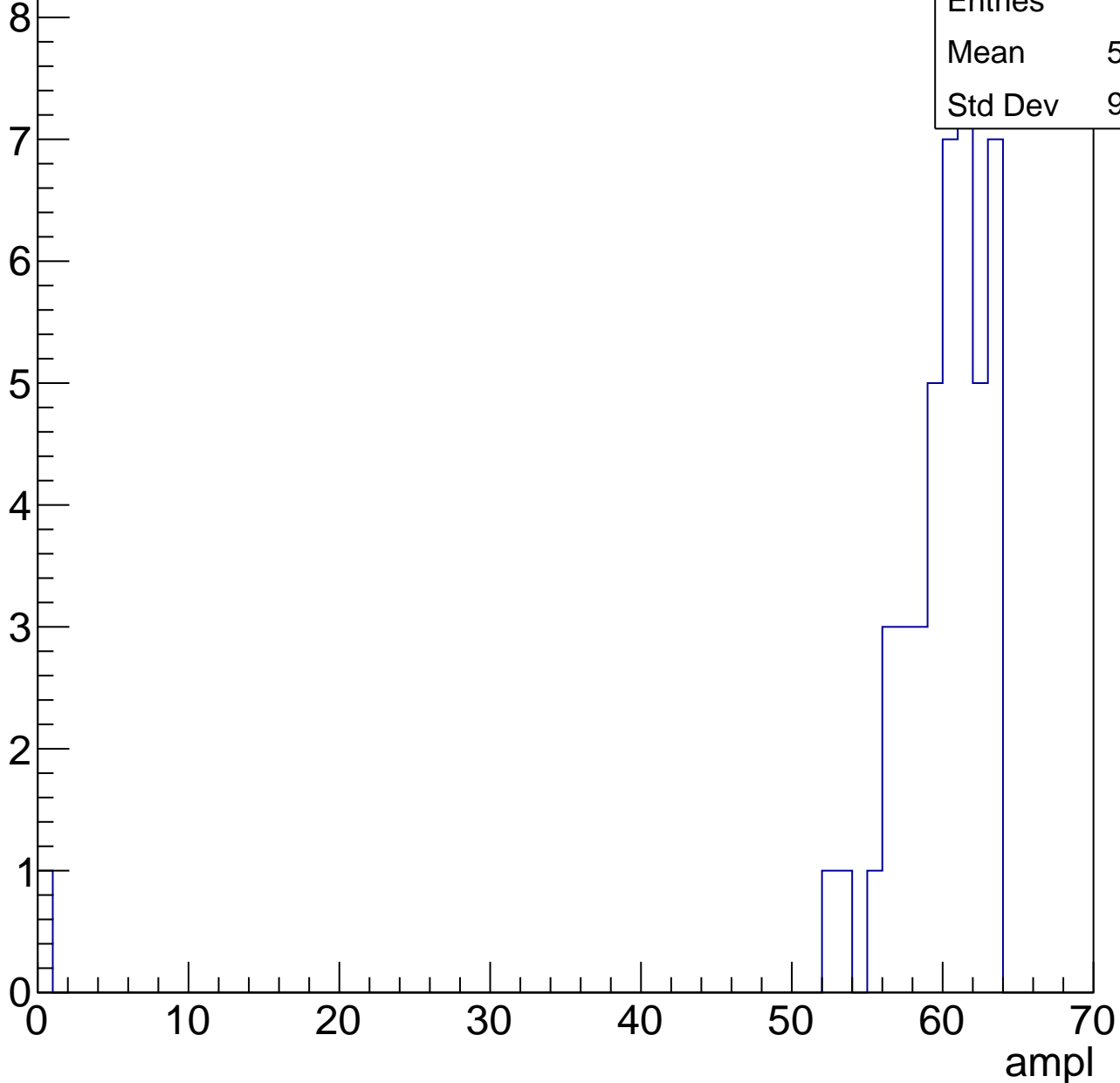
Entries	61
Mean	55.21
Std Dev	3.111

B1L103S, U24-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.38
Std Dev	9.192



B1L103S, U24-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U24-ch60, adc0

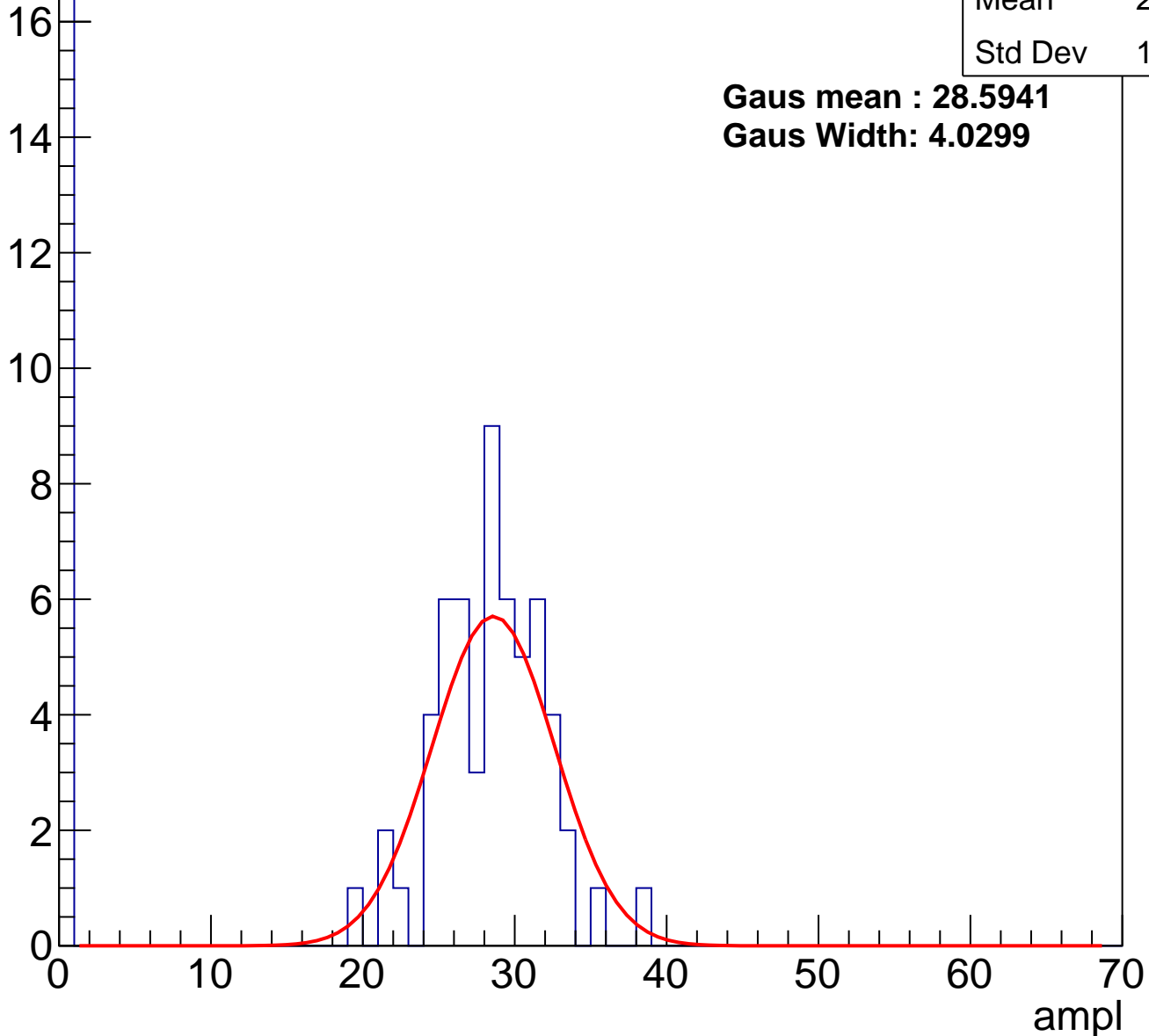
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	21.55
Std Dev	12.17

Gaus mean : 28.5941

Gaus Width: 4.0299

Entry



B1L103S, U24-ch60, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	30.33
Std Dev	11.89

Gaus mean : 35.4359

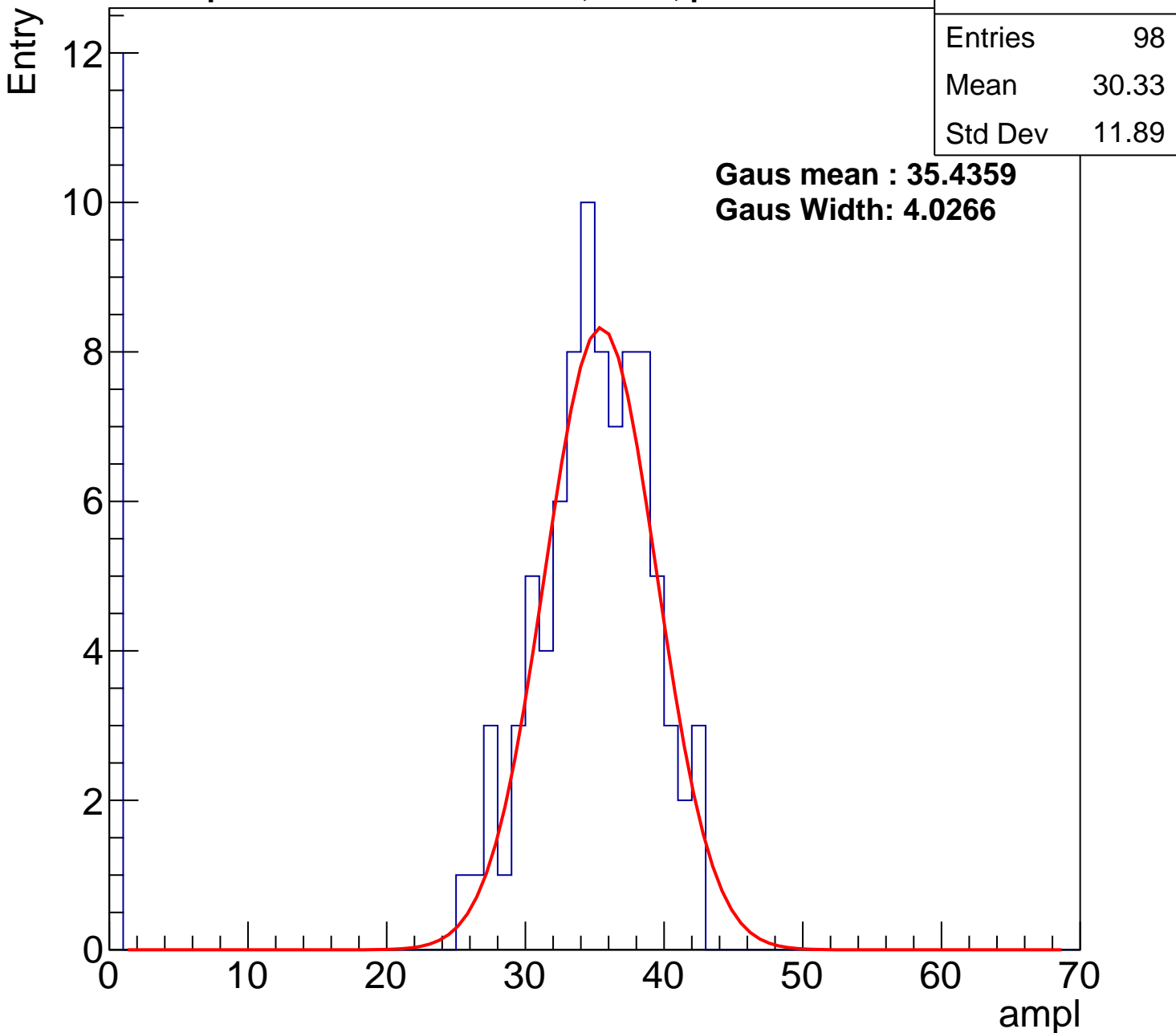
Gaus Width: 4.0266

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch60, adc2

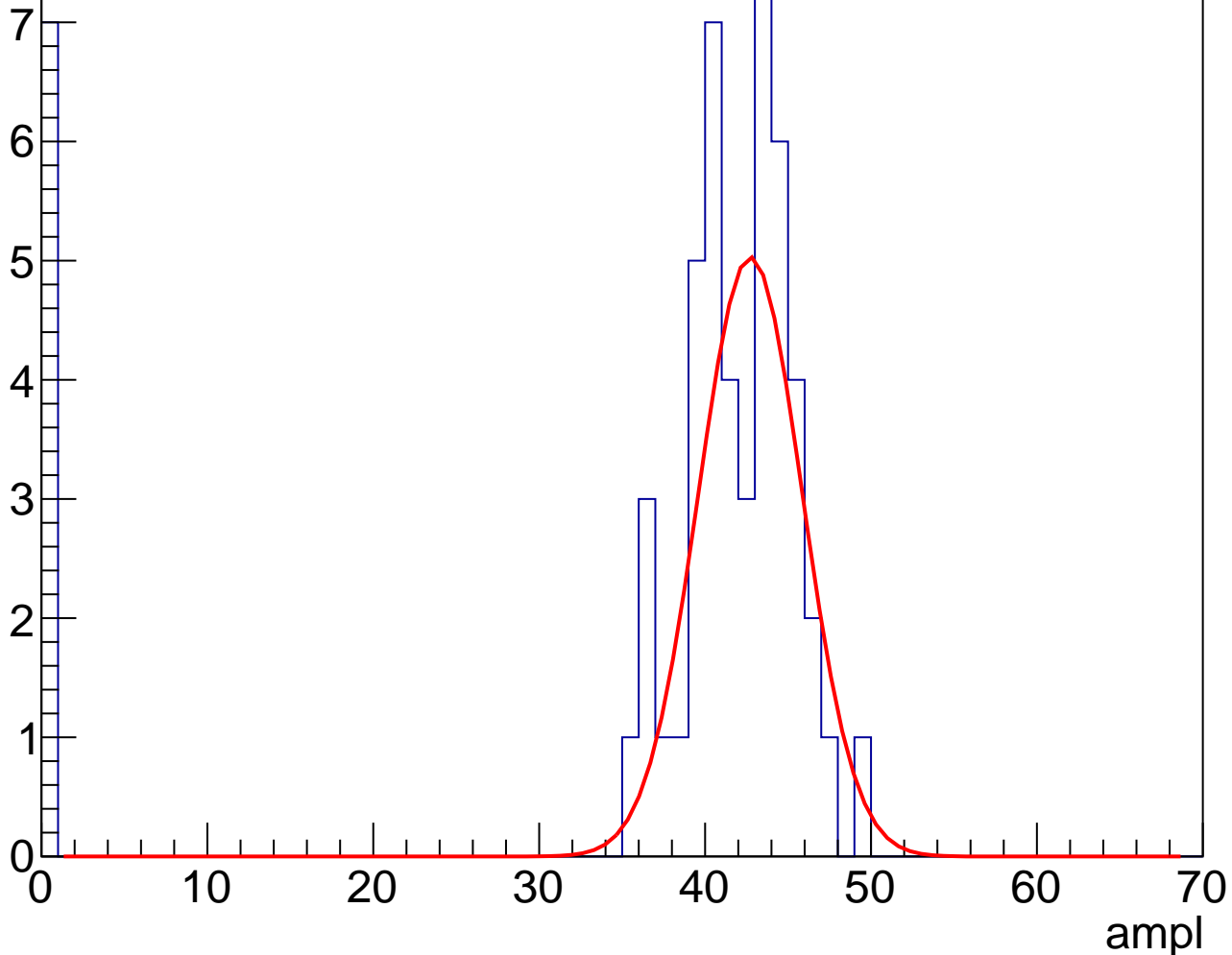
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	36.4
Std Dev	14.19

Gaus mean : 42.7285

Gaus Width: 3.1284

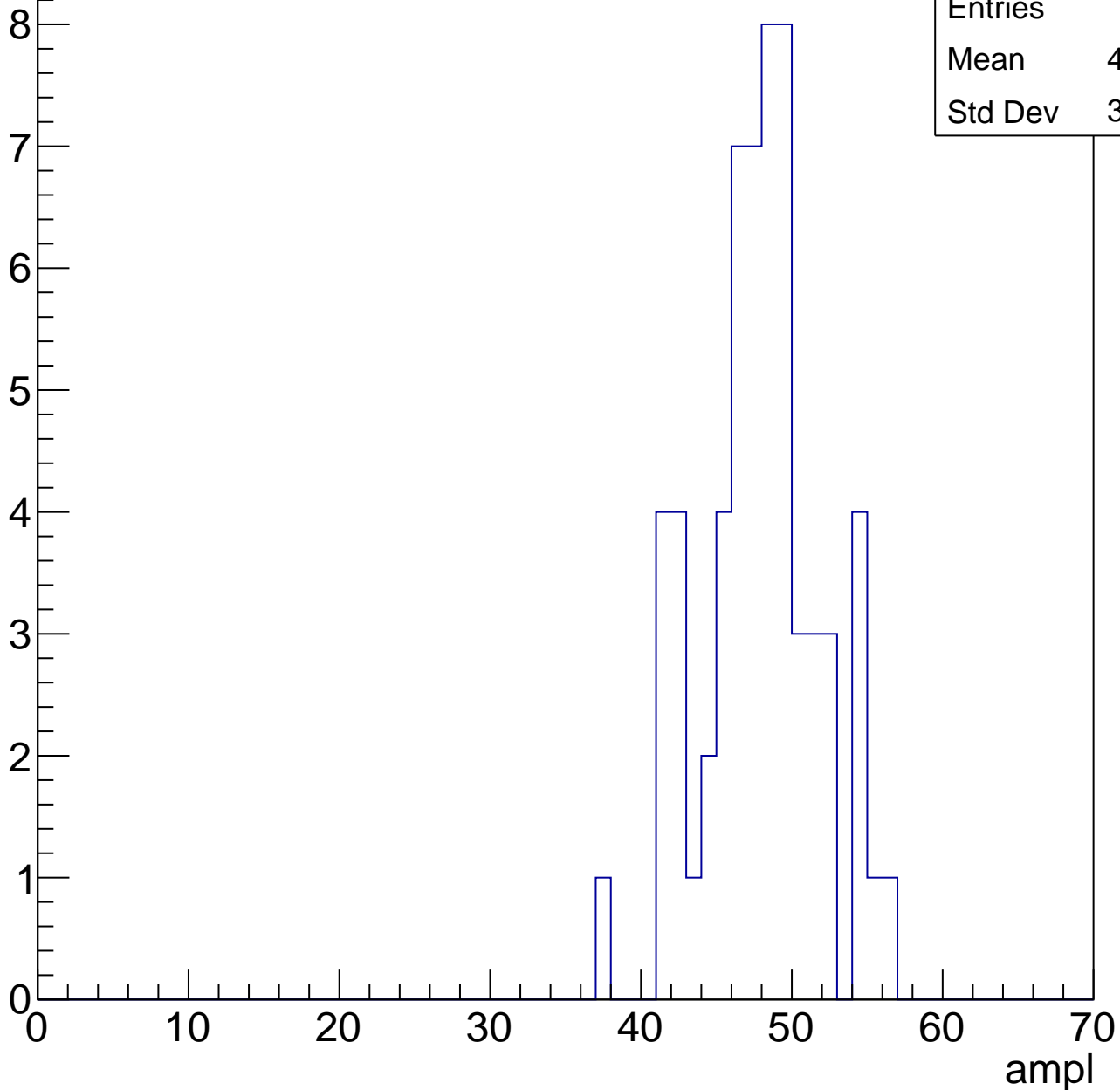


B1L103S, U24-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

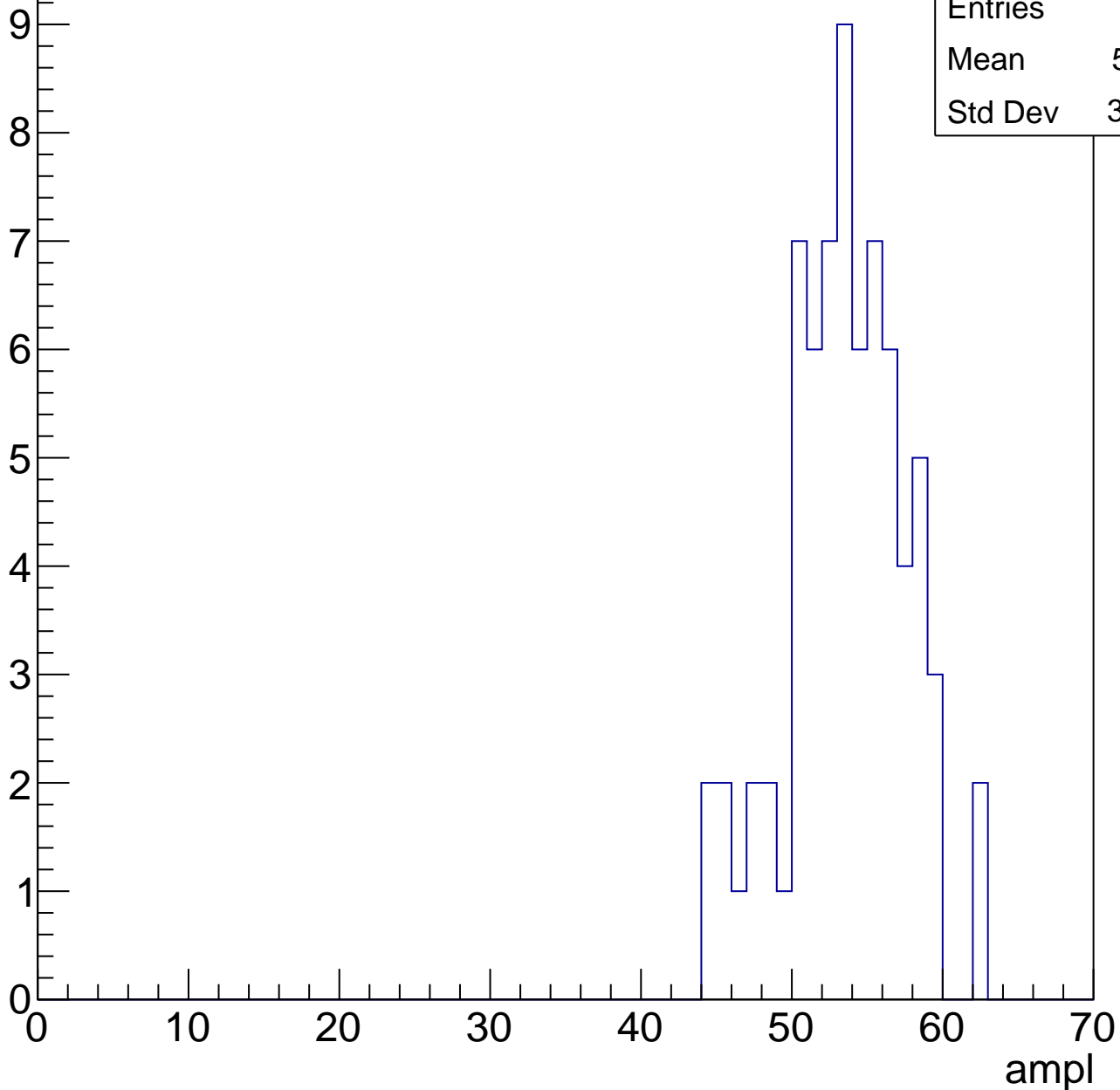
Entries	61
Mean	47.43
Std Dev	3.885



B1L103S, U24-ch60, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

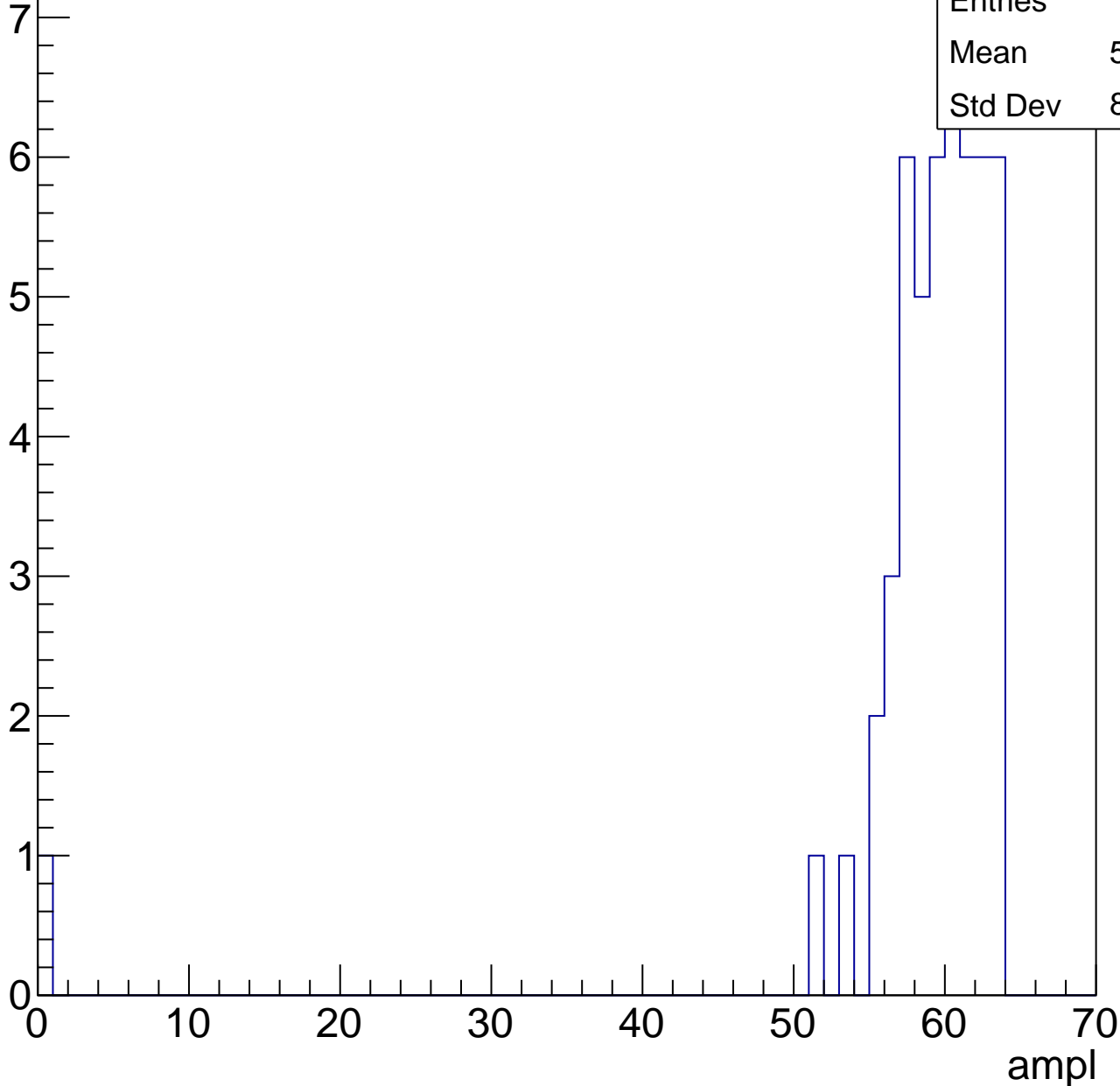


B1L103S, U24-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

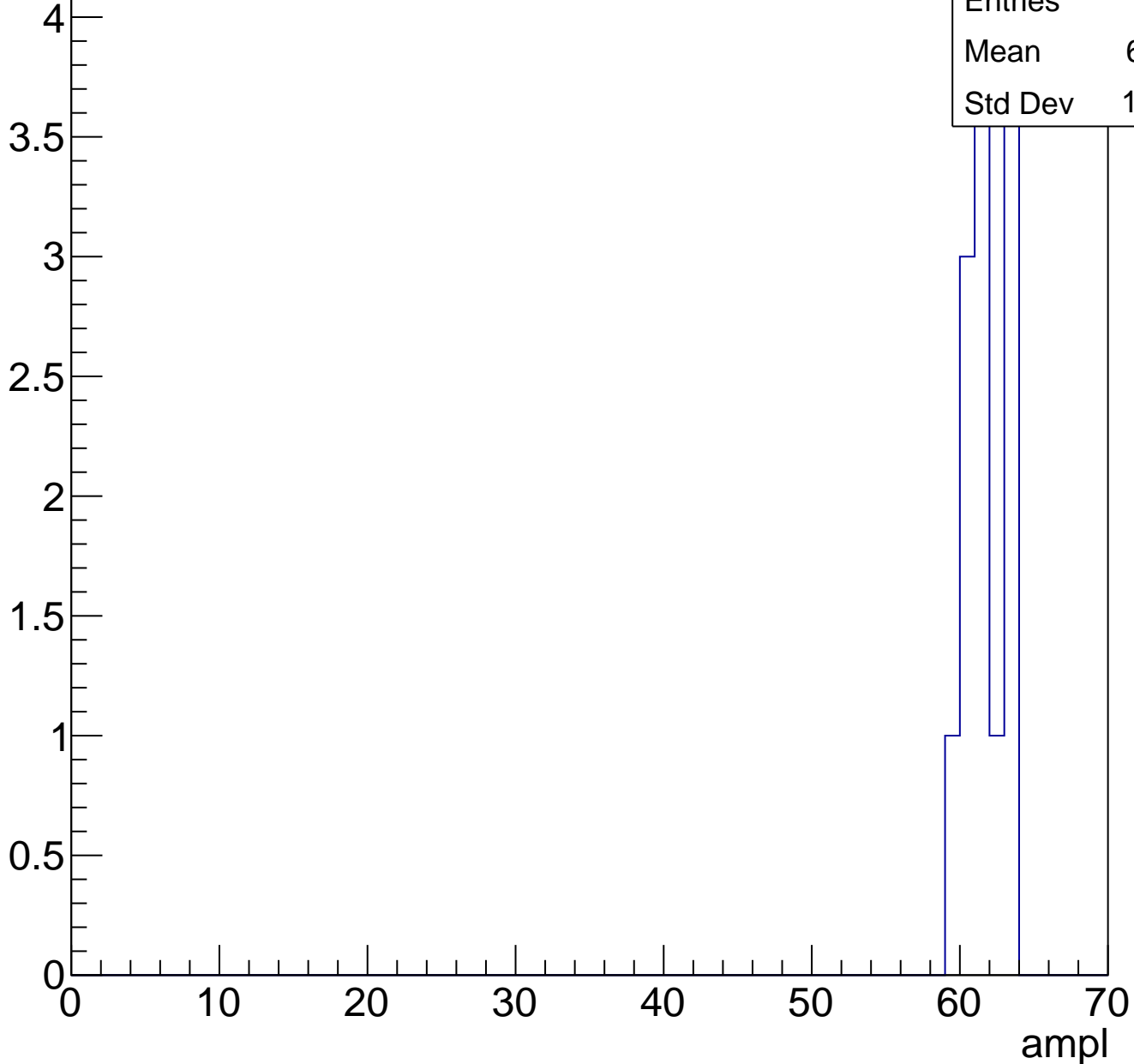
Entries	50
Mean	58.08
Std Dev	8.727



B1L103S, U24-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U24-ch61, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	23.21
Std Dev	11.78

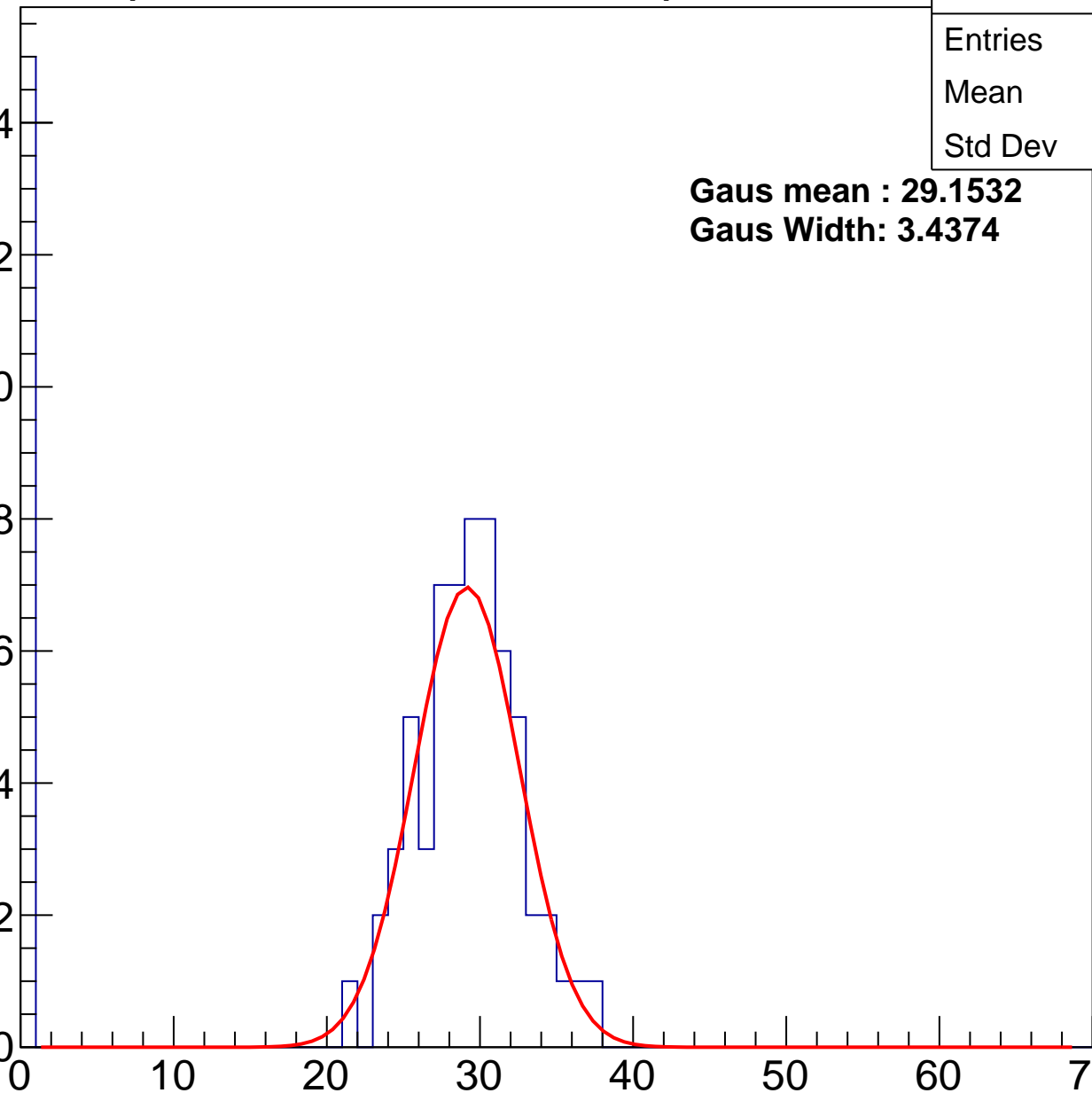
Gaus mean : 29.1532

Gaus Width: 3.4374

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch61, adc1

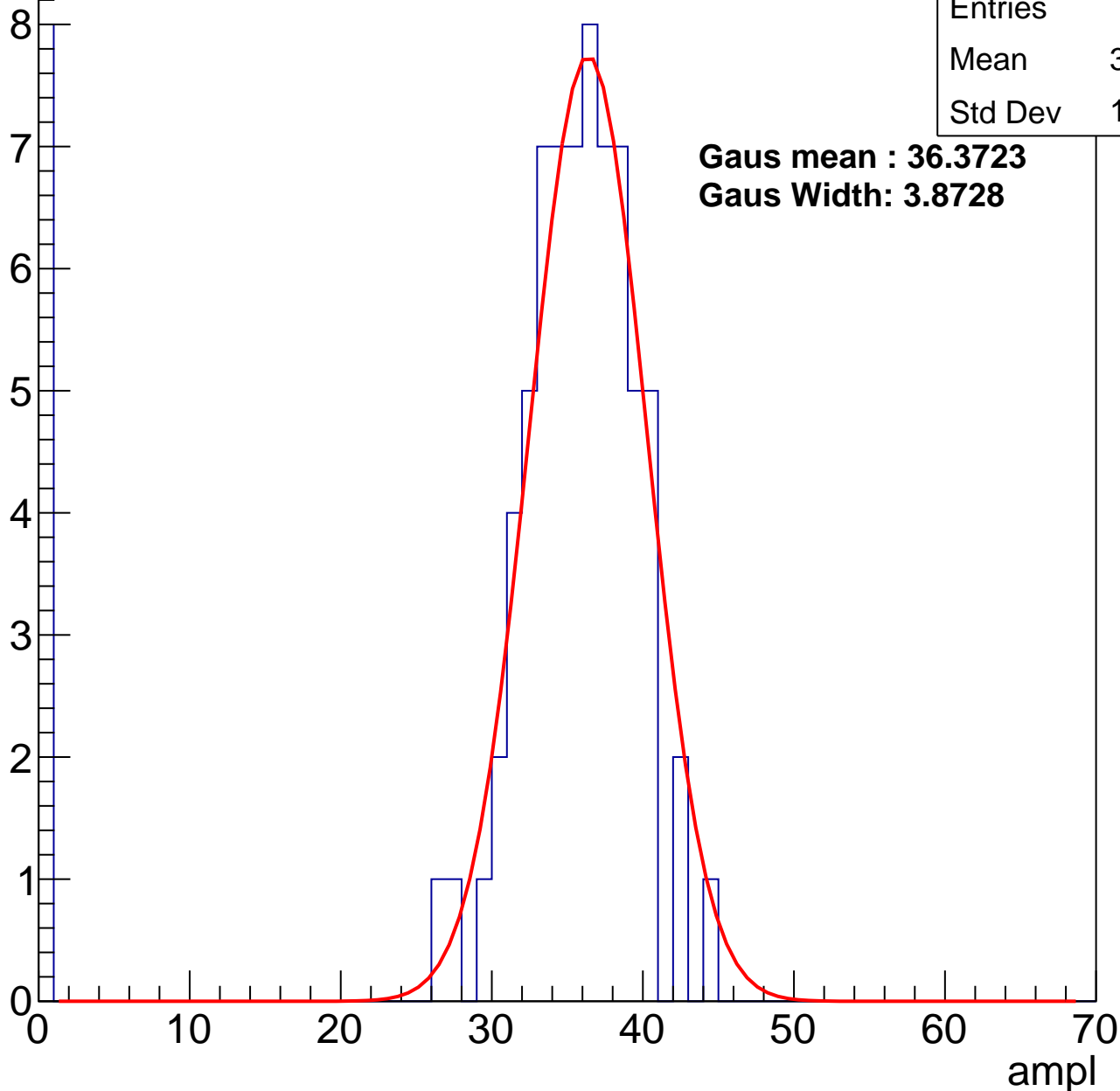
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	31.74
Std Dev	11.23

Gaus mean : 36.3723

Gaus Width: 3.8728



B1L103S, U24-ch61, adc2

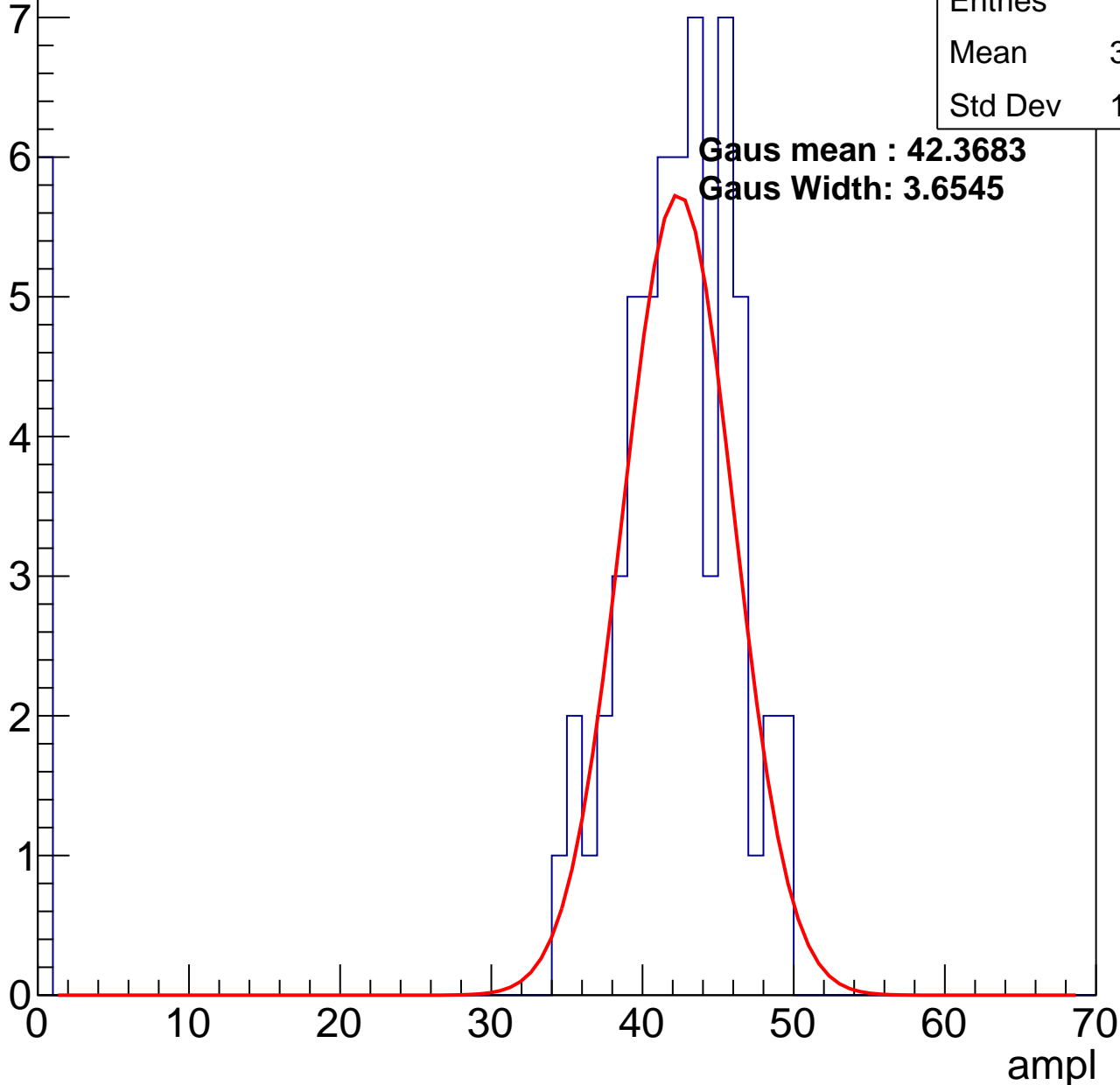
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	38.12
Std Dev	12.72

Gaus mean : 42.3683

Gaus Width: 3.6545

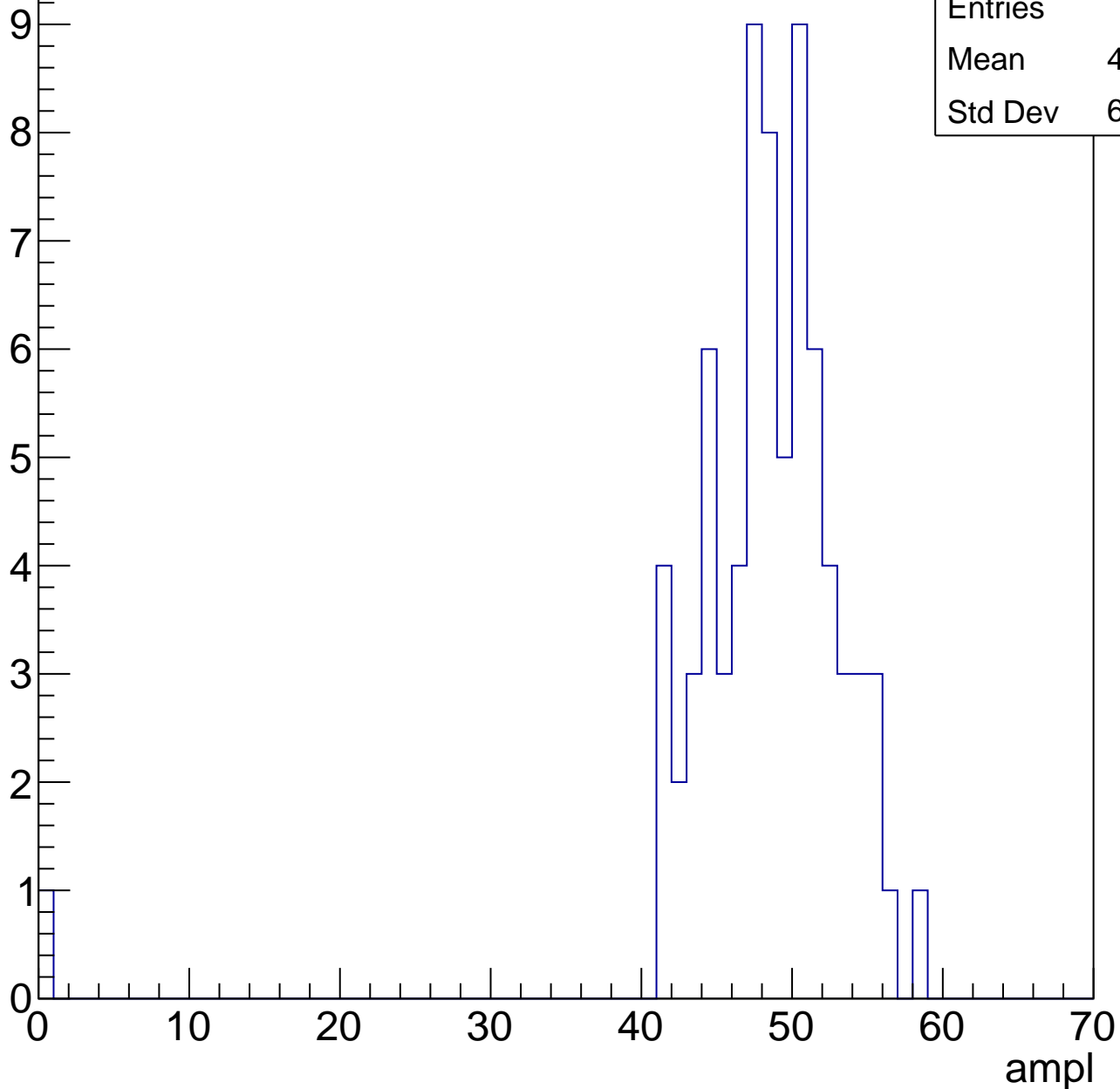


B1L103S, U24-ch61, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	47.68
Std Dev	6.773

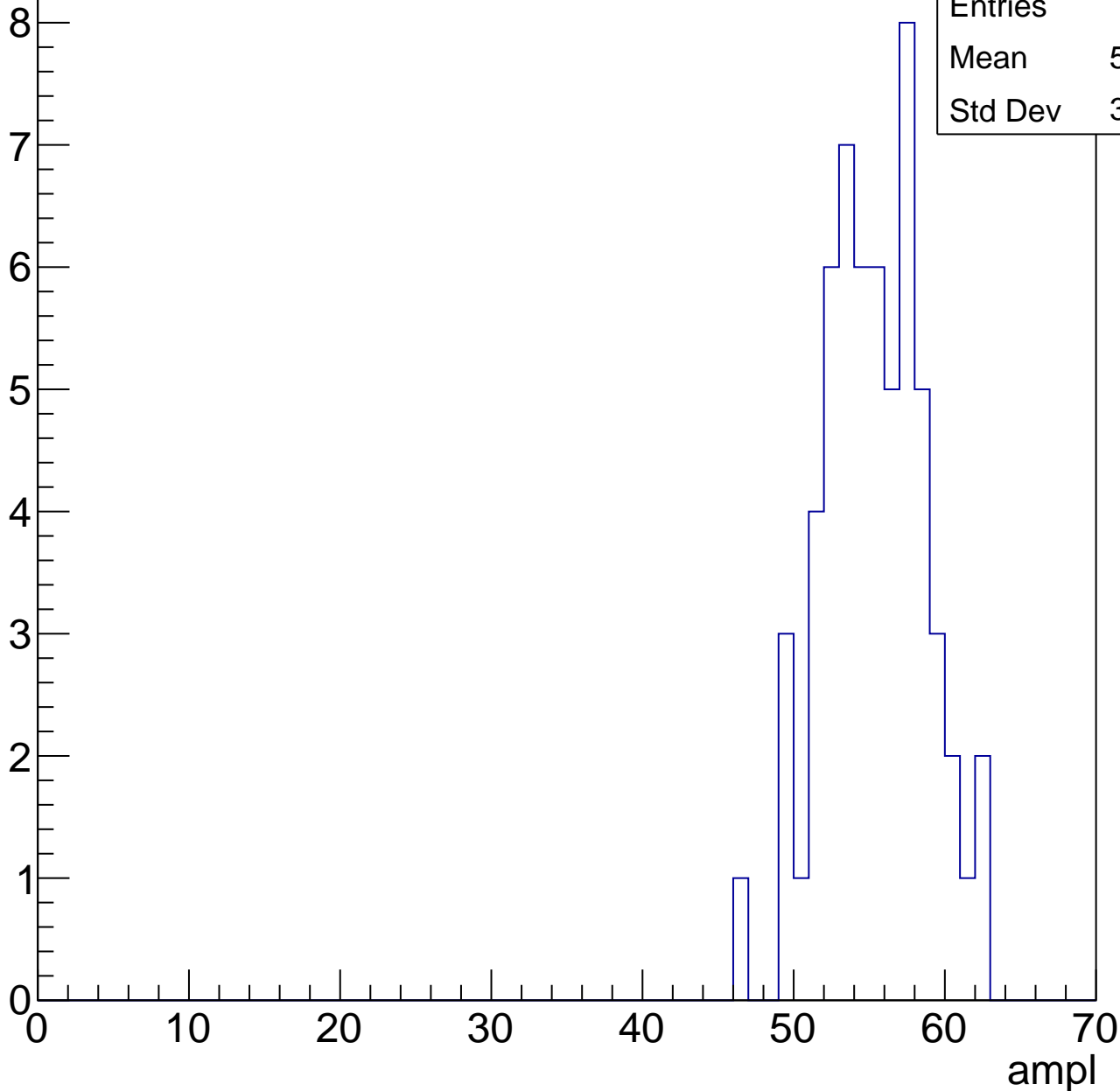


B1L103S, U24-ch61, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.87
Std Dev	3.364



B1L103S, U24-ch61, adc5

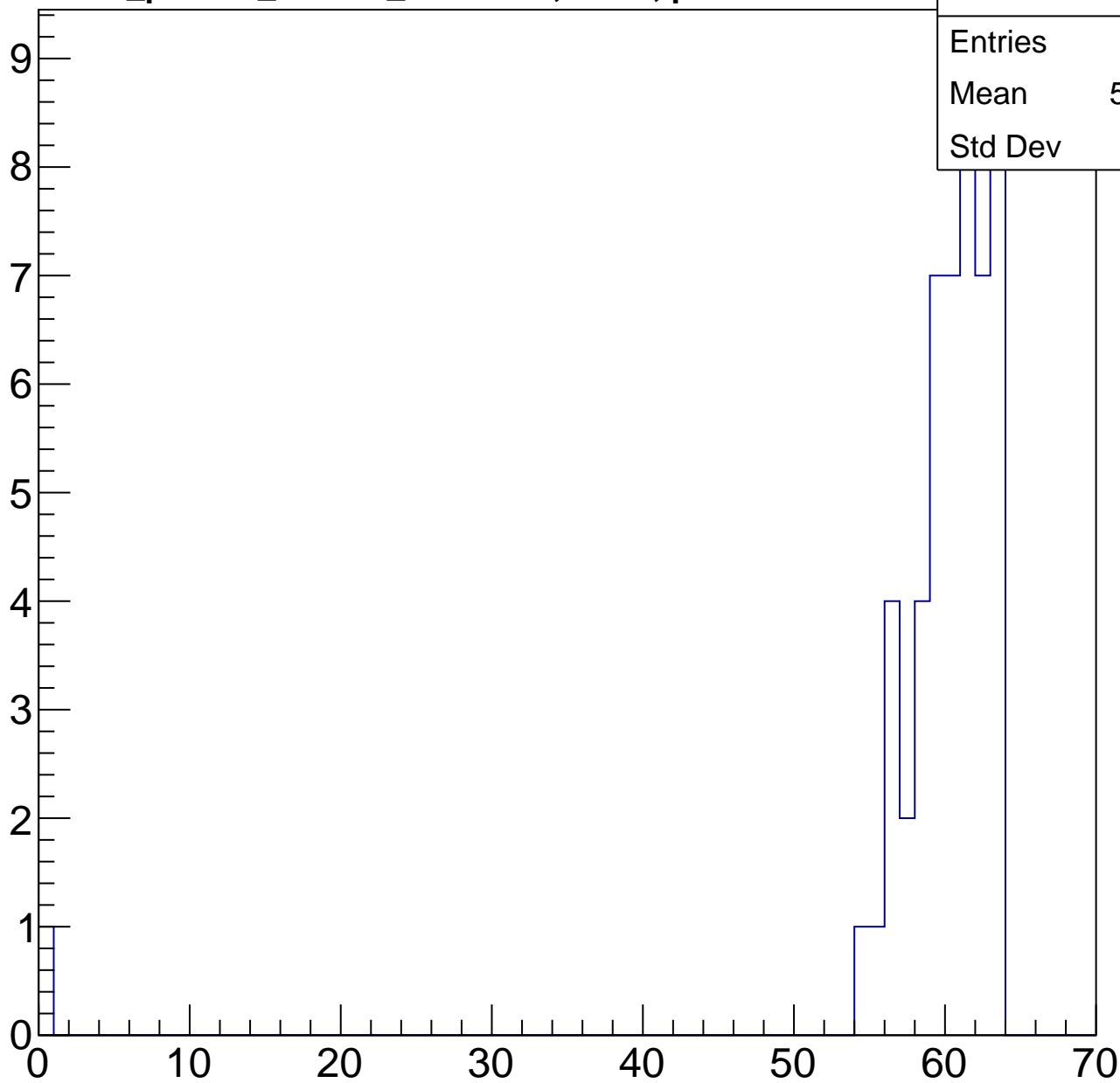
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.88
Std Dev	8.57

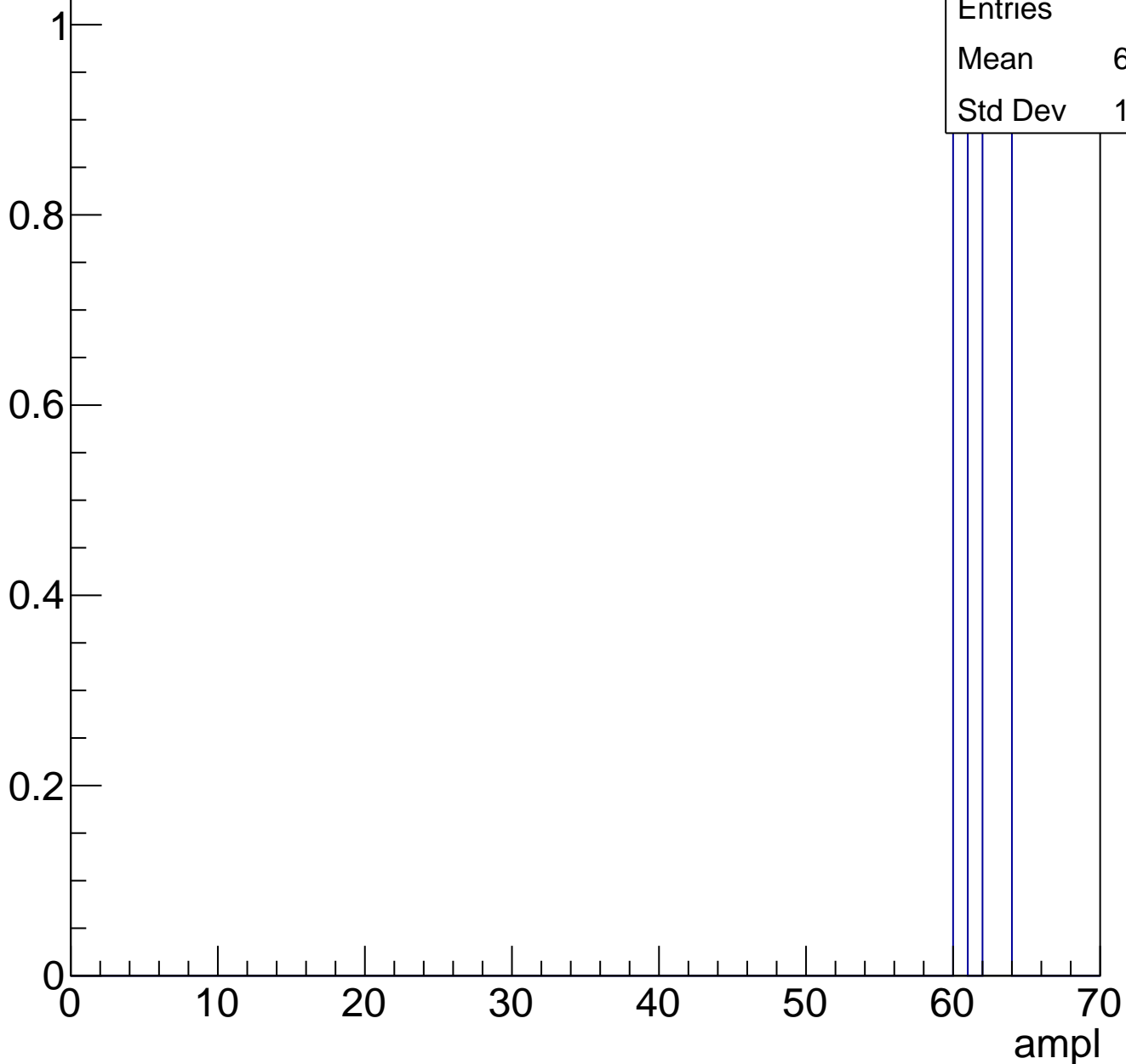
ampl



B1L103S, U24-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch62, adc0

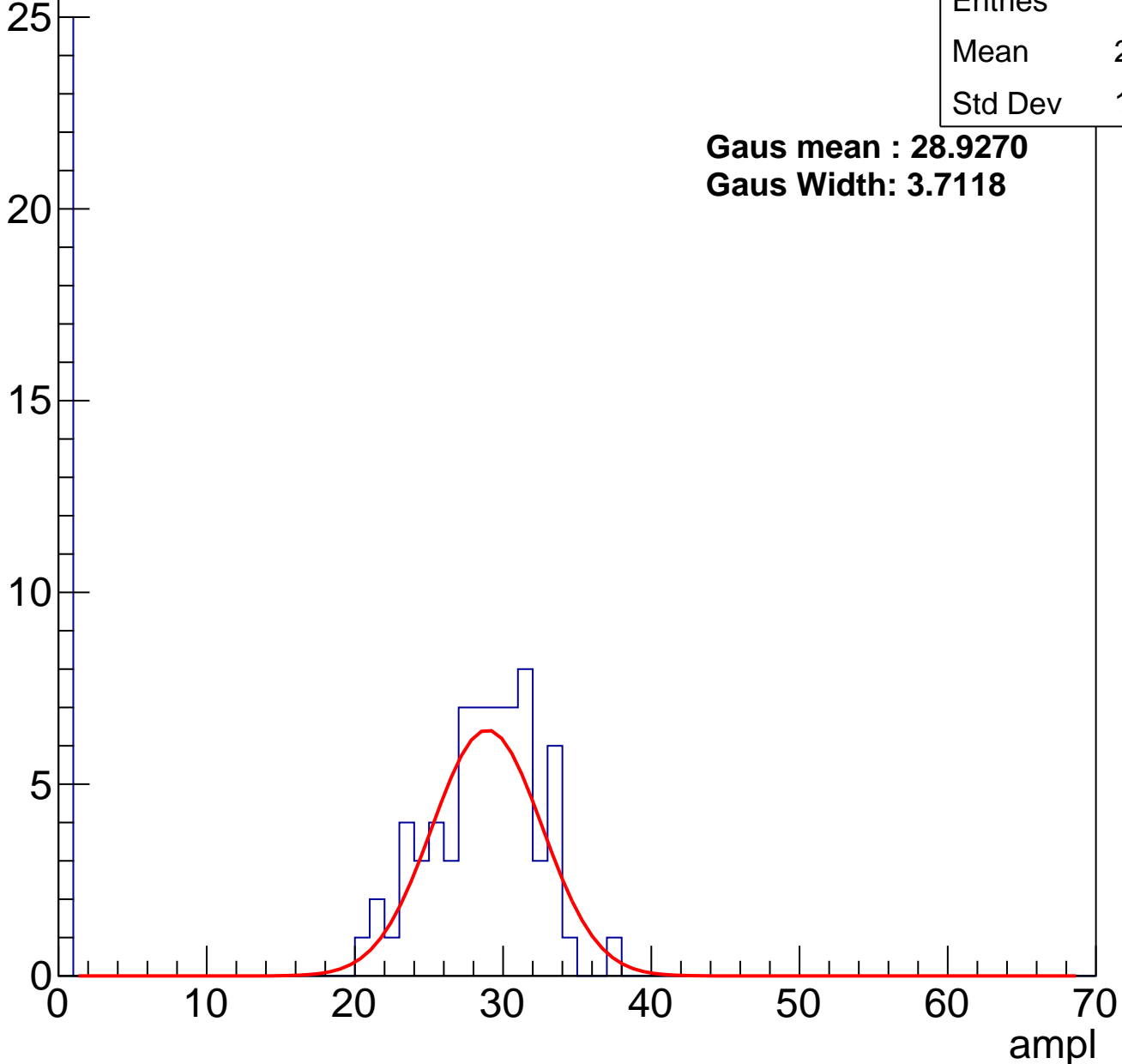
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	20.41
Std Dev	13.01

Gaus mean : 28.9270

Gaus Width: 3.7118

Entry



B1L103S, U24-ch62, adc1

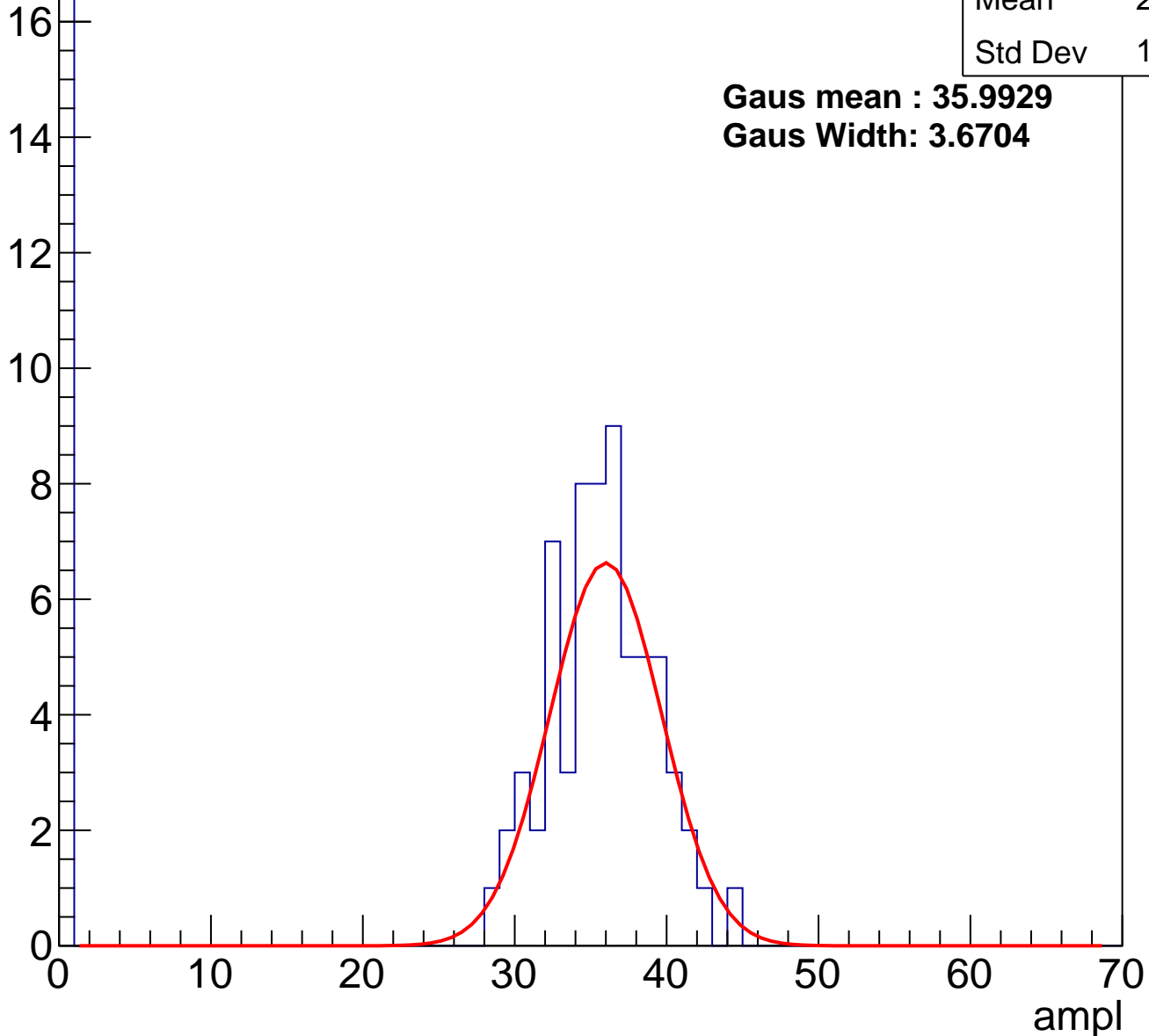
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	27.99
Std Dev	14.62

Gaus mean : 35.9929

Gaus Width: 3.6704

Entry



B1L103S, U24-ch62, adc2

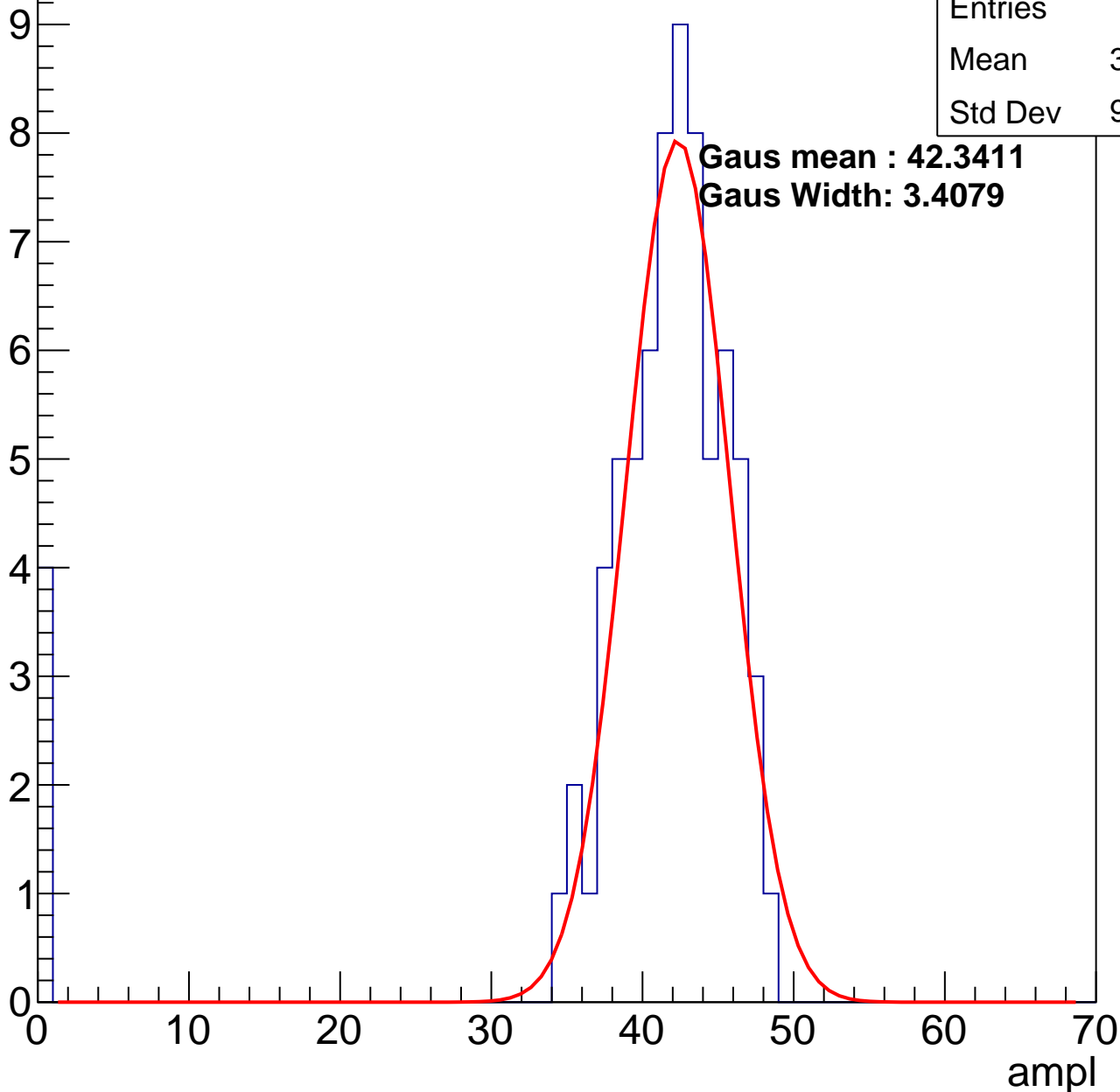
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	39.34
Std Dev	9.978

Gaus mean : 42.3411

Gaus Width: 3.4079

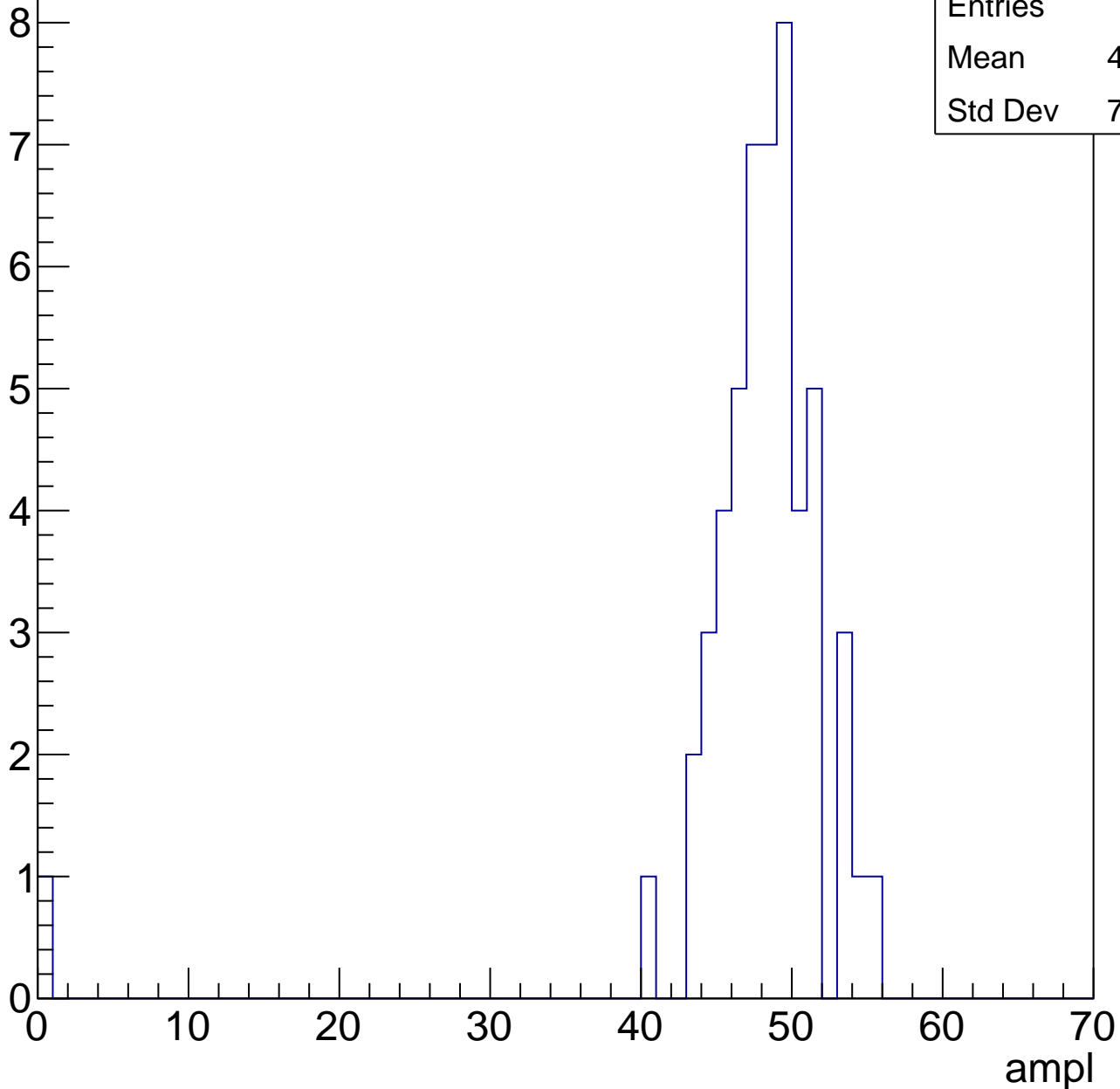


B1L103S, U24-ch62, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	47.08
Std Dev	7.224

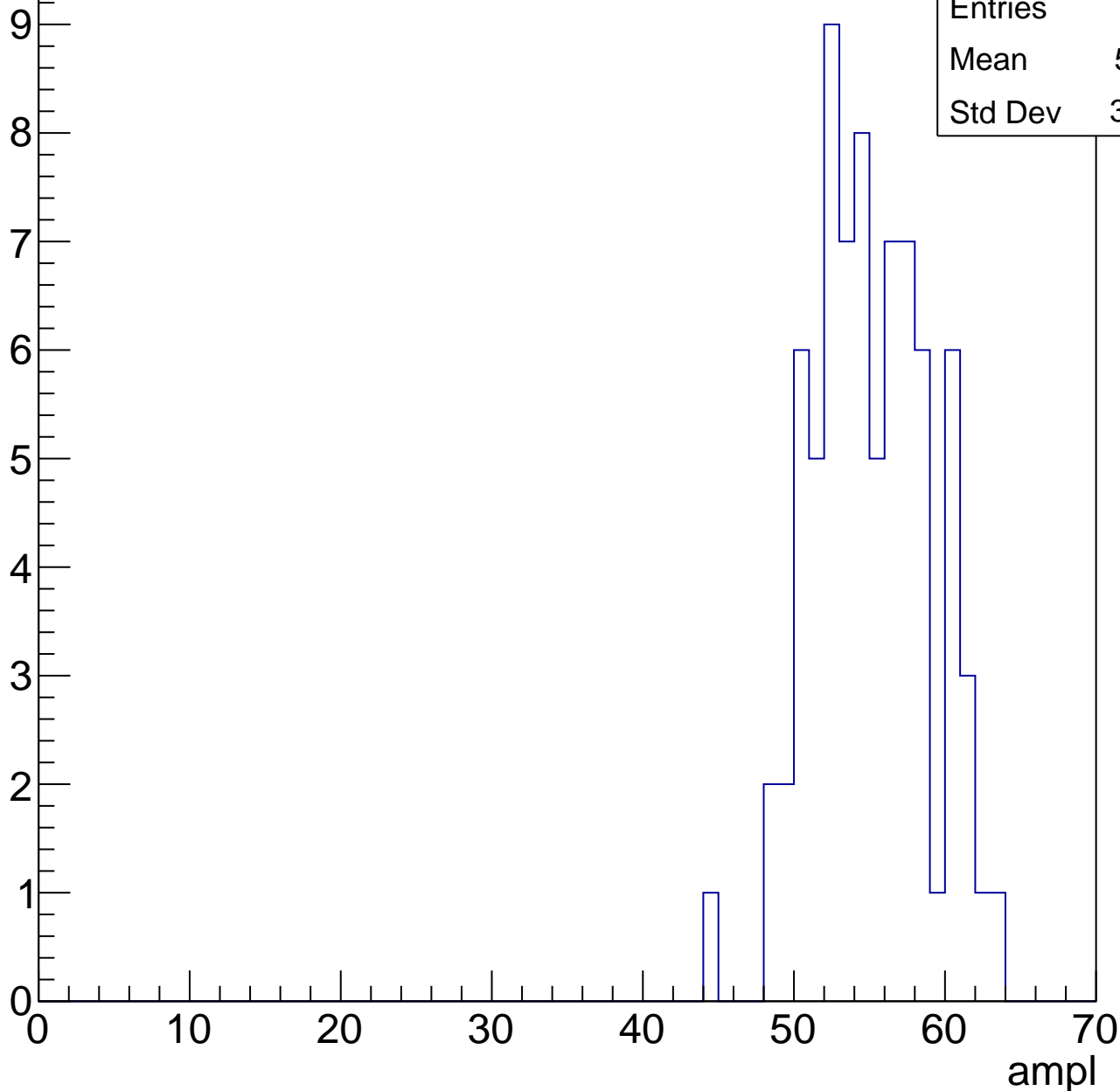


B1L103S, U24-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

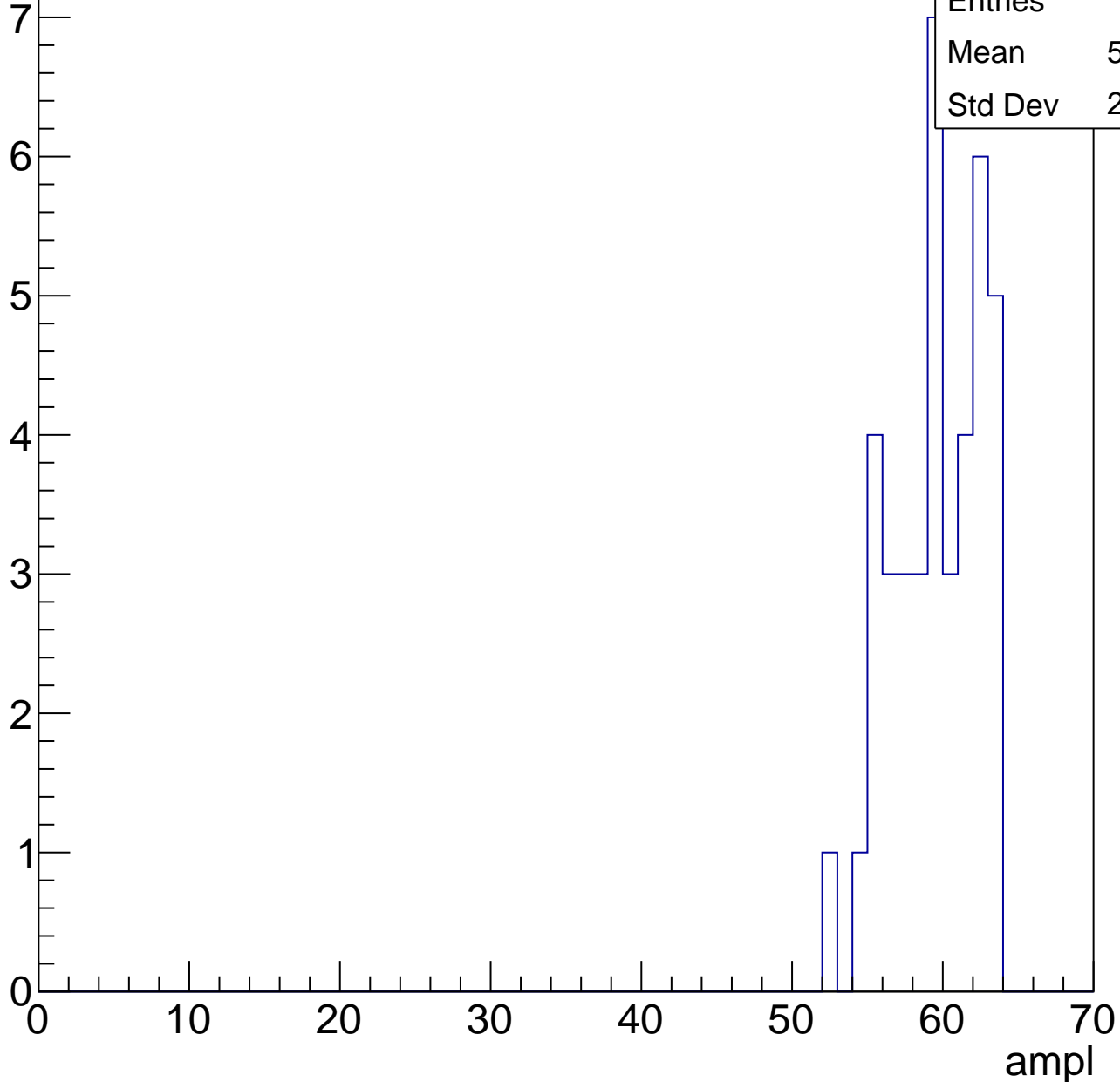
Entries	77
Mean	54.61
Std Dev	3.784



B1L103S, U24-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

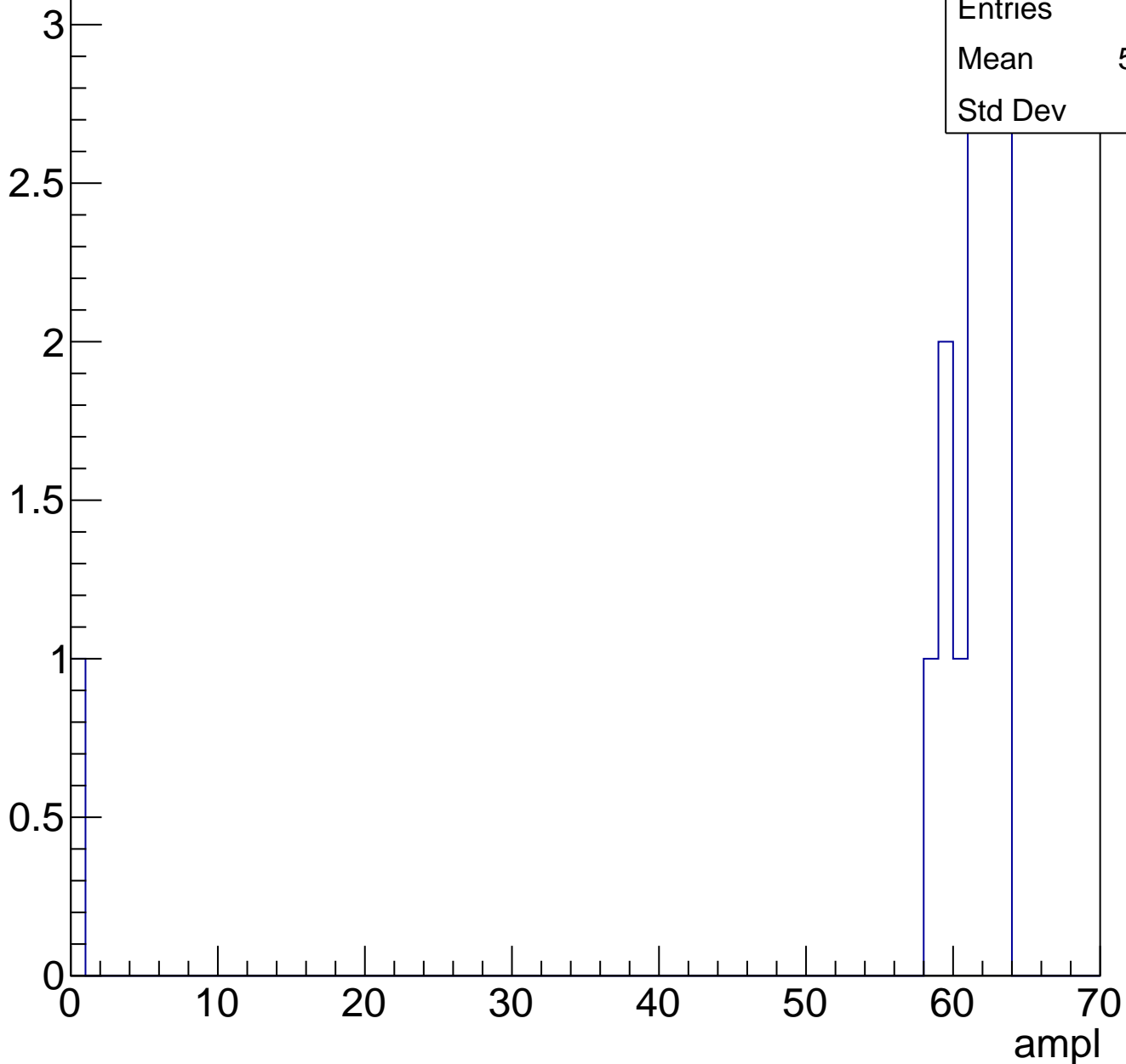
Entry



B1L103S, U24-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U24-ch63, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	22.92
Std Dev	11.71

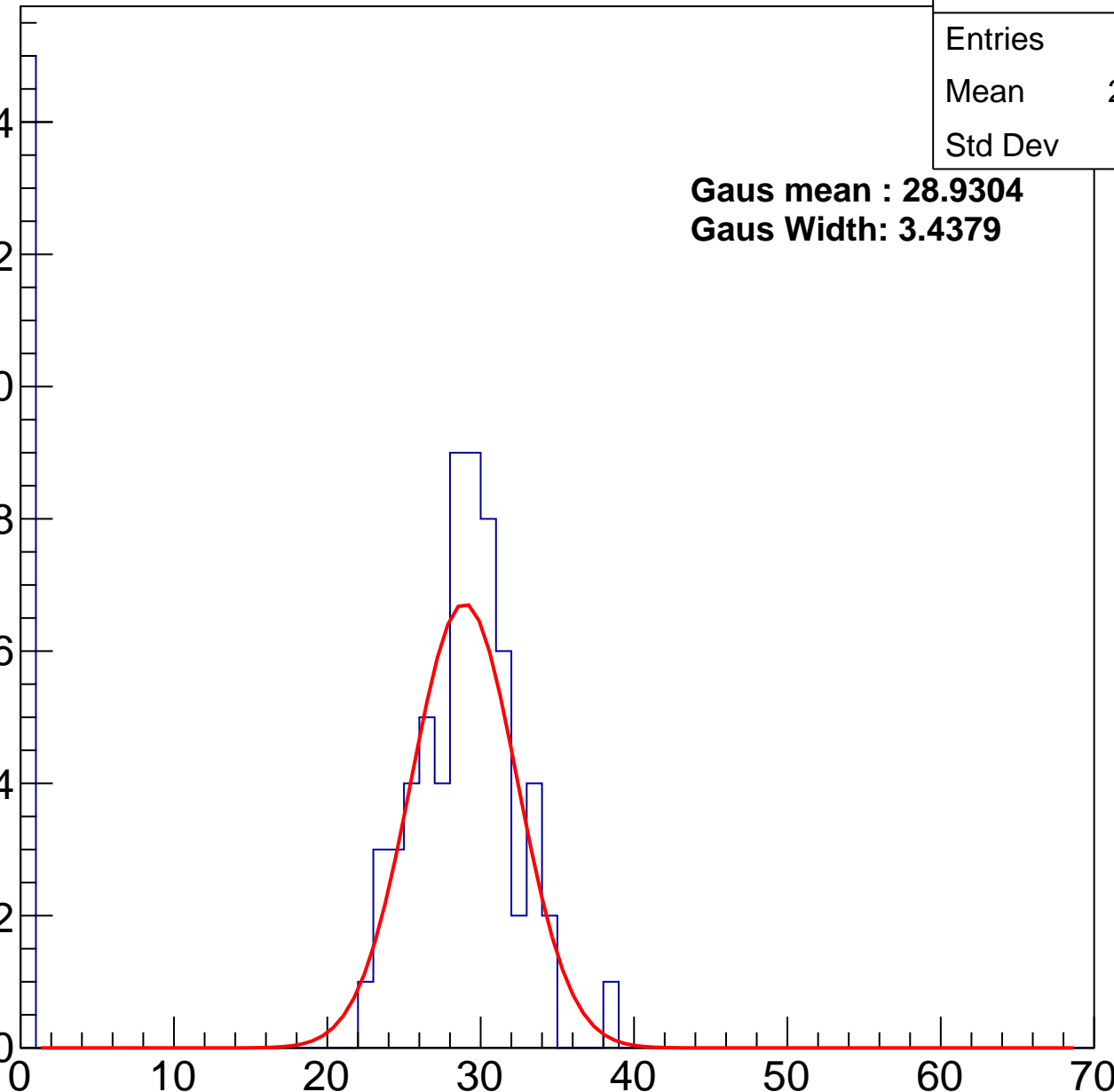
Gaus mean : 28.9304

Gaus Width: 3.4379

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch63, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	32.6
Std Dev	9.904

Gaus mean : 36.1087

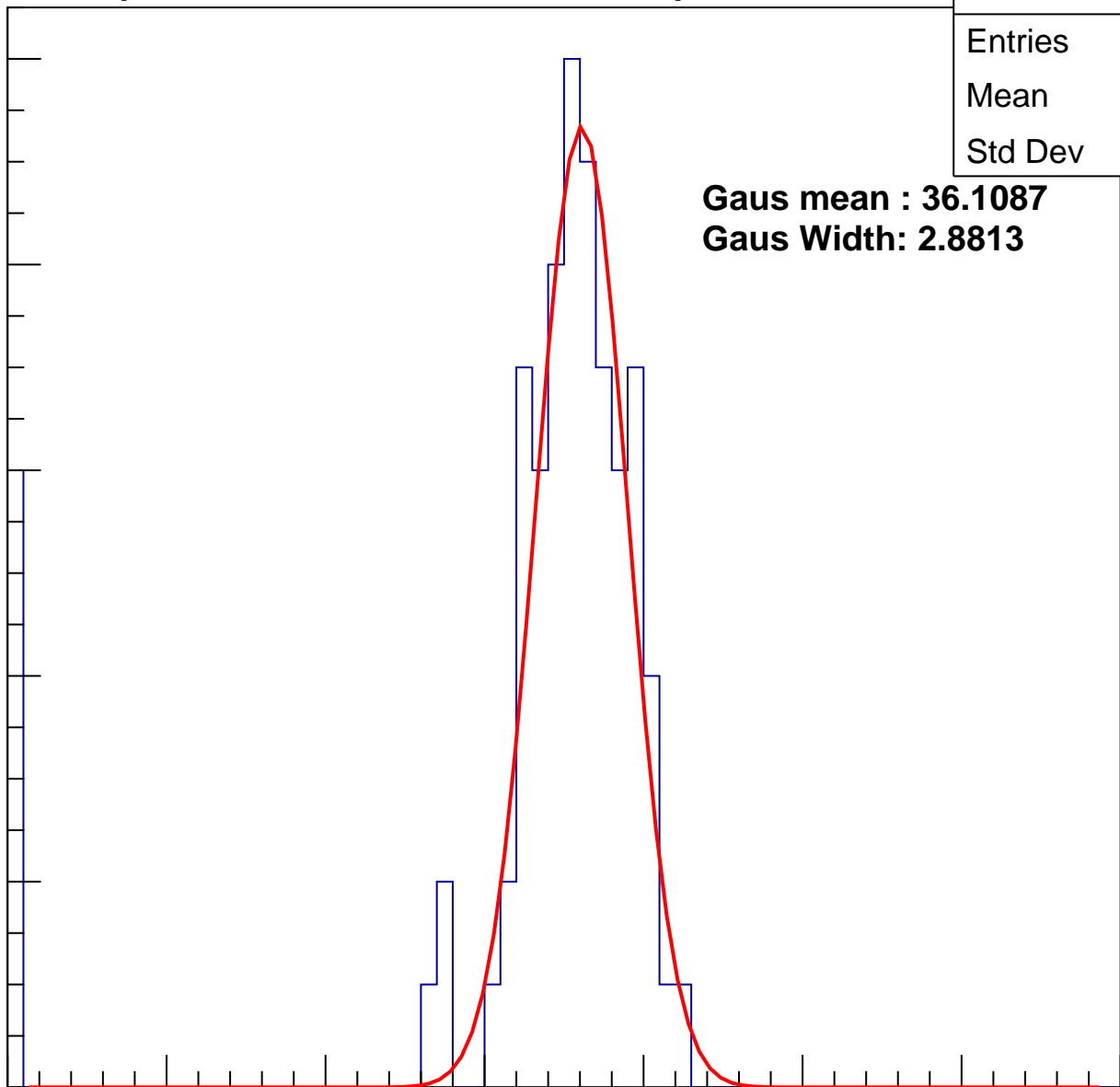
Gaus Width: 2.8813

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch63, adc2

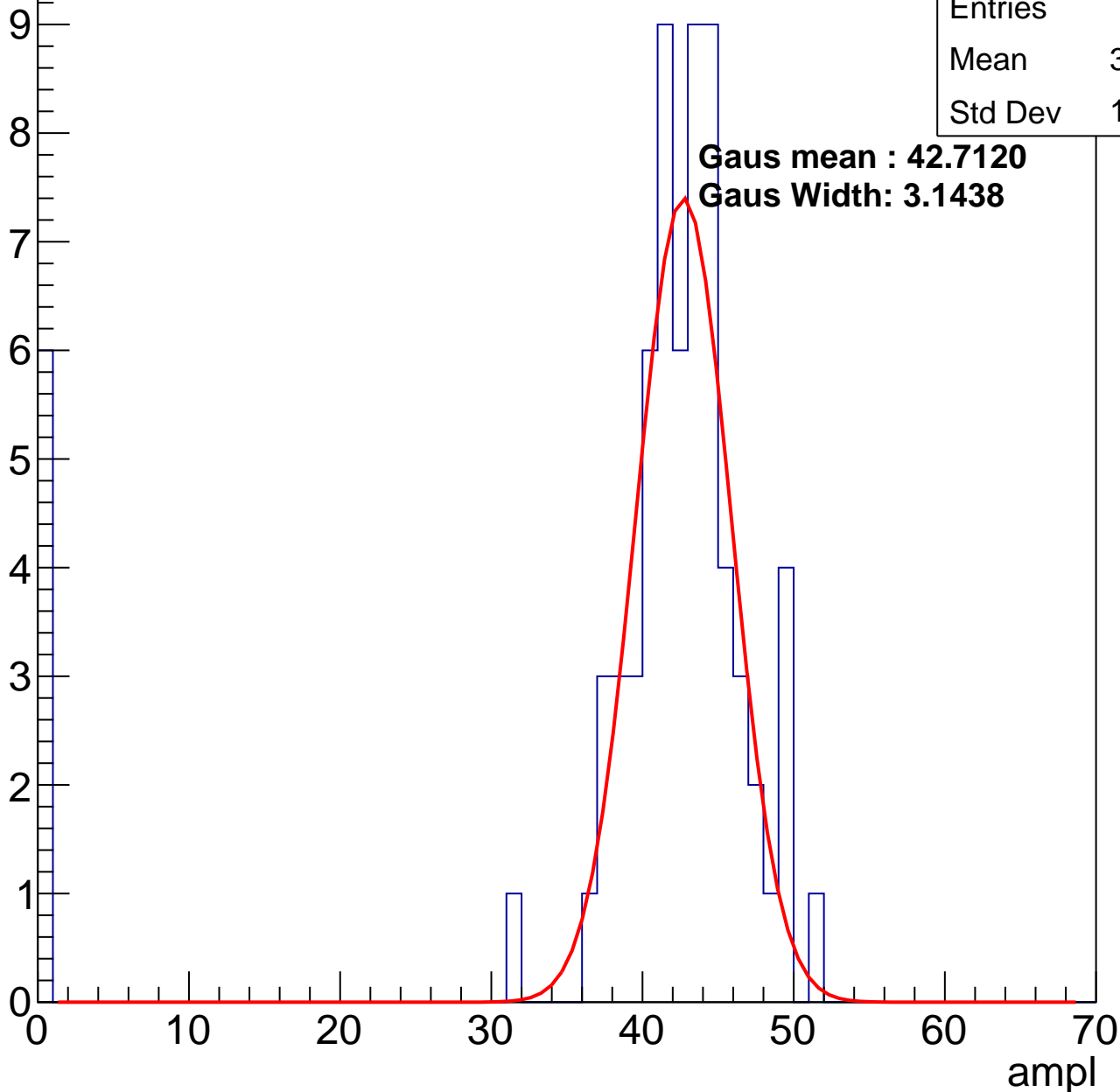
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	38.87
Std Dev	12.29

Gaus mean : 42.7120

Gaus Width: 3.1438

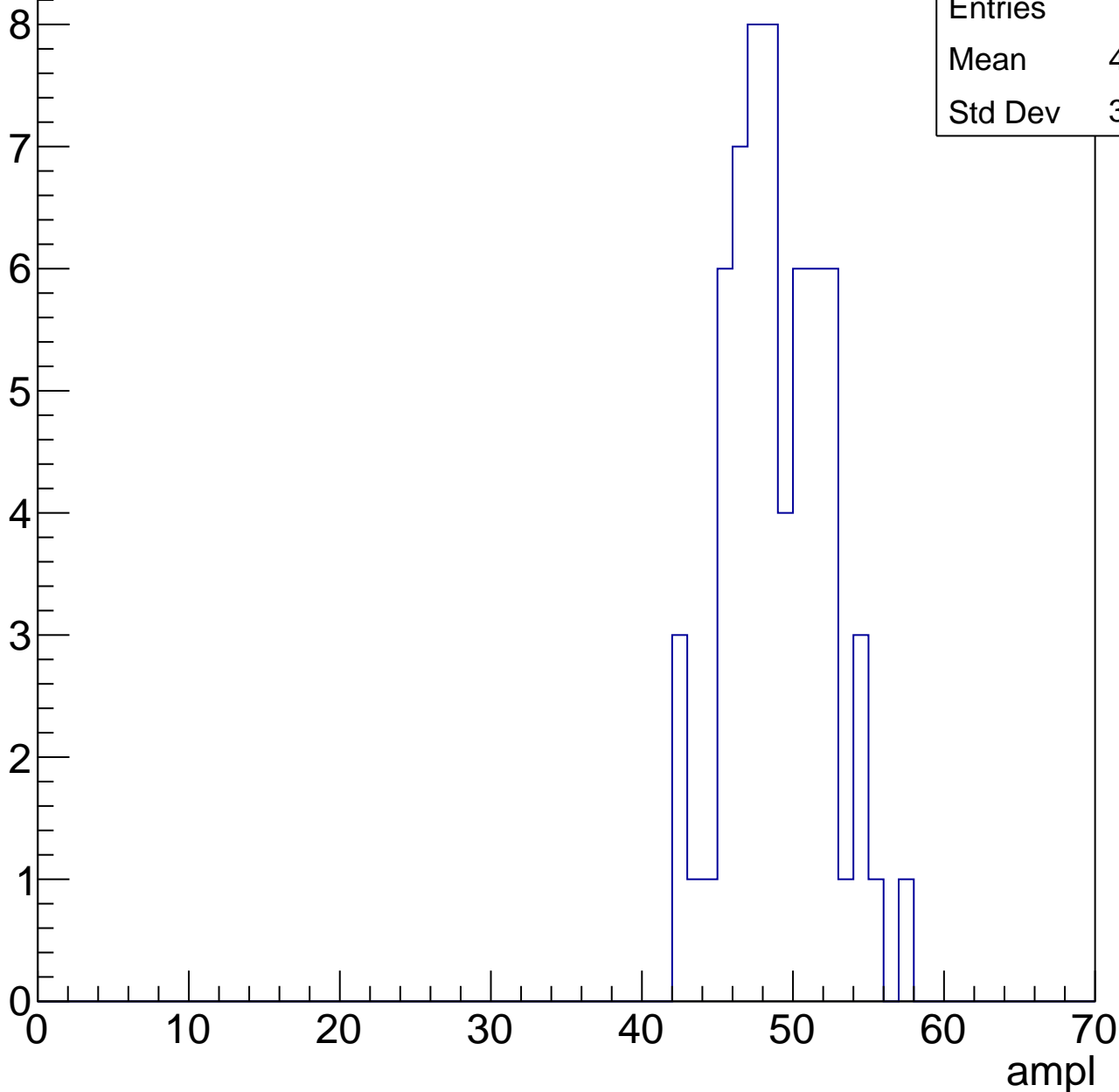


B1L103S, U24-ch63, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48.48
Std Dev	3.276

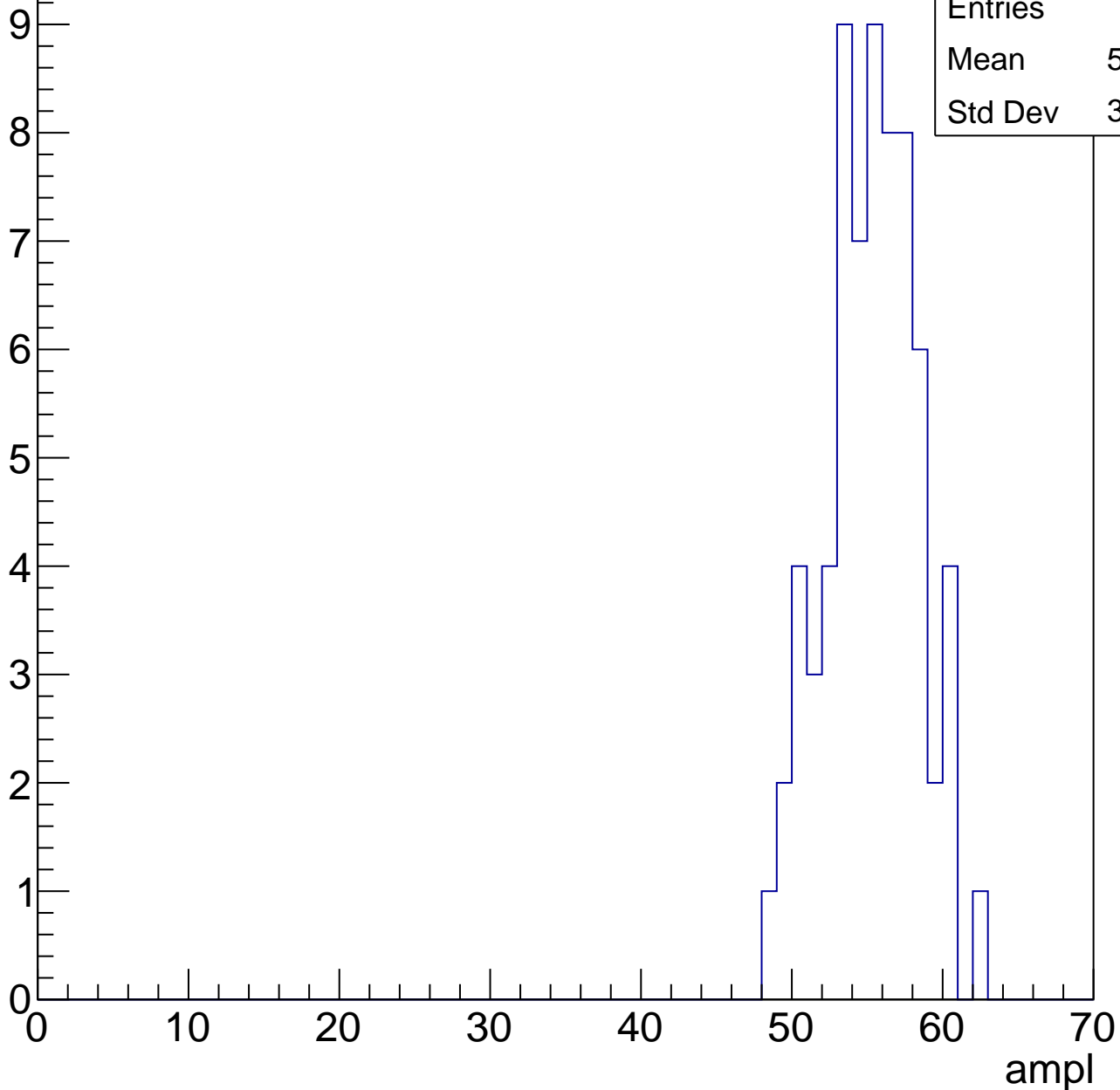


B1L103S, U24-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

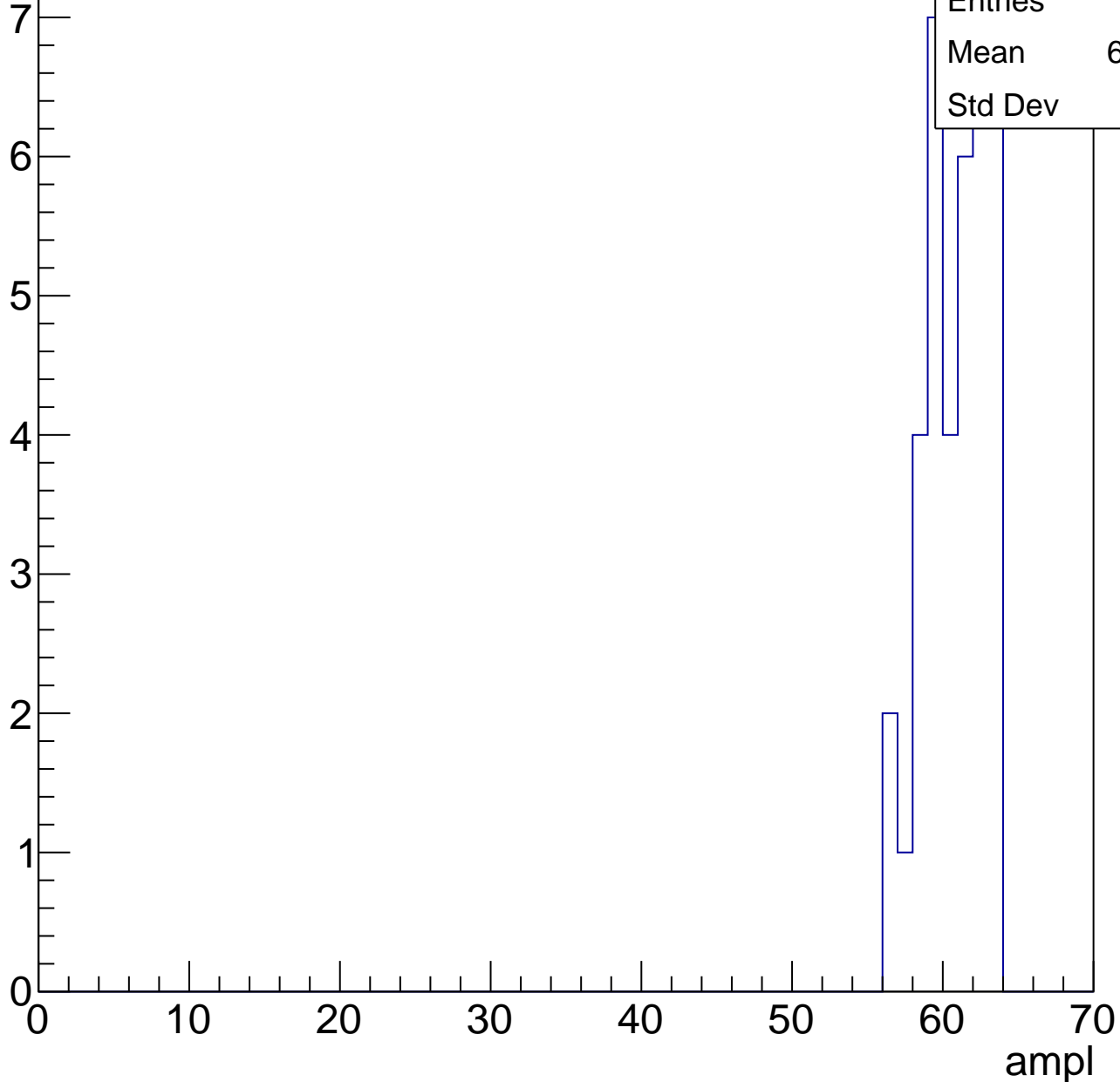
Entries	68
Mean	54.84
Std Dev	3.023



B1L103S, U24-ch63, adc5

calib_packv5_041523_1651.root, FC#0, port C2

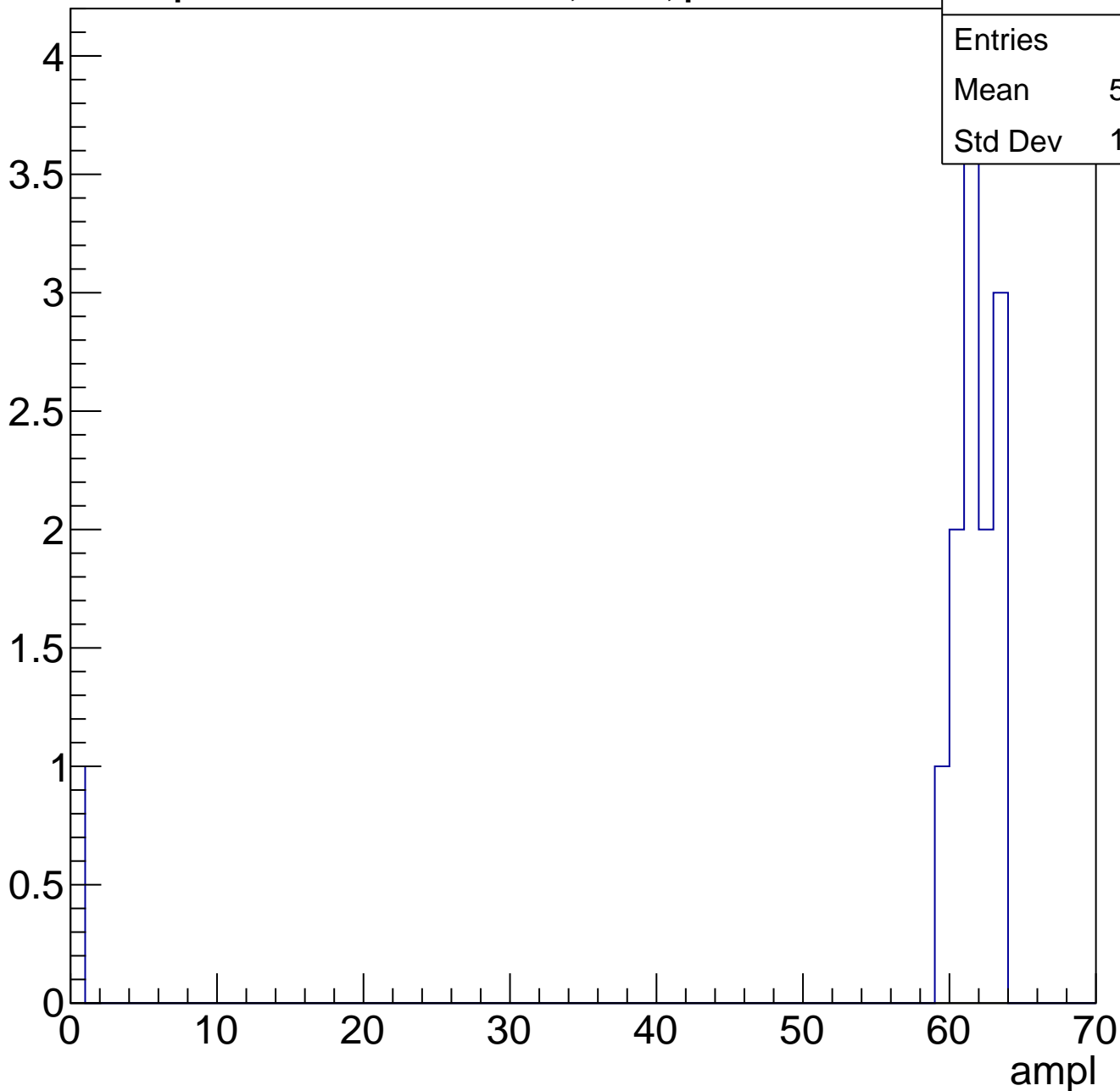
Entry



B1L103S, U24-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch63, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch64, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	113
Mean	21.38
Std Dev	12.68

Gaus mean : 29.1267

Gaus Width: 3.7922

Entry

25

20

15

10

5

0

0

10

20

30

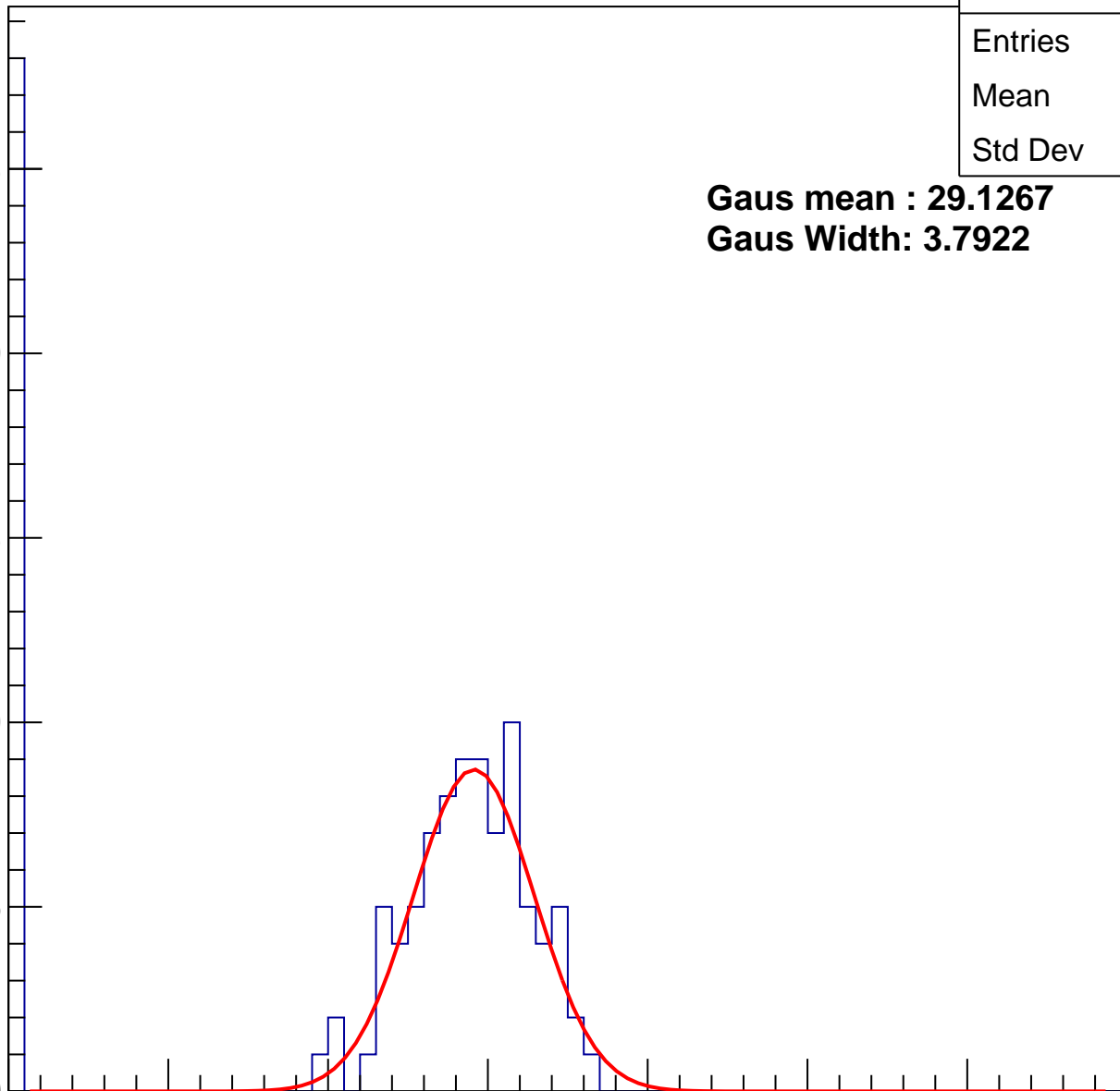
40

50

60

70

ampl



B1L103S, U24-ch64, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	29.19
Std Dev	14.01

Gaus mean : 36.2806

Gaus Width: 3.8148

Entry

12

10

8

6

4

2

0

0

10

20

30

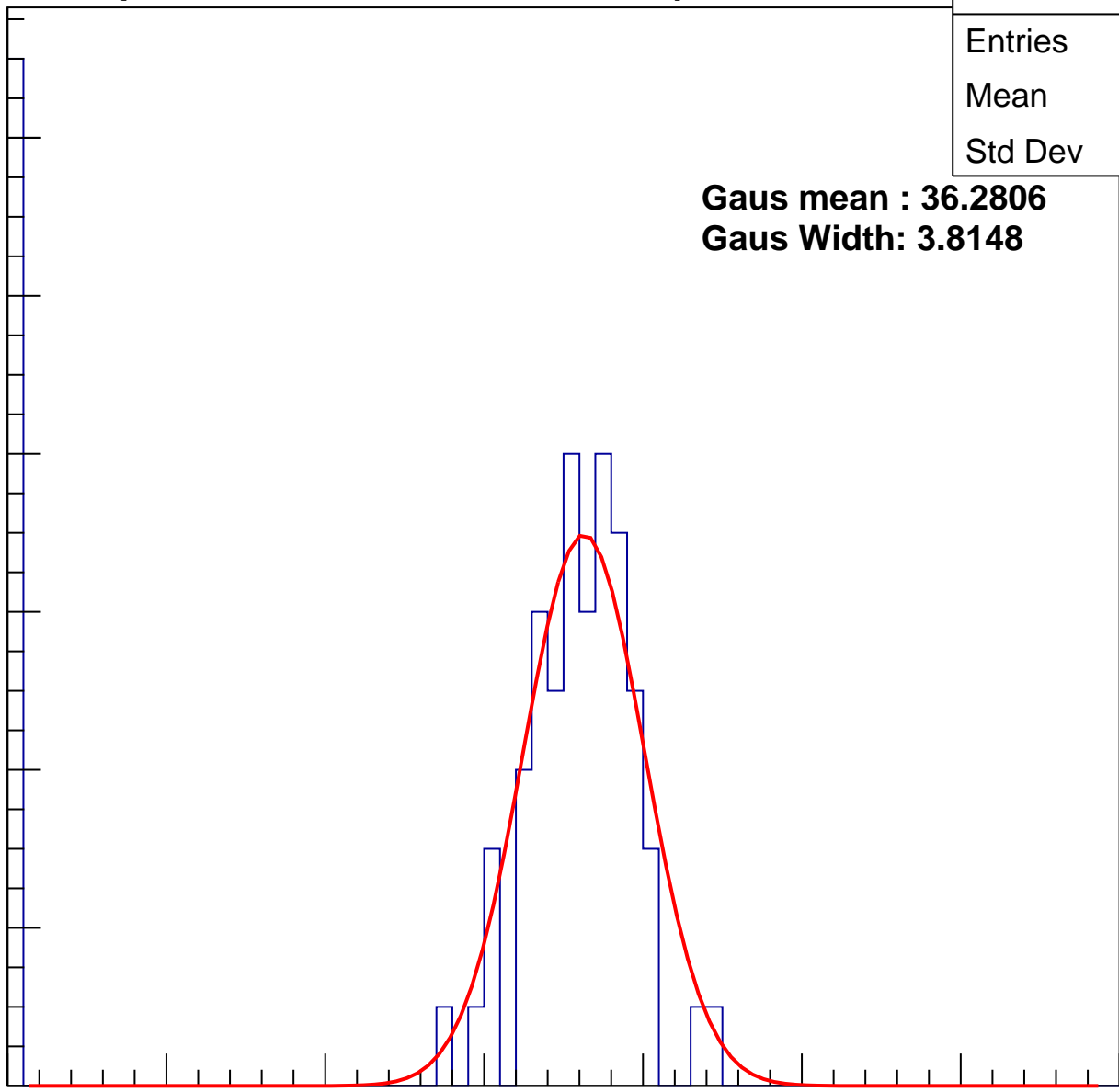
40

50

60

70

ampl



B1L103S, U24-ch64, adc2

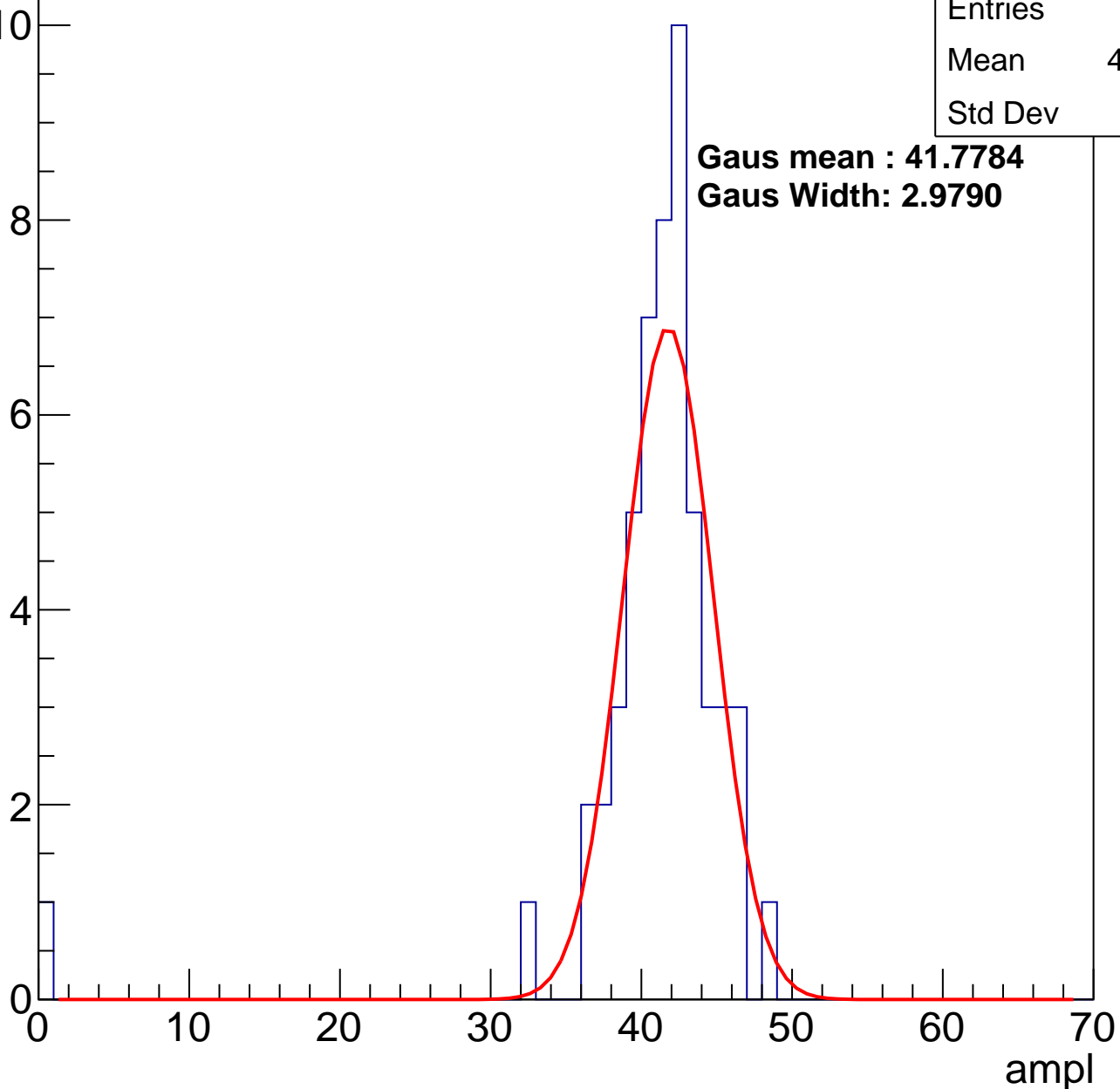
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	40.43
Std Dev	6.25

Gaus mean : 41.7784

Gaus Width: 2.9790

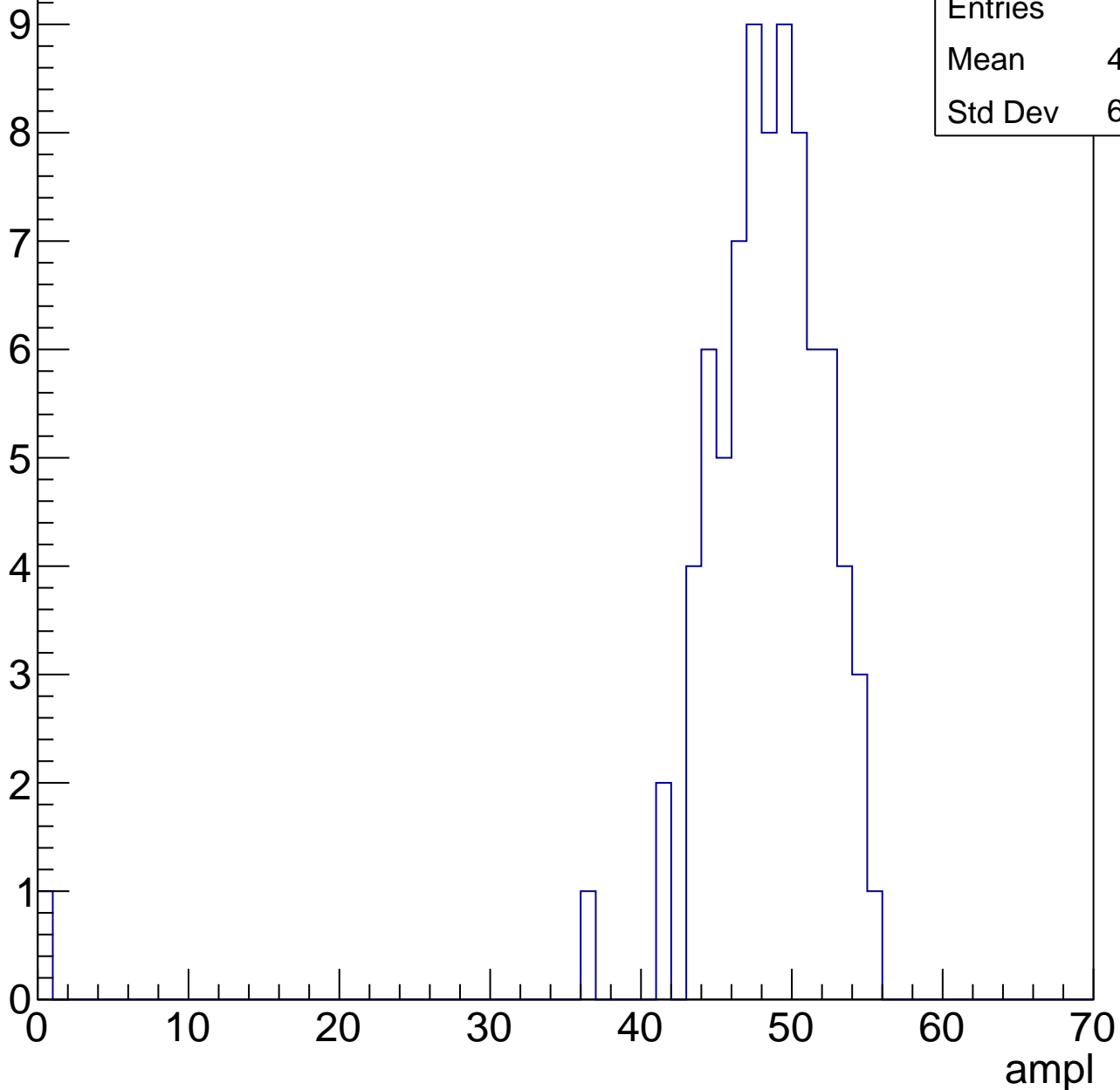


B1L103S, U24-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	47.45
Std Dev	6.372

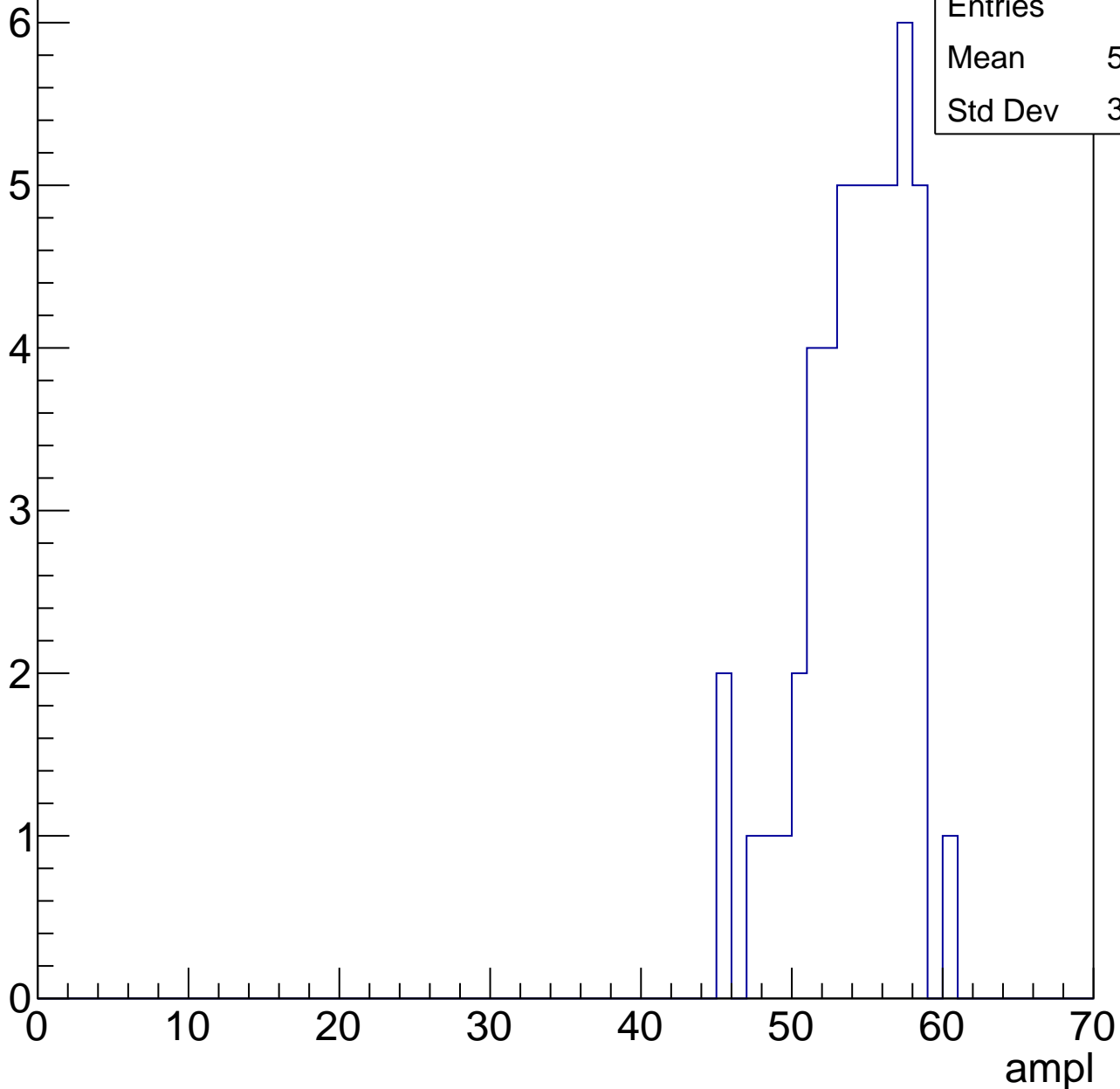


B1L103S, U24-ch64, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

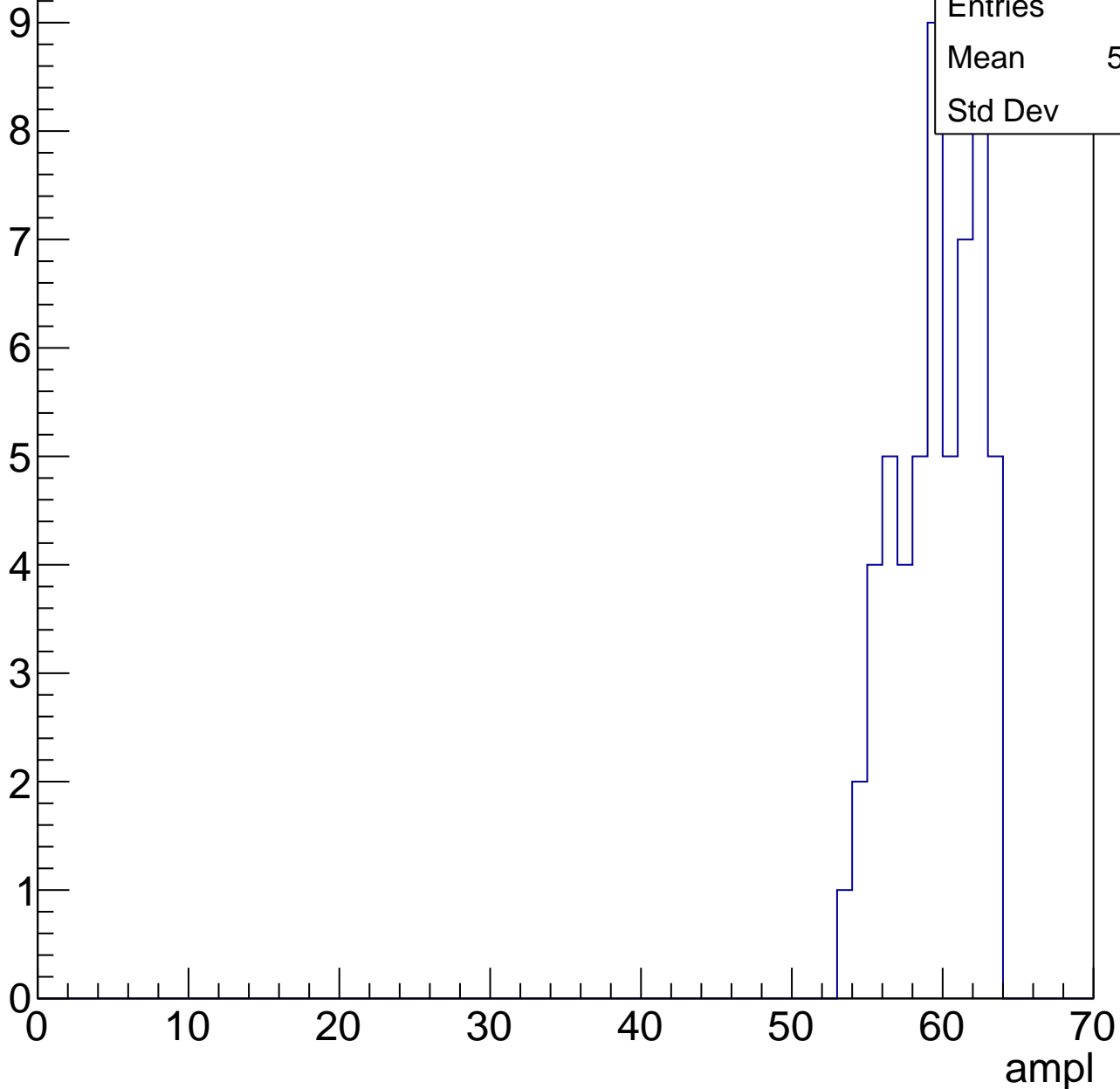
Entries	47
Mean	53.79
Std Dev	3.433



B1L103S, U24-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



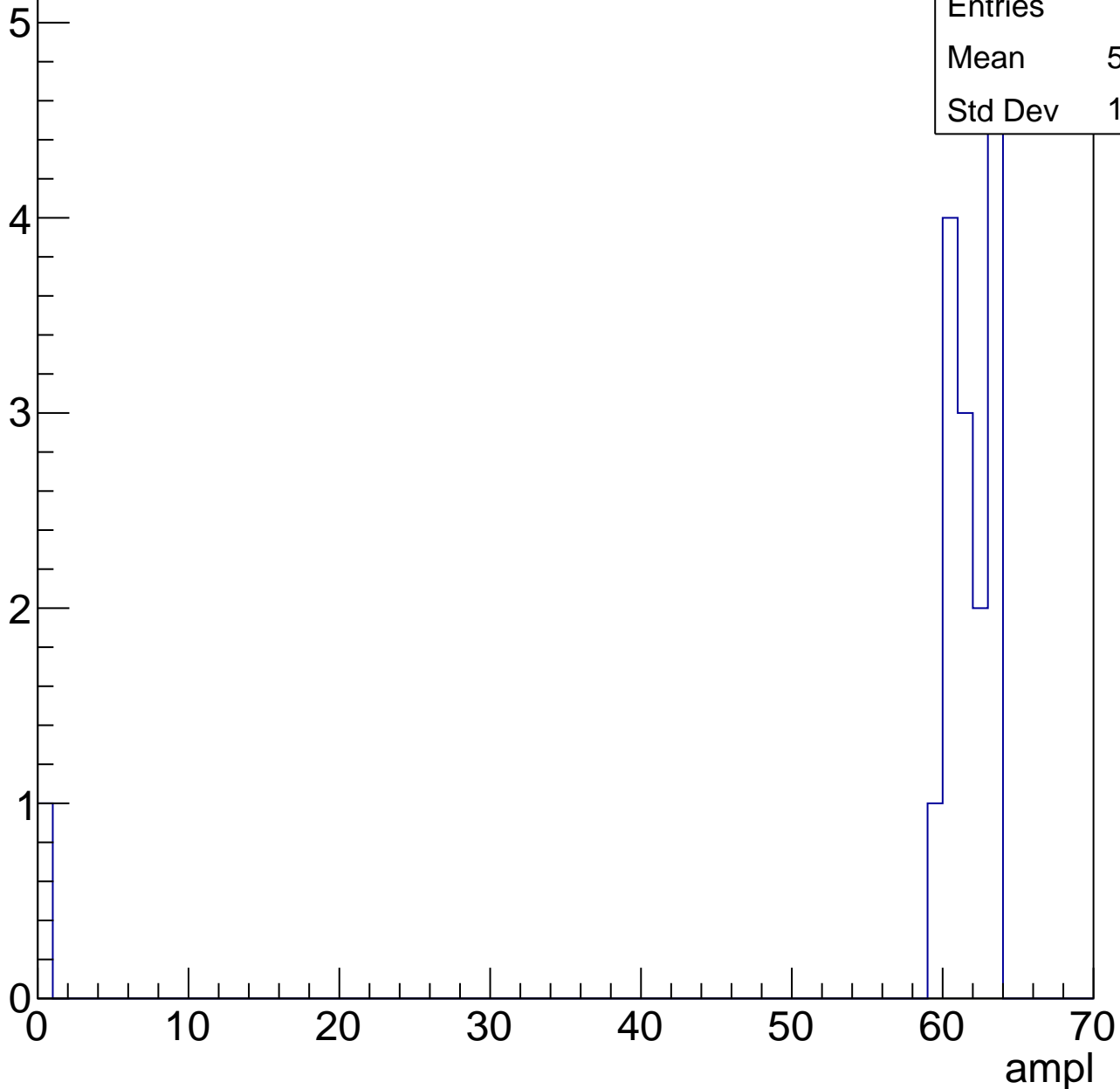
Entries	55
Mean	59.05
Std Dev	2.7

B1L103S, U24-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.56
Std Dev	14.92



B1L103S, U24-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



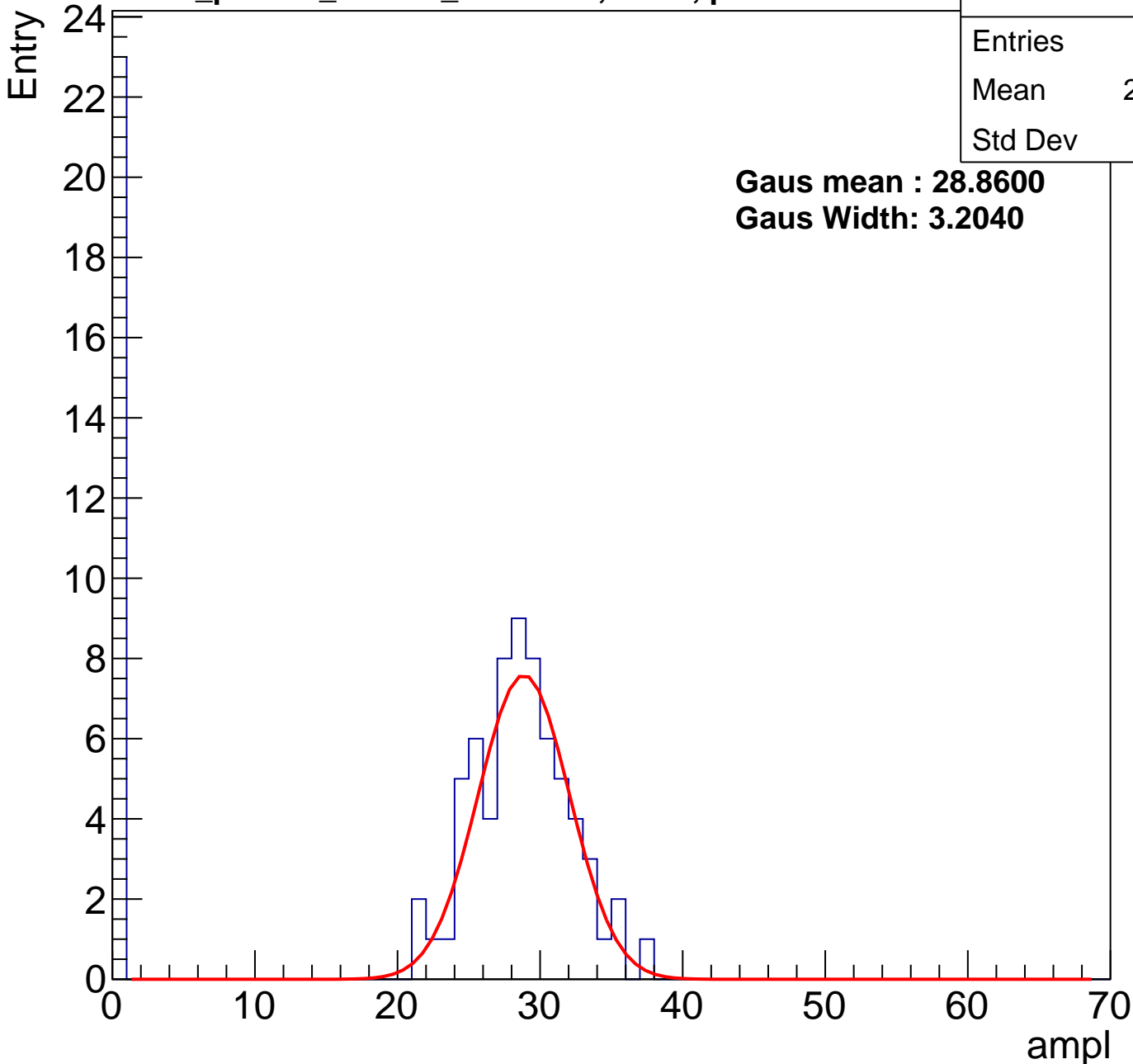
B1L103S, U24-ch65, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	20.94
Std Dev	12.7

Gaus mean : 28.8600

Gaus Width: 3.2040



B1L103S, U24-ch65, adc1

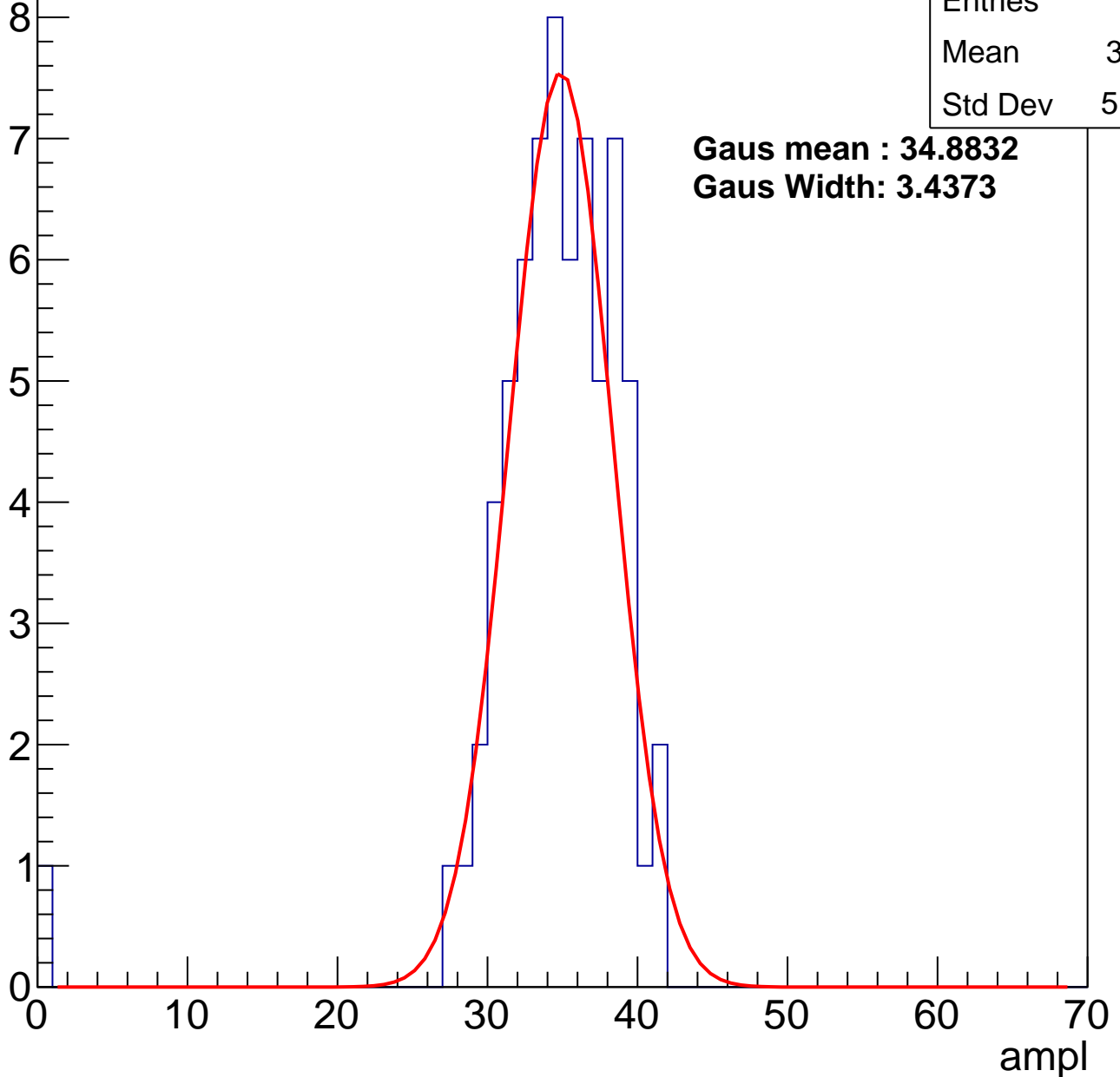
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34.01
Std Dev	5.259

Gaus mean : 34.8832

Gaus Width: 3.4373



B1L103S, U24-ch65, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	35.04
Std Dev	15.54

Gaus mean : 41.8976

Gaus Width: 3.6675

Entry

12

10

8

6

4

2

0

0

10

20

30

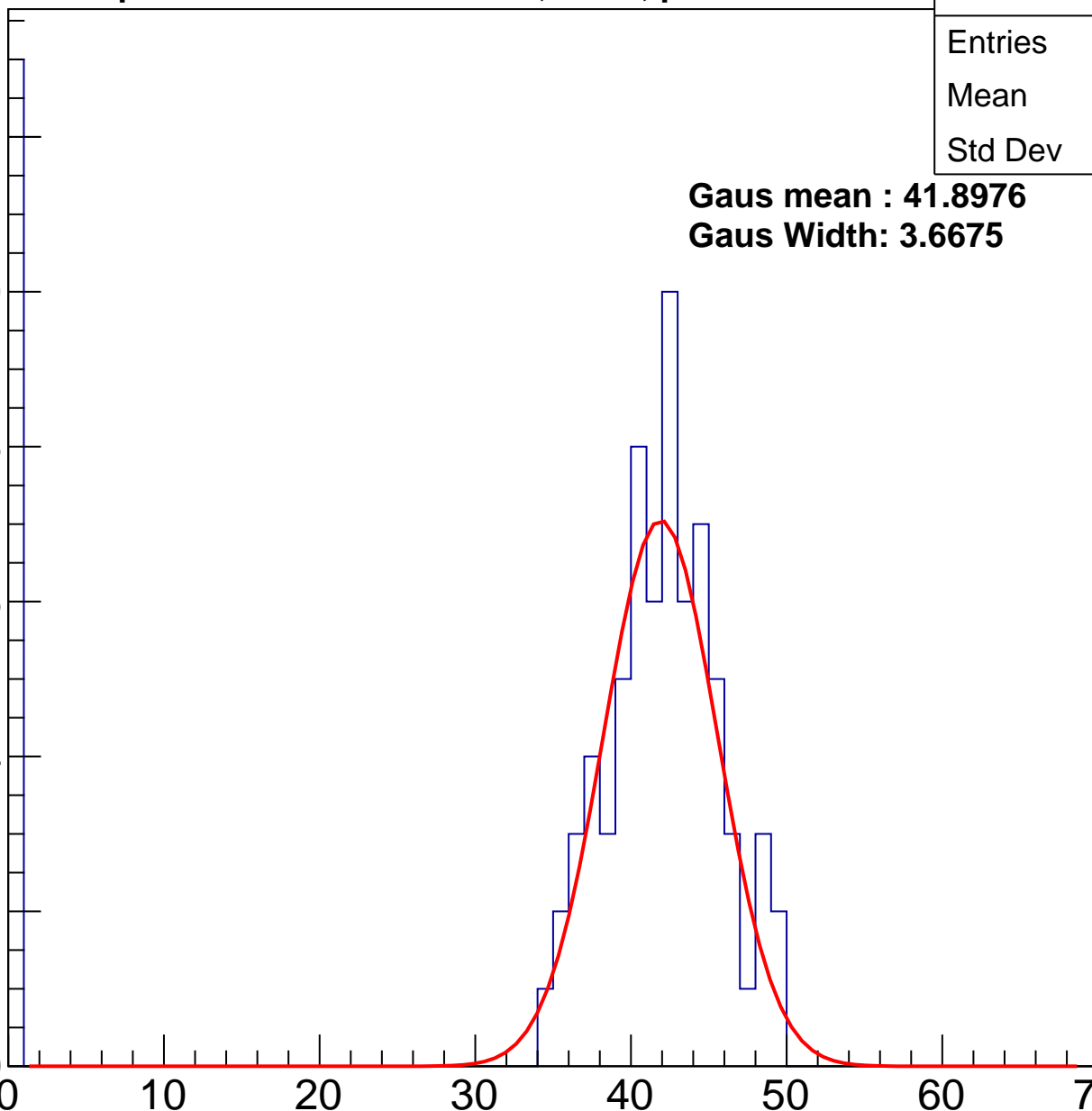
40

50

60

70

ampl

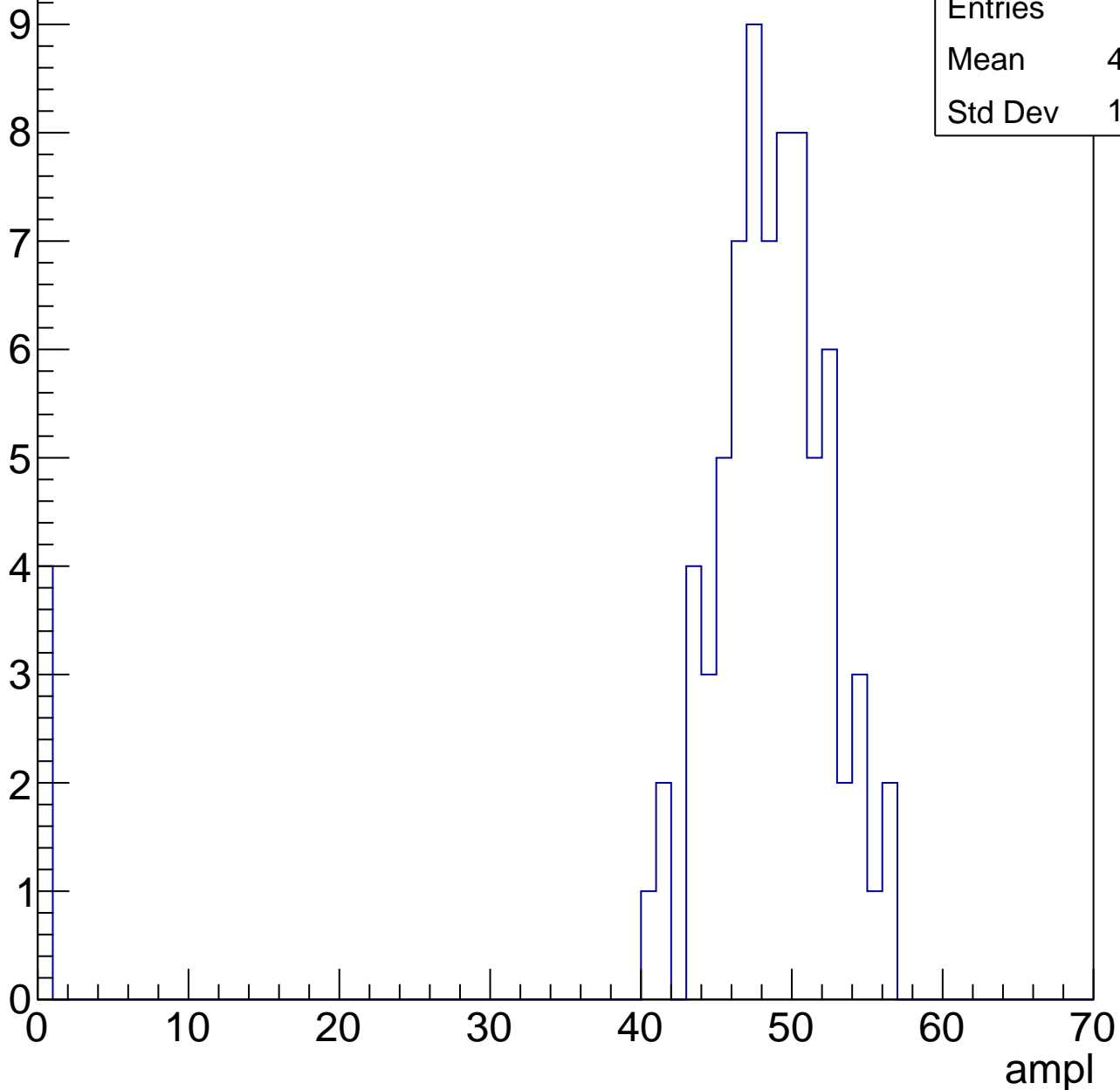


B1L103S, U24-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	45.79
Std Dev	11.25

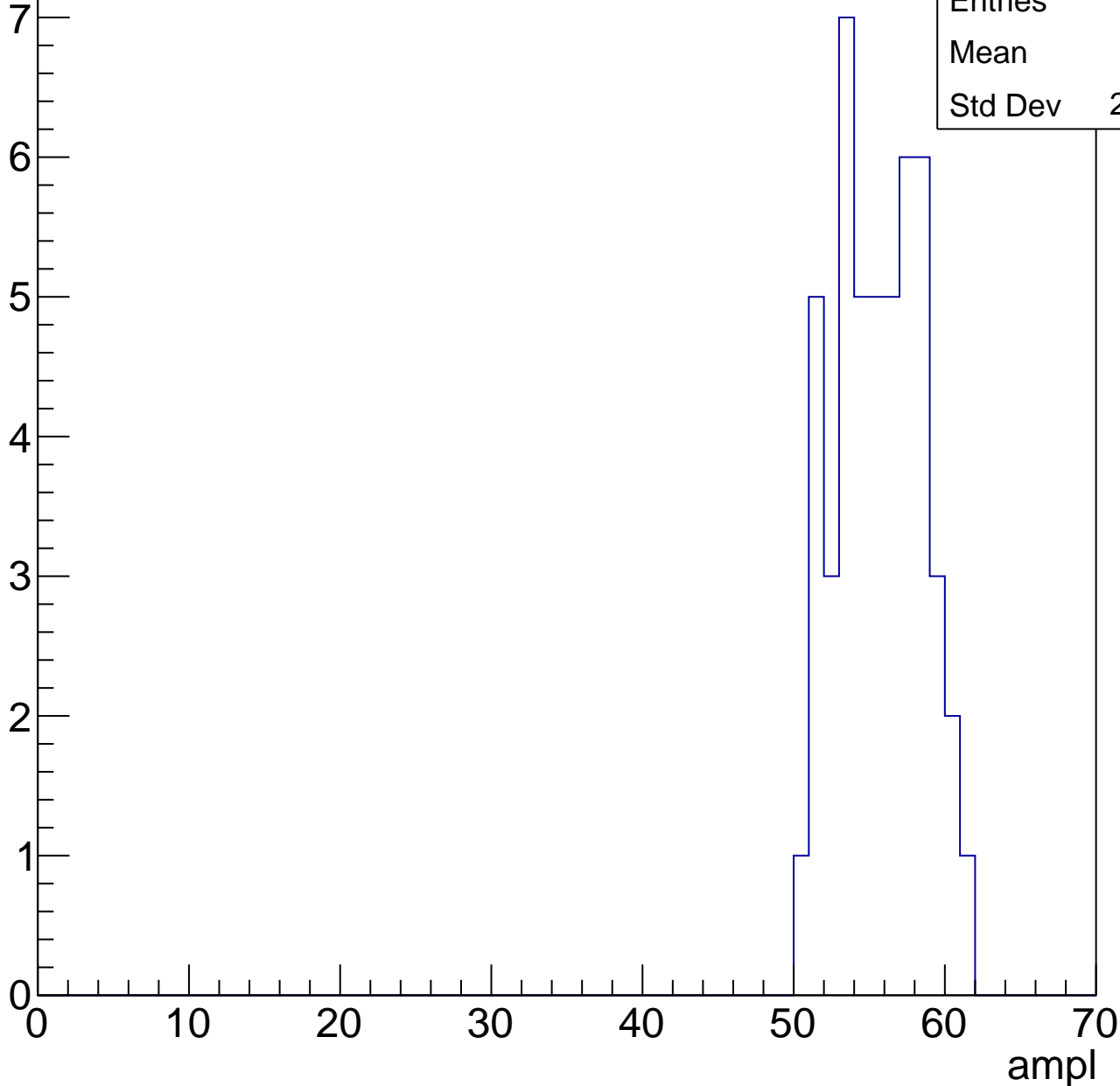


B1L103S, U24-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.2
Std Dev	2.785

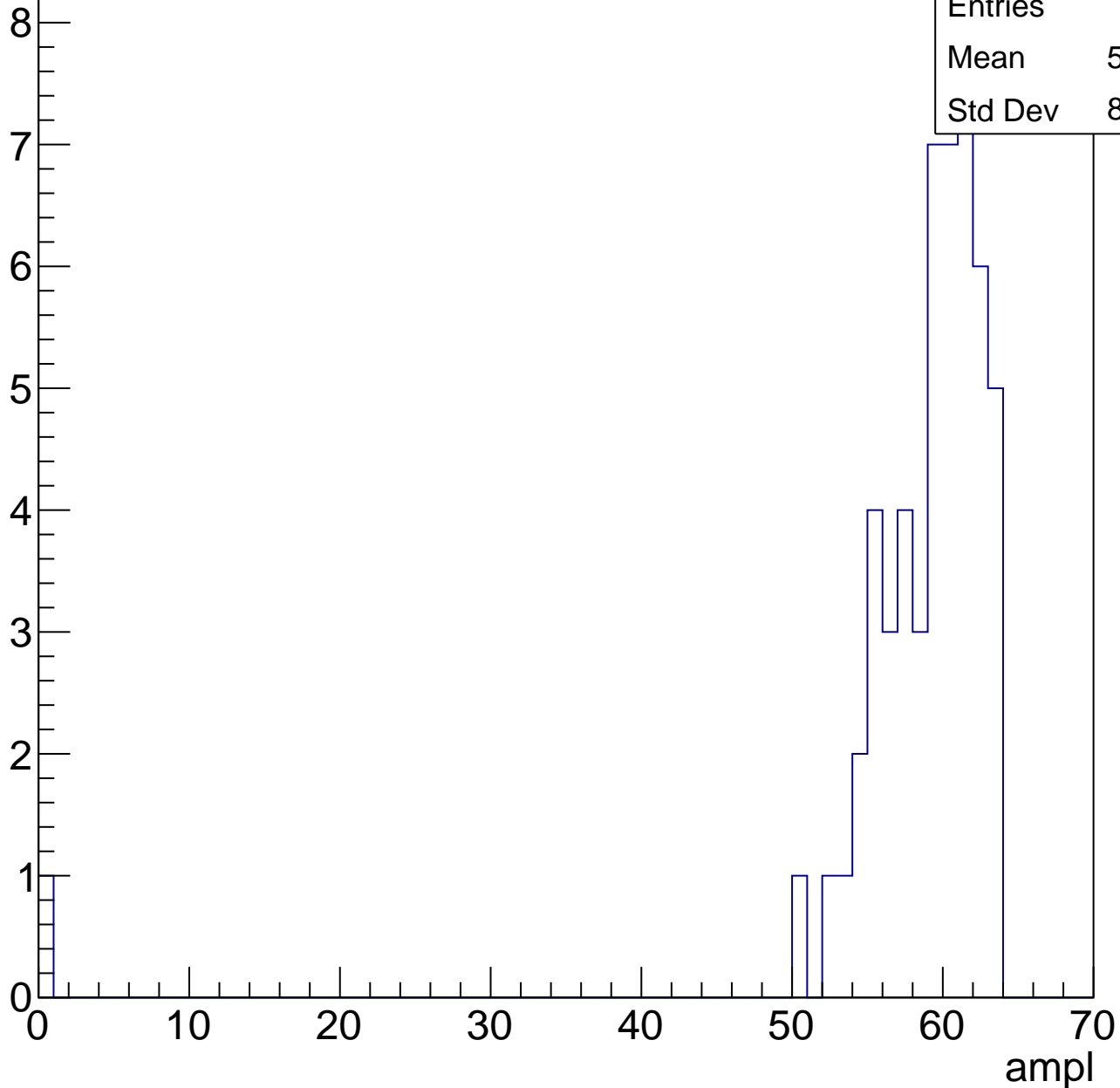


B1L103S, U24-ch65, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

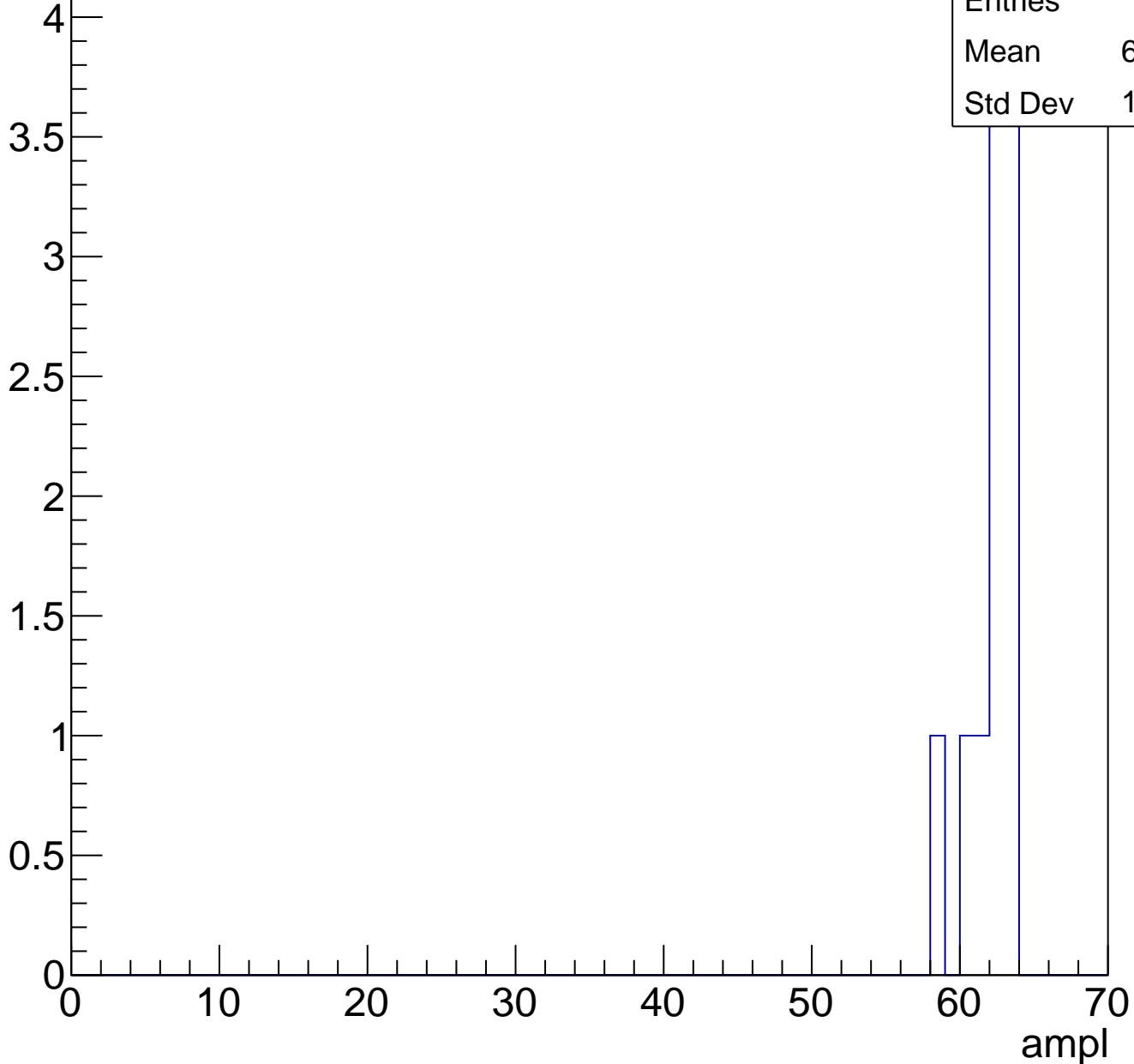
Entries	53
Mean	57.75
Std Dev	8.576



B1L103S, U24-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U24-ch66, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	19.74
Std Dev	13.3

Gaus mean : 28.7480

Gaus Width: 3.5158

Entry

25

20

15

10

5

0

0

10

20

30

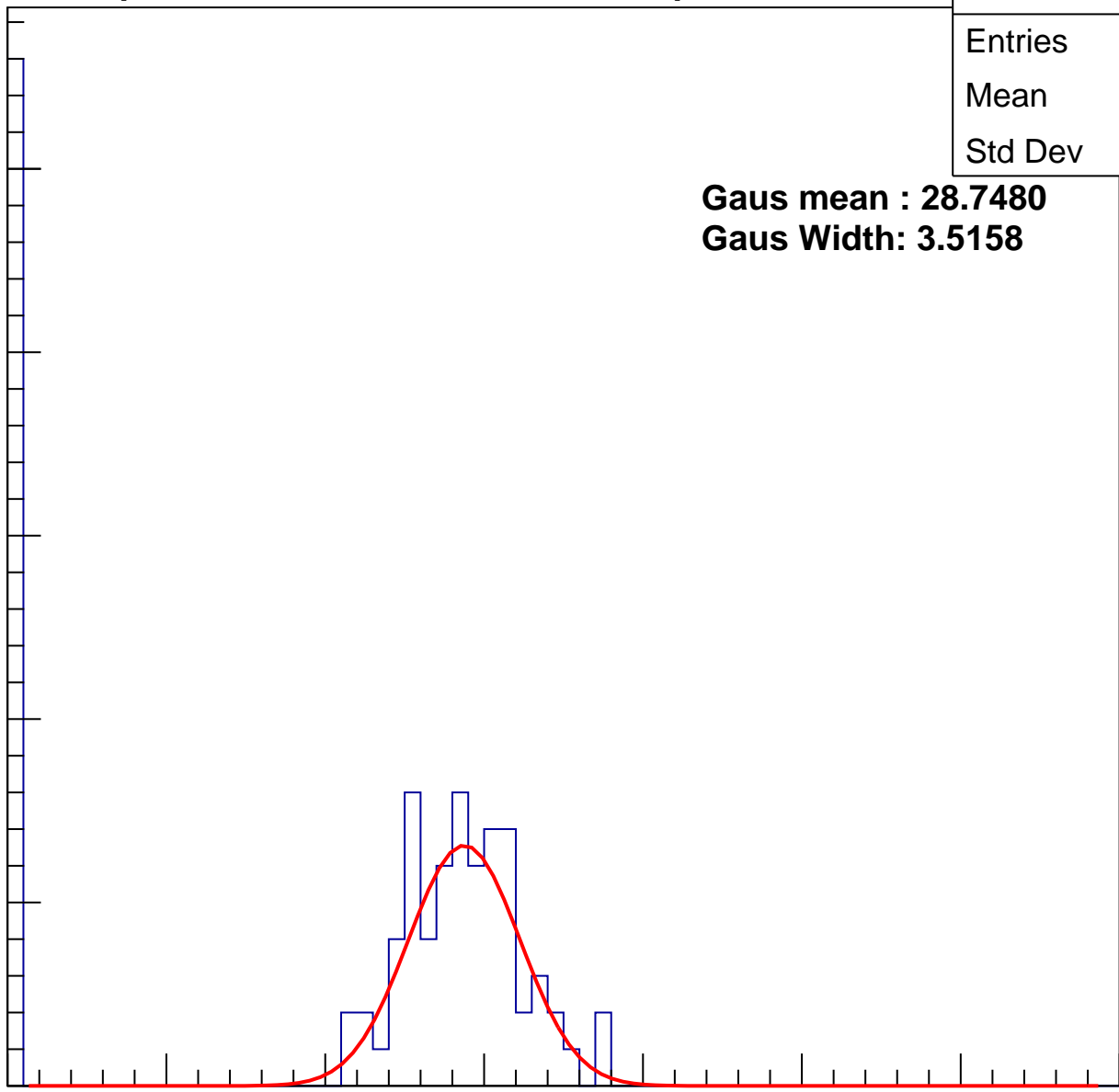
40

50

60

70

ampl



B1L103S, U24-ch66, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	29.56
Std Dev	13.69

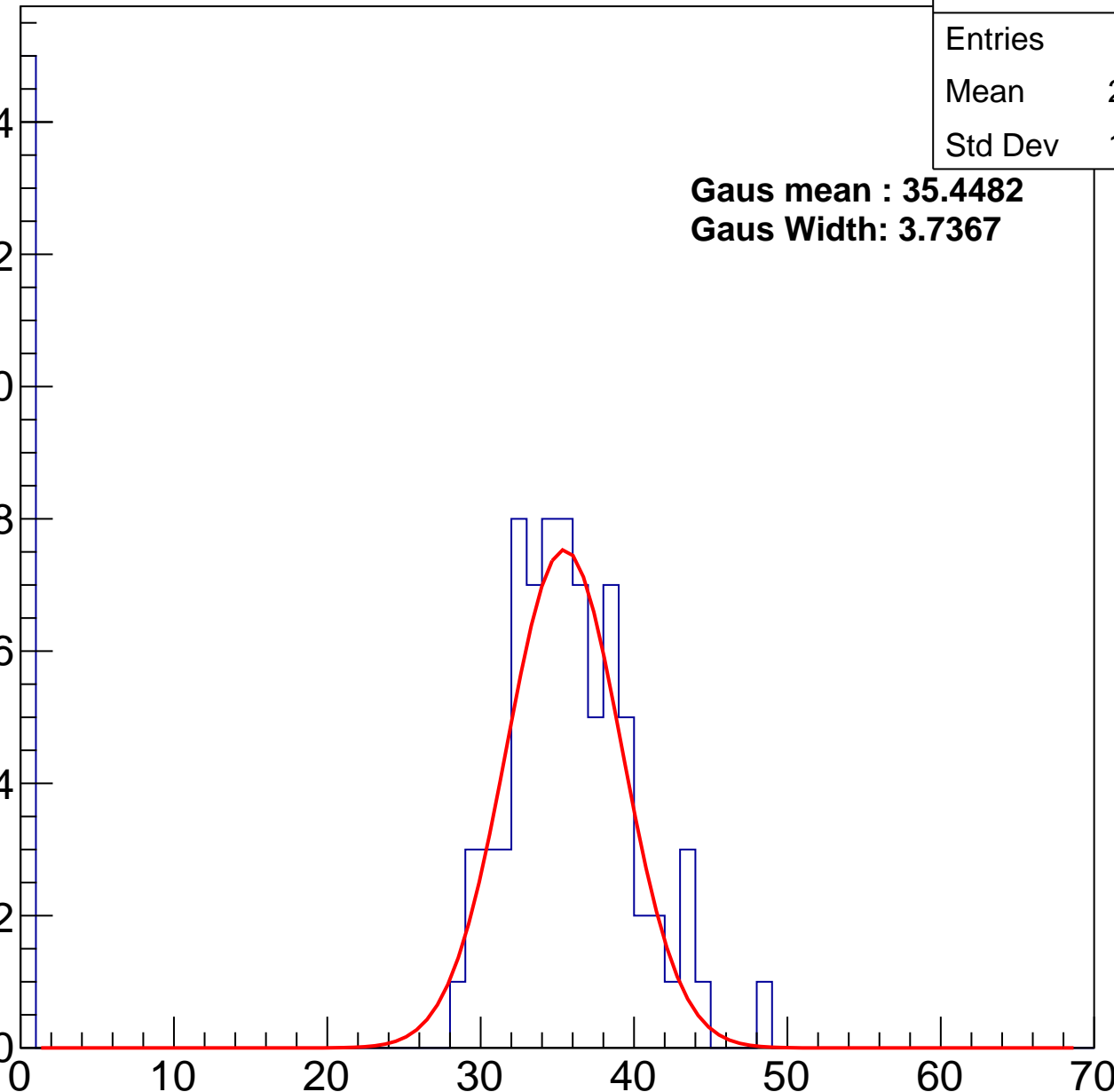
Gaus mean : 35.4482

Gaus Width: 3.7367

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch66, adc2

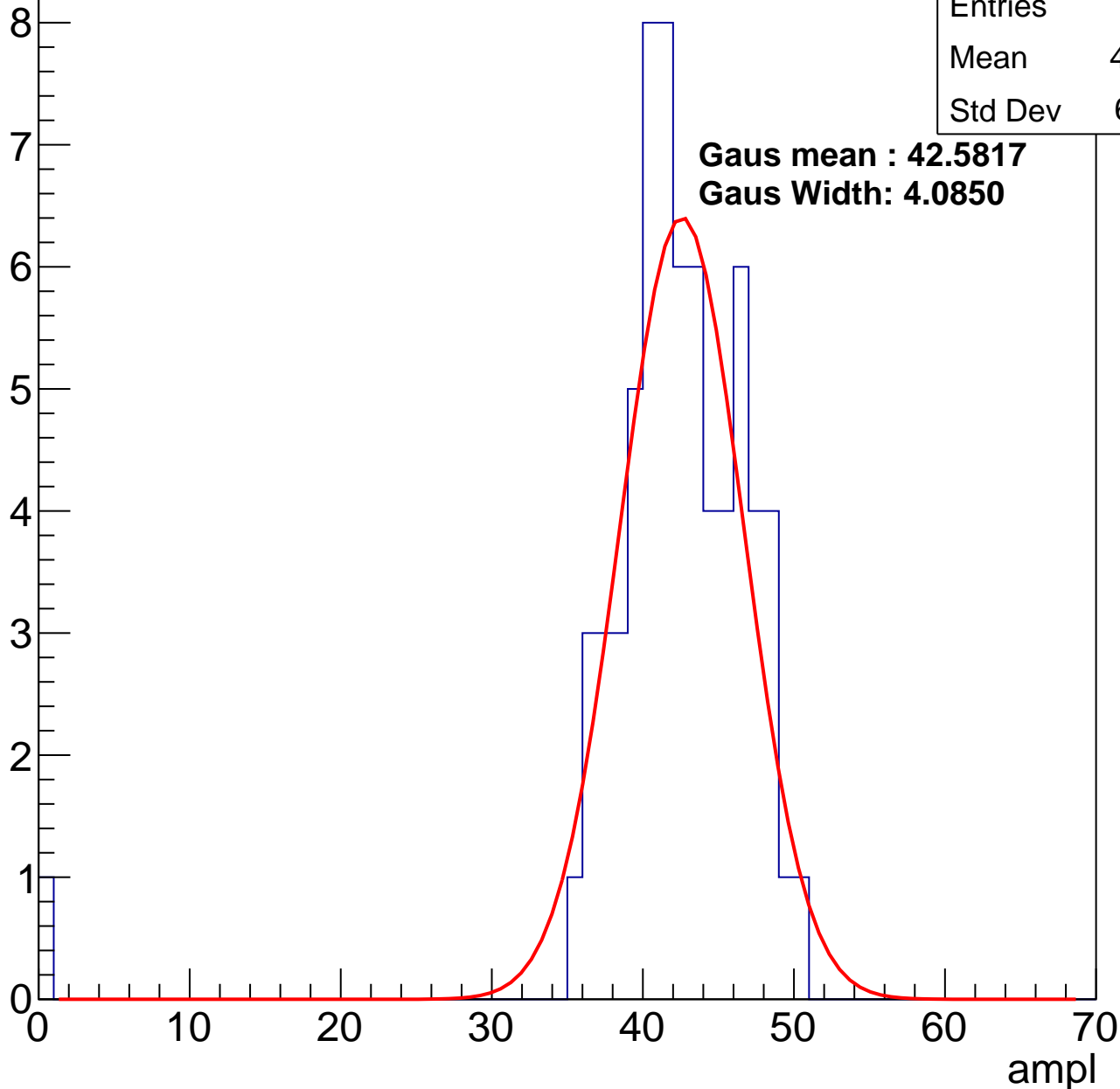
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	41.65
Std Dev	6.221

Gaus mean : 42.5817

Gaus Width: 4.0850

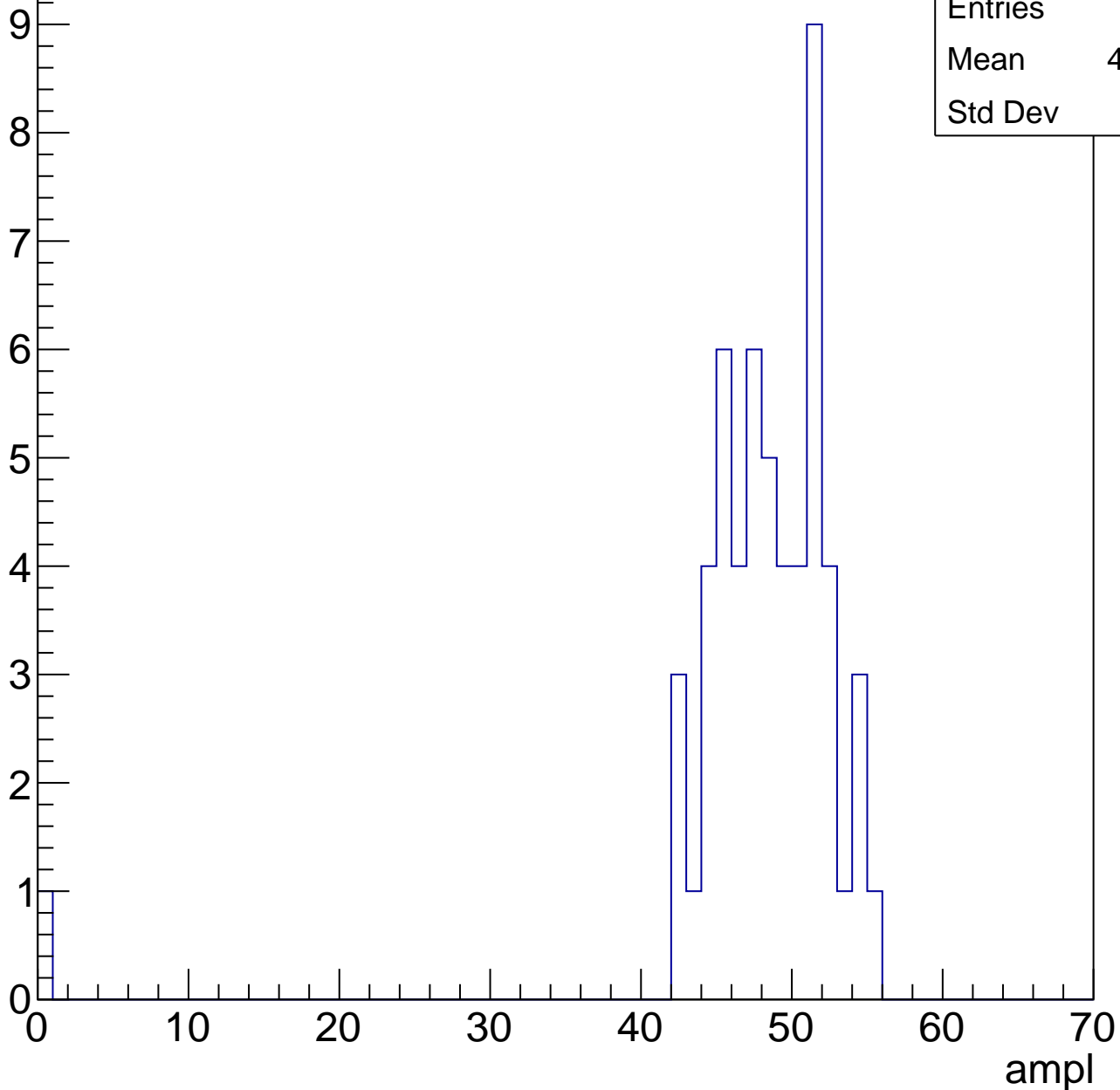


B1L103S, U24-ch66, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	47.39
Std Dev	7.2

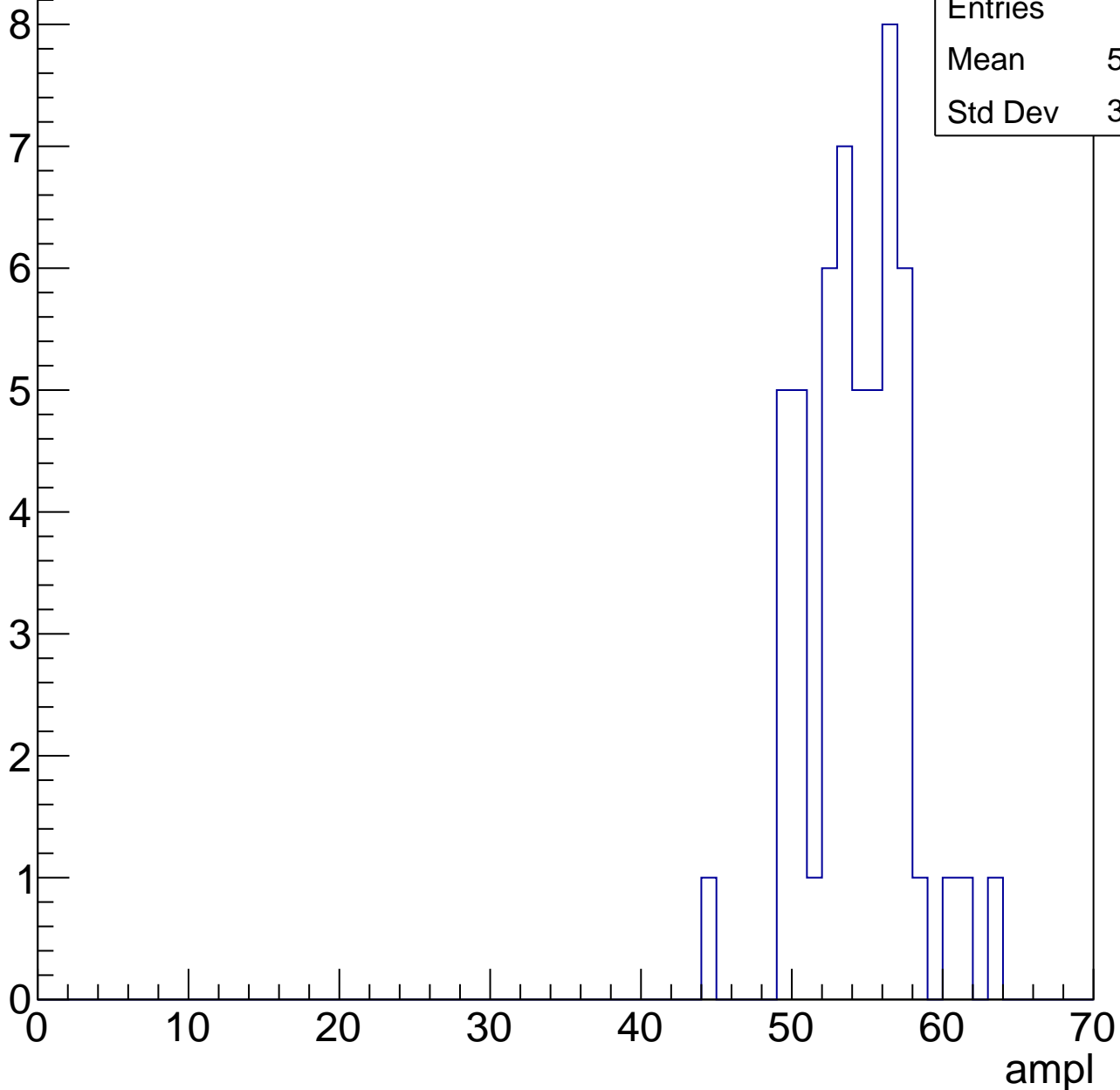


B1L103S, U24-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

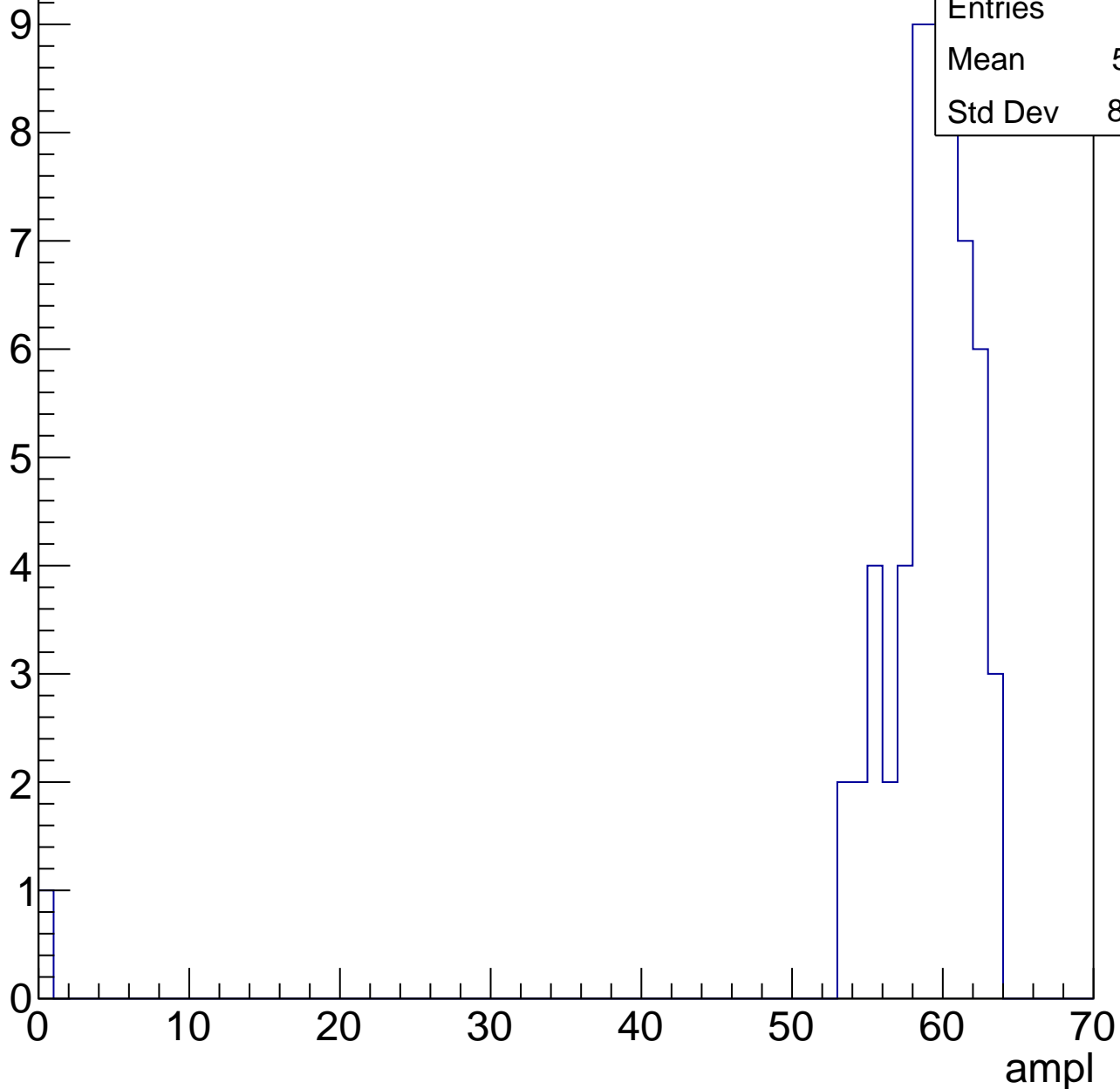
Entries	53
Mean	53.77
Std Dev	3.407



B1L103S, U24-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

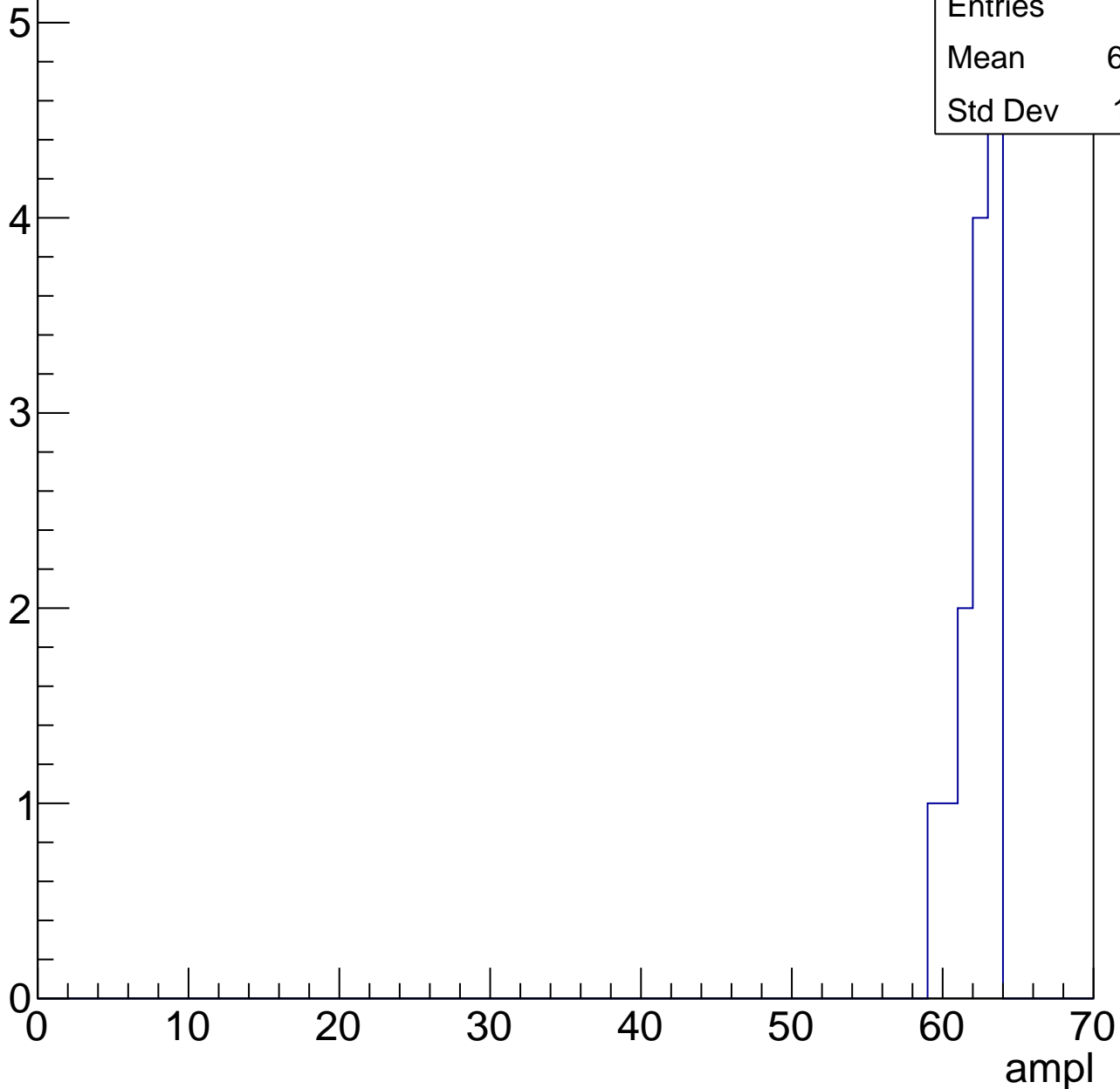


B1L103S, U24-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.85
Std Dev	1.231



B1L103S, U24-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U24-ch67, adc0

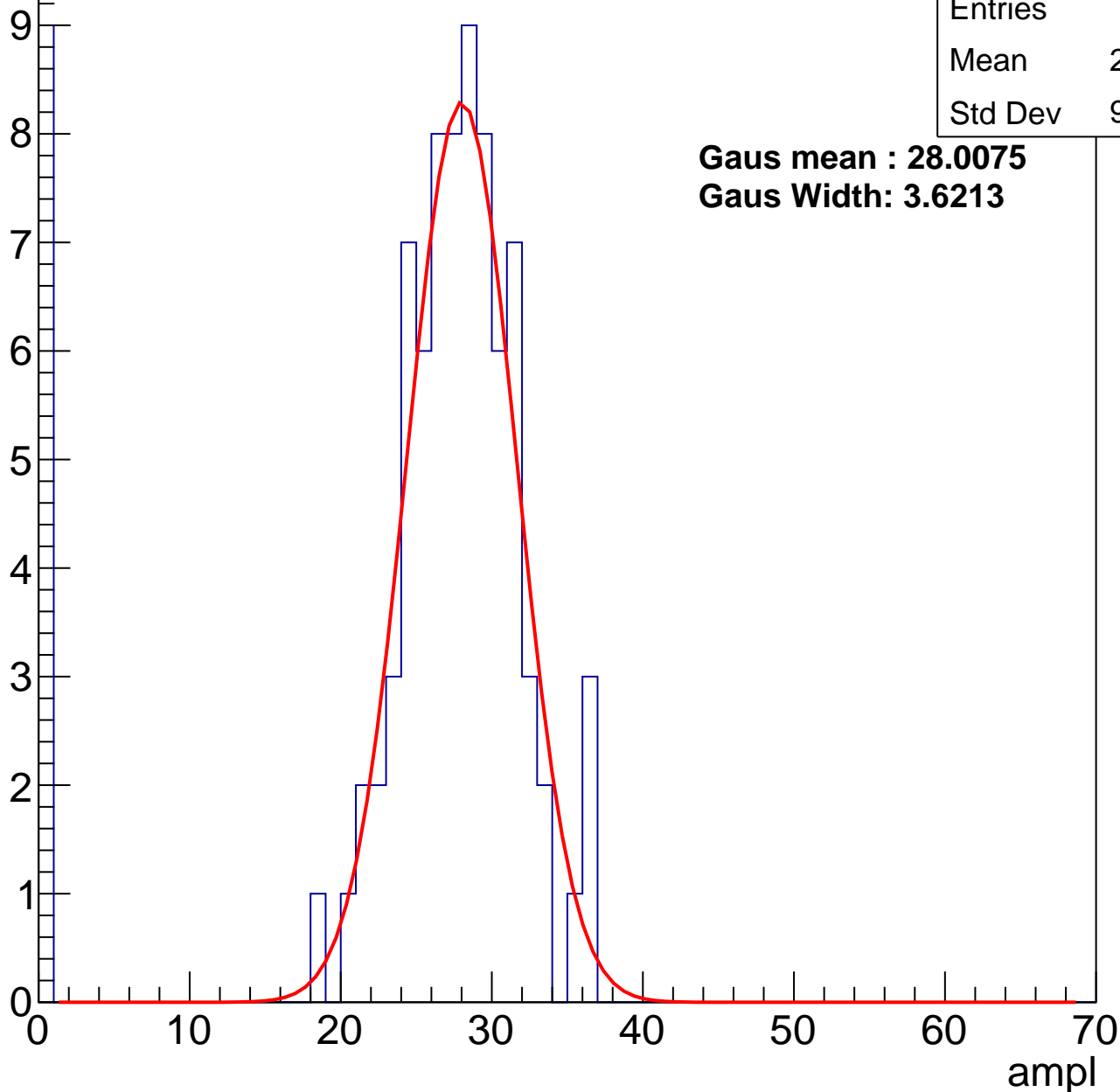
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	24.66
Std Dev	9.116

Gaus mean : 28.0075

Gaus Width: 3.6213



B1L103S, U24-ch67, adc1

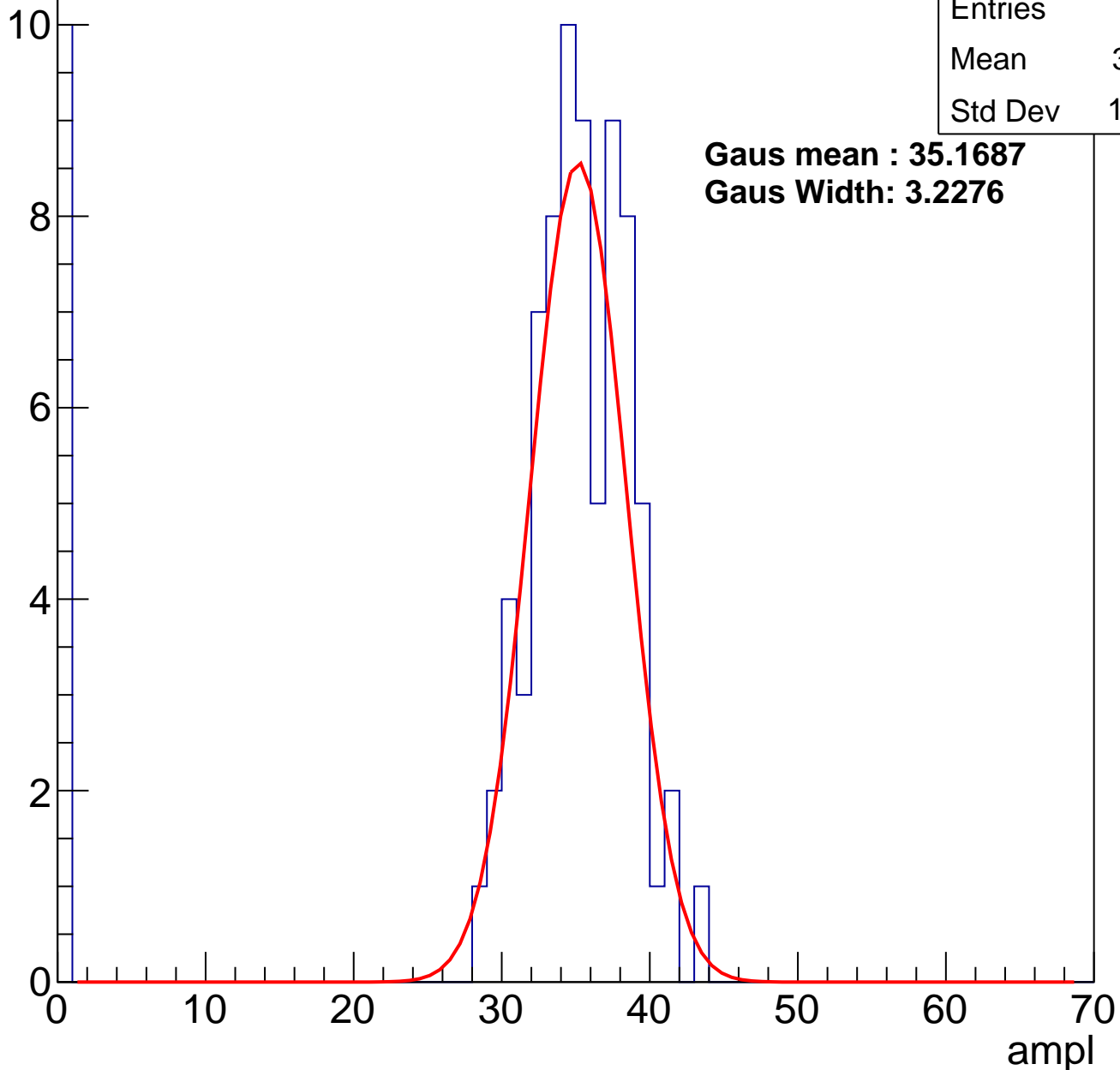
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	30.81
Std Dev	11.63

Gaus mean : 35.1687

Gaus Width: 3.2276

Entry



B1L103S, U24-ch67, adc2

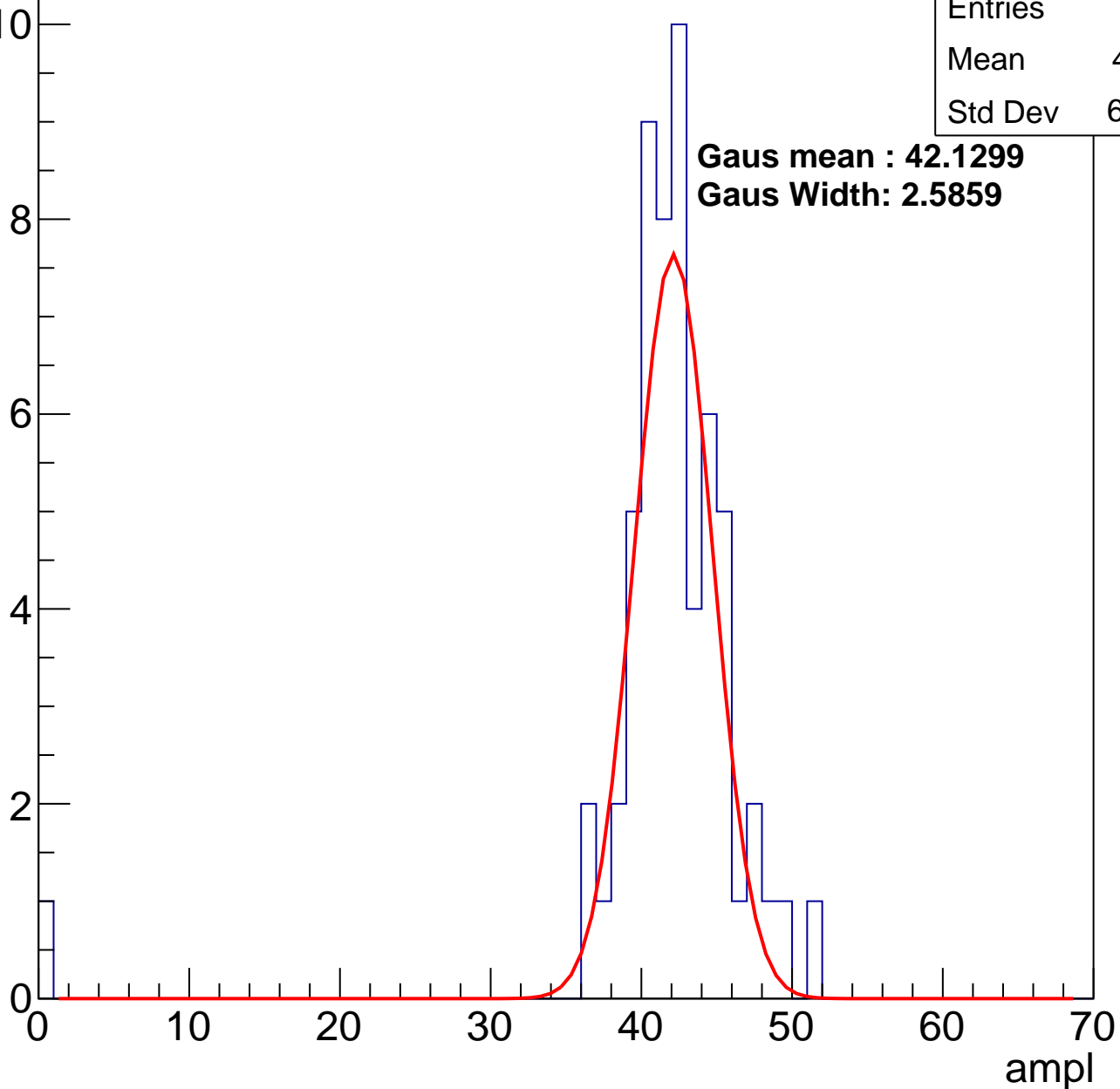
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	41.31
Std Dev	6.187

Gaus mean : 42.1299

Gaus Width: 2.5859

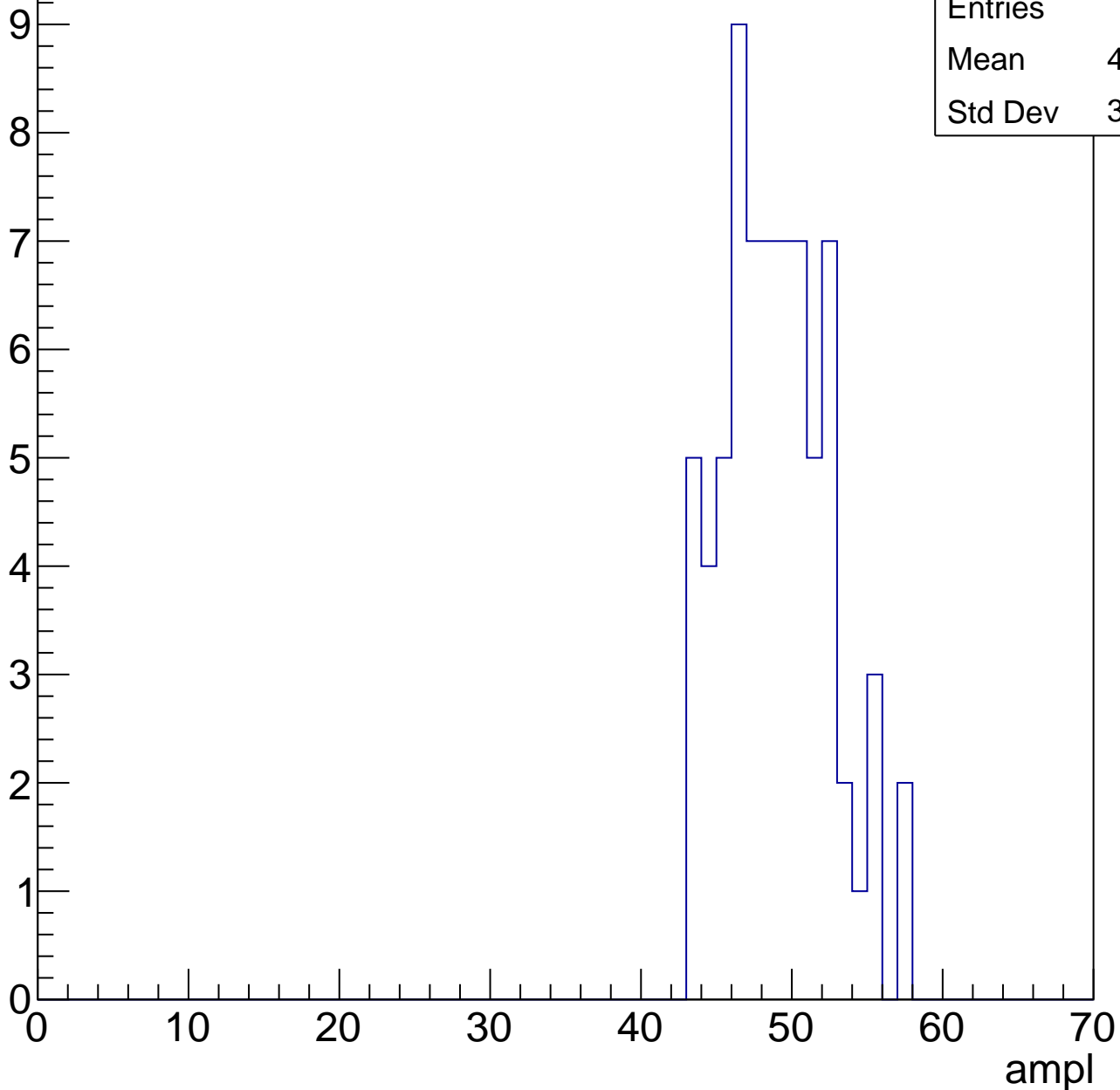


B1L103S, U24-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	48.54
Std Dev	3.447

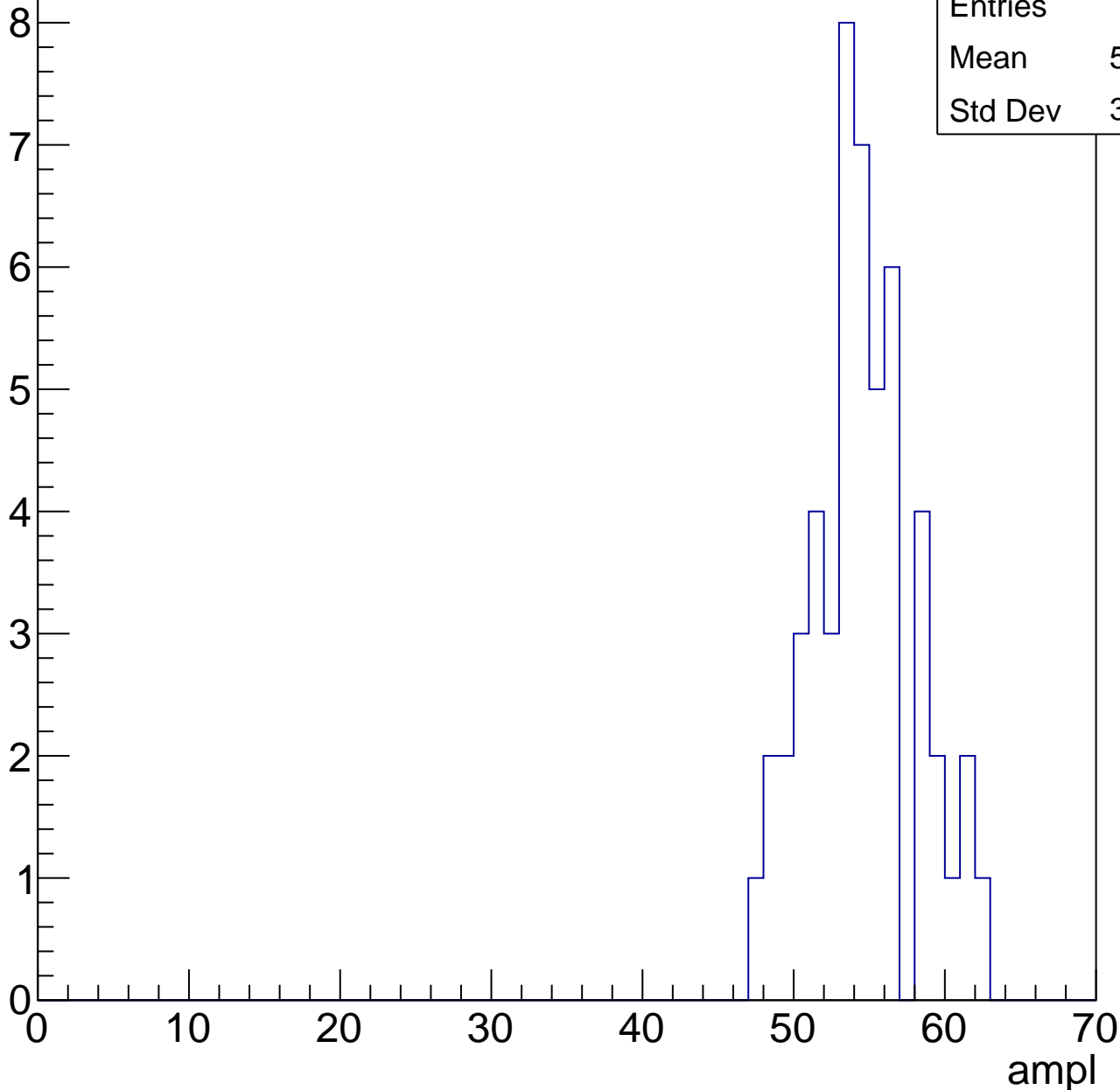


B1L103S, U24-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

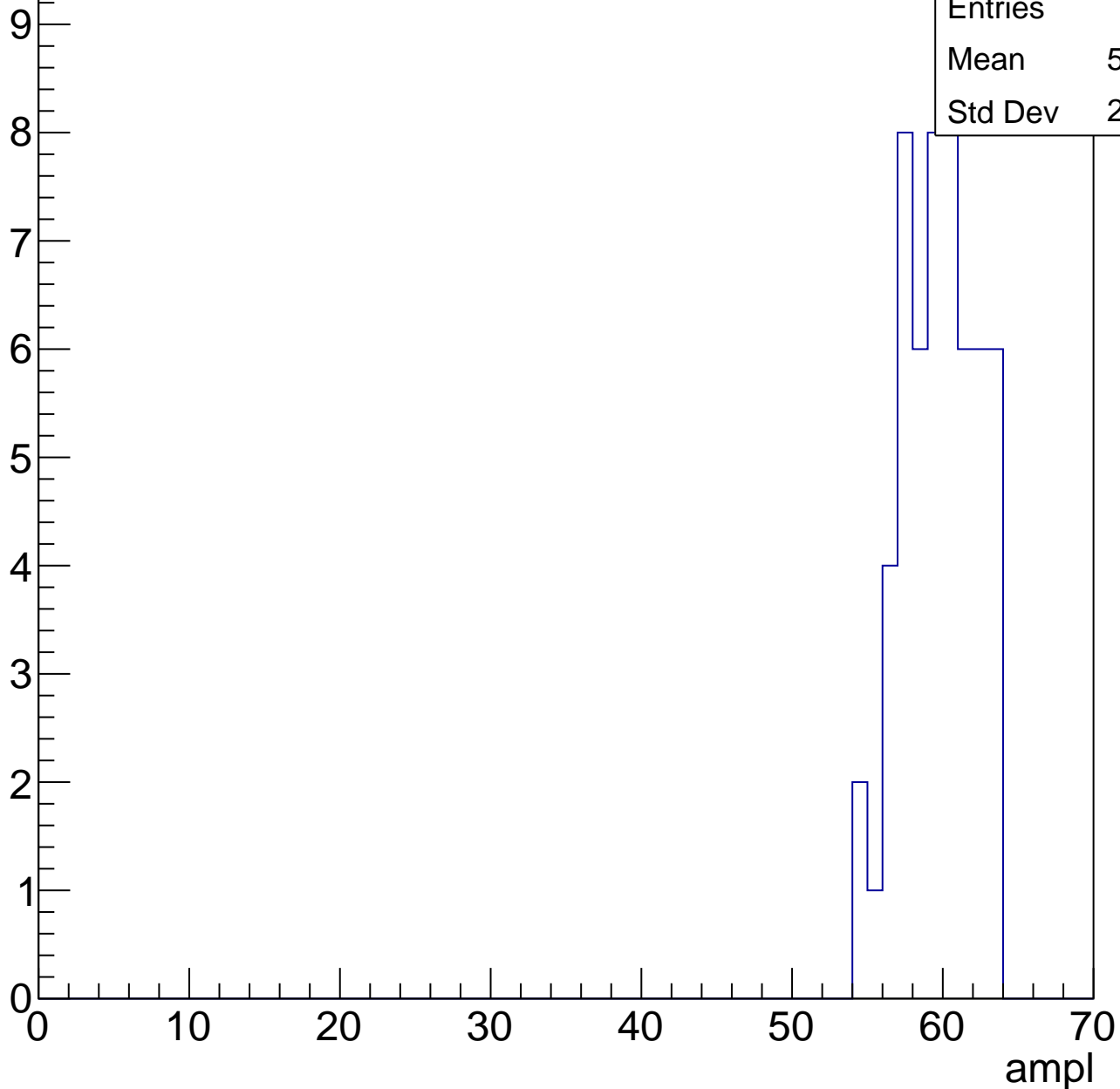
Entries	51
Mean	54.08
Std Dev	3.475



B1L103S, U24-ch67, adc5

calib_packv5_041523_1651.root, FC#0, port C2

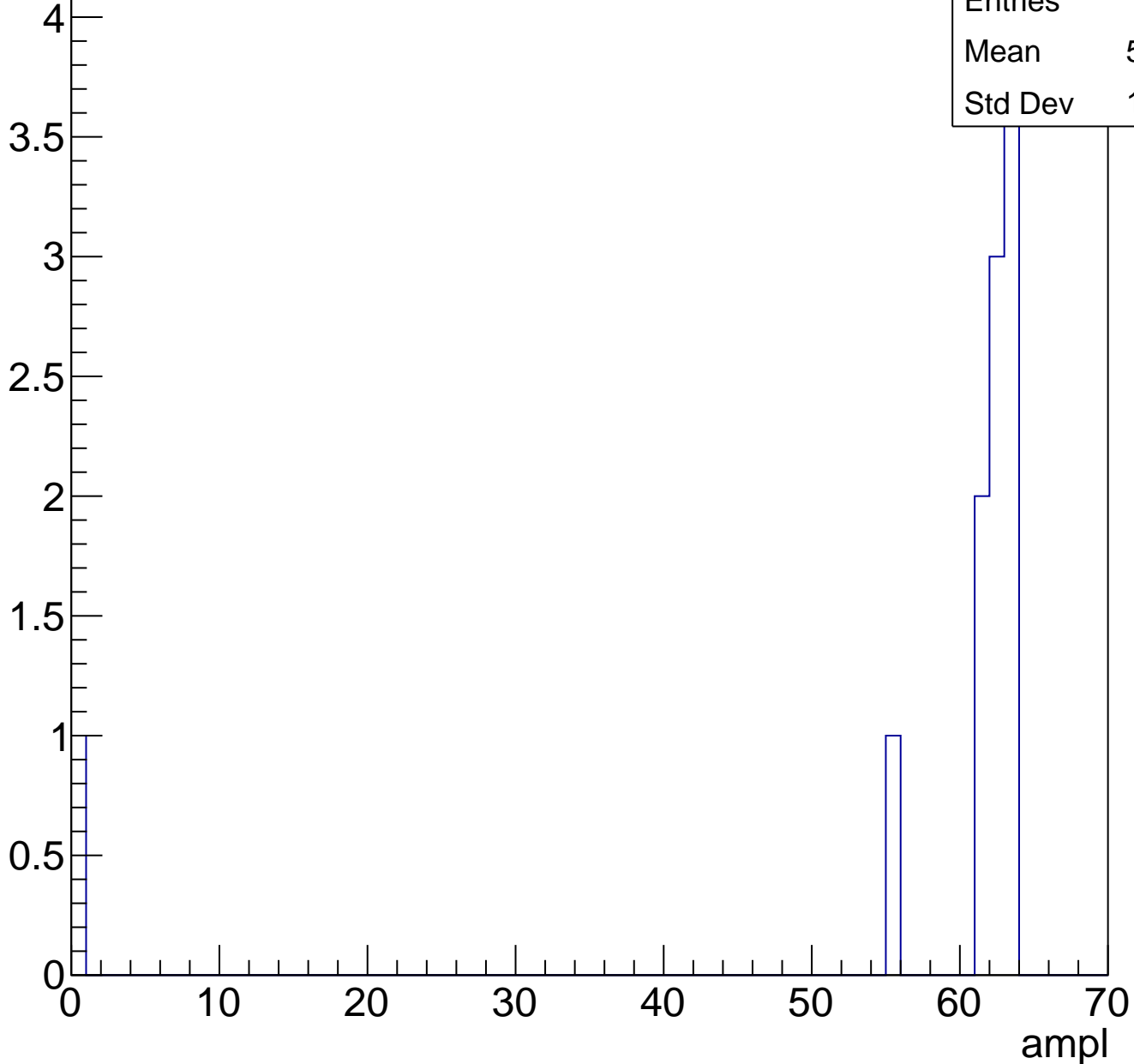
Entry



B1L103S, U24-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch68, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	20.14
Std Dev	11.4

Gaus mean : 27.3249

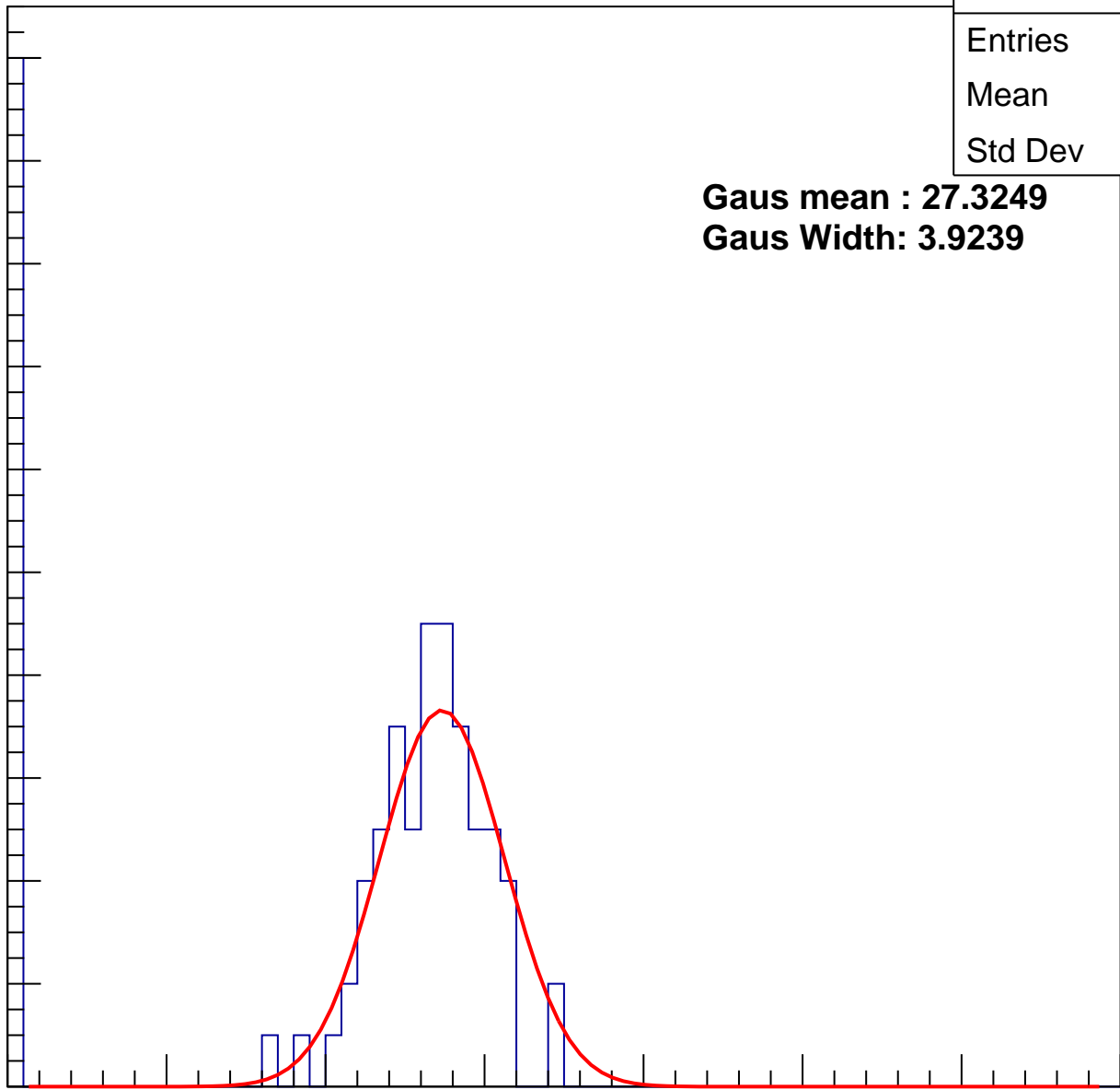
Gaus Width: 3.9239

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch68, adc1

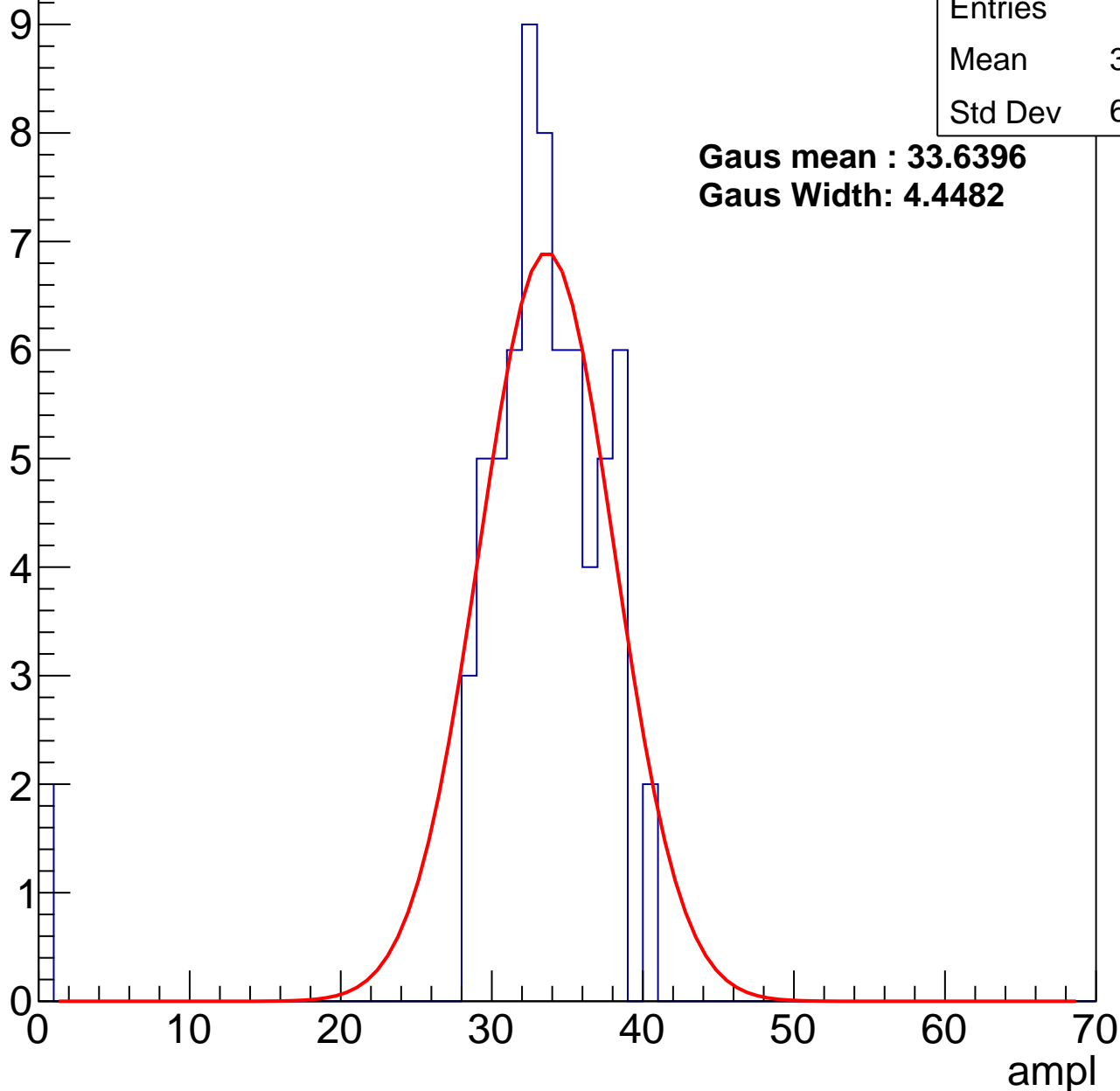
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.36
Std Dev	6.438

Gaus mean : 33.6396

Gaus Width: 4.4482



B1L103S, U24-ch68, adc2

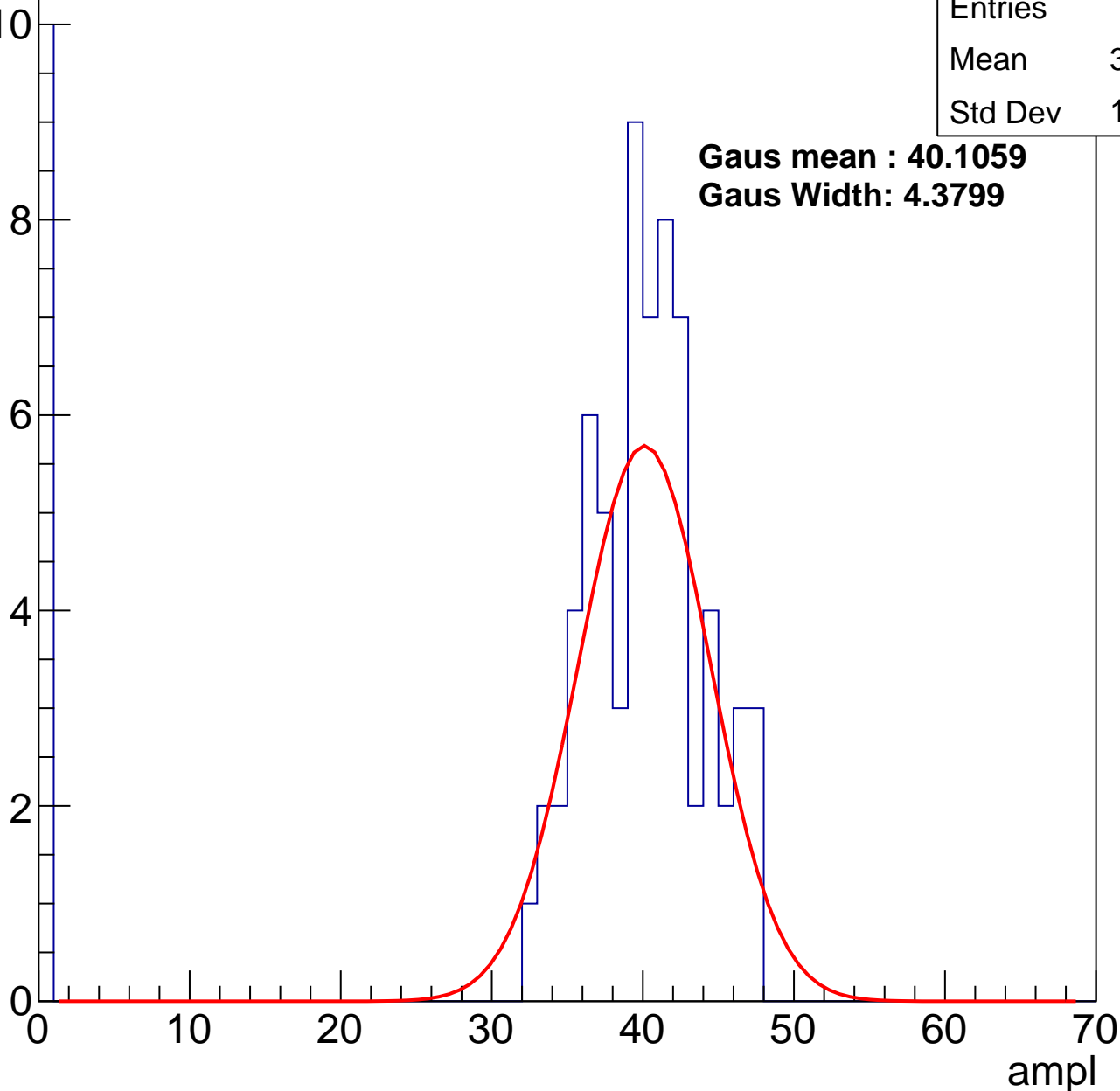
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.68
Std Dev	13.73

Gaus mean : 40.1059

Gaus Width: 4.3799

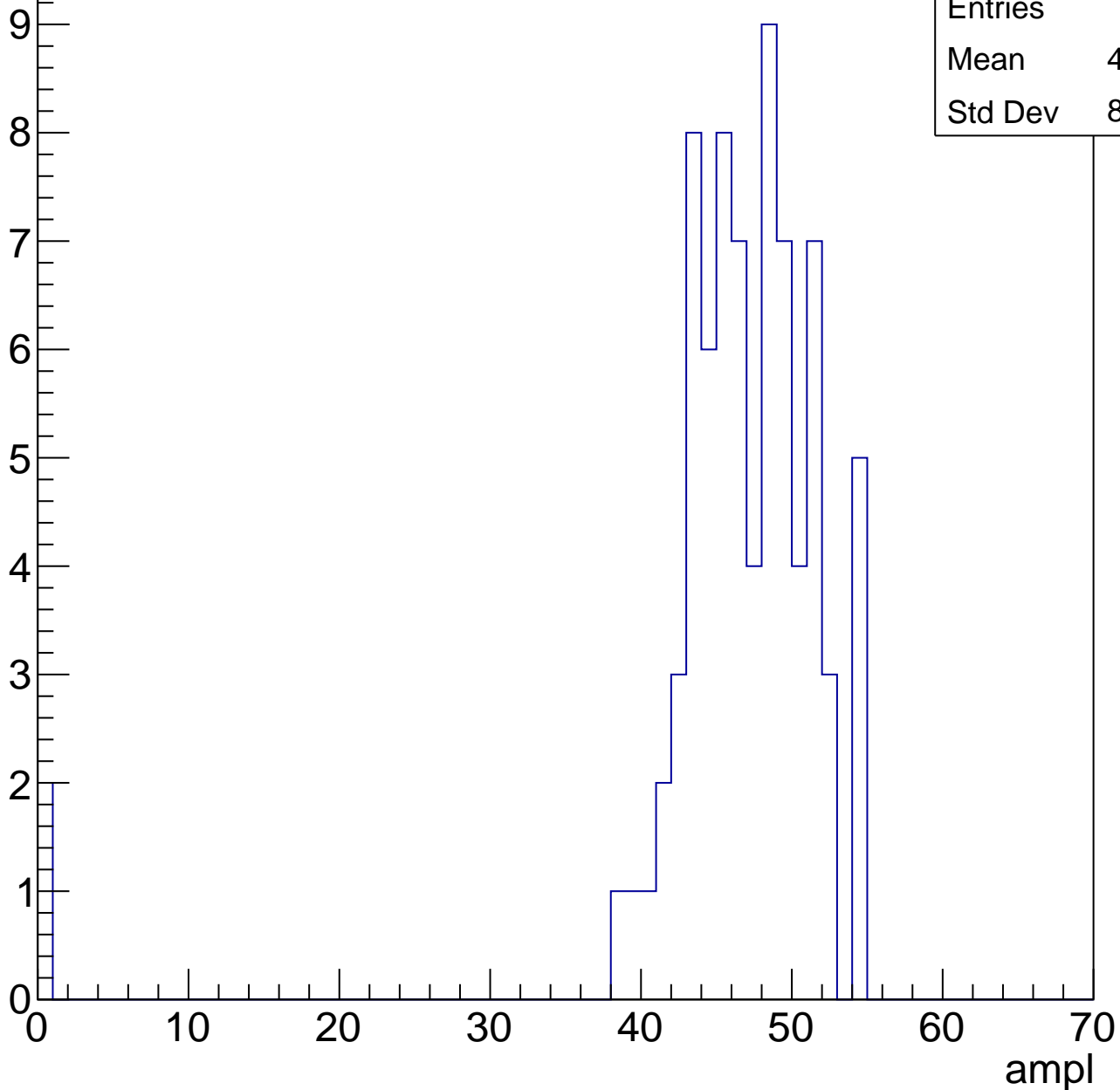


B1L103S, U24-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	45.65
Std Dev	8.277

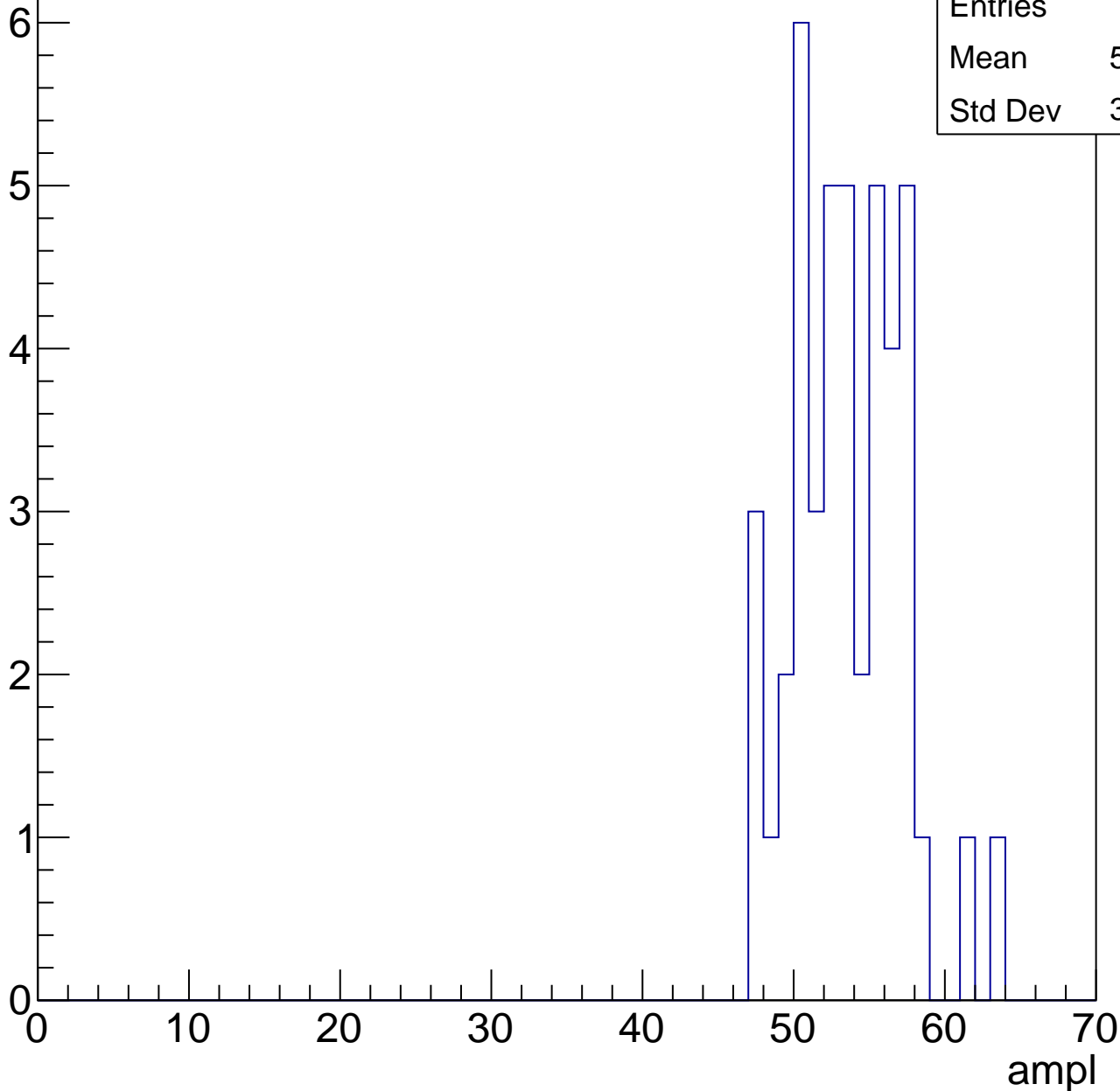


B1L103S, U24-ch68, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	53.16
Std Dev	3.574

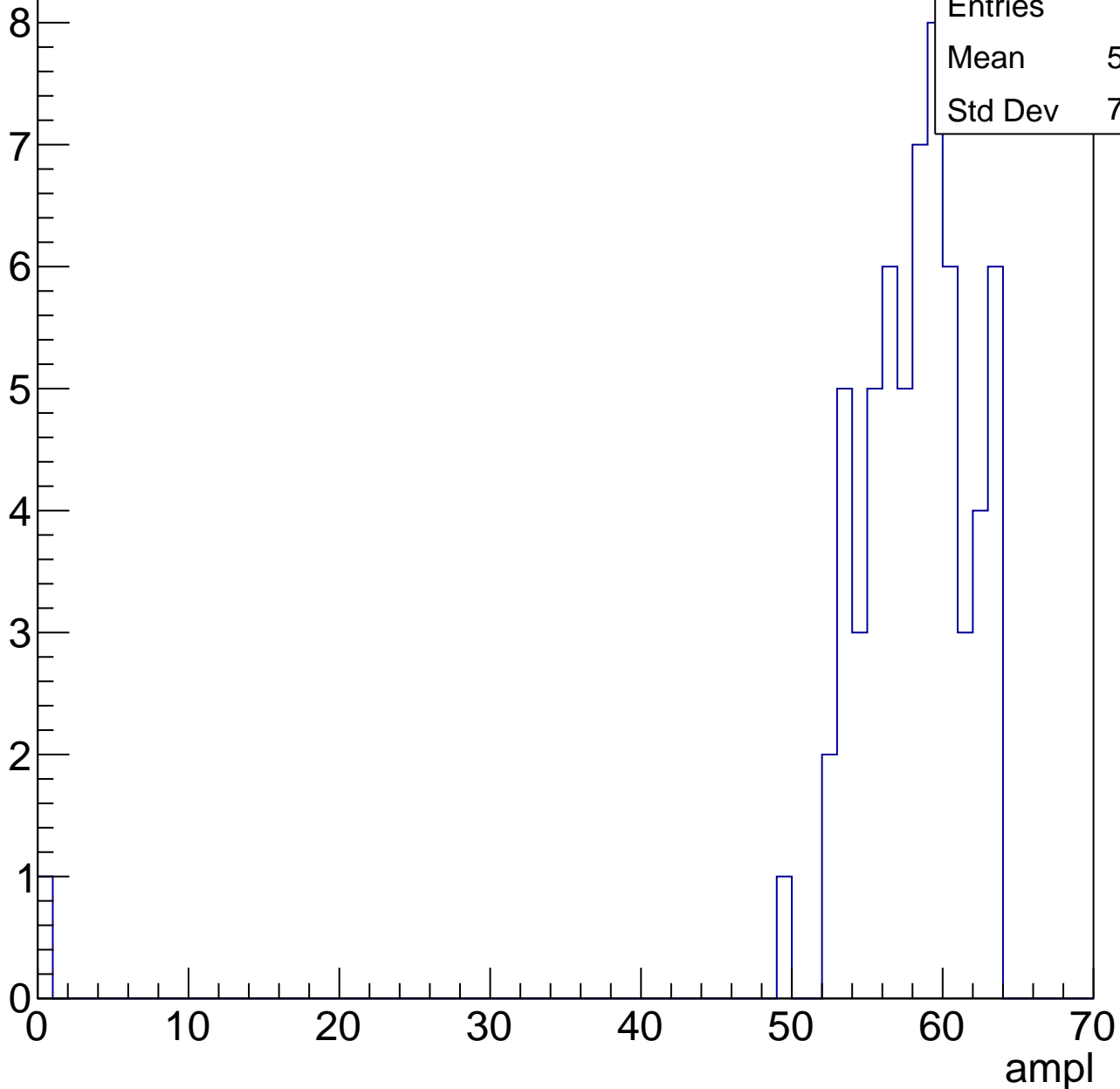


B1L103S, U24-ch68, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	56.82
Std Dev	7.983



B1L103S, U24-ch68, adc6

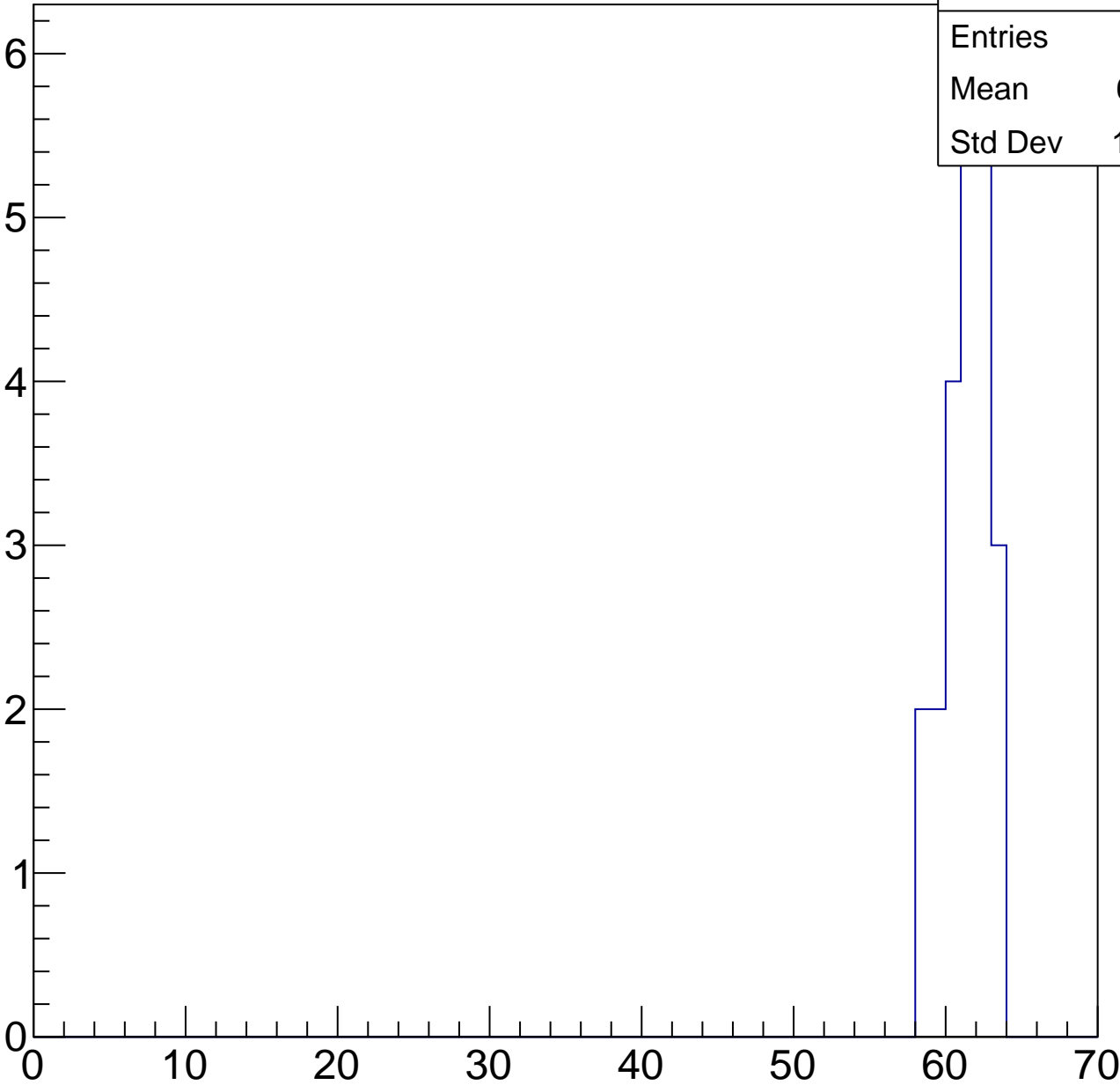
calib_packv5_041523_1651.root, FC#0, port C2

Entry

6
5
4
3
2
1
0

Entries	23
Mean	60.91
Std Dev	1.442

ampl



B1L103S, U24-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch69, adc0

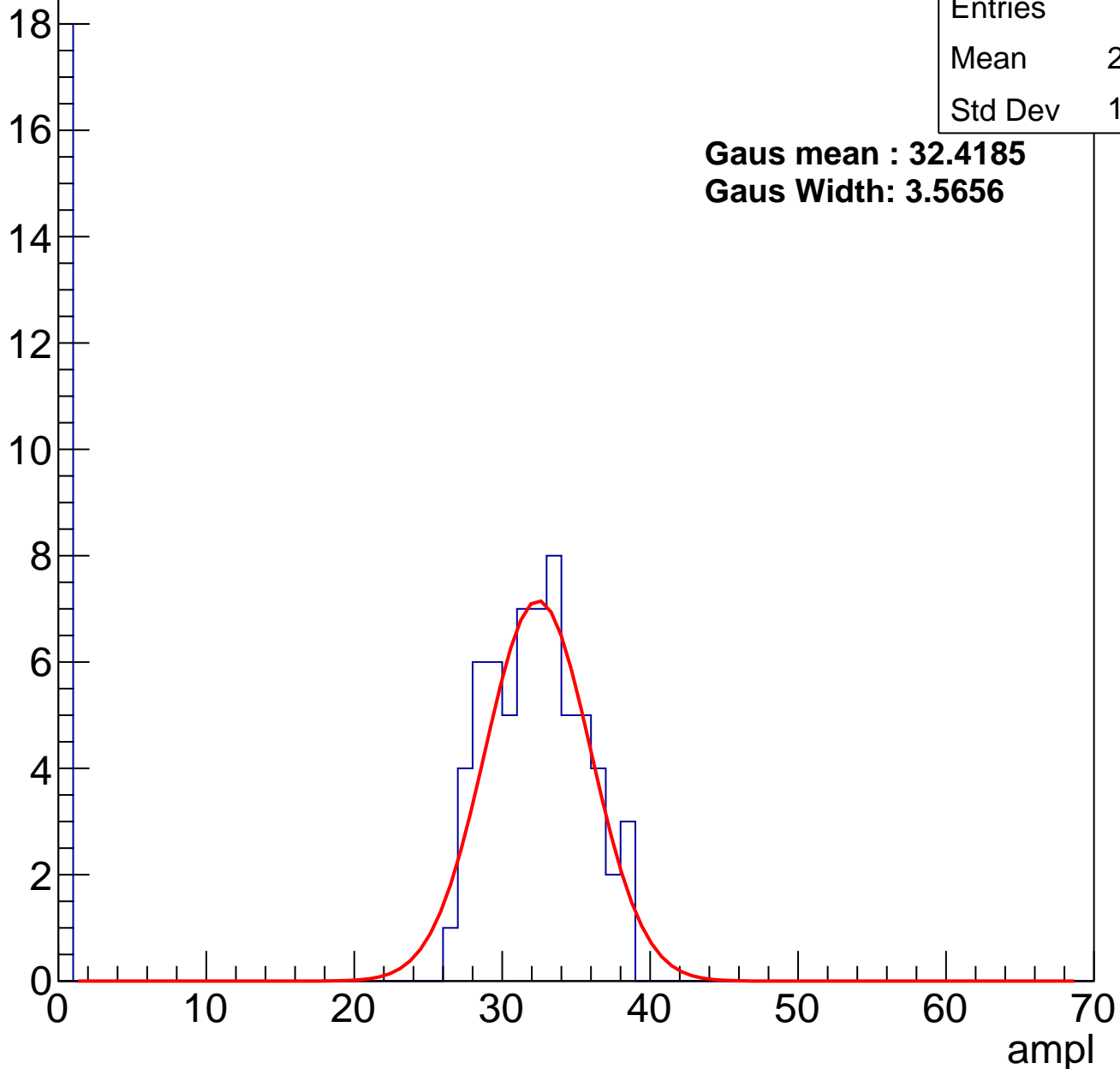
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	24.79
Std Dev	13.53

Gaus mean : 32.4185

Gaus Width: 3.5656

Entry



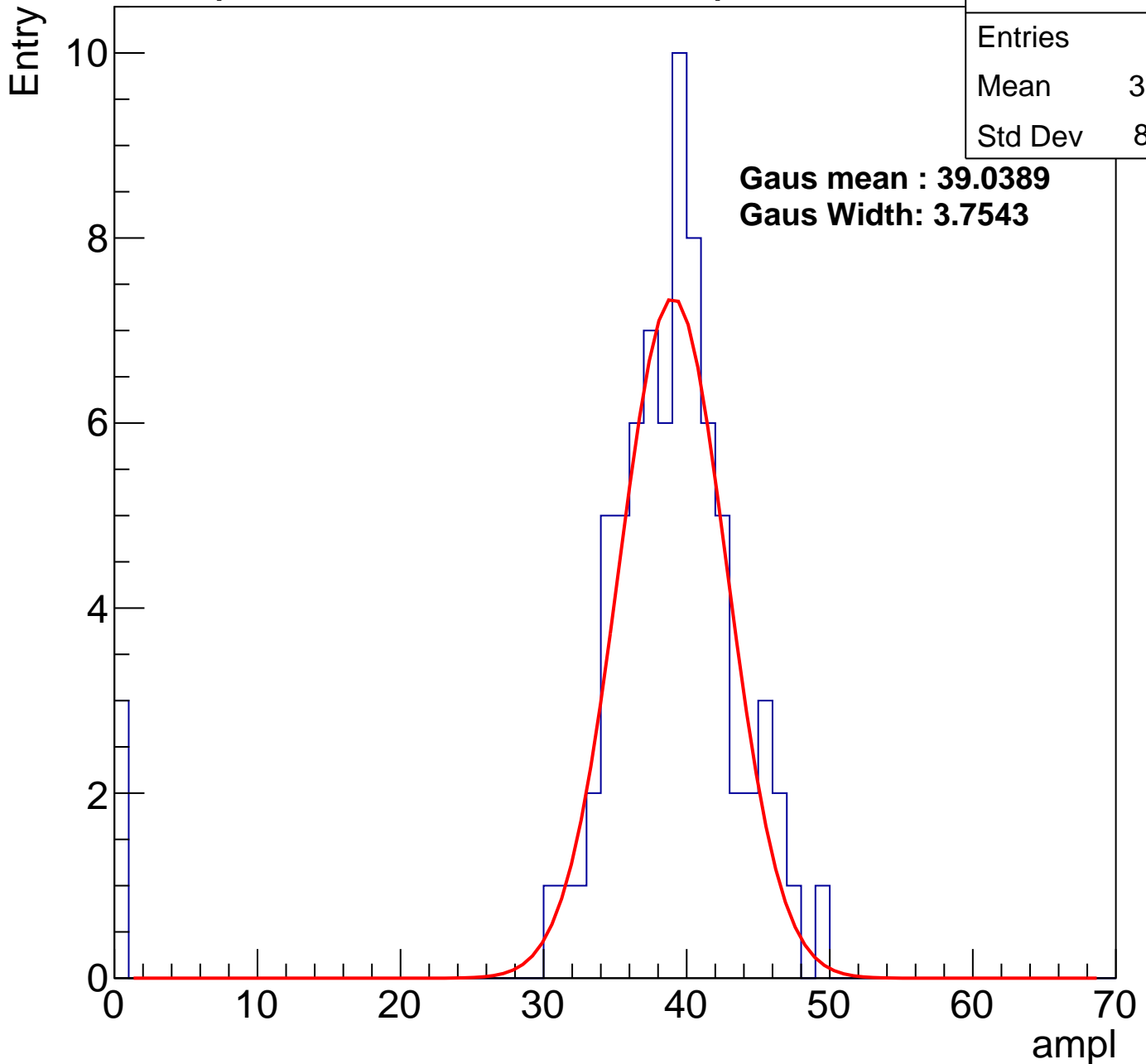
B1L103S, U24-ch69, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	37.27
Std Dev	8.391

Gaus mean : 39.0389

Gaus Width: 3.7543



B1L103S, U24-ch69, adc2

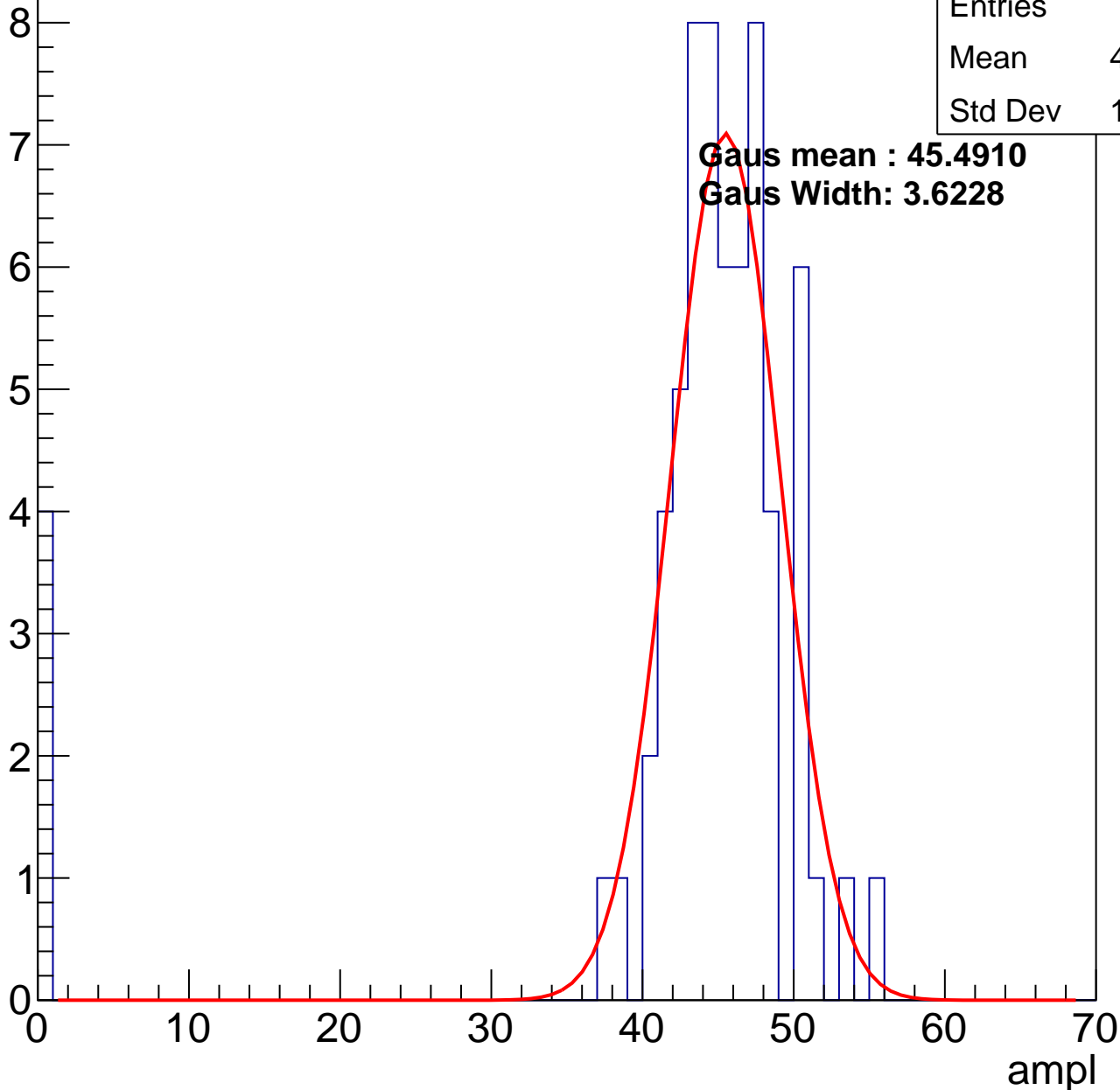
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	42.39
Std Dev	11.27

Gaus mean : 45.4910

Gaus Width: 3.6228

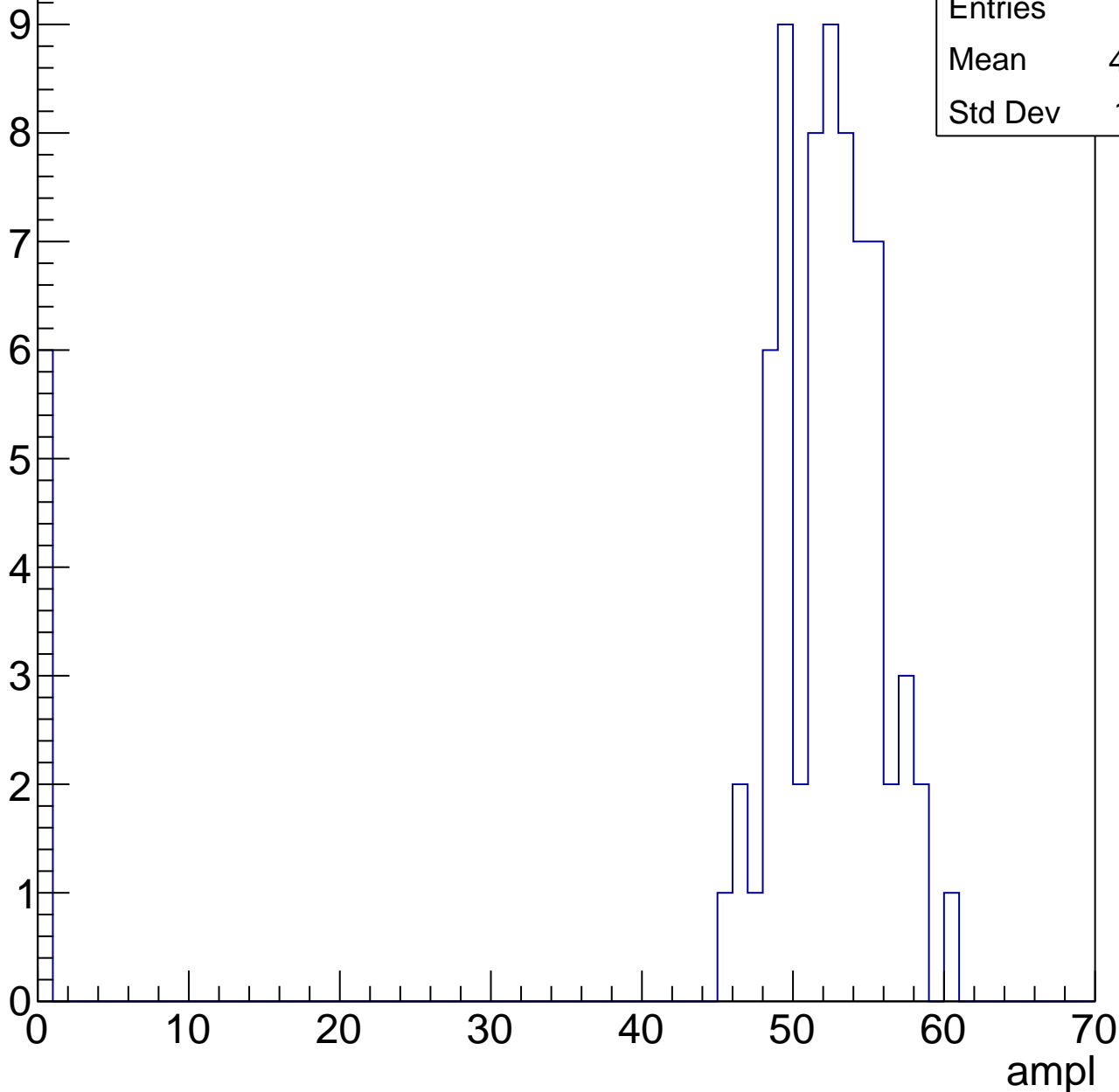


B1L103S, U24-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

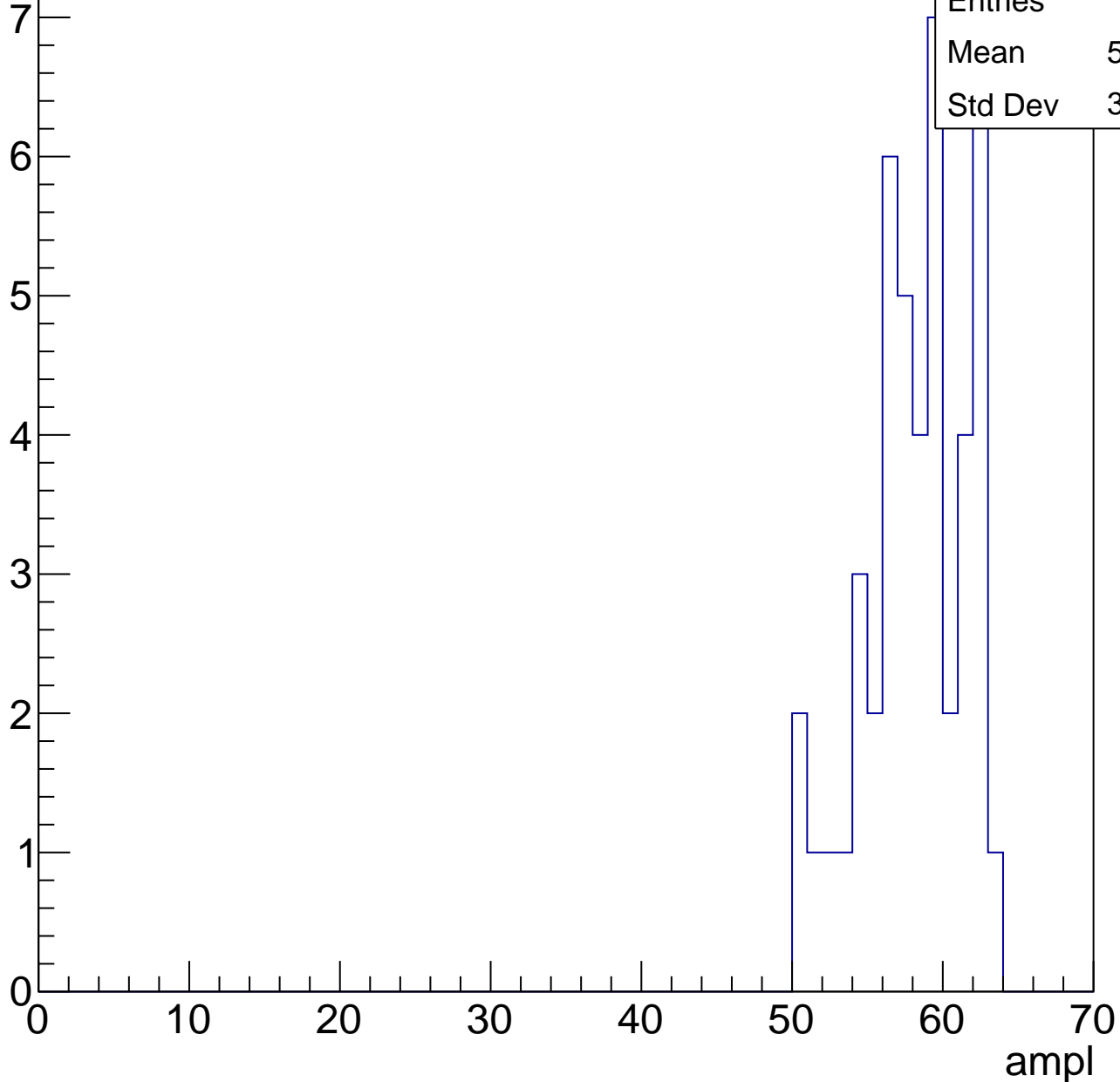
Entries	74
Mean	47.77
Std Dev	14.51



B1L103S, U24-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



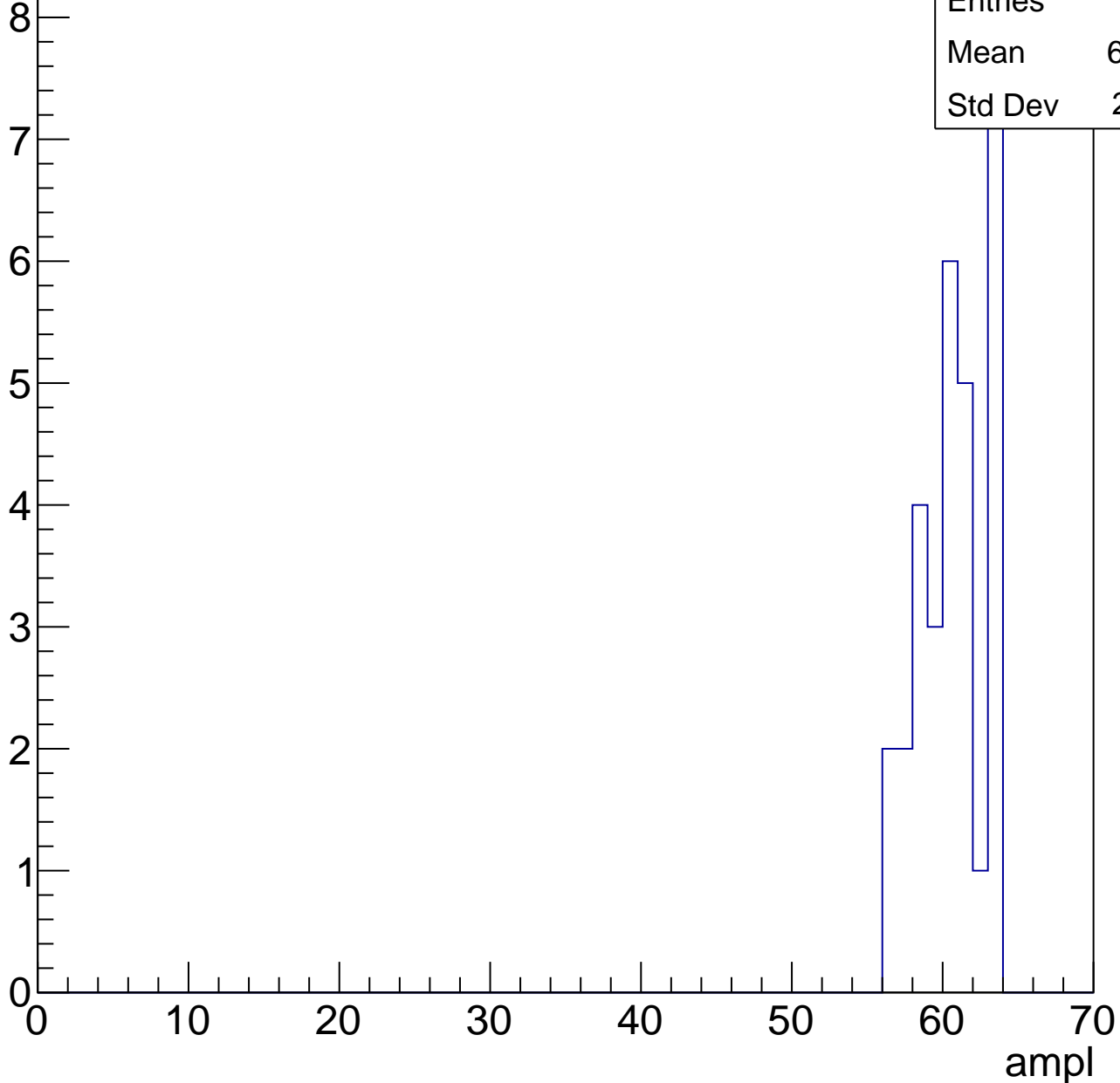
Entries	46
Mean	57.72
Std Dev	3.354

B1L103S, U24-ch69, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	31
Mean	60.19
Std Dev	2.191



B1L103S, U24-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	20
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch70, adc0

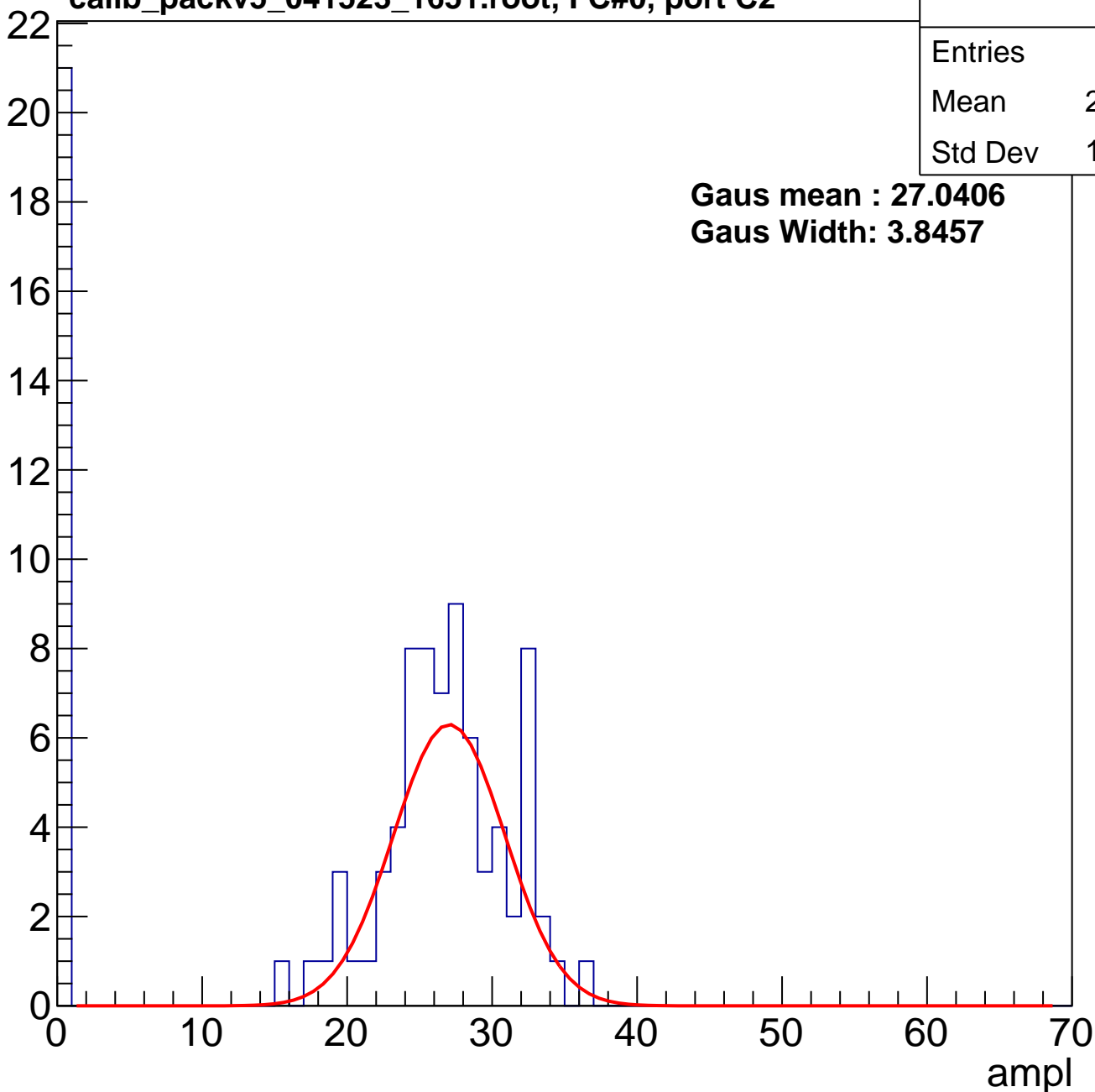
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	20.55
Std Dev	11.56

Gaus mean : 27.0406

Gaus Width: 3.8457

Entry



B1L103S, U24-ch70, adc1

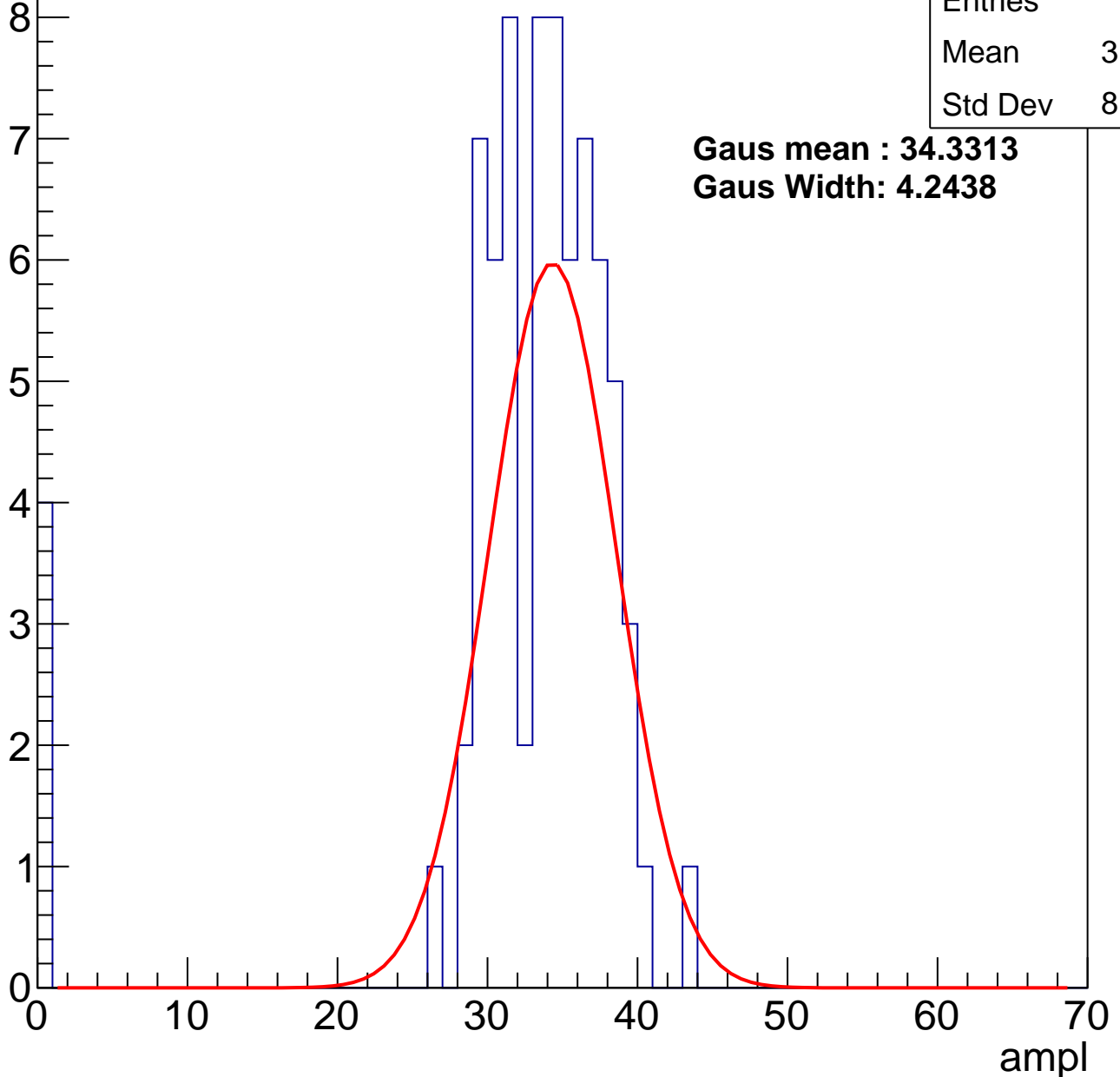
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	31.83
Std Dev	8.266

Gaus mean : 34.3313

Gaus Width: 4.2438



B1L103S, U24-ch70, adc2

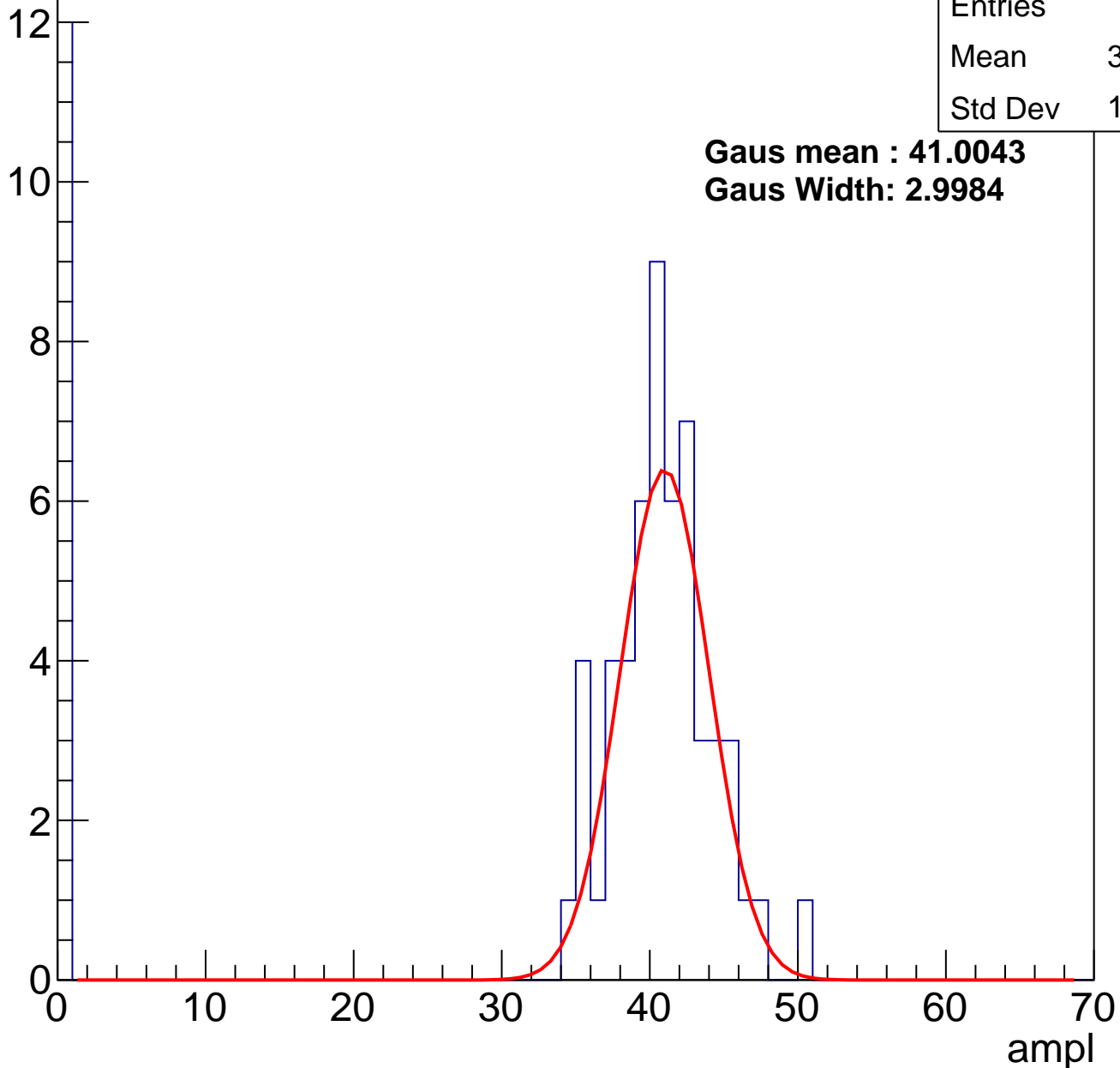
calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	33.08
Std Dev	15.87

Gaus mean : 41.0043

Gaus Width: 2.9984

Entry

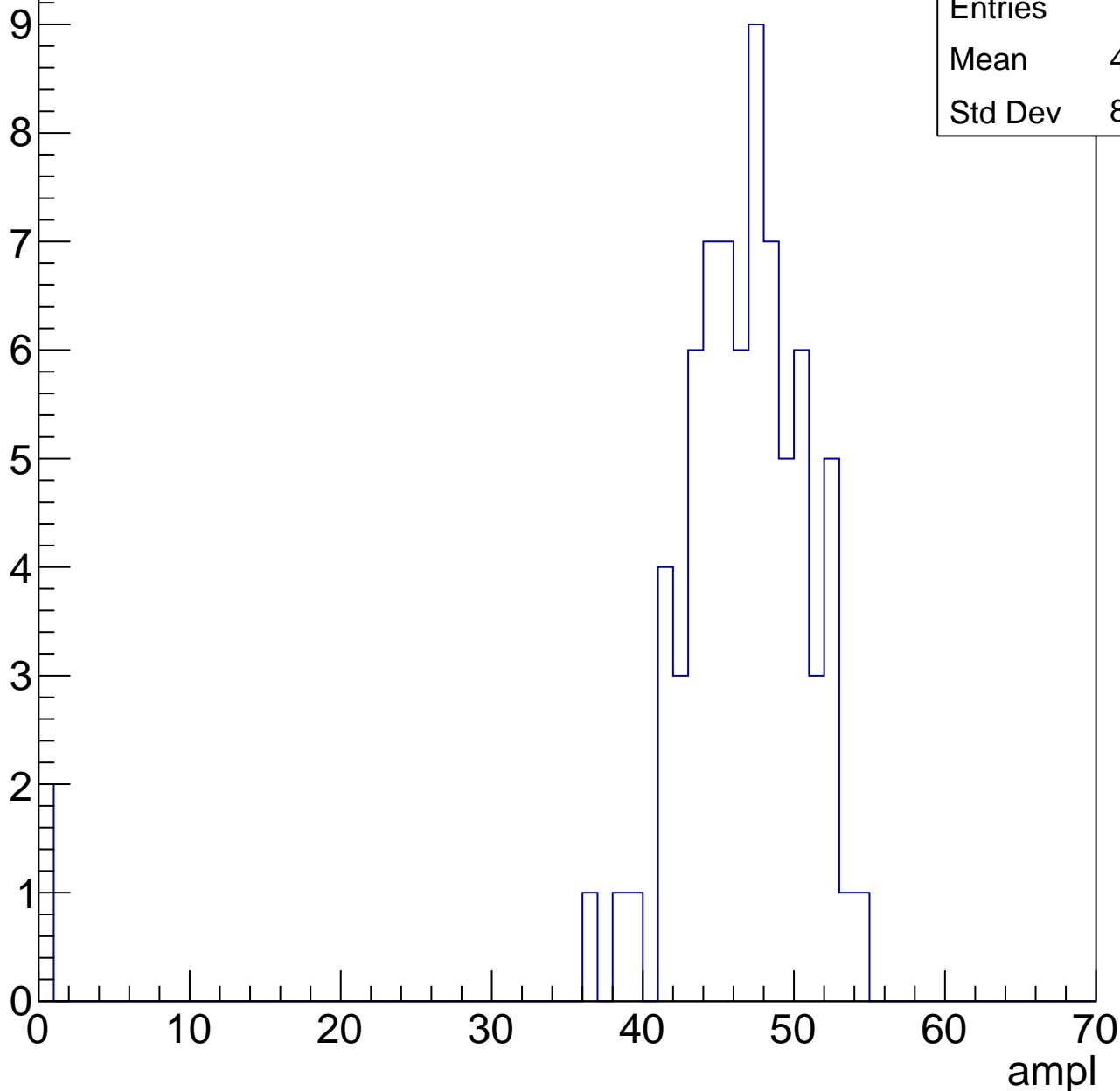


B1L103S, U24-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	45.12
Std Dev	8.303

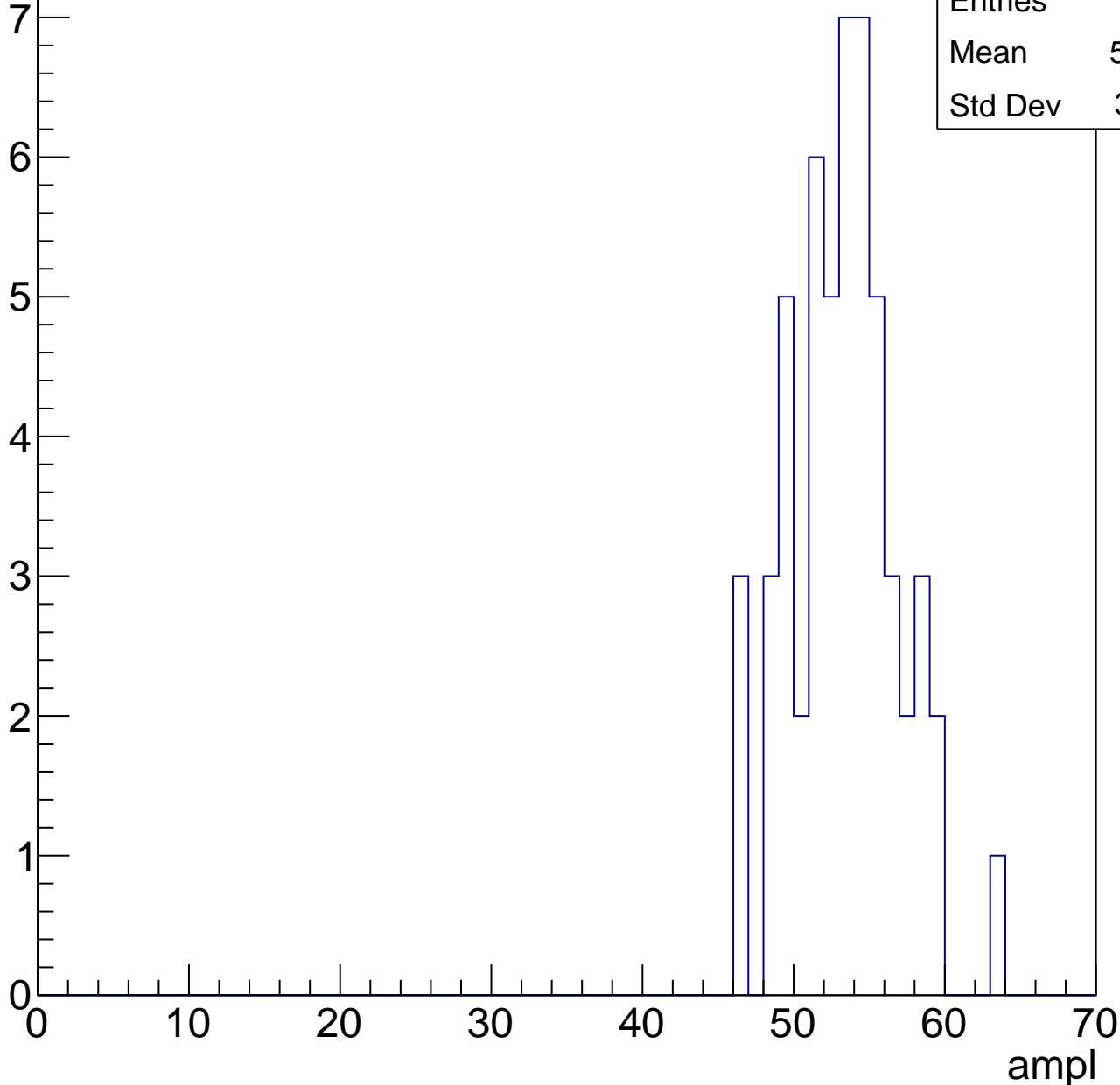


B1L103S, U24-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

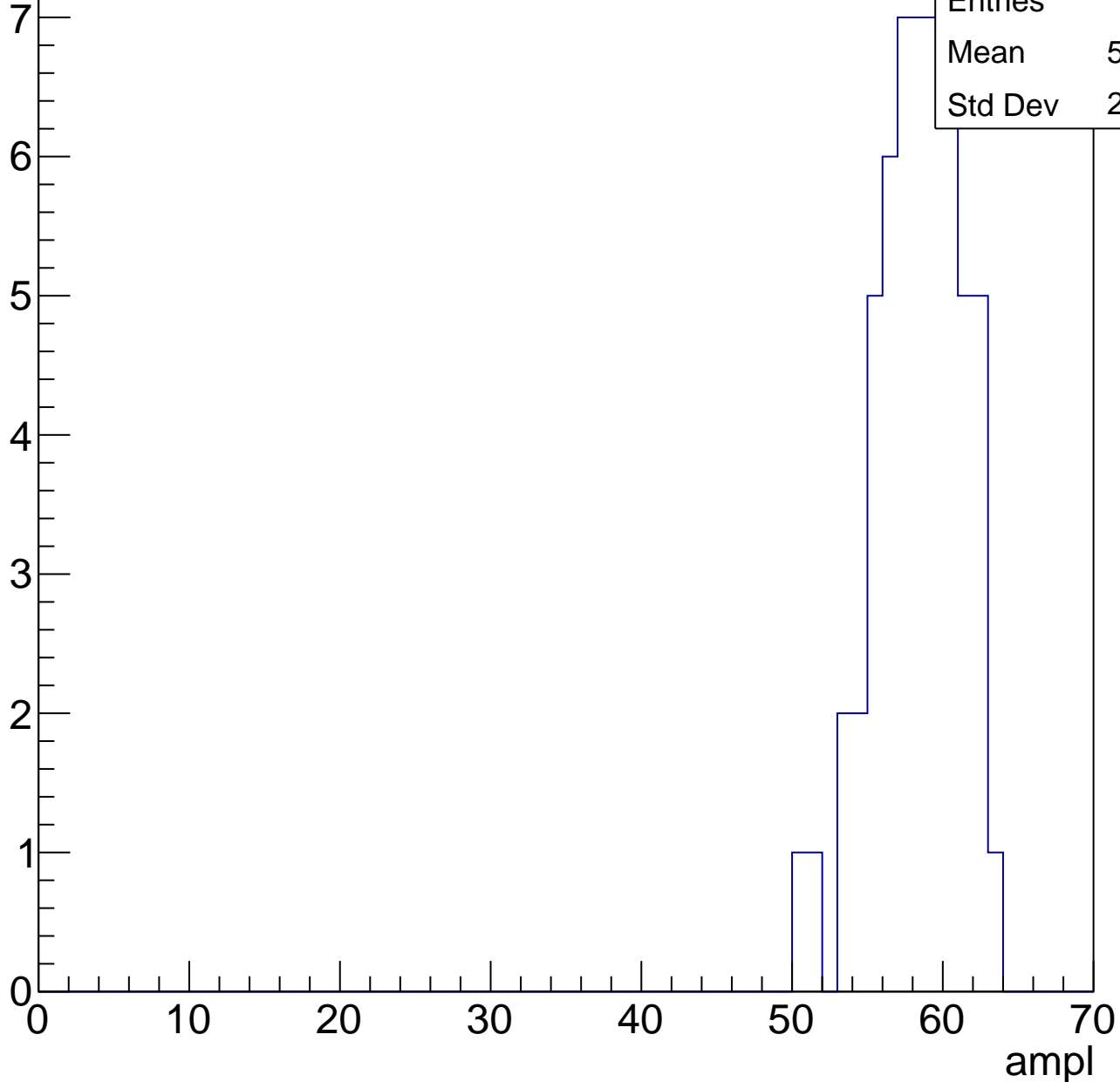
Entries	54
Mean	52.85
Std Dev	3.551



B1L103S, U24-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

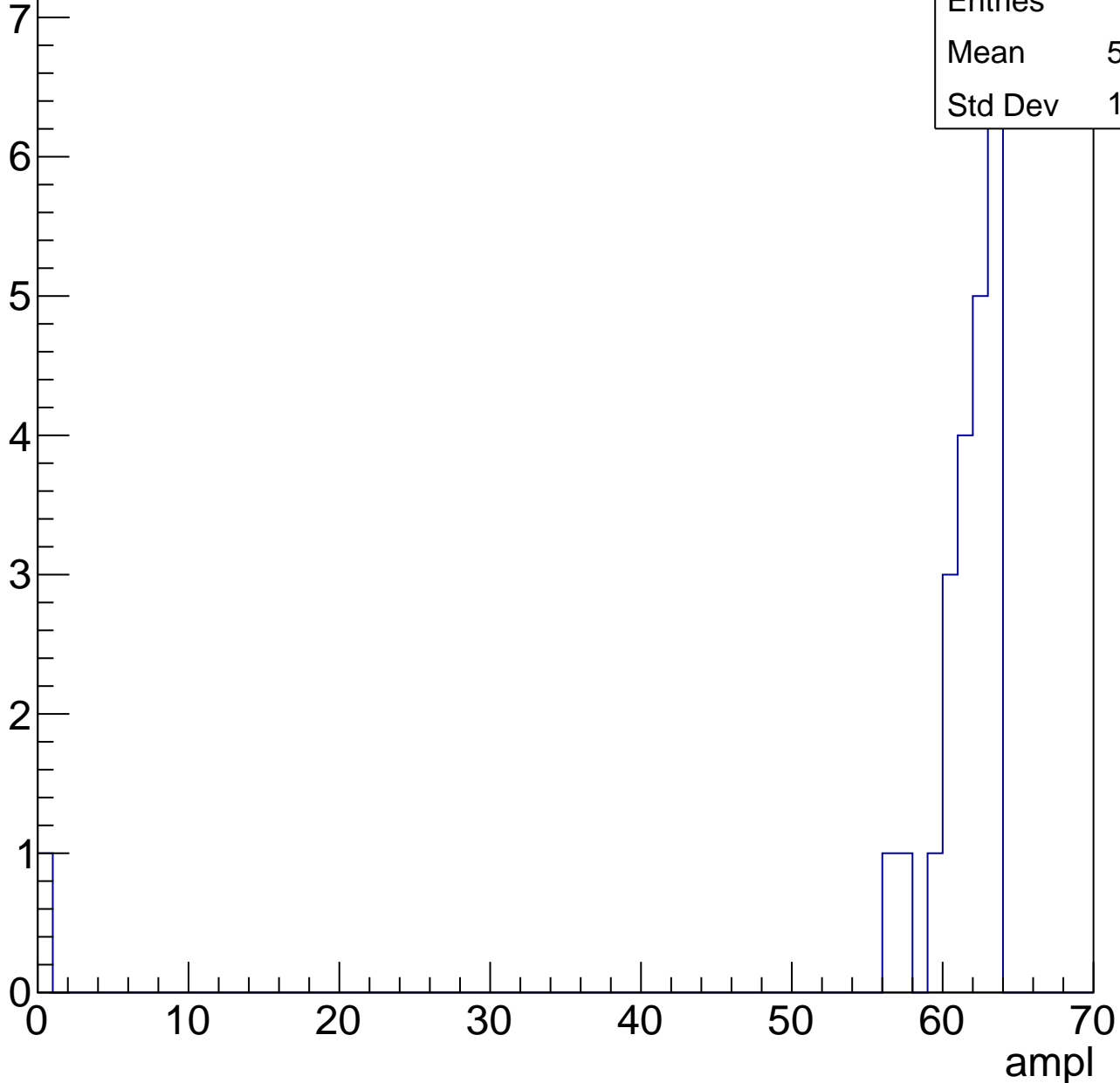


B1L103S, U24-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.57
Std Dev	12.62

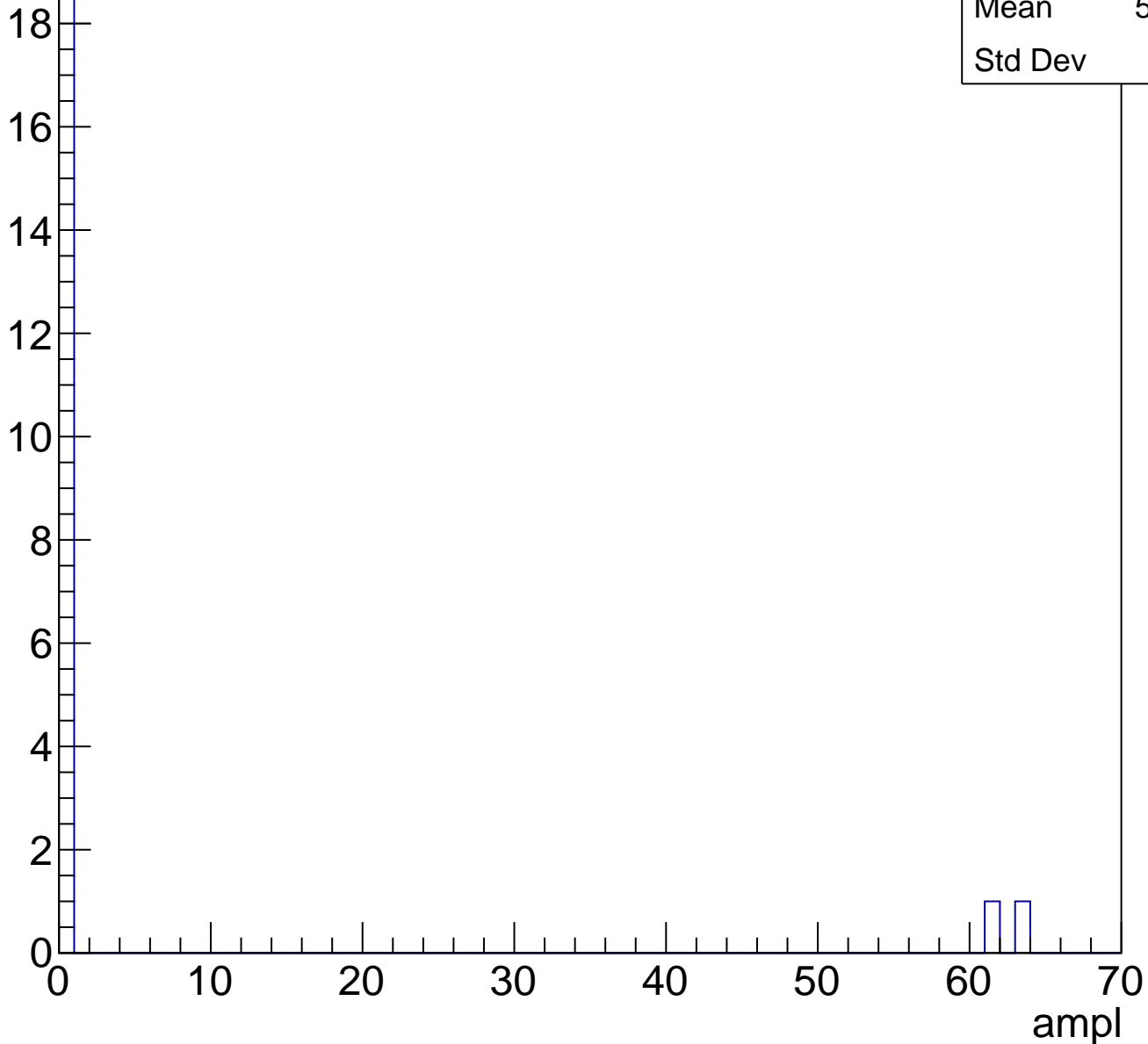


B1L103S, U24-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U24-ch71, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	23.43
Std Dev	10.6

Gaus mean : 28.0132

Gaus Width: 3.3063

Entry

12

10

8

6

4

2

0

0

10

20

30

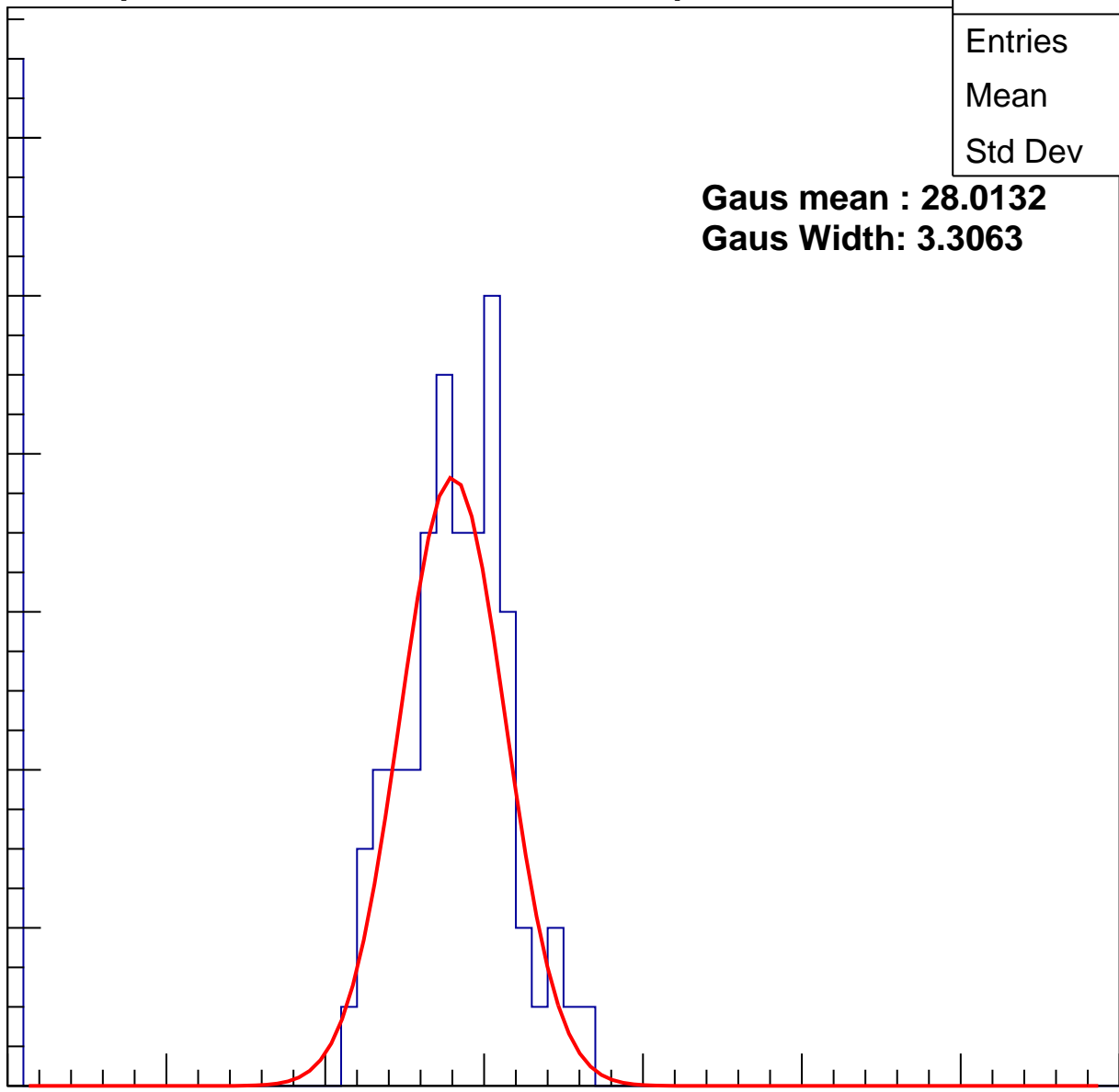
40

50

60

70

ampl



B1L103S, U24-ch71, adc1

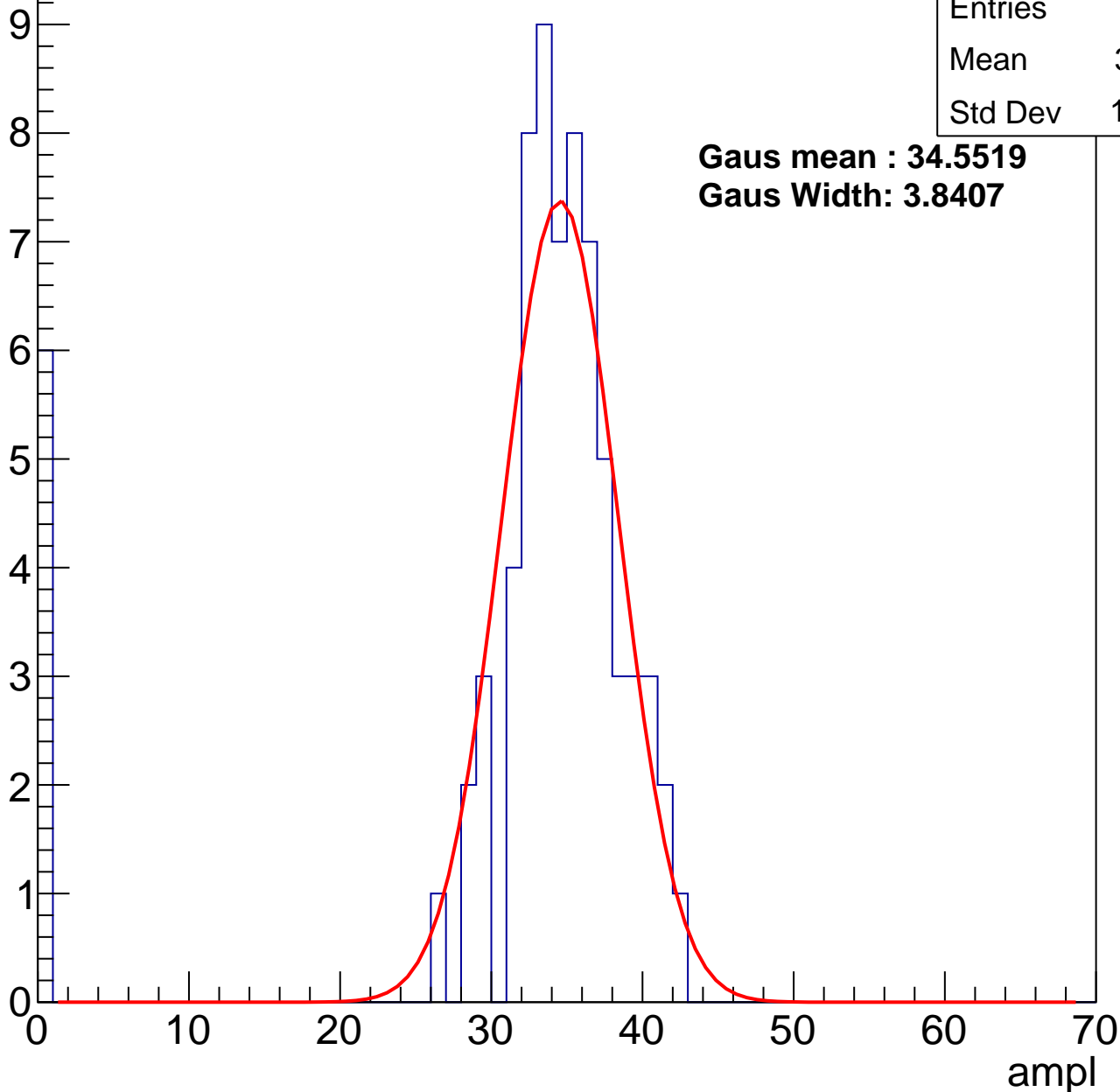
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	31.61
Std Dev	10.06

Gaus mean : 34.5519

Gaus Width: 3.8407



B1L103S, U24-ch71, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	34.32
Std Dev	14.94

Gaus mean : 40.8257

Gaus Width: 2.8401

Entry

10

8

6

4

2

0

0

10

20

30

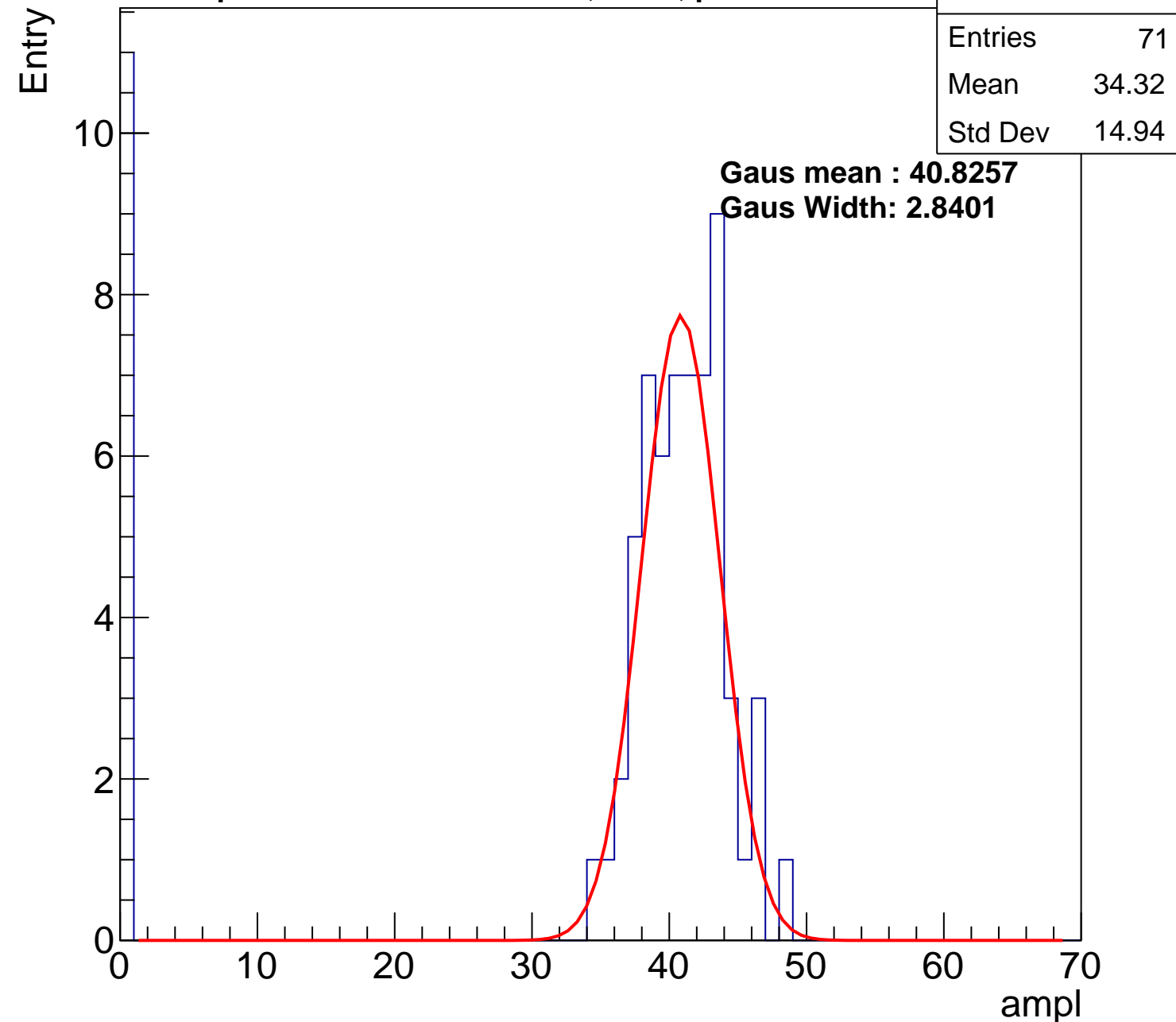
40

50

60

70

ampl



B1L103S, U24-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

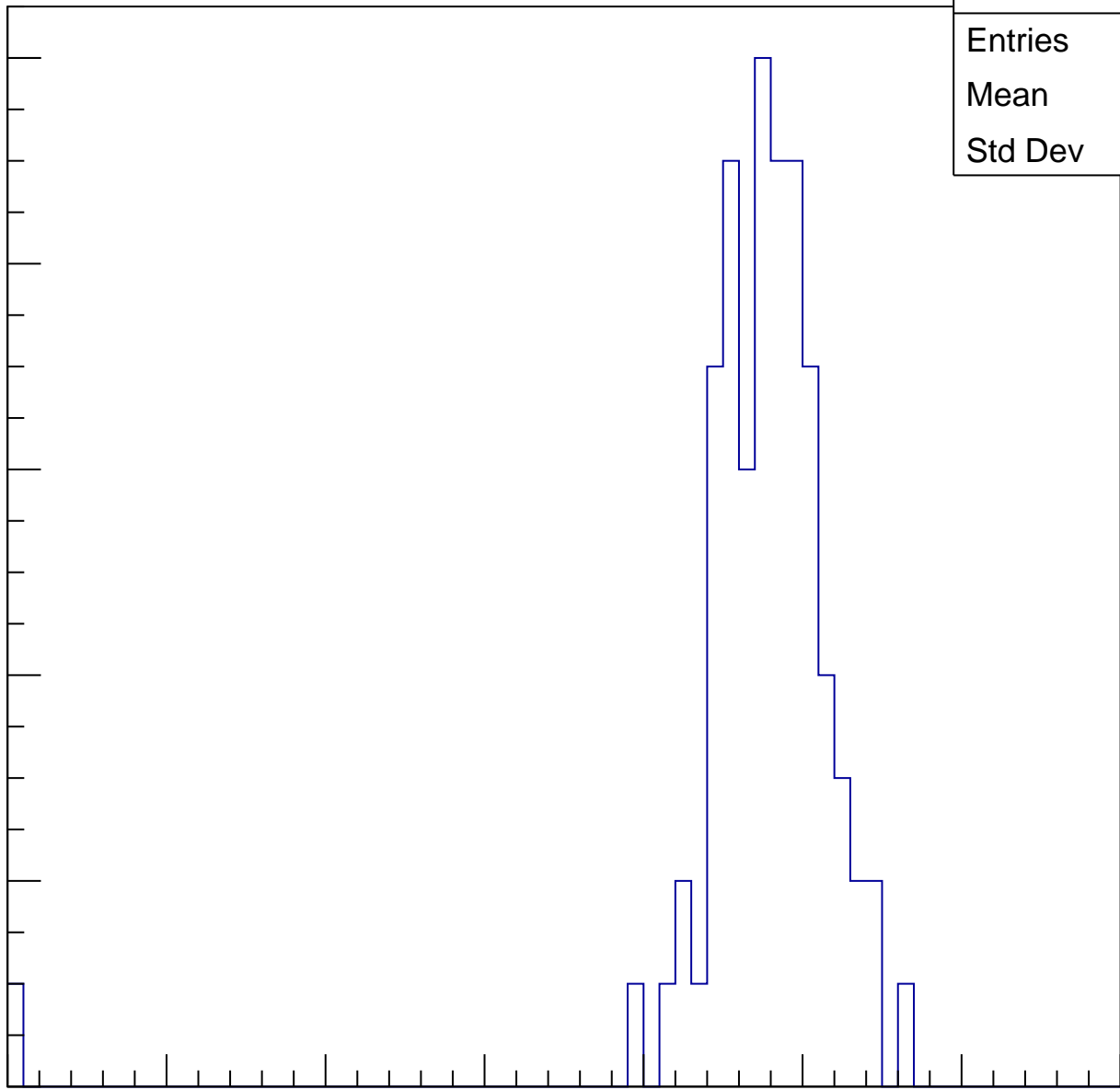
Entries	75
Mean	46.92
Std Dev	6.301

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

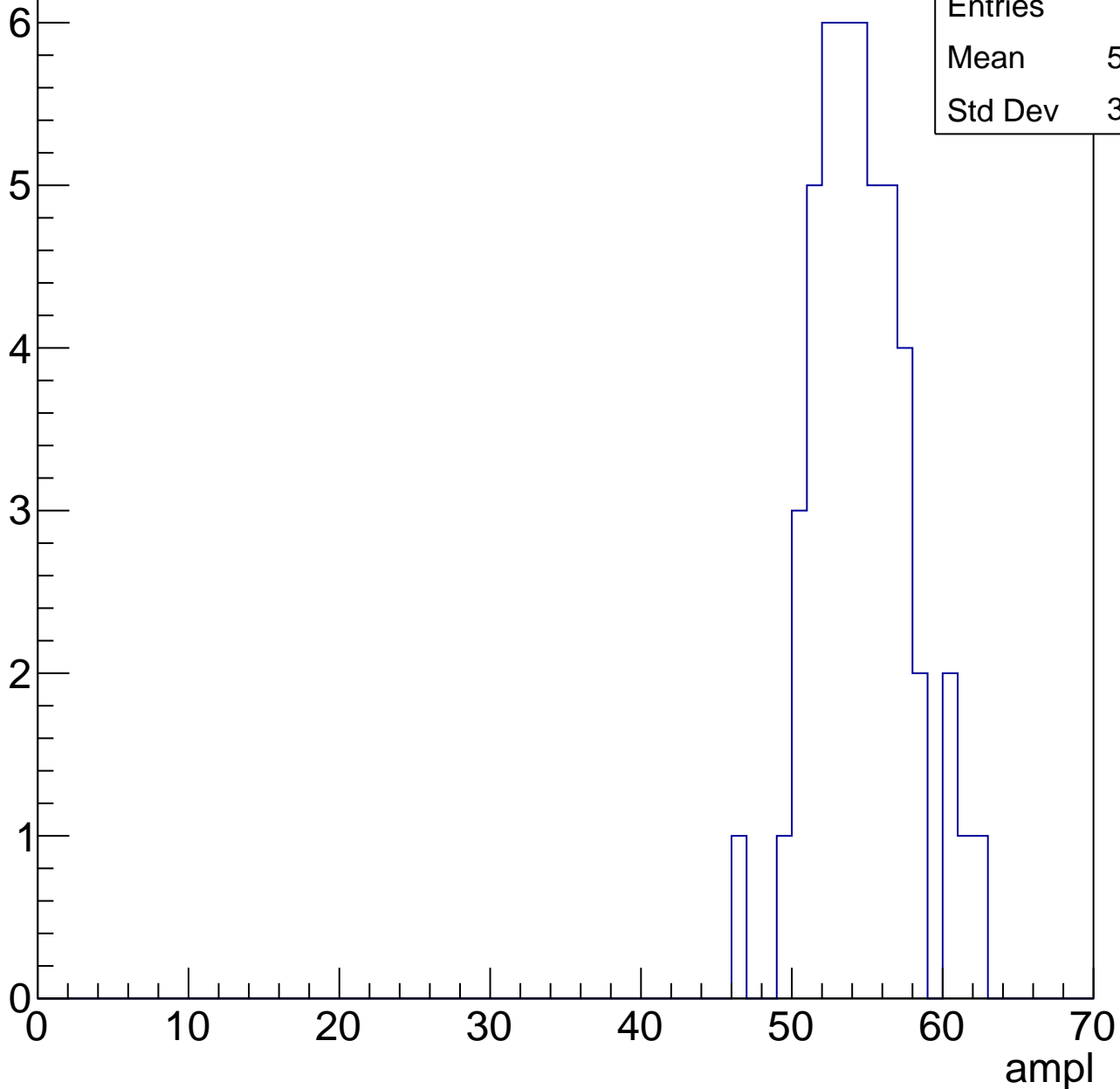


B1L103S, U24-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

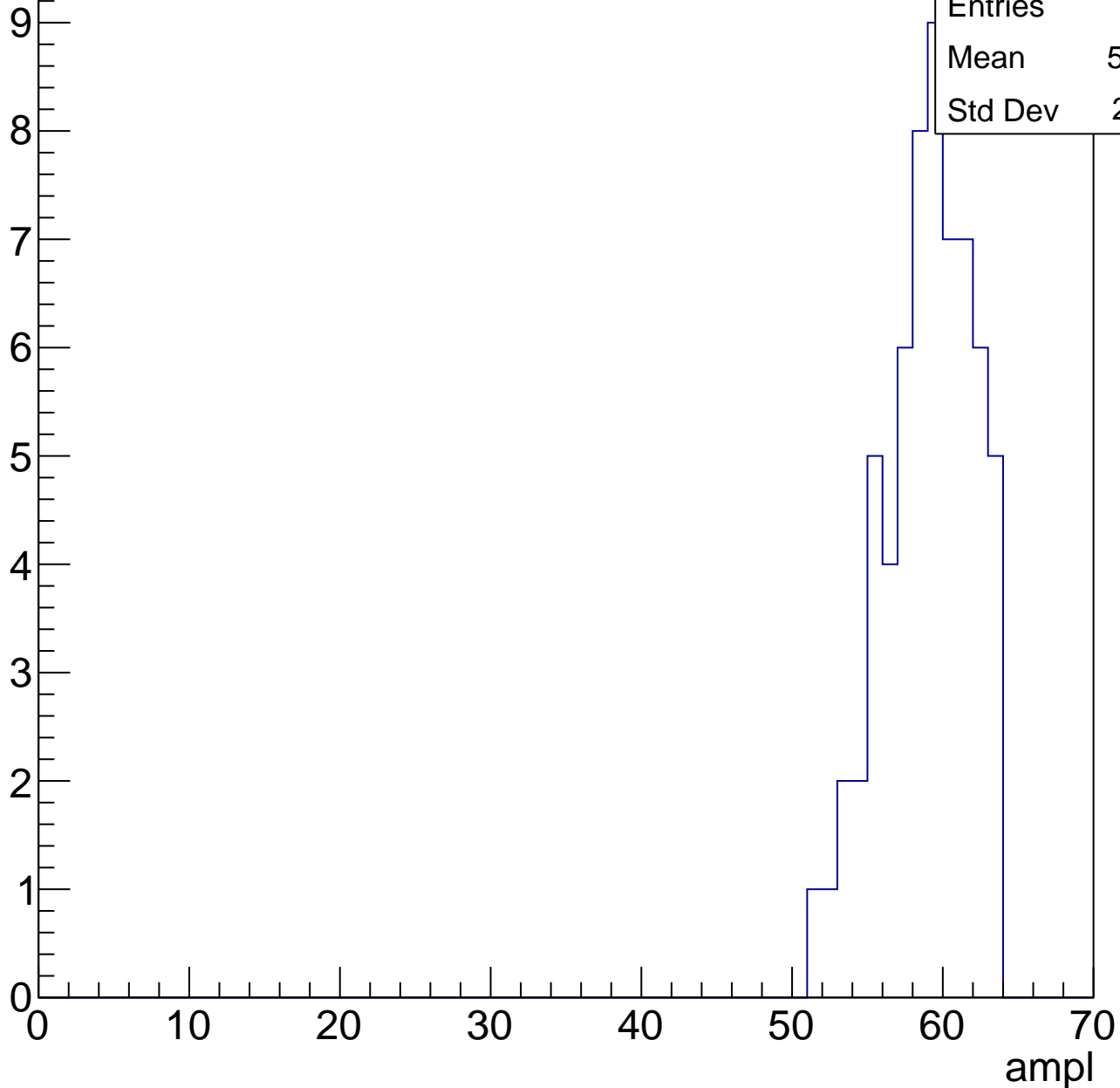
Entries	48
Mean	54.08
Std Dev	3.194



B1L103S, U24-ch71, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



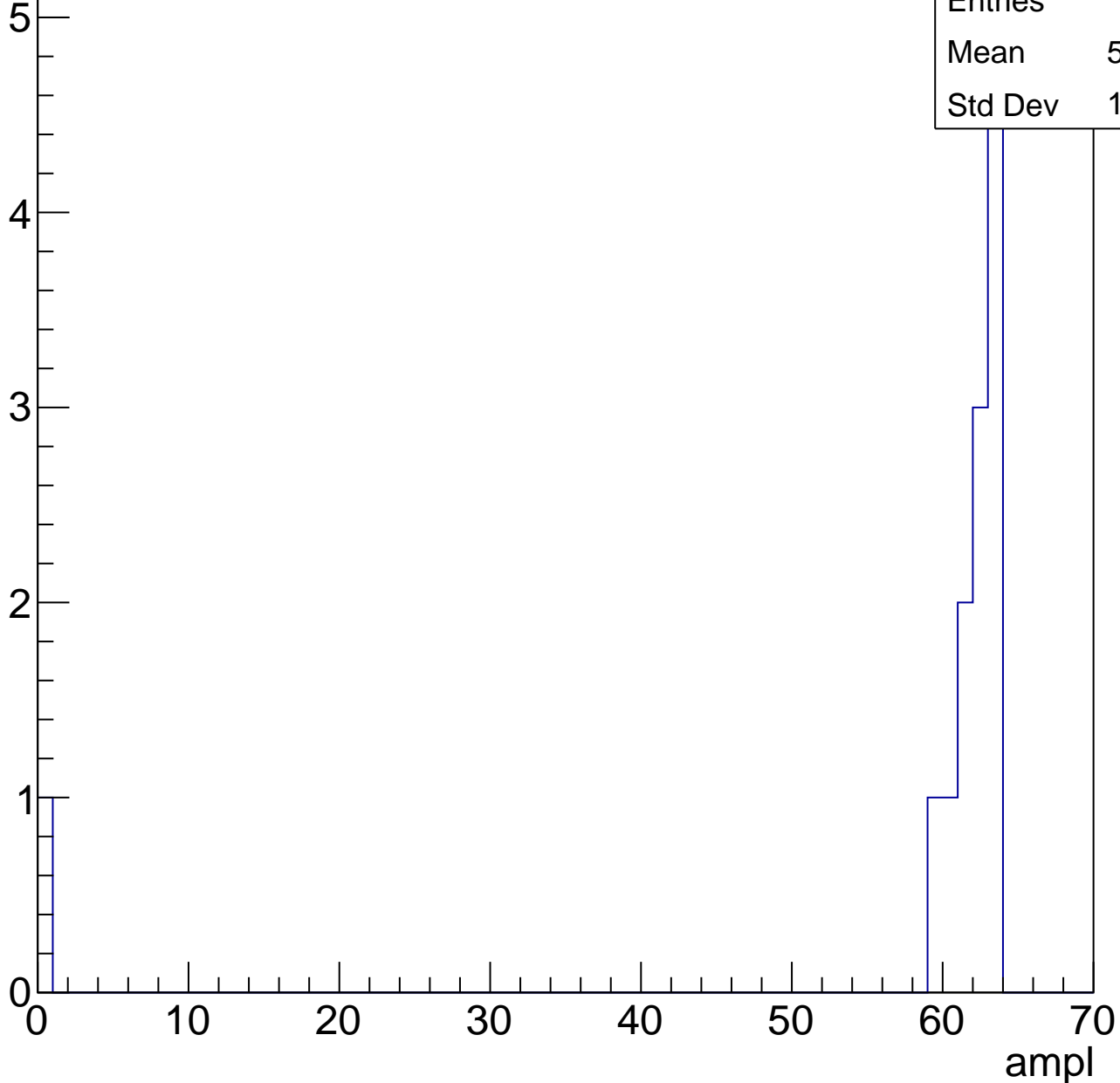
Entries	63
Mean	58.52
Std Dev	2.921

B1L103S, U24-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.08
Std Dev	16.52



B1L103S, U24-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch72, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	25.7
Std Dev	9.688

Gaus mean : 28.3983

Gaus Width: 3.7993

Entry

10

8

6

4

2

0

0

10

20

30

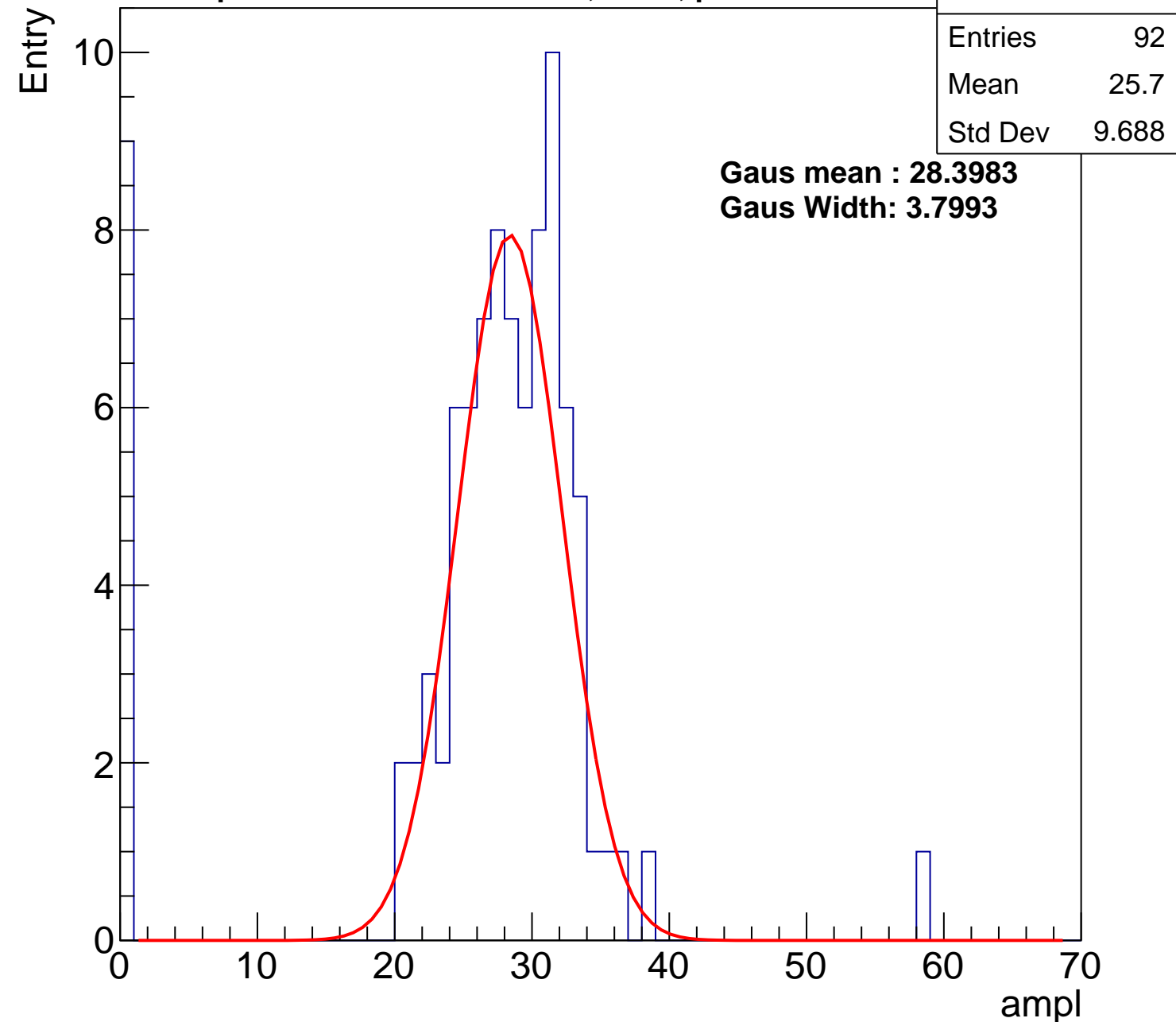
40

50

60

70

ampl



B1L103S, U24-ch72, adc1

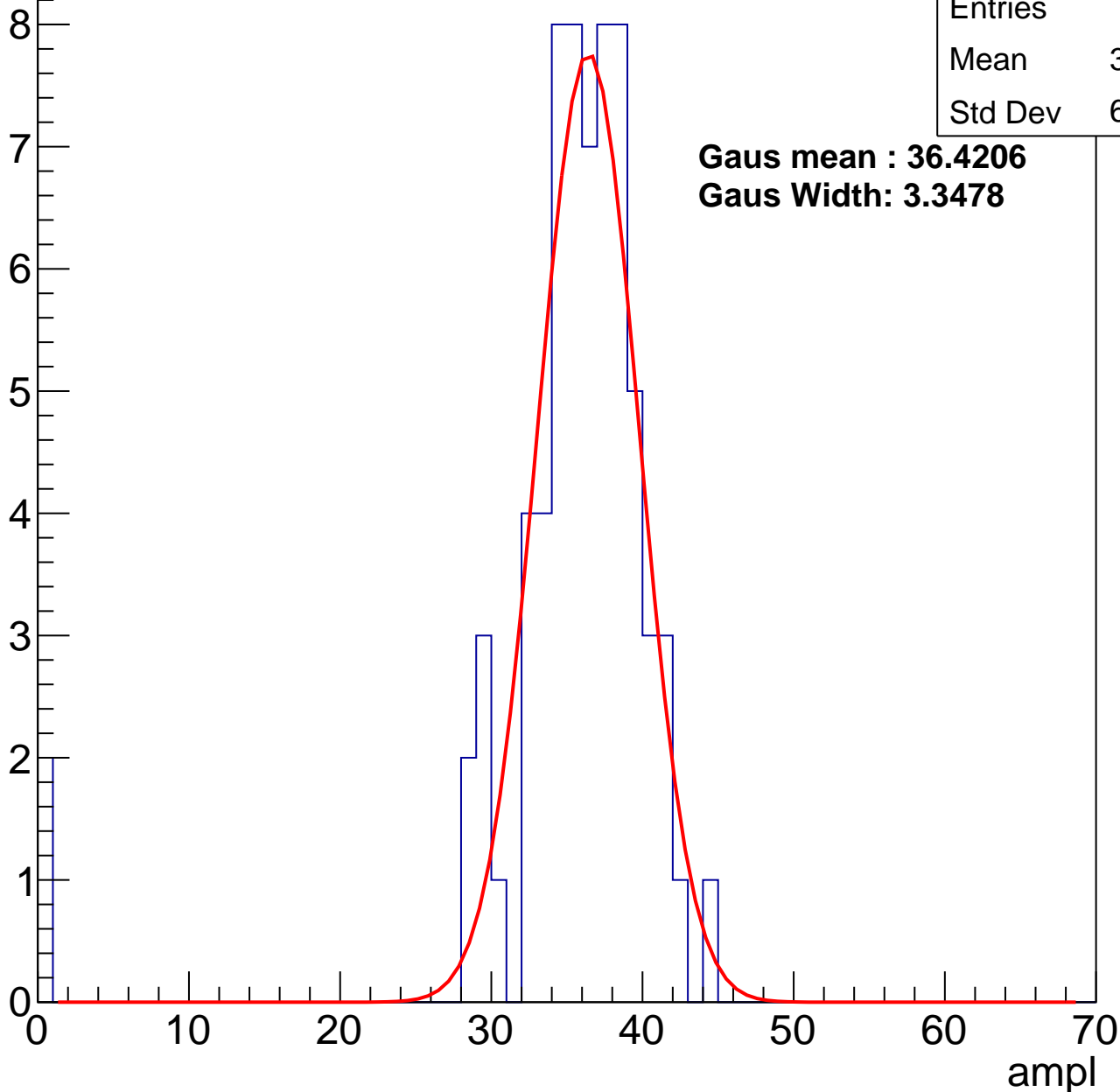
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34.72
Std Dev	6.902

Gaus mean : 36.4206

Gaus Width: 3.3478



B1L103S, U24-ch72, adc2

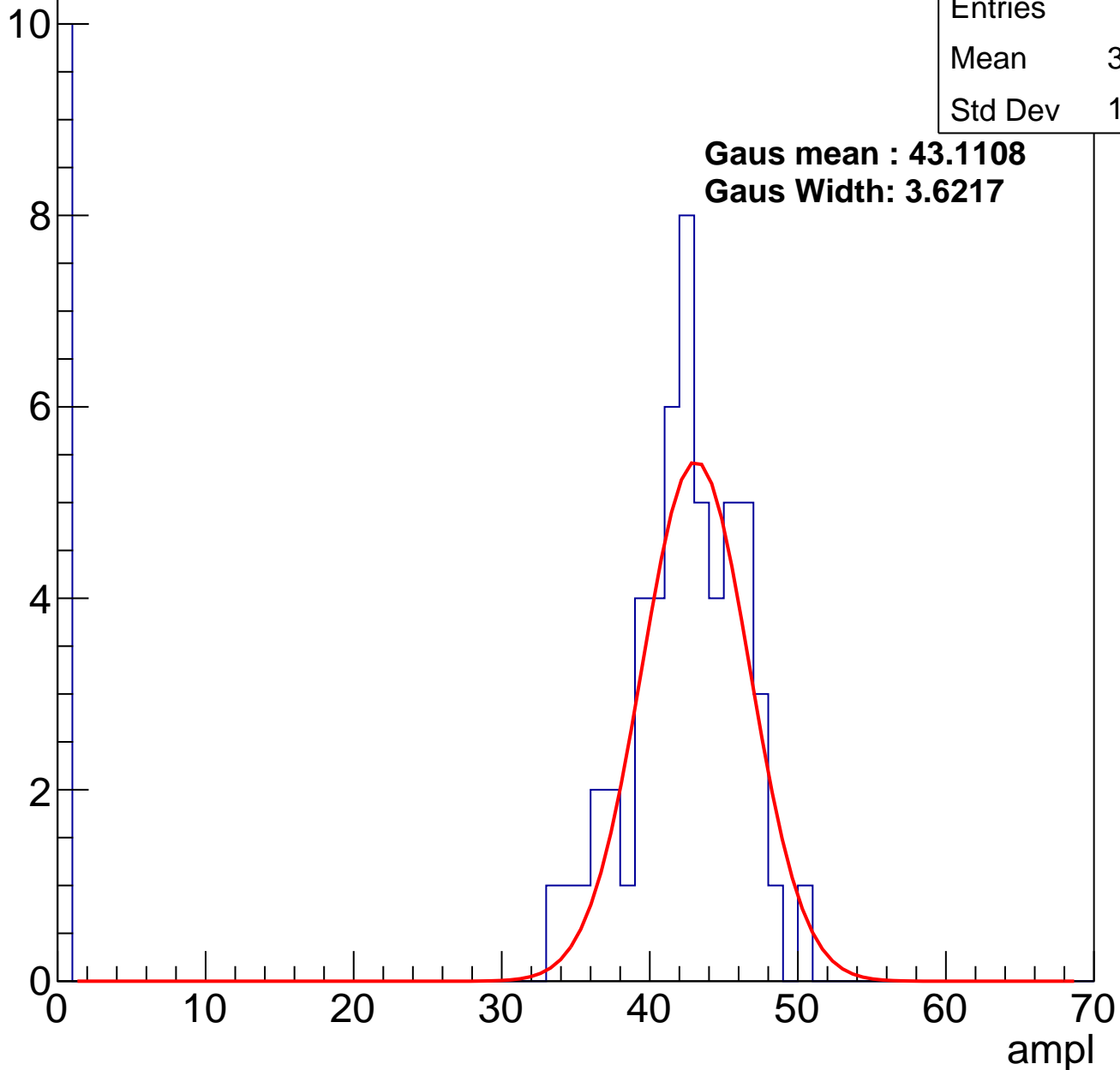
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	35.45
Std Dev	15.62

Gaus mean : 43.1108

Gaus Width: 3.6217

Entry

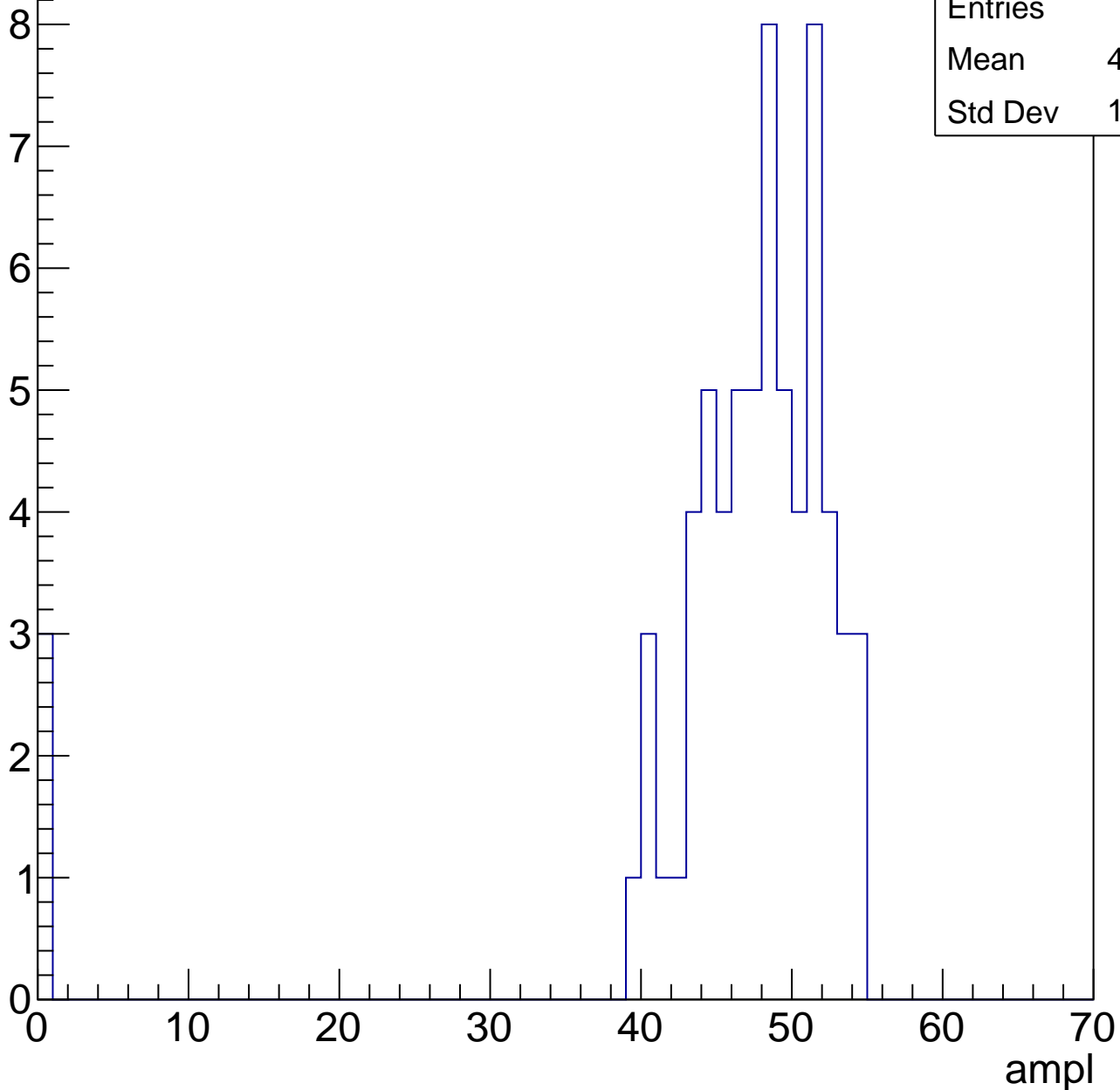


B1L103S, U24-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	45.45
Std Dev	10.52

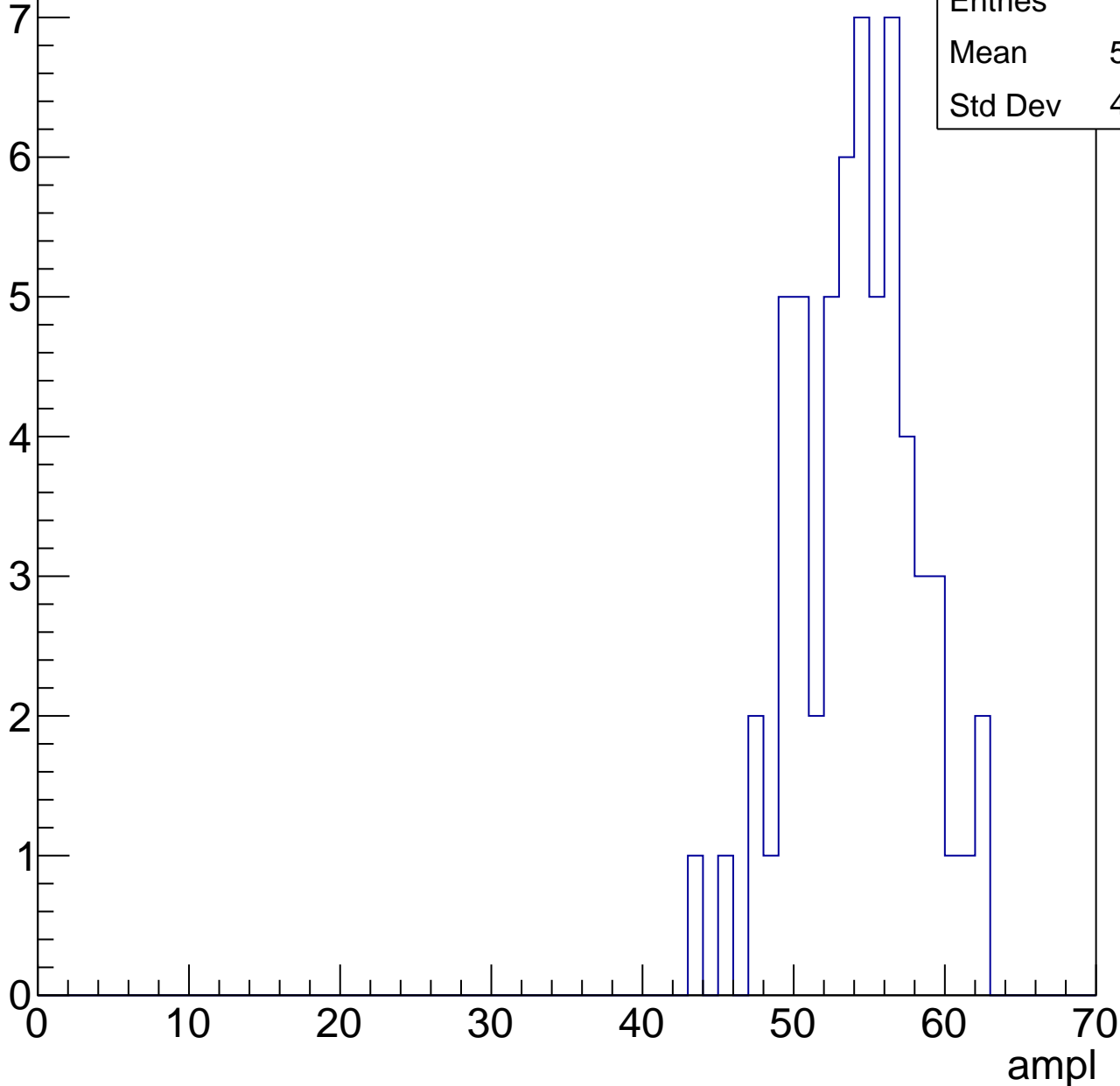


B1L103S, U24-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	53.67
Std Dev	4.015

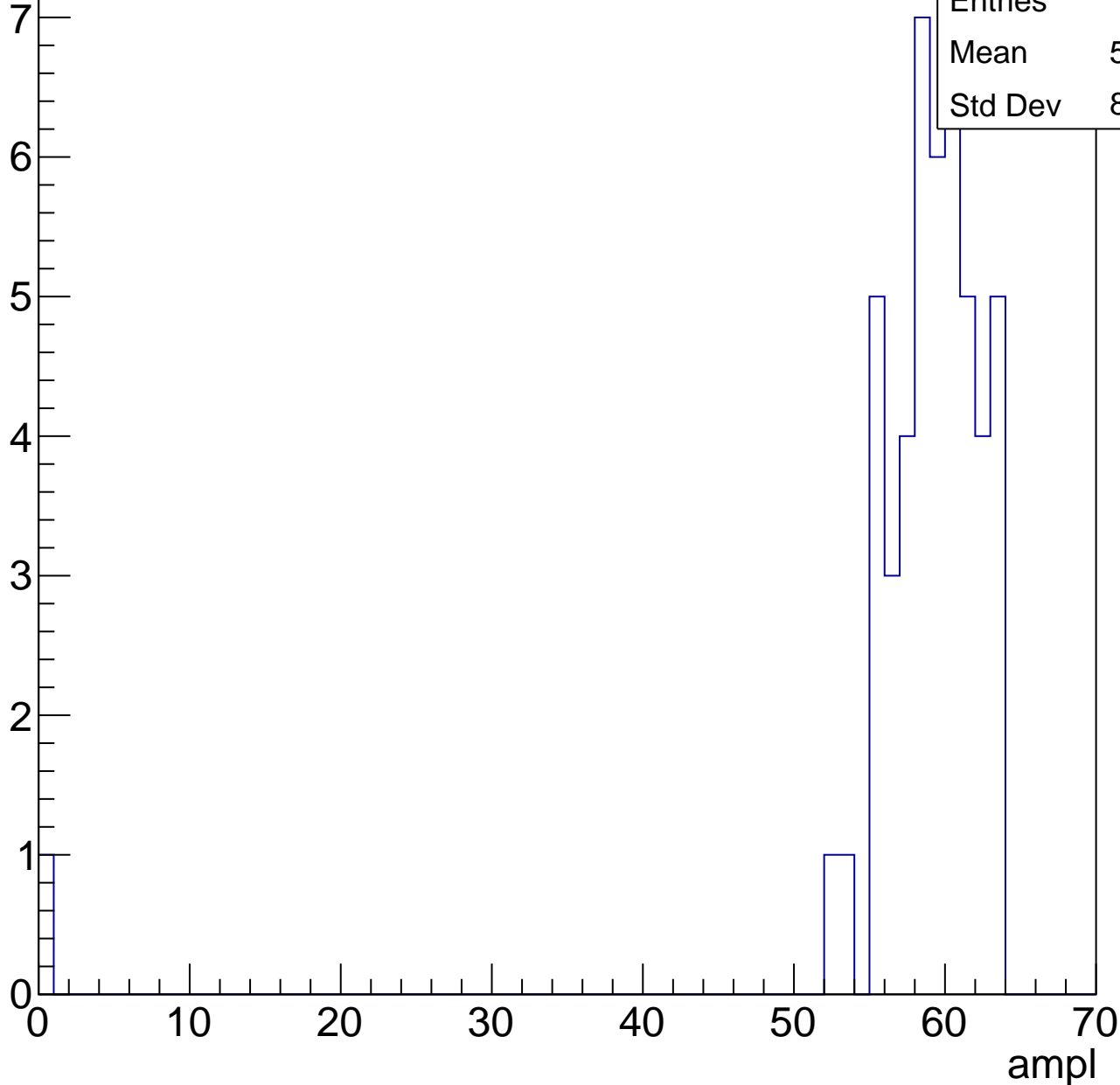


B1L103S, U24-ch72, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	57.63
Std Dev	8.745

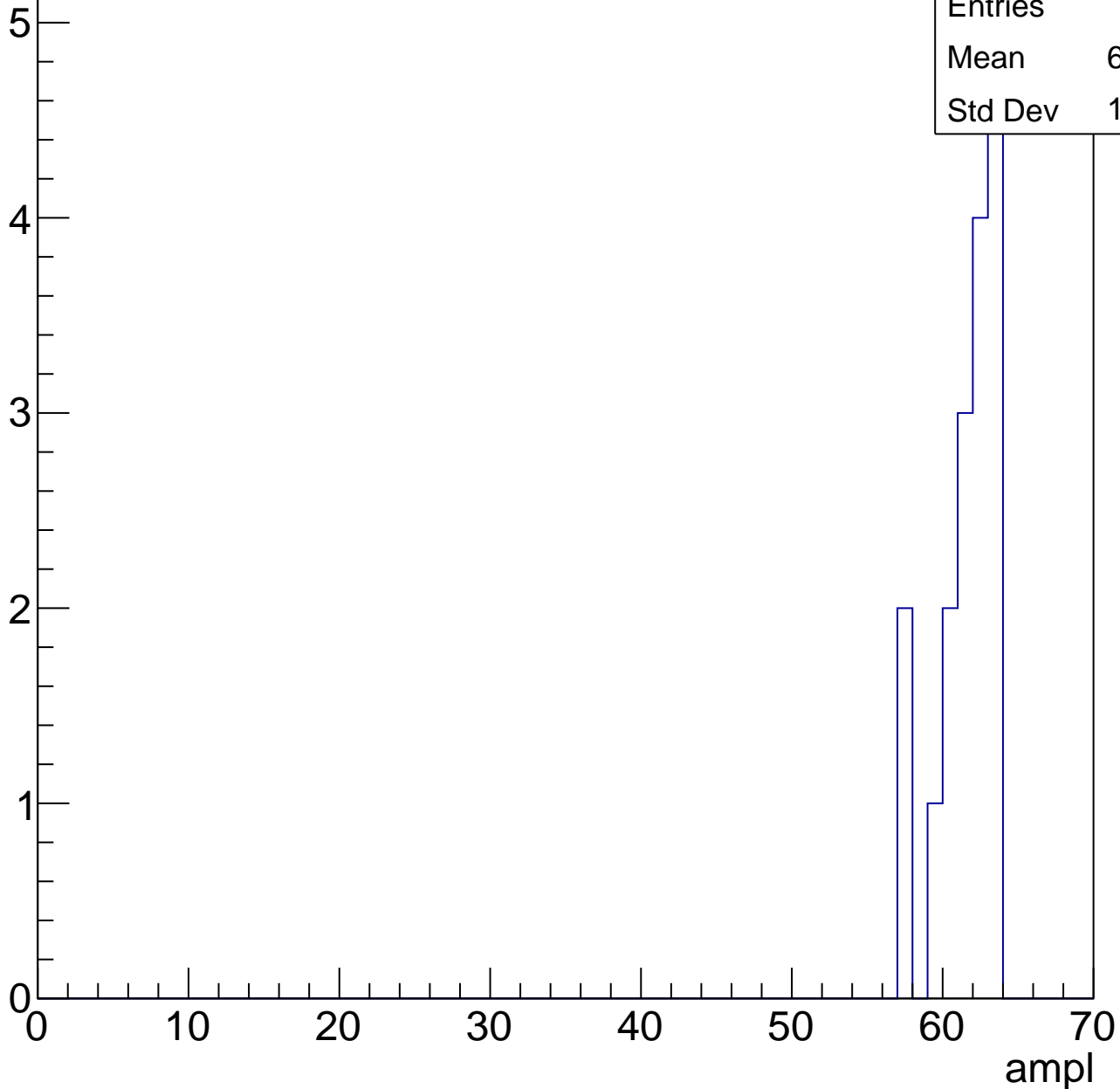


B1L103S, U24-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.12
Std Dev	1.906



B1L103S, U24-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U24-ch73, adc0

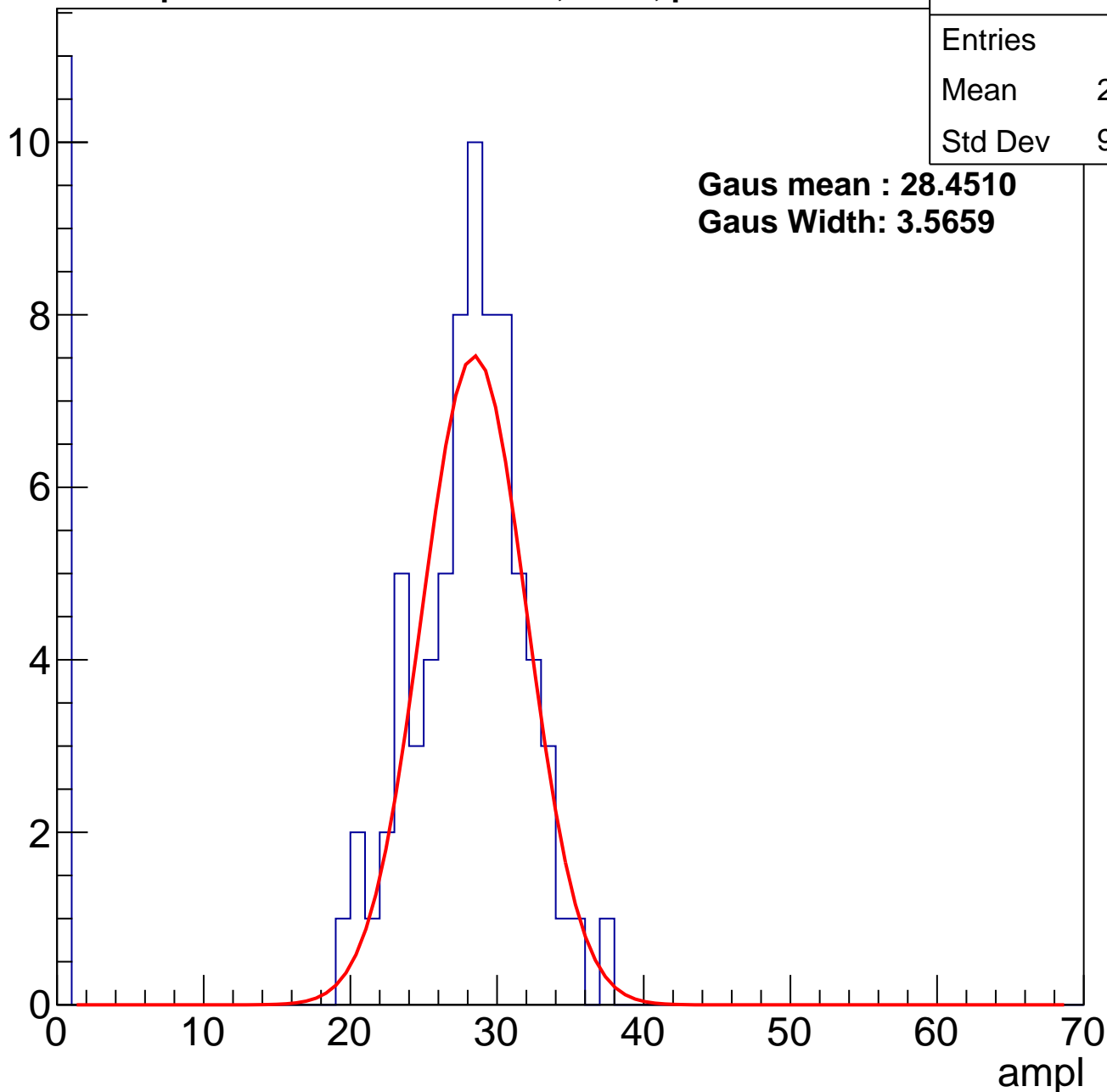
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	24.06
Std Dev	9.996

Gaus mean : 28.4510

Gaus Width: 3.5659

Entry



B1L103S, U24-ch73, adc1

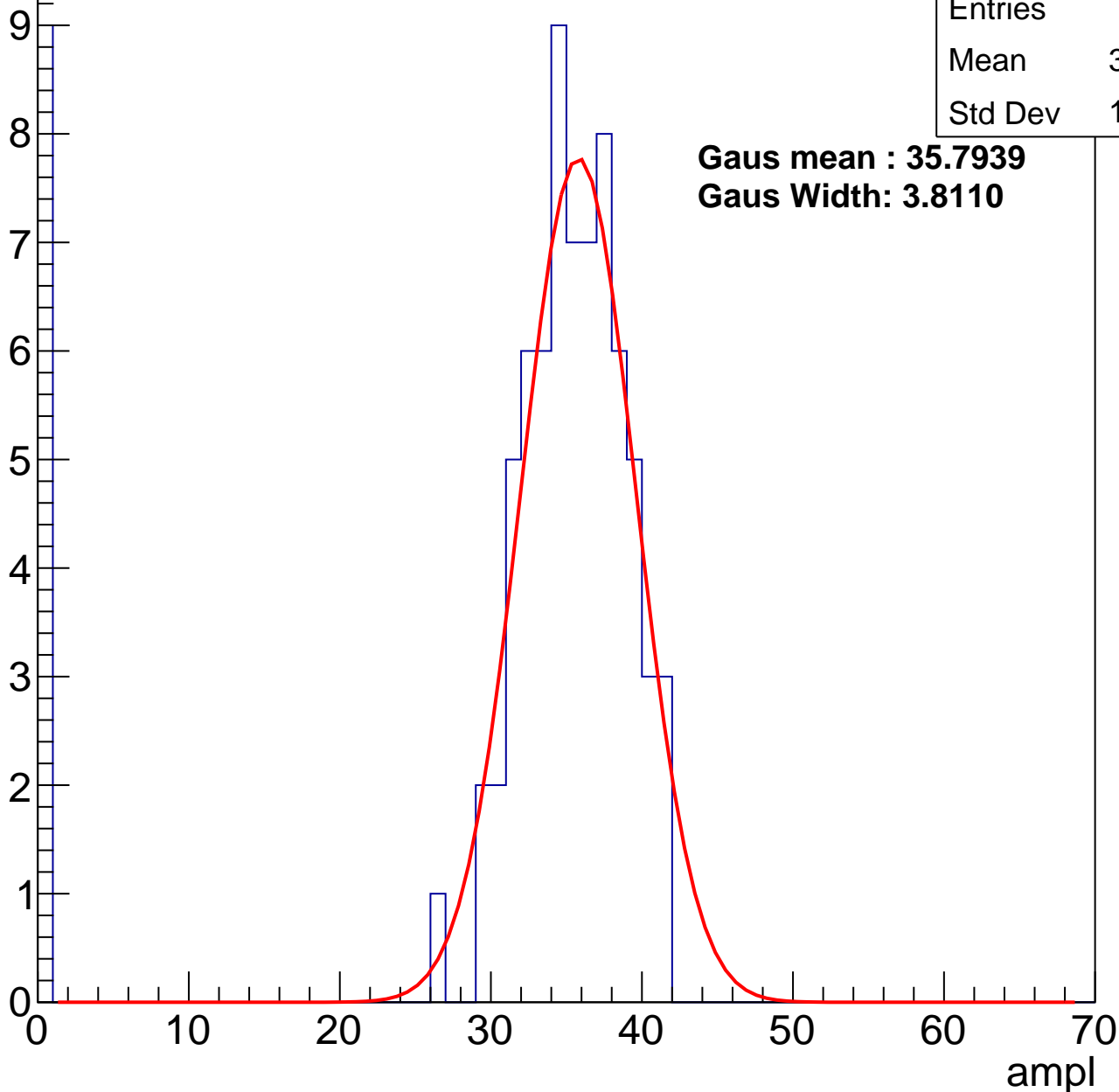
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	31.06
Std Dev	11.54

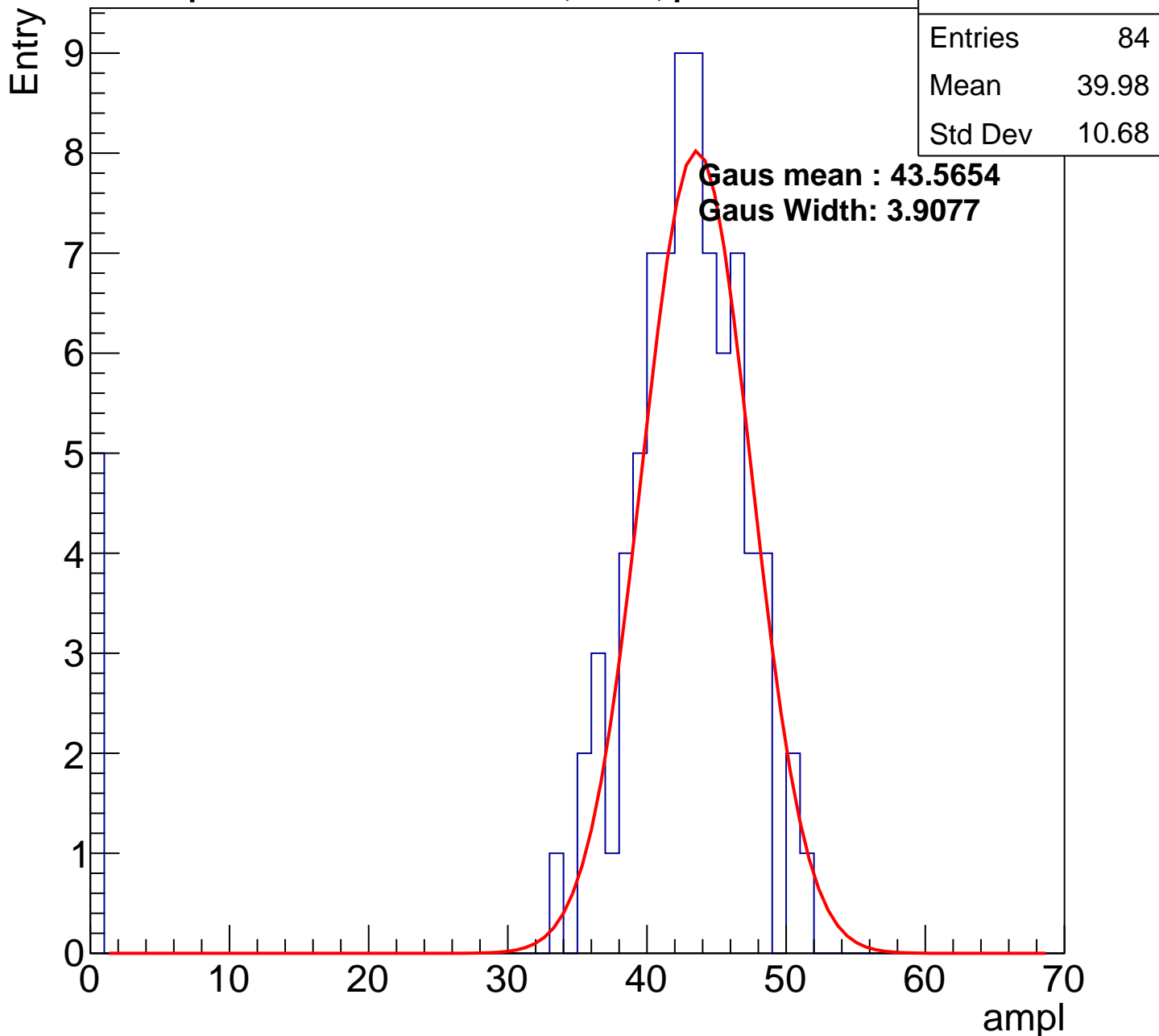
Gaus mean : 35.7939

Gaus Width: 3.8110



B1L103S, U24-ch73, adc2

calib_packv5_041523_1651.root, FC#0, port C2

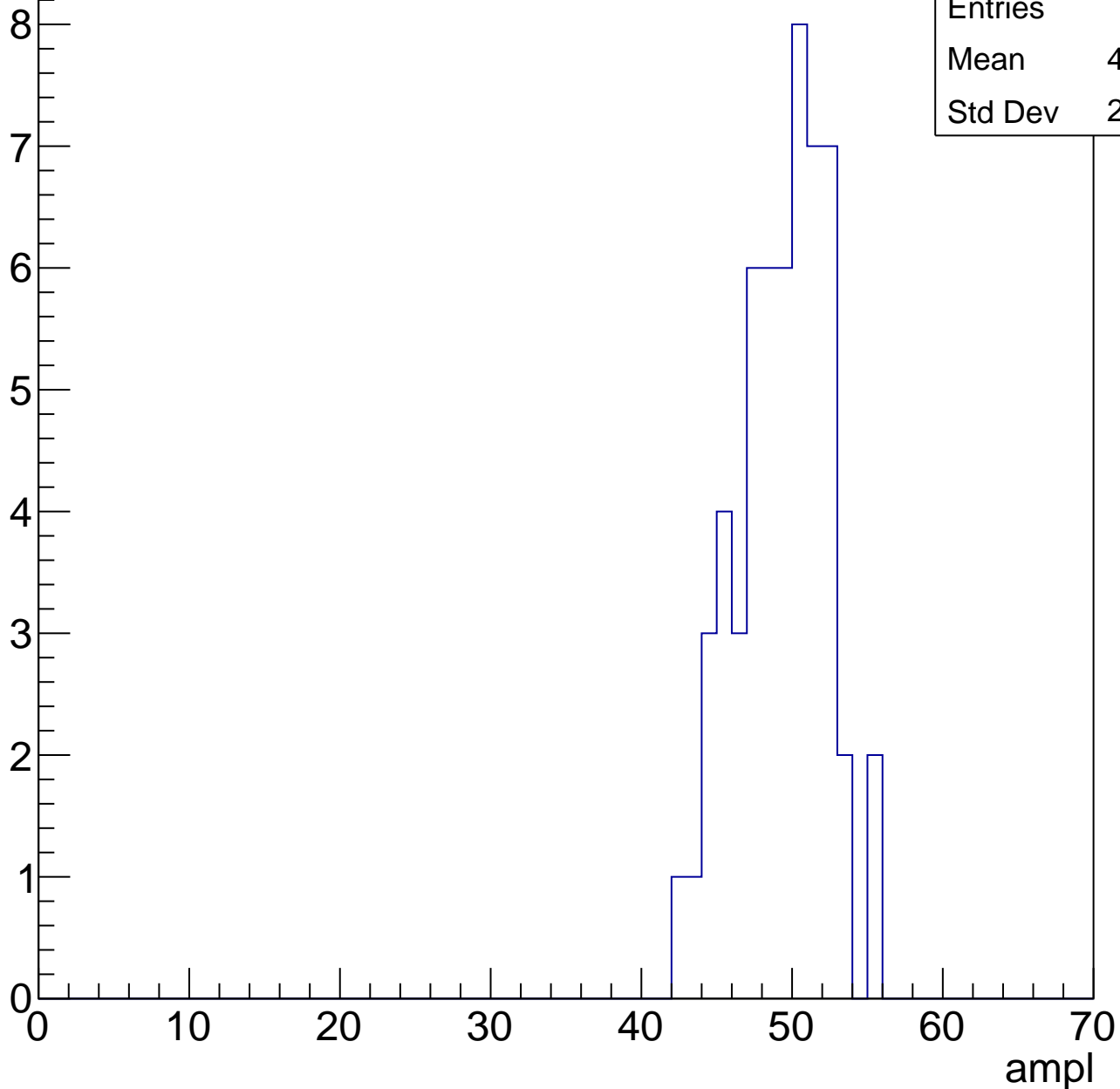


B1L103S, U24-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	48.86
Std Dev	2.936

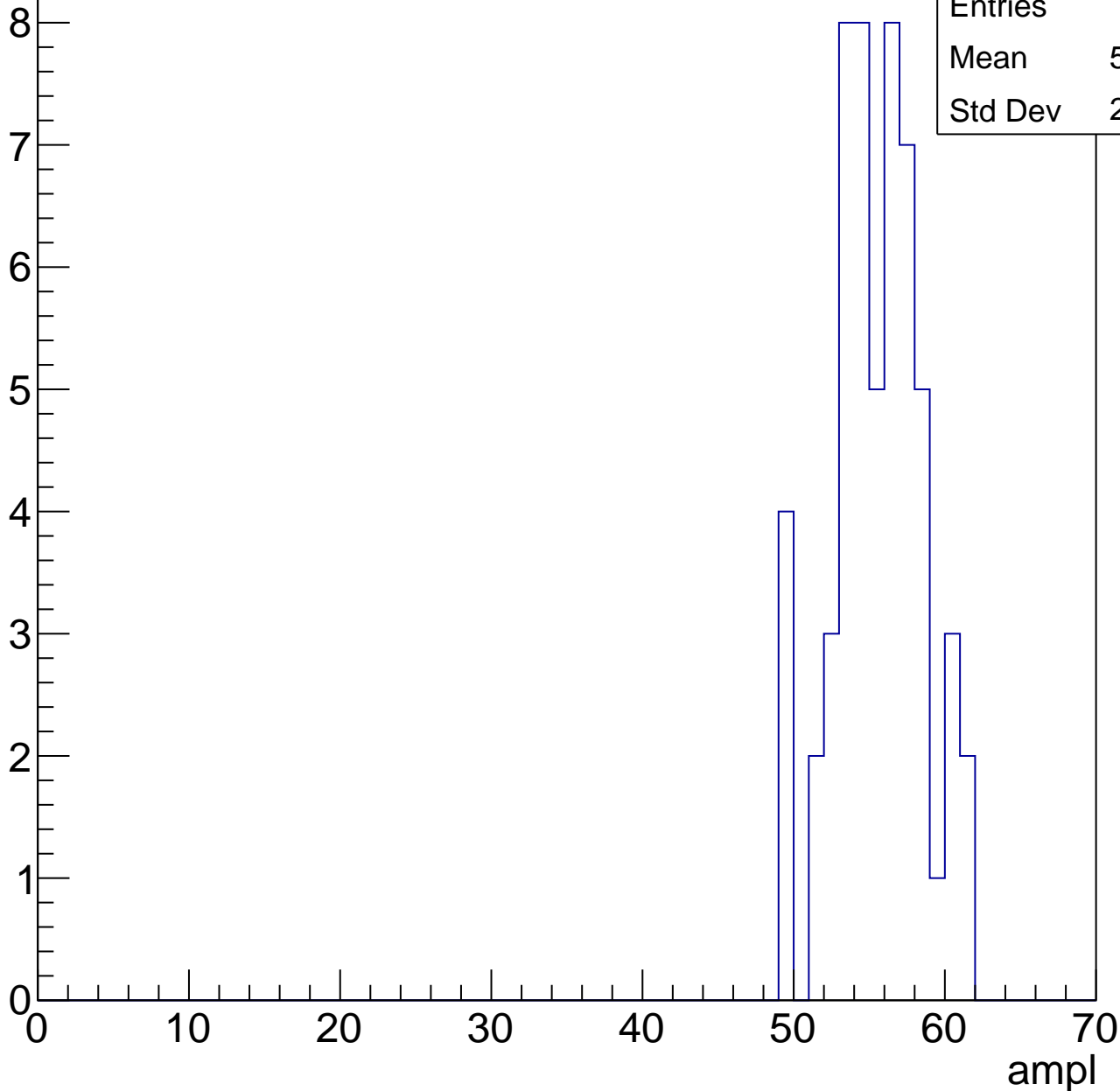


B1L103S, U24-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

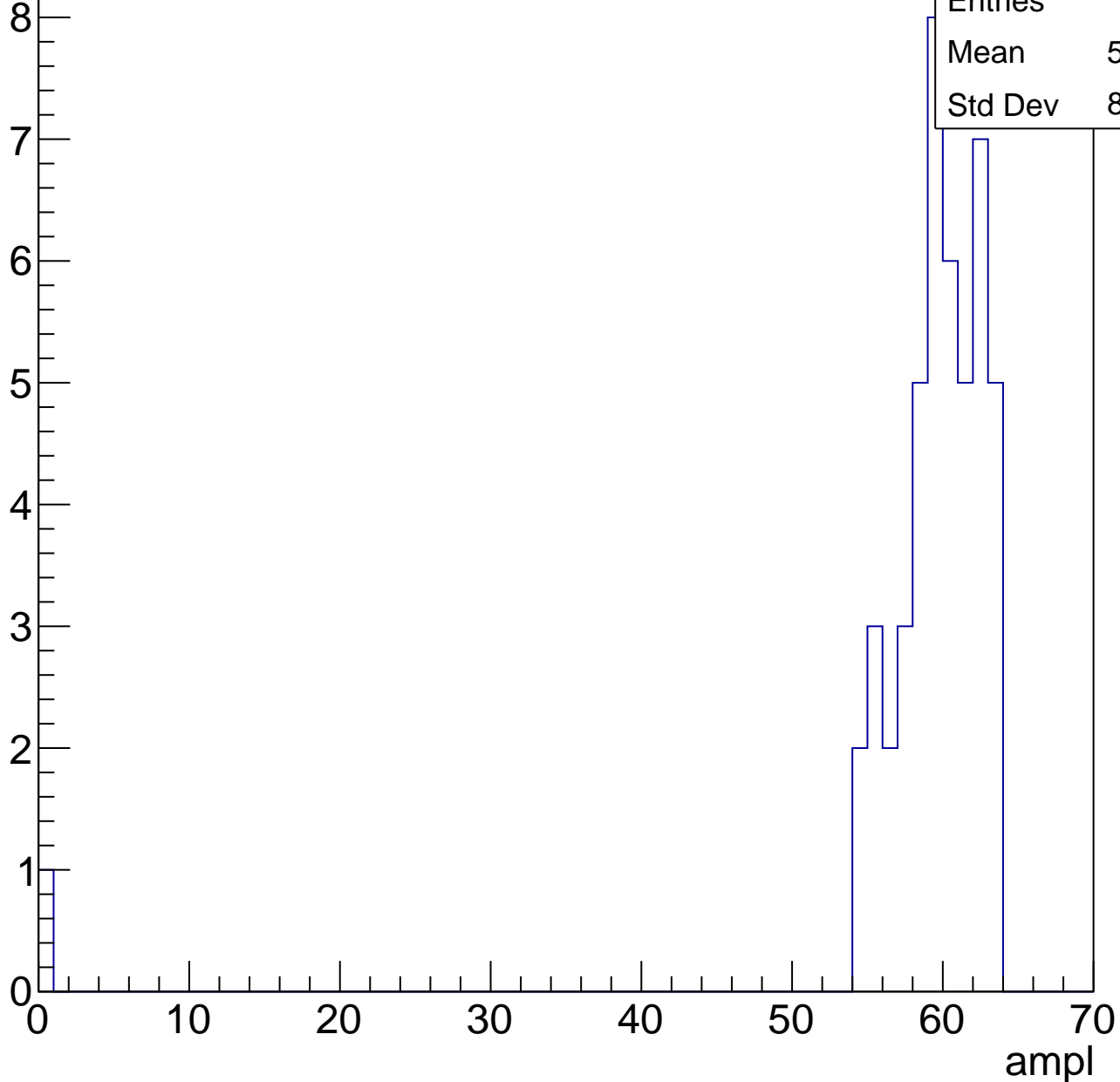
Entries	56
Mean	55.05
Std Dev	2.948



B1L103S, U24-ch73, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

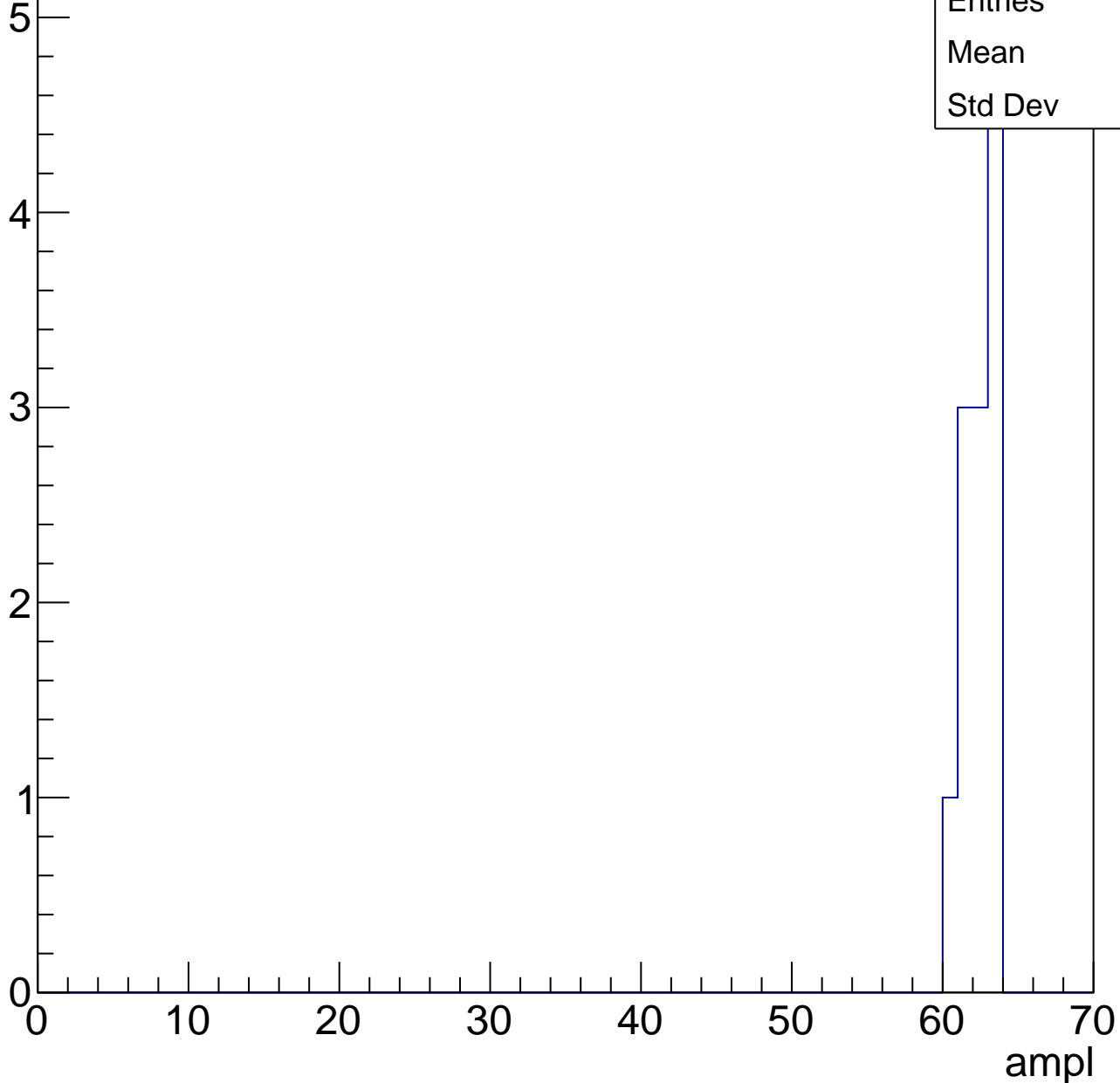


B1L103S, U24-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62
Std Dev	1

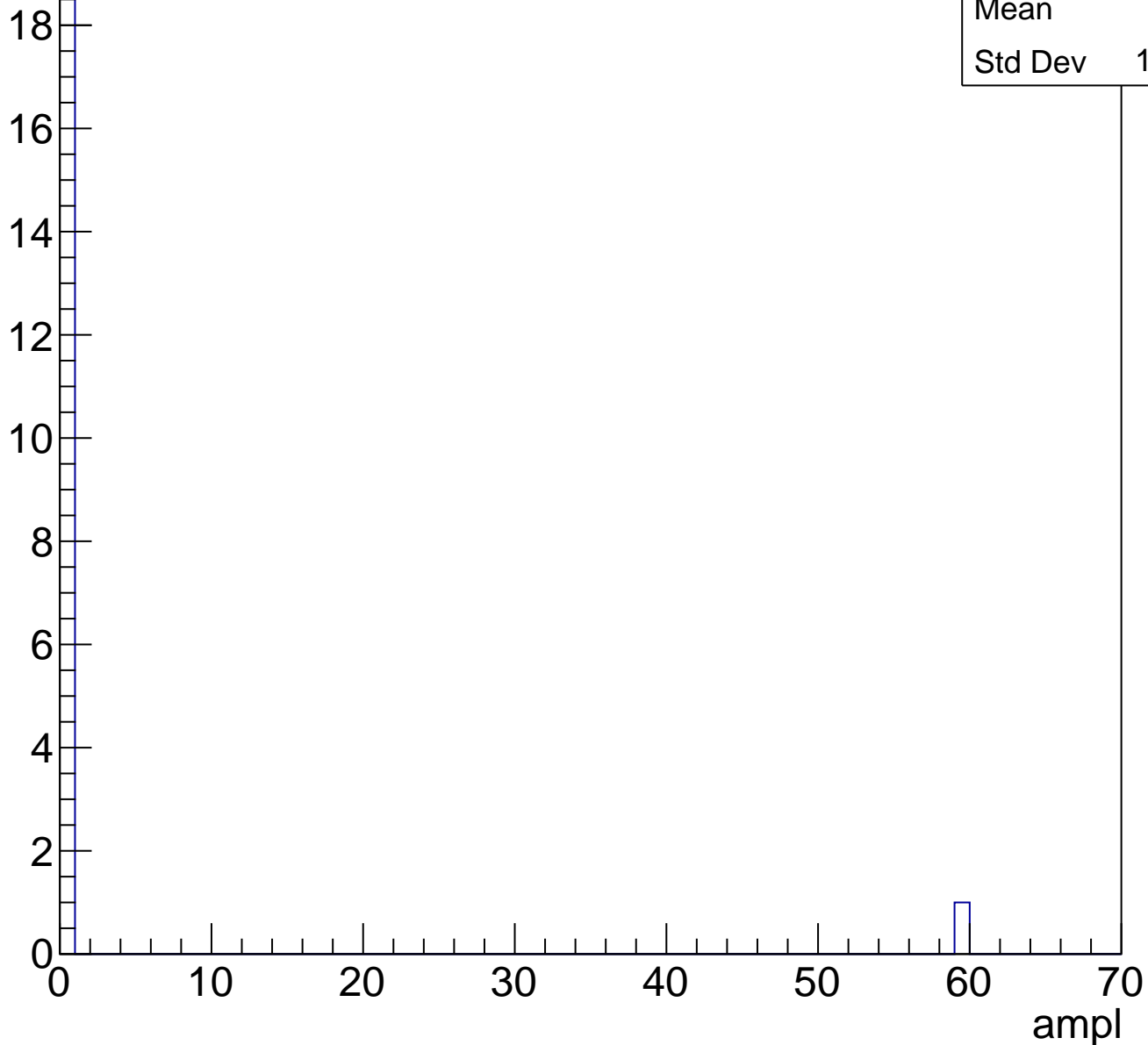


B1L103S, U24-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	2.95
Std Dev	12.86

Entry



B1L103S, U24-ch74, adc0

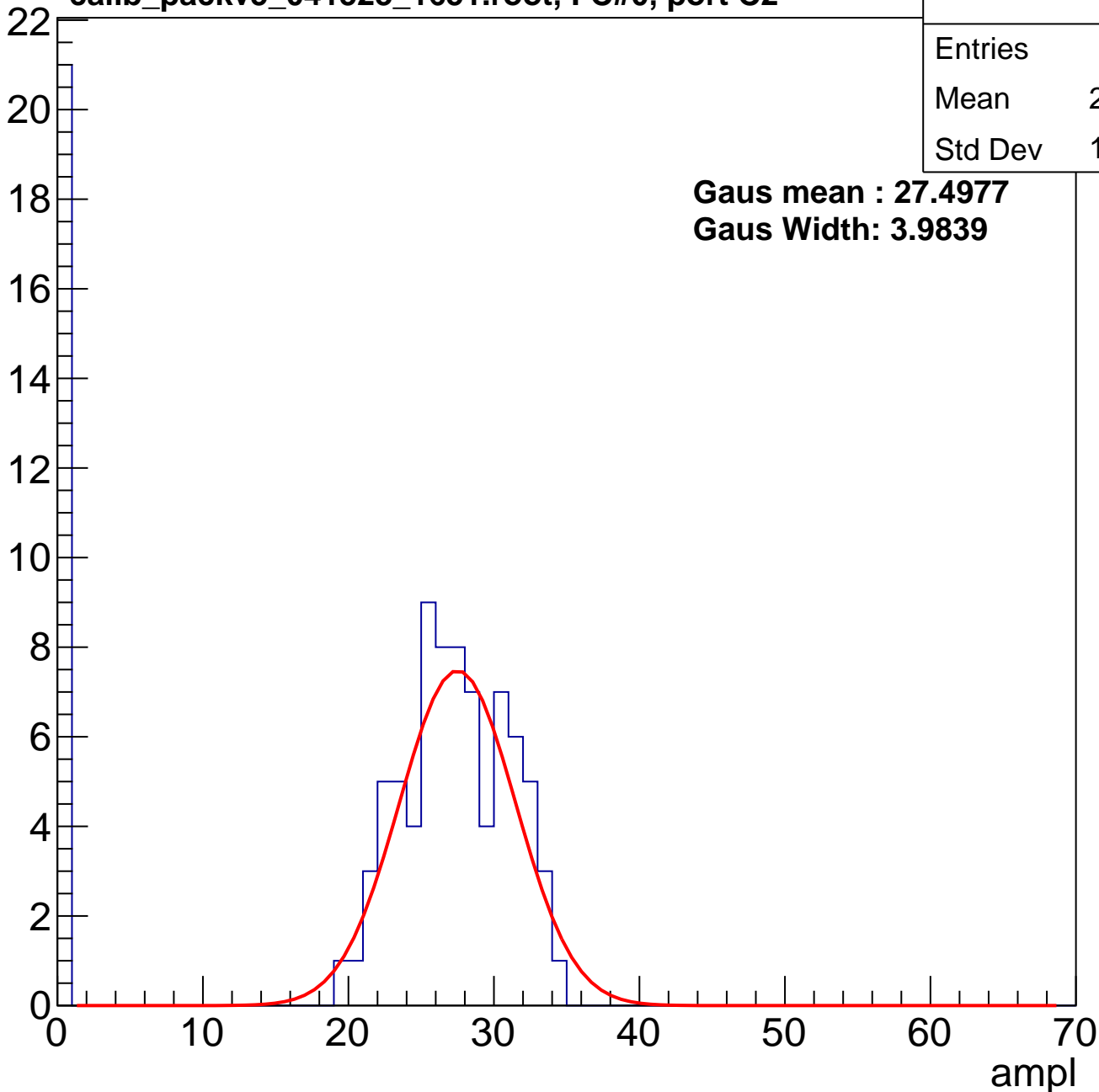
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	21.15
Std Dev	11.49

Gaus mean : 27.4977

Gaus Width: 3.9839

Entry



B1L103S, U24-ch74, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	29.18
Std Dev	12.07

Gaus mean : 35.1352

Gaus Width: 3.7208

Entry

10

8

6

4

2

0

0

10

20

30

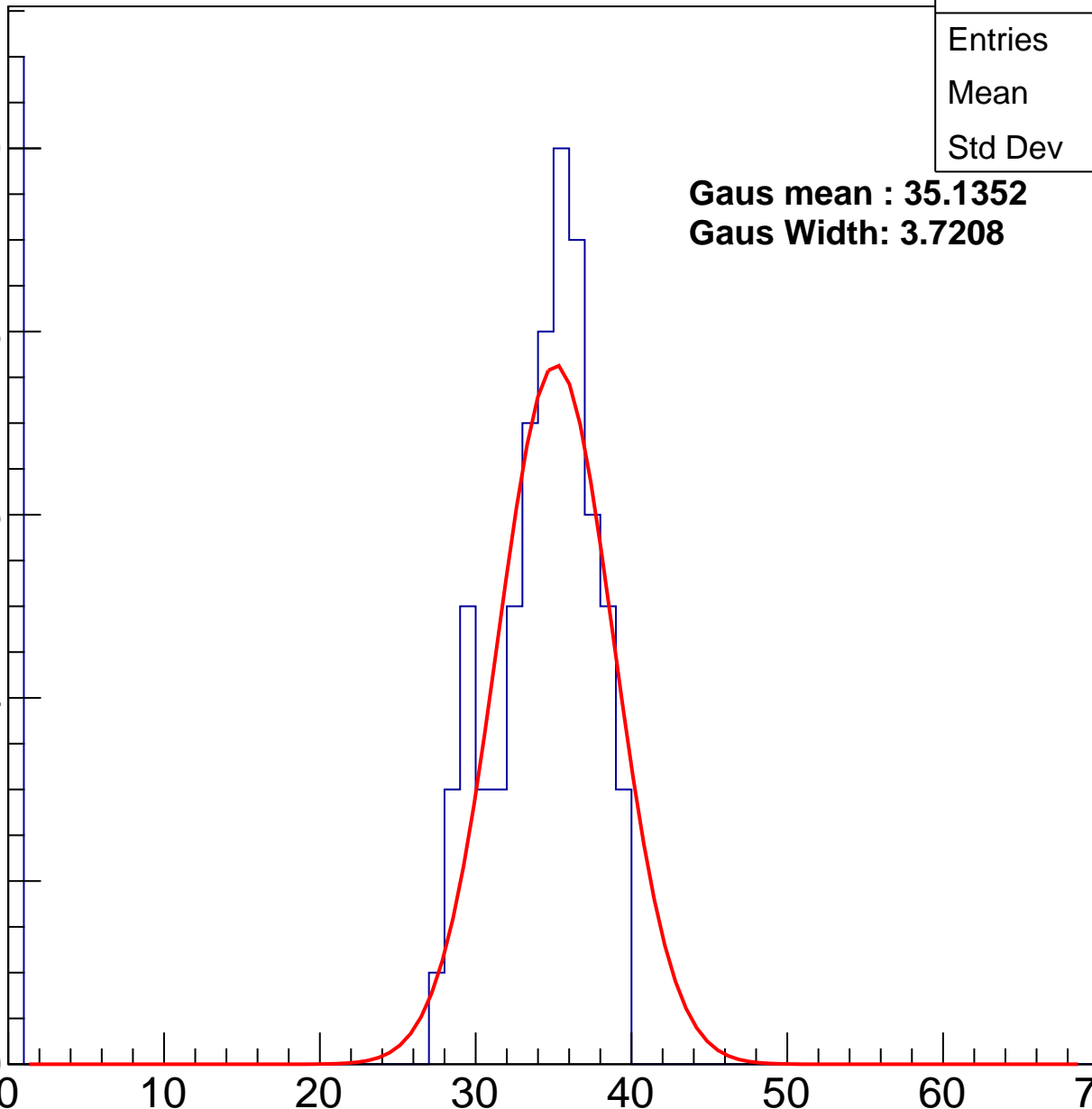
40

50

60

70

ampl



B1L103S, U24-ch74, adc2

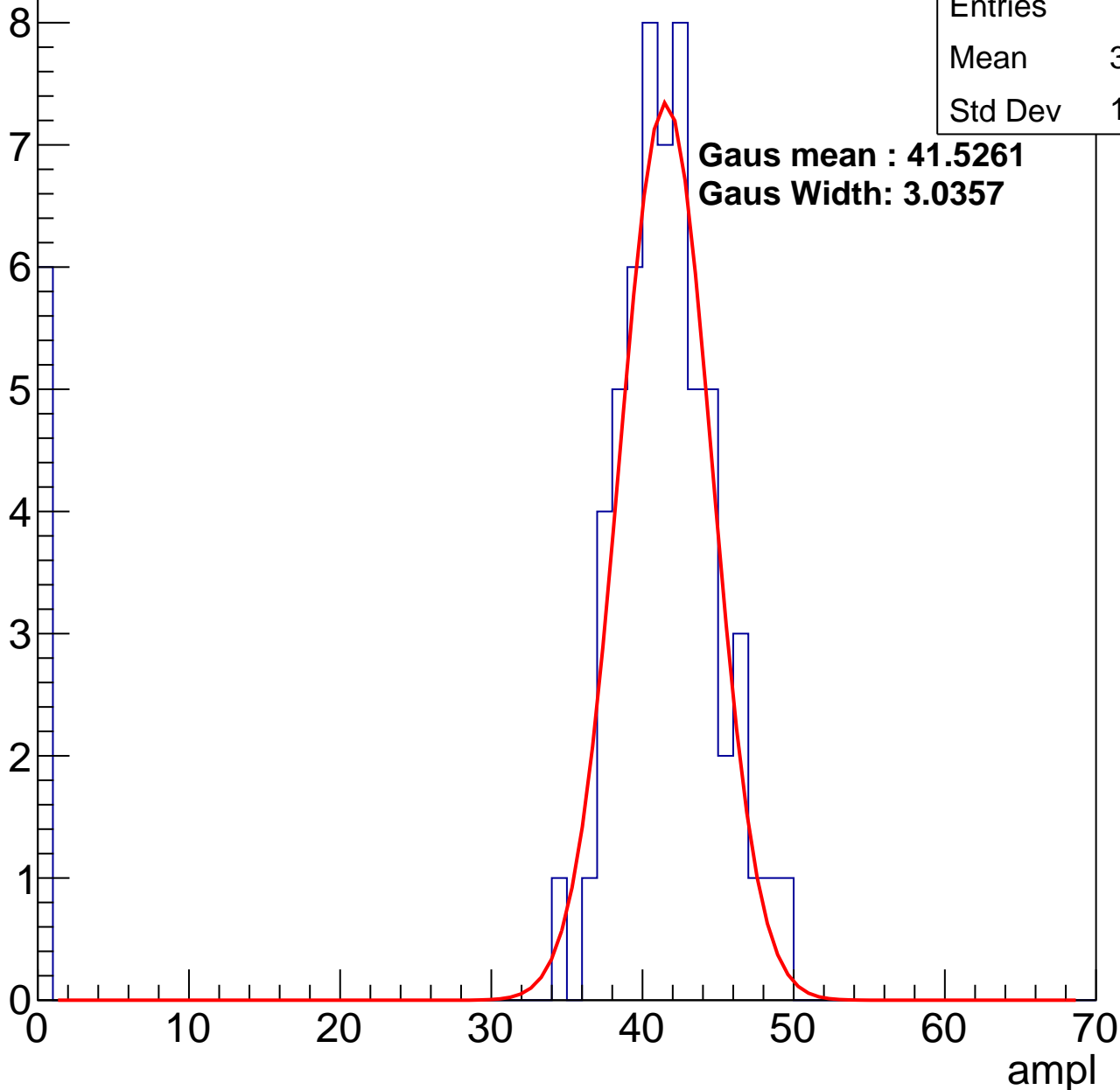
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.38
Std Dev	12.37

Gaus mean : 41.5261

Gaus Width: 3.0357

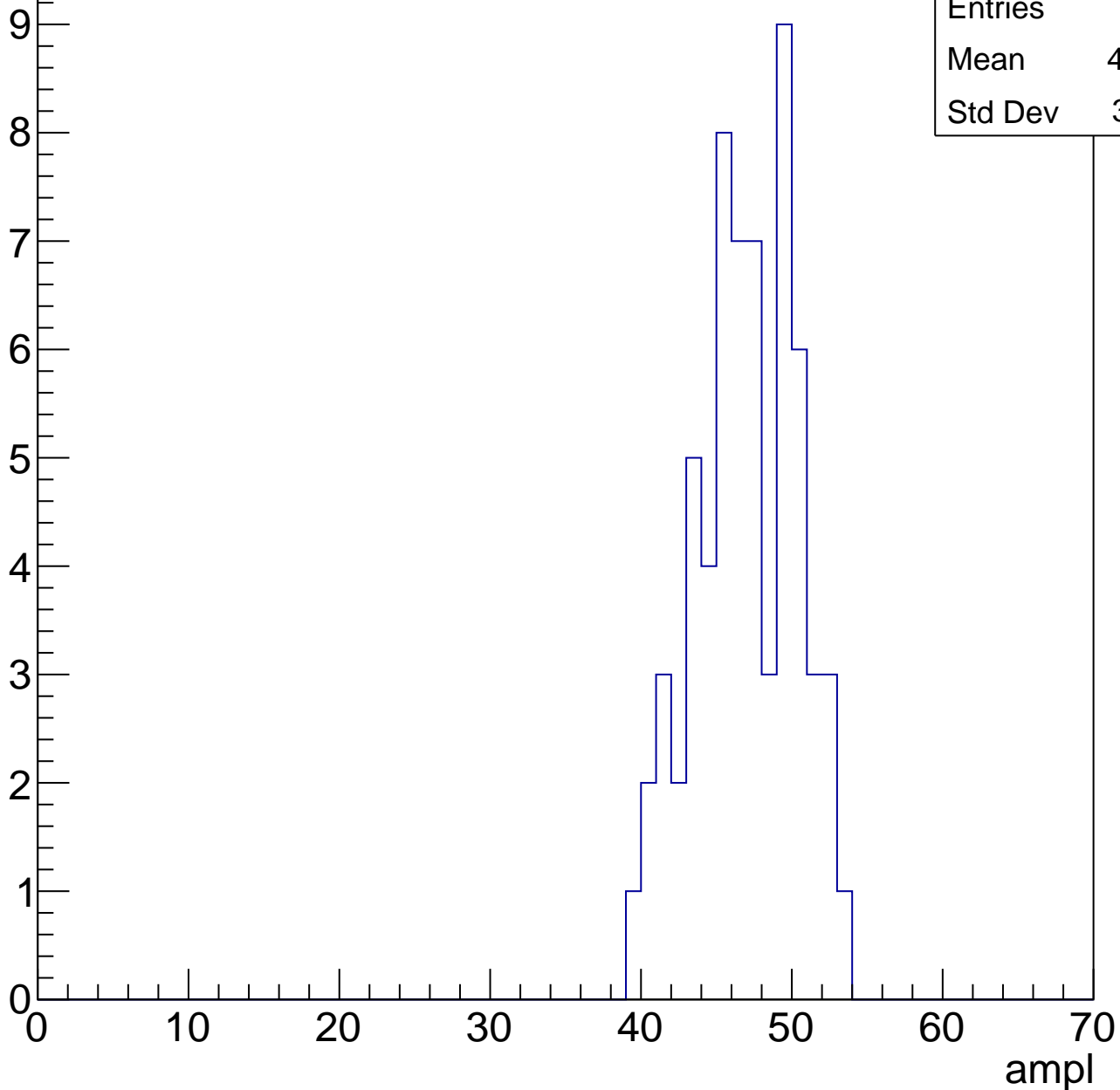


B1L103S, U24-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	46.48
Std Dev	3.331

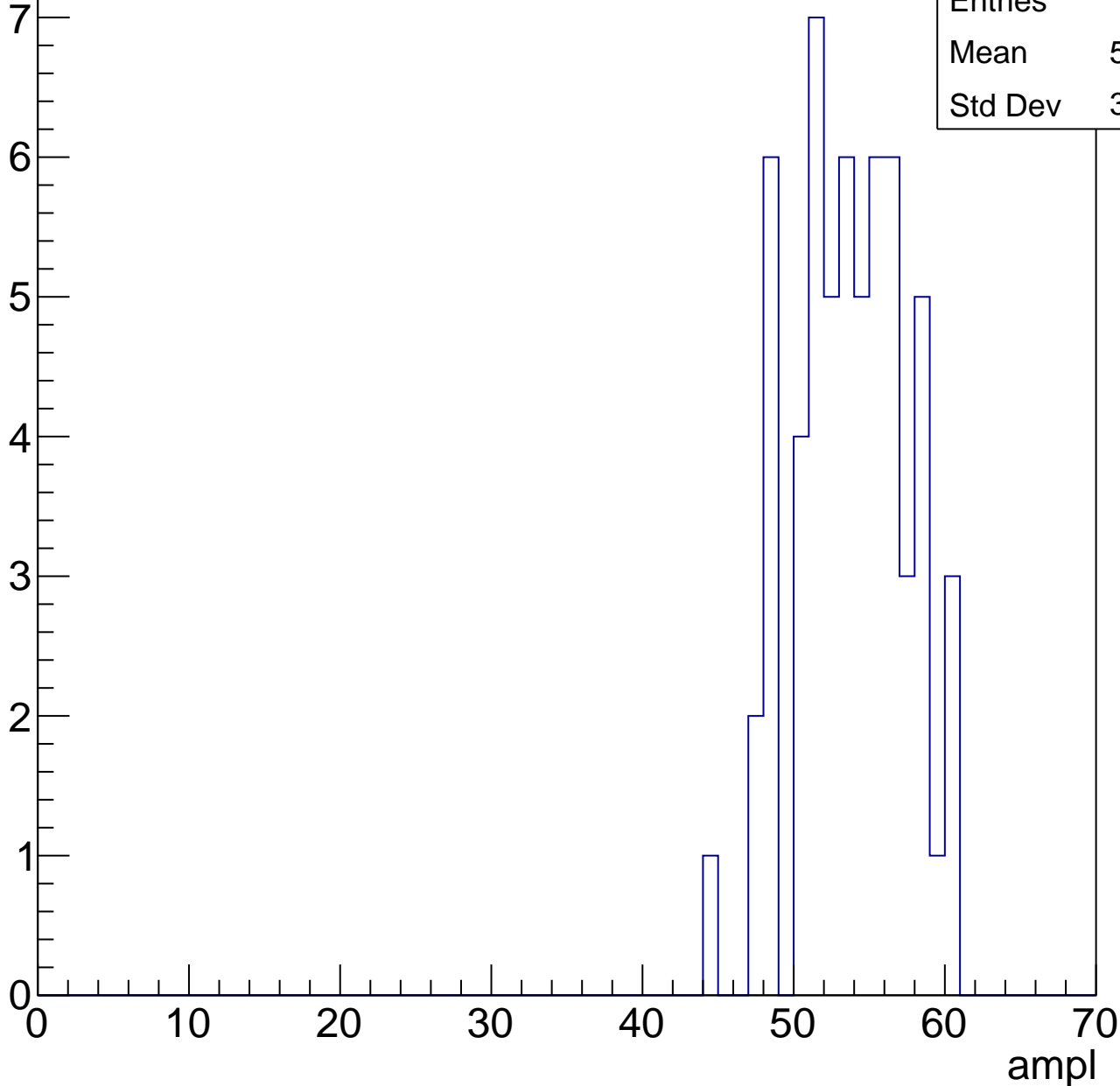


B1L103S, U24-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

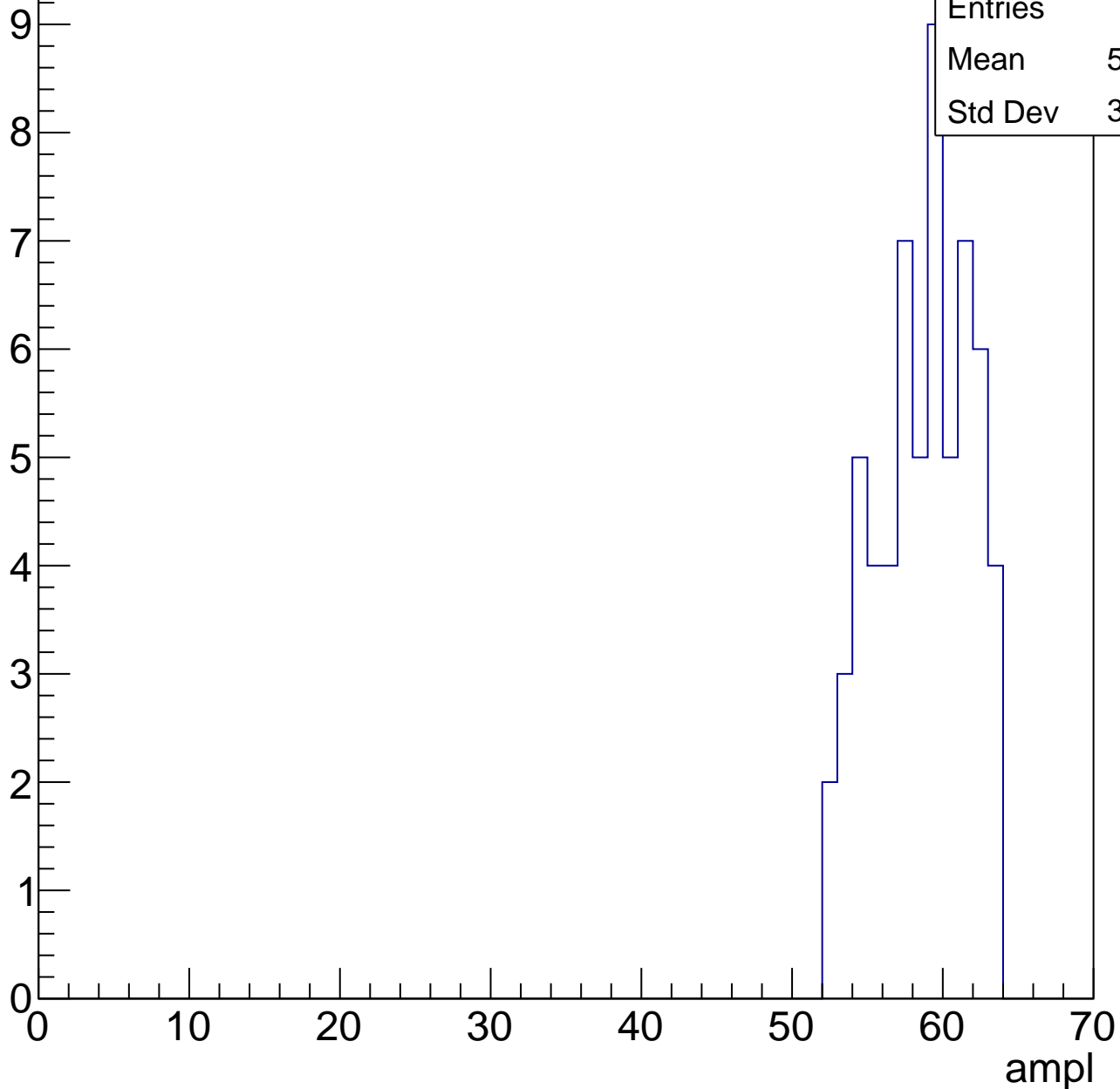
Entries	60
Mean	53.28
Std Dev	3.666



B1L103S, U24-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

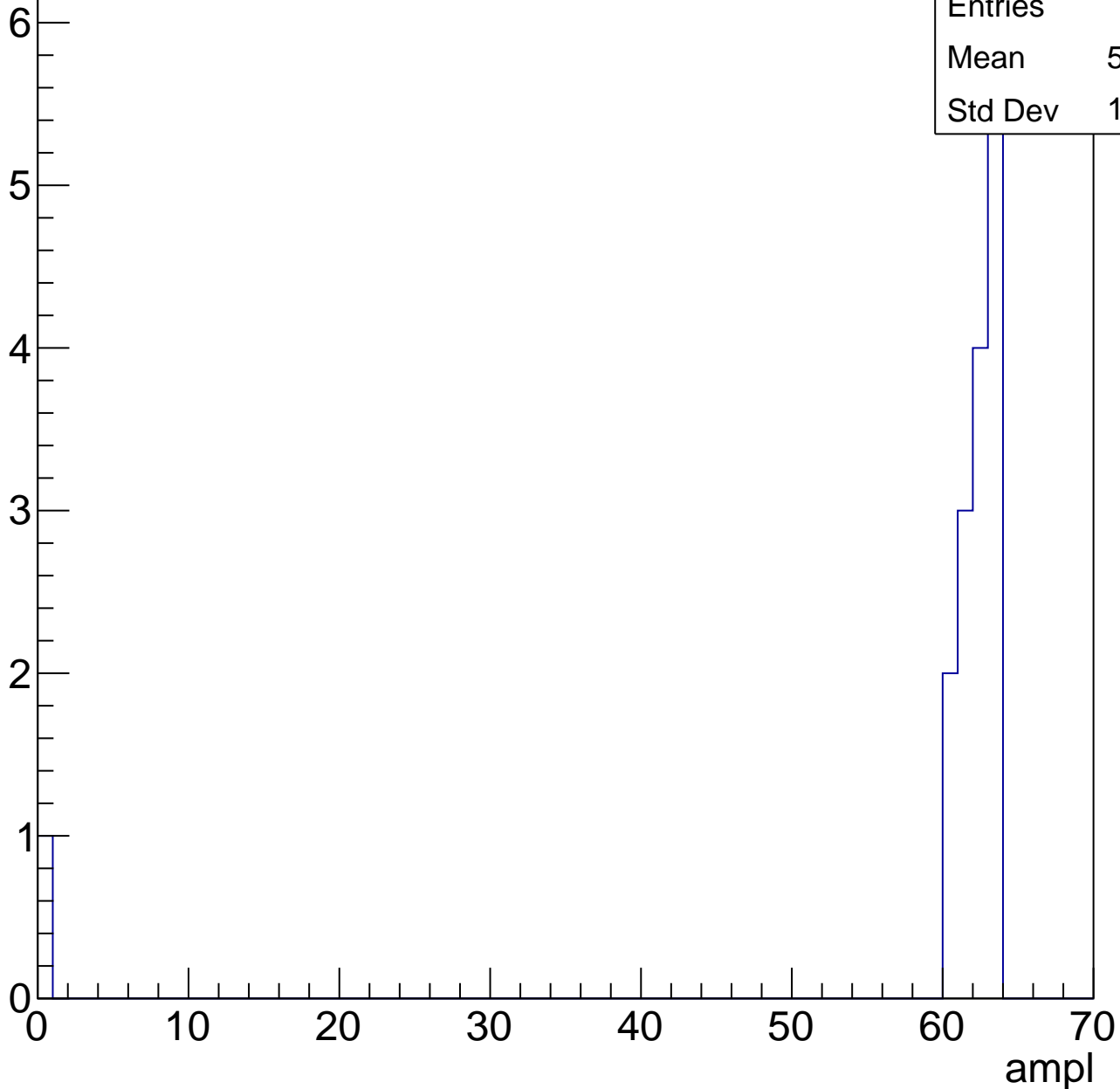


B1L103S, U24-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	58.06
Std Dev	15.03



B1L103S, U24-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch75, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	25.34
Std Dev	11.52

Gaus mean : 31.2137

Gaus Width: 3.3913

Entry

12

10

8

6

4

2

0

0

10

20

30

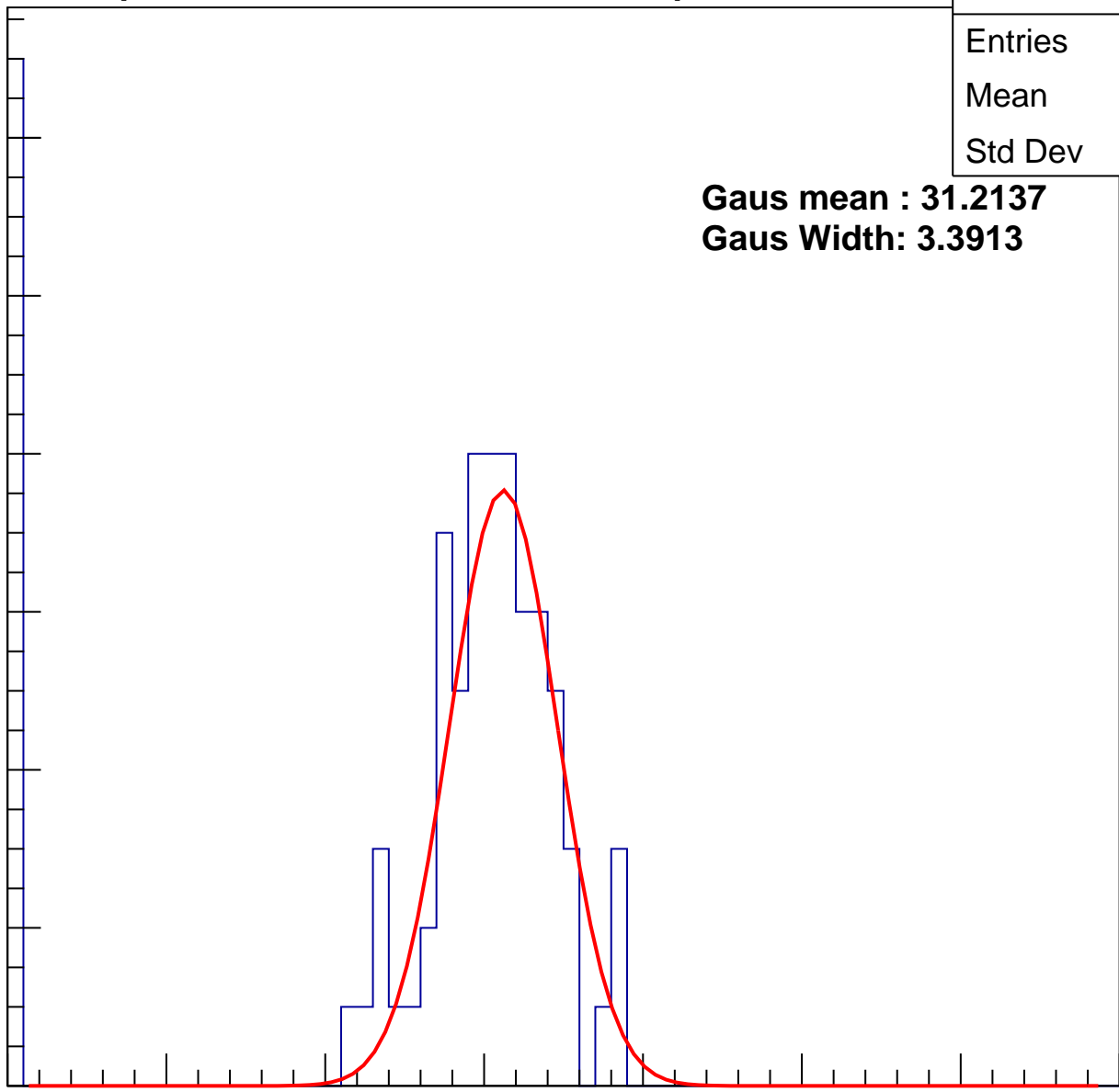
40

50

60

70

ampl



B1L103S, U24-ch75, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	36.57
Std Dev	5.508

Gaus mean : 37.5221

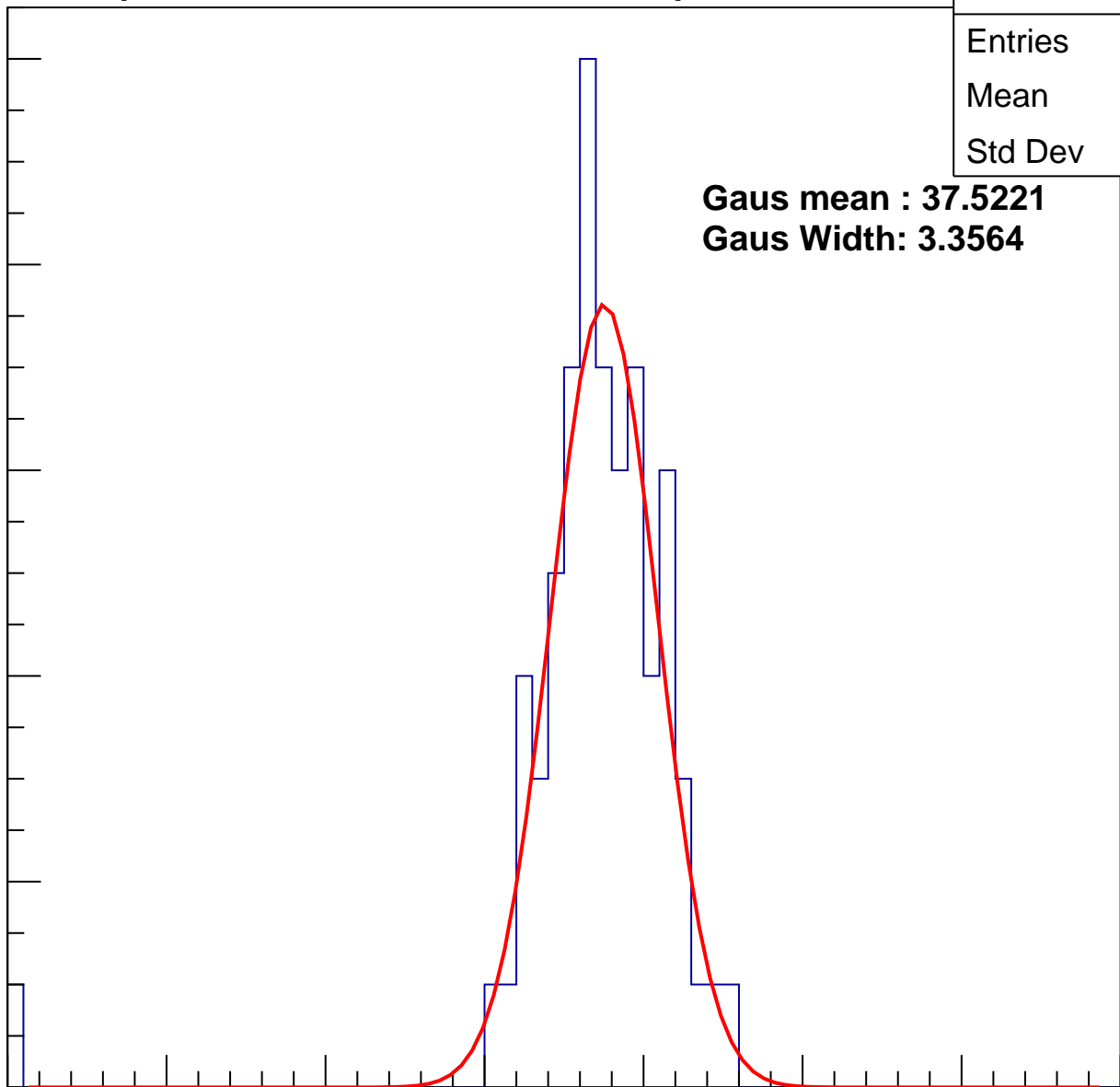
Gaus Width: 3.3564

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch75, adc2

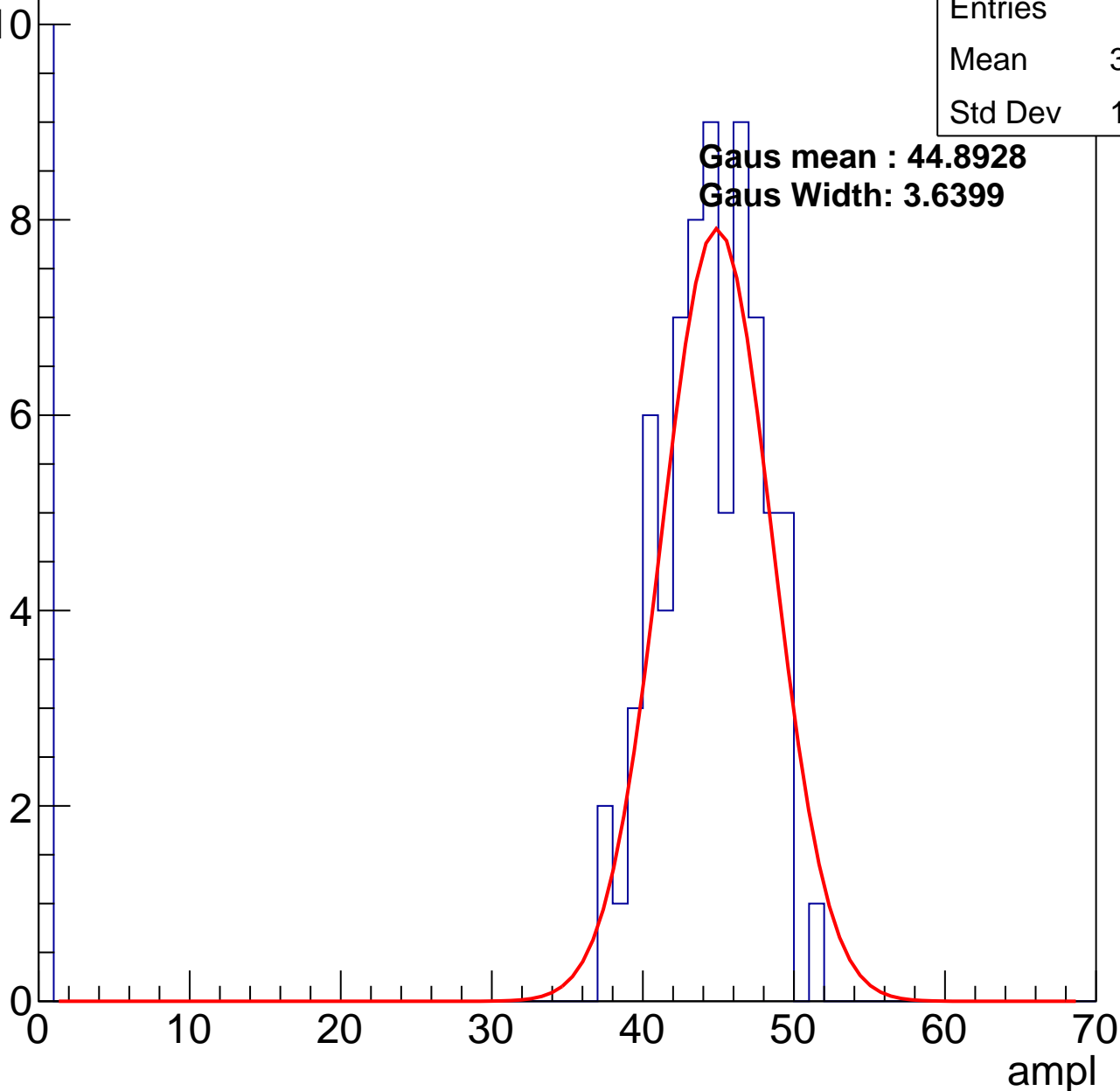
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	38.67
Std Dev	14.72

Gaus mean : 44.8928

Gaus Width: 3.6399

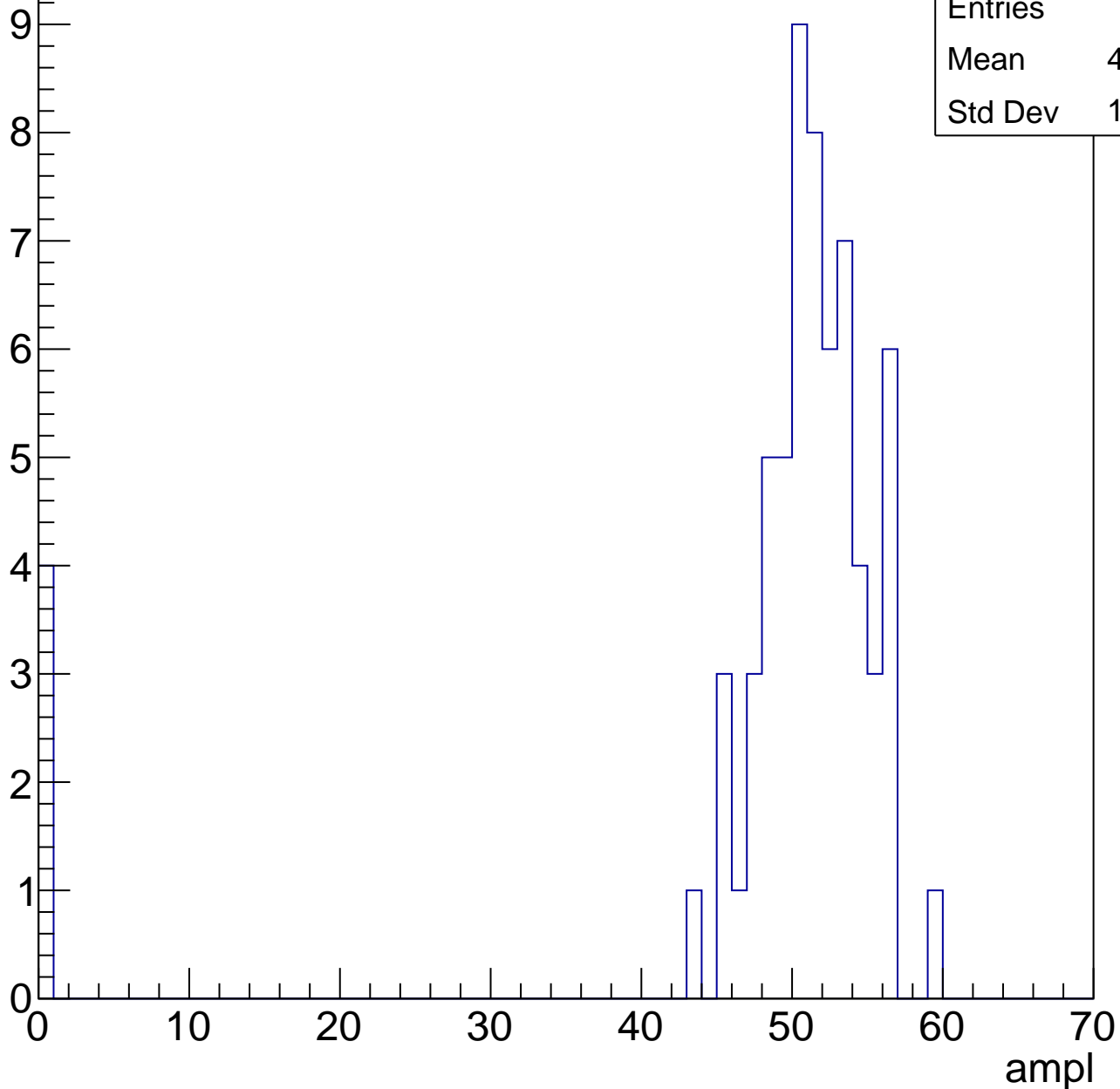


B1L103S, U24-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	47.98
Std Dev	12.59

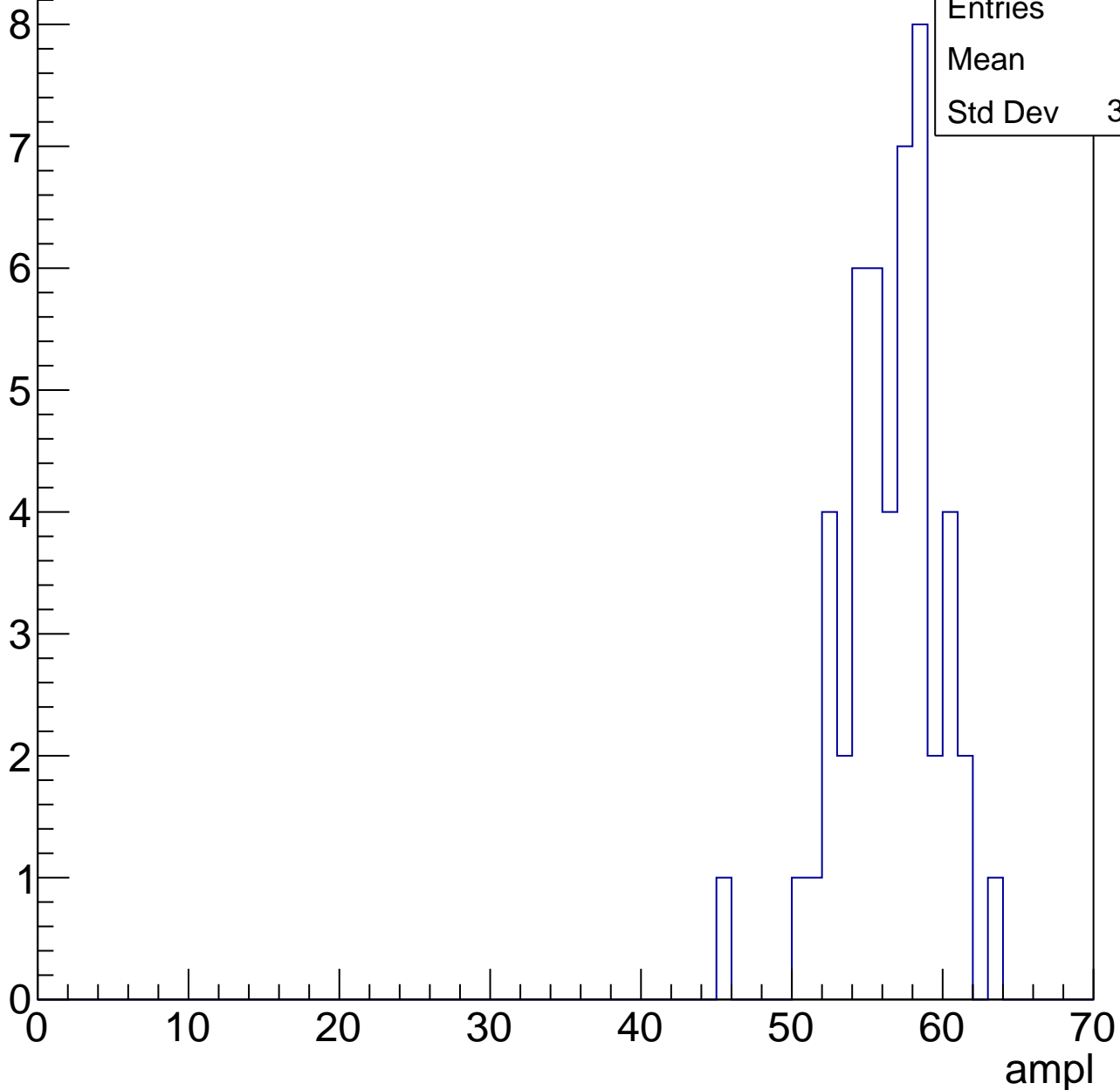


B1L103S, U24-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	56
Std Dev	3.239

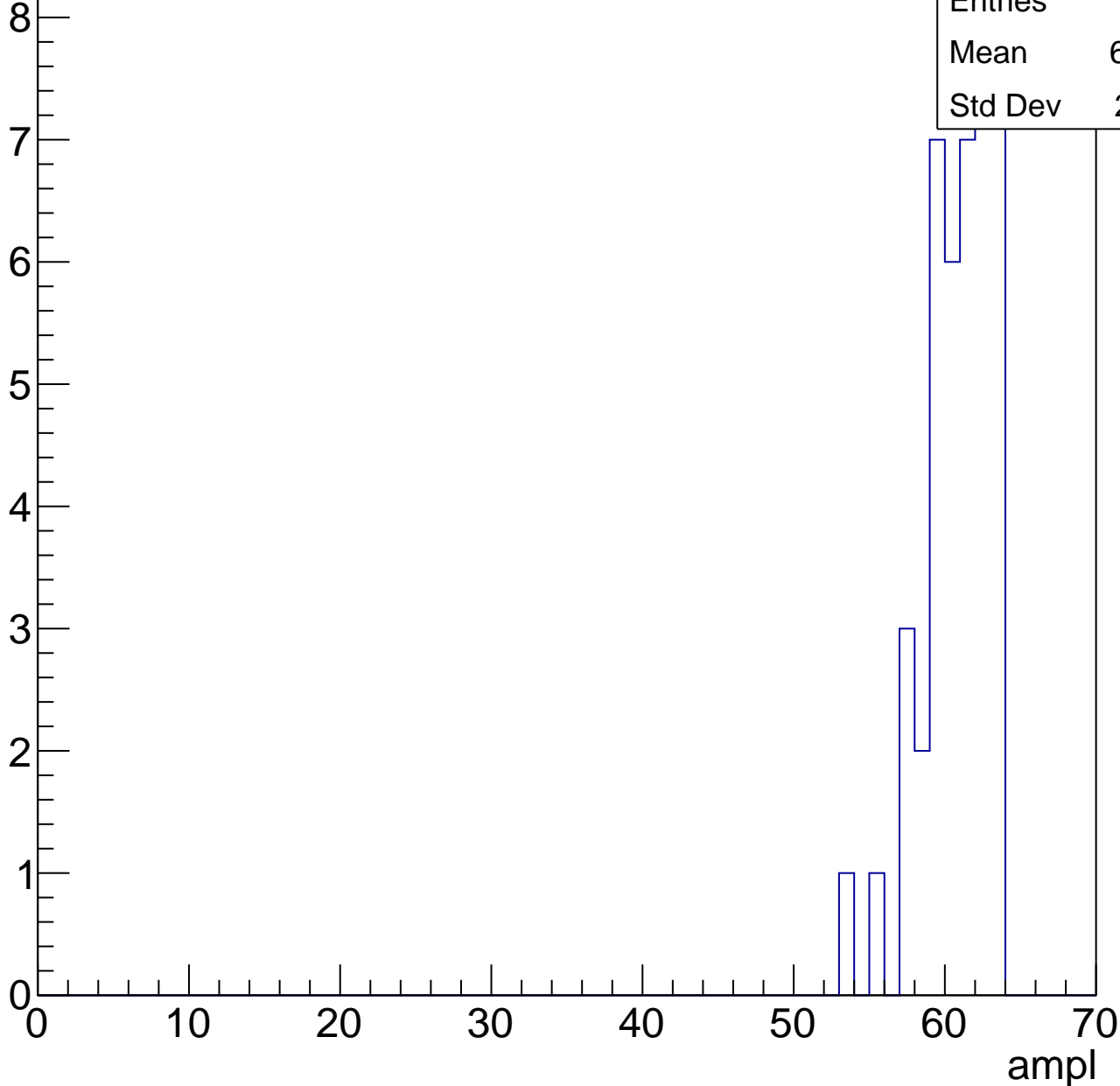


B1L103S, U24-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	60.35
Std Dev	2.271



B1L103S, U24-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	5
Mean	49.6
Std Dev	24.81

B1L103S, U24-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch76, adc0

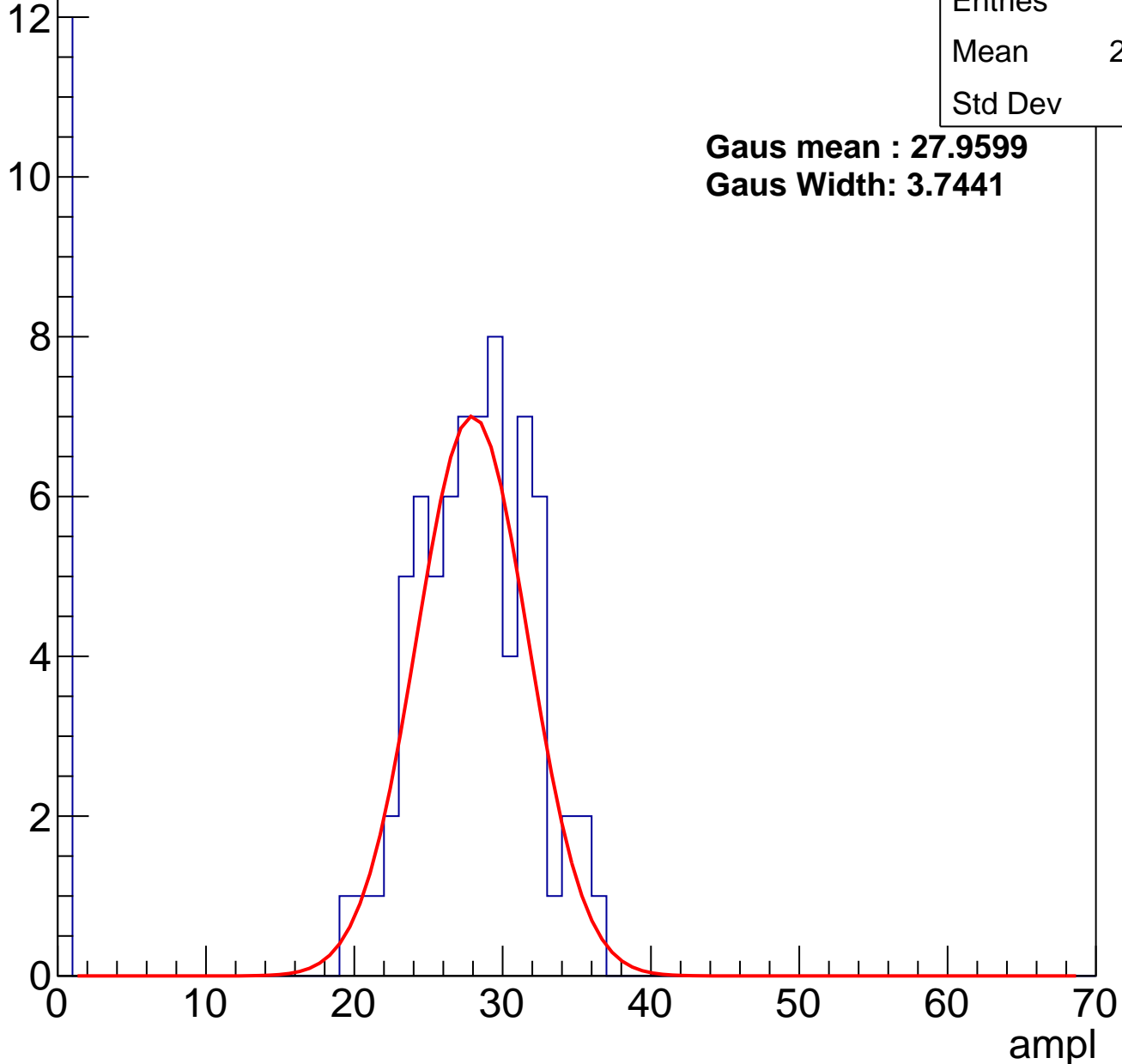
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	23.77
Std Dev	10.3

Gaus mean : 27.9599

Gaus Width: 3.7441

Entry



B1L103S, U24-ch76, adc1

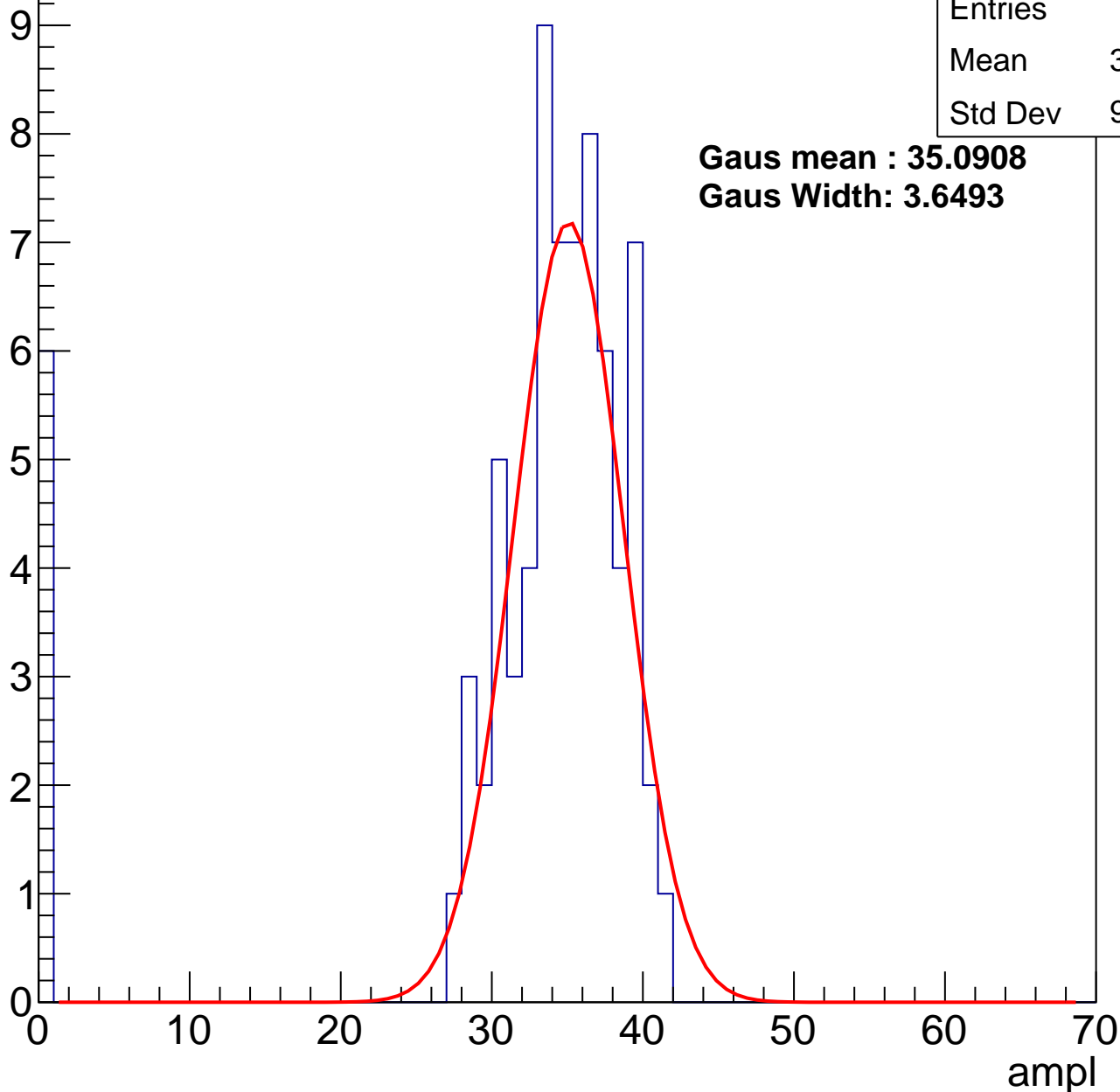
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	31.68
Std Dev	9.882

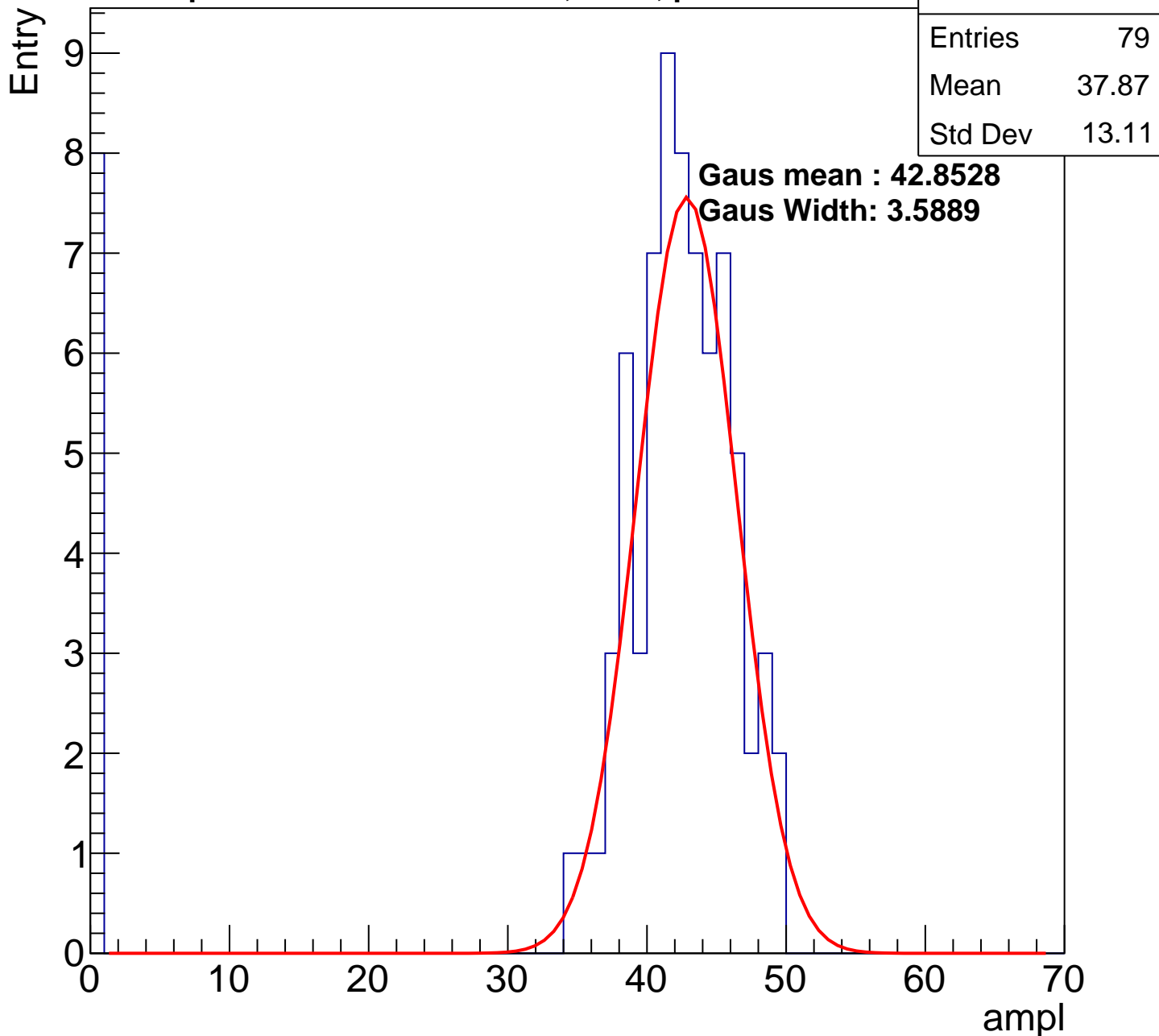
Gaus mean : 35.0908

Gaus Width: 3.6493



B1L103S, U24-ch76, adc2

calib_packv5_041523_1651.root, FC#0, port C2

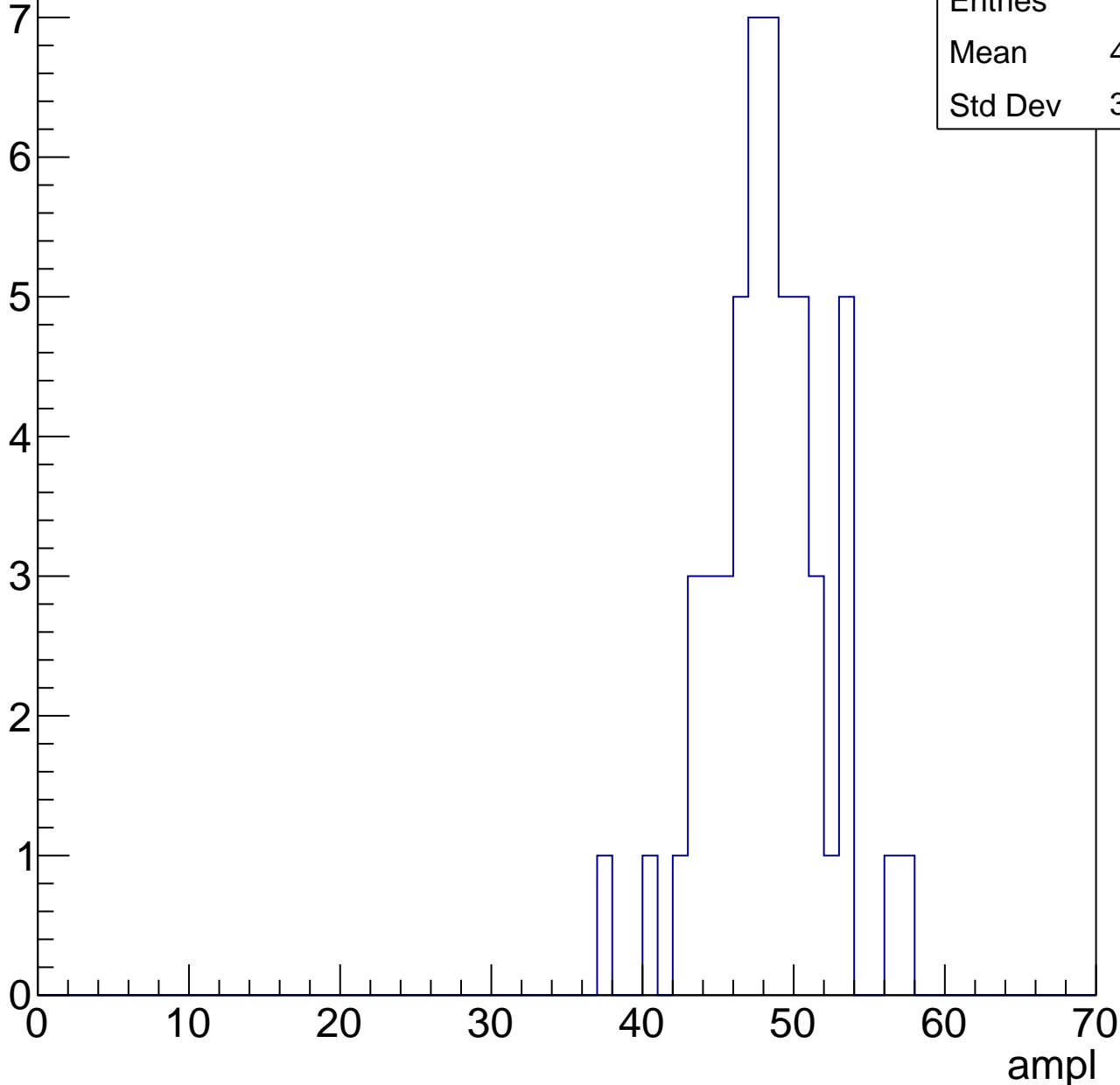


B1L103S, U24-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	47.85
Std Dev	3.759

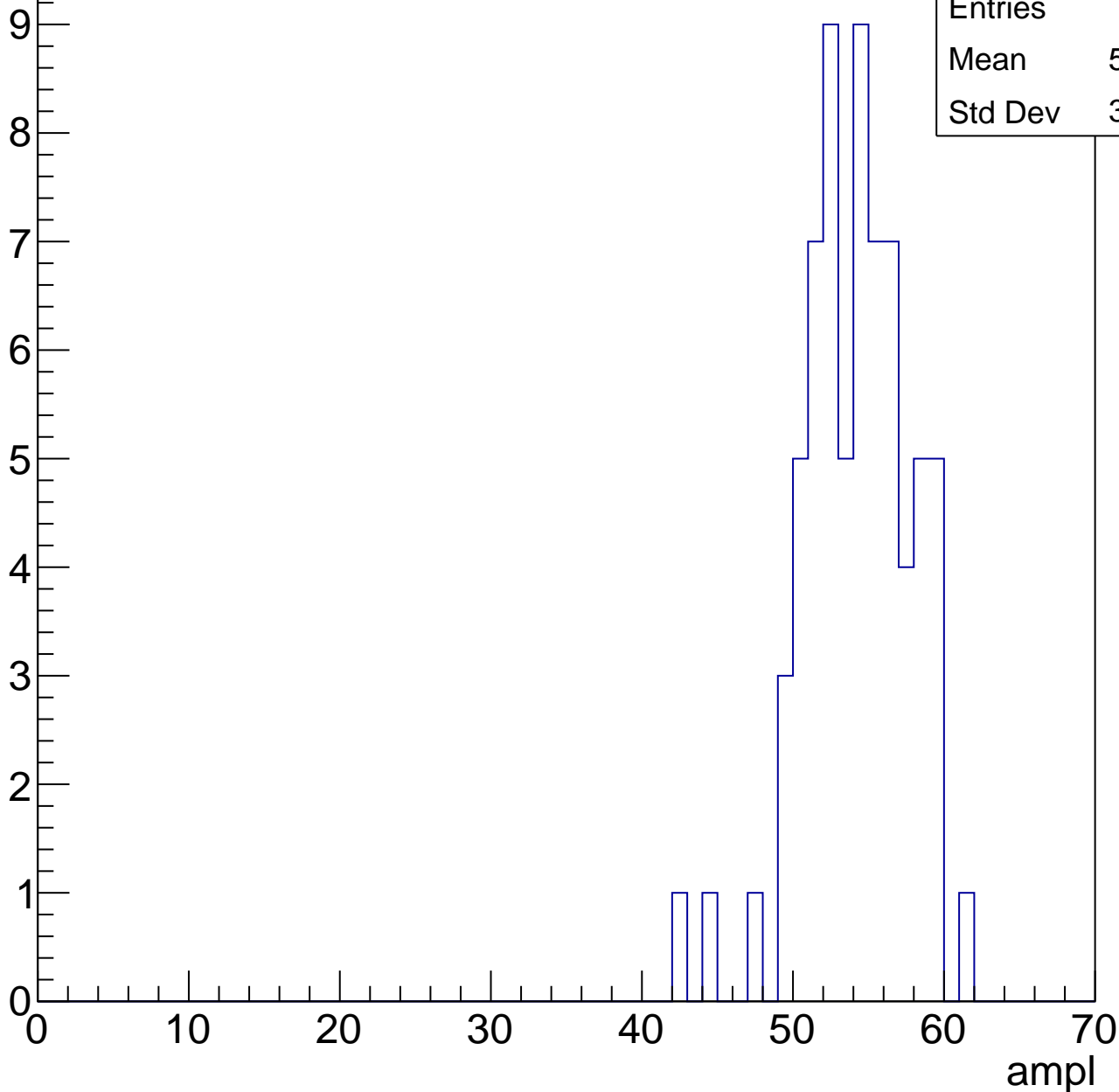


B1L103S, U24-ch76, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	53.67
Std Dev	3.524

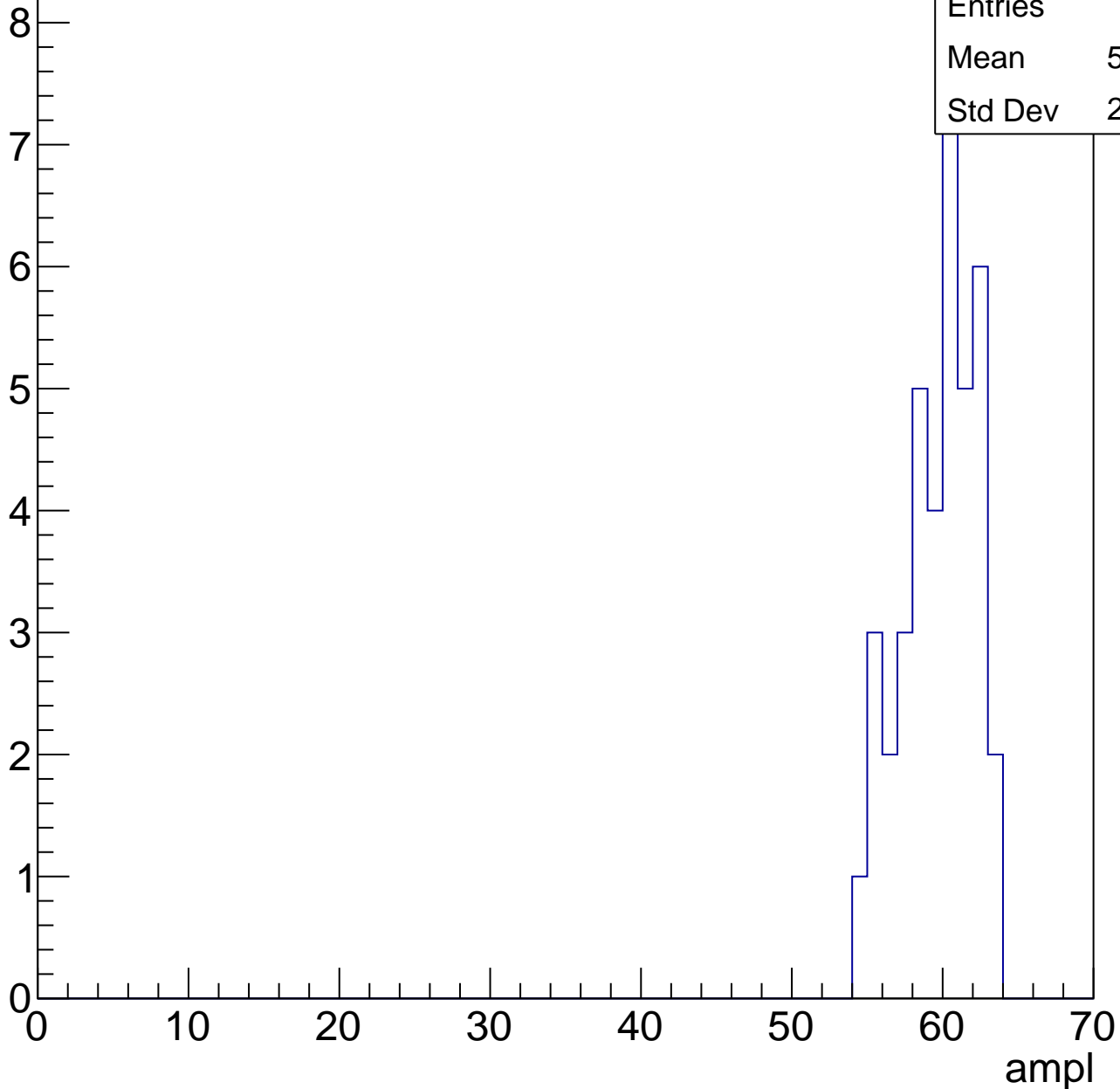


B1L103S, U24-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	39
Mean	59.26
Std Dev	2.372

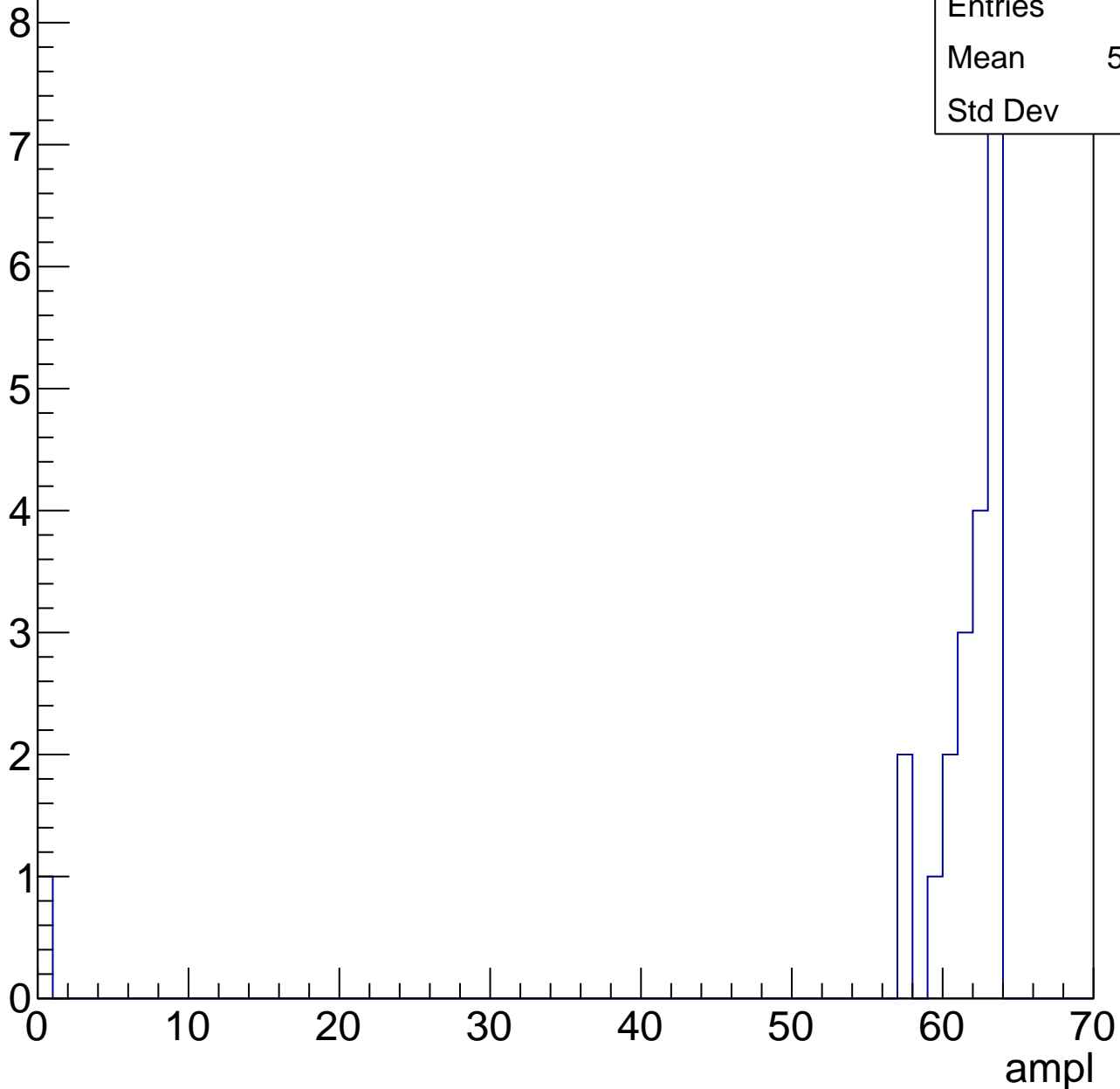


B1L103S, U24-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

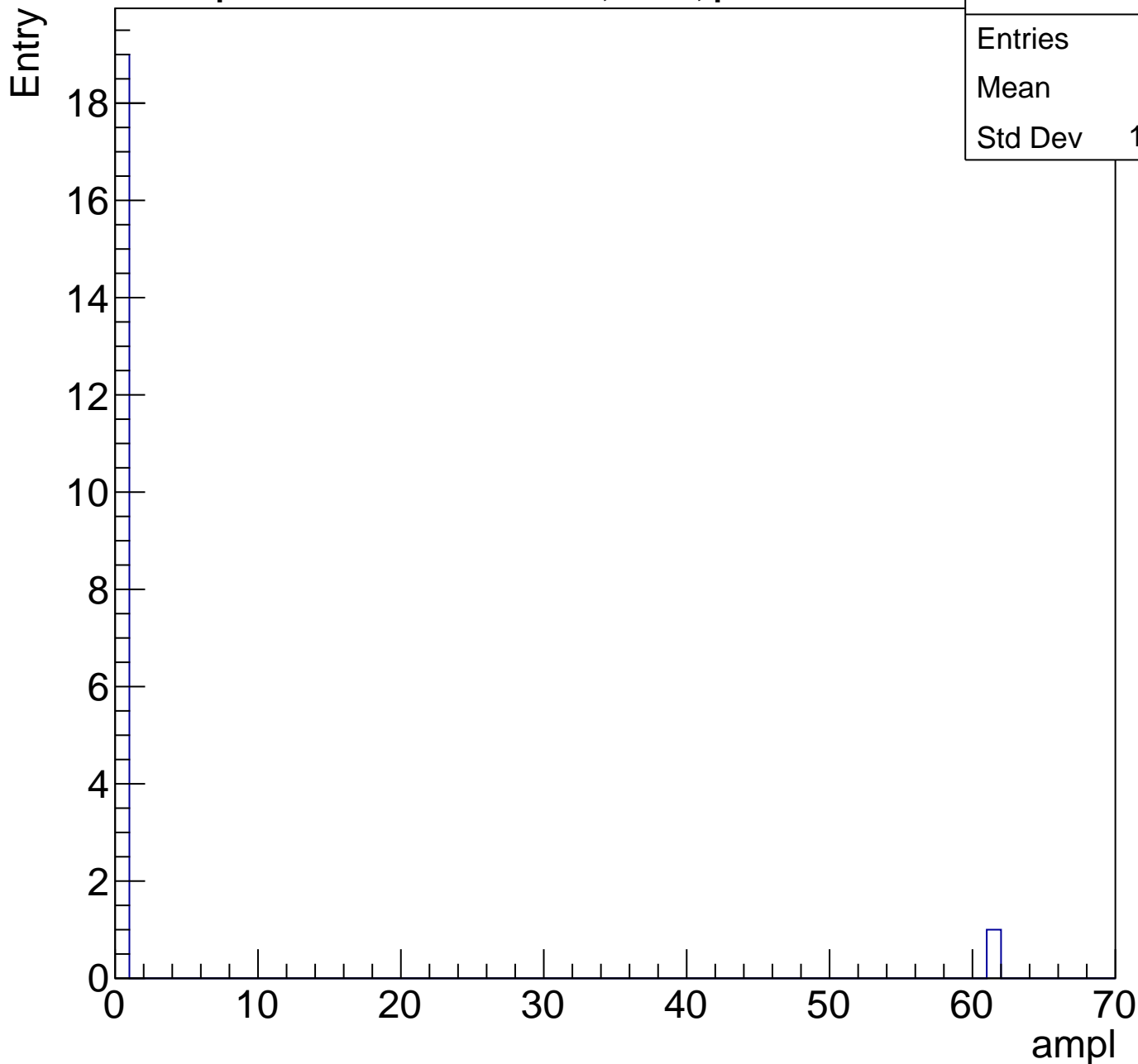
Entries	21
Mean	58.48
Std Dev	13.2



B1L103S, U24-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29



B1L103S, U24-ch77, adc0

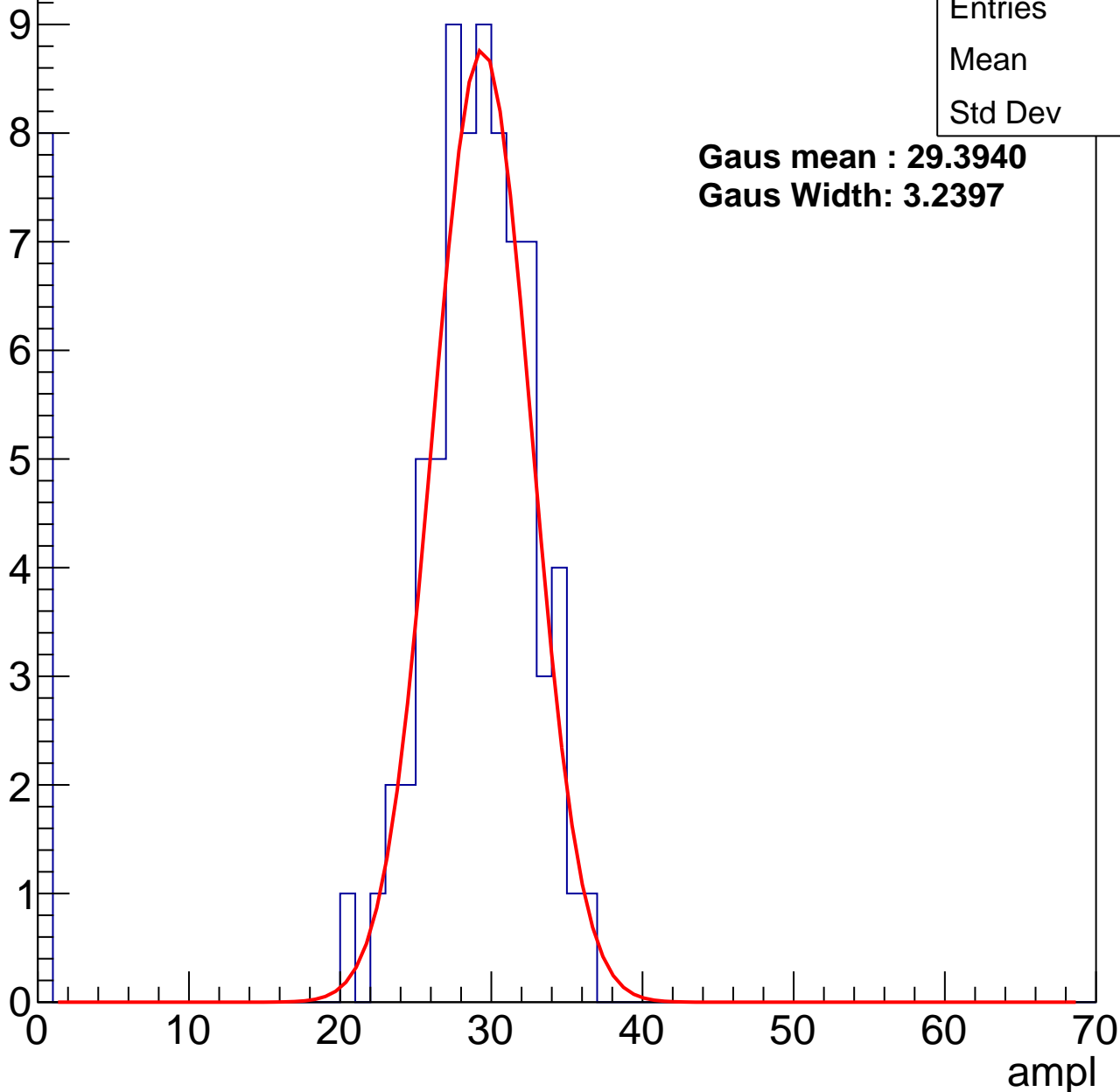
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	26
Std Dev	9.13

Gaus mean : 29.3940

Gaus Width: 3.2397



B1L103S, U24-ch77, adc1

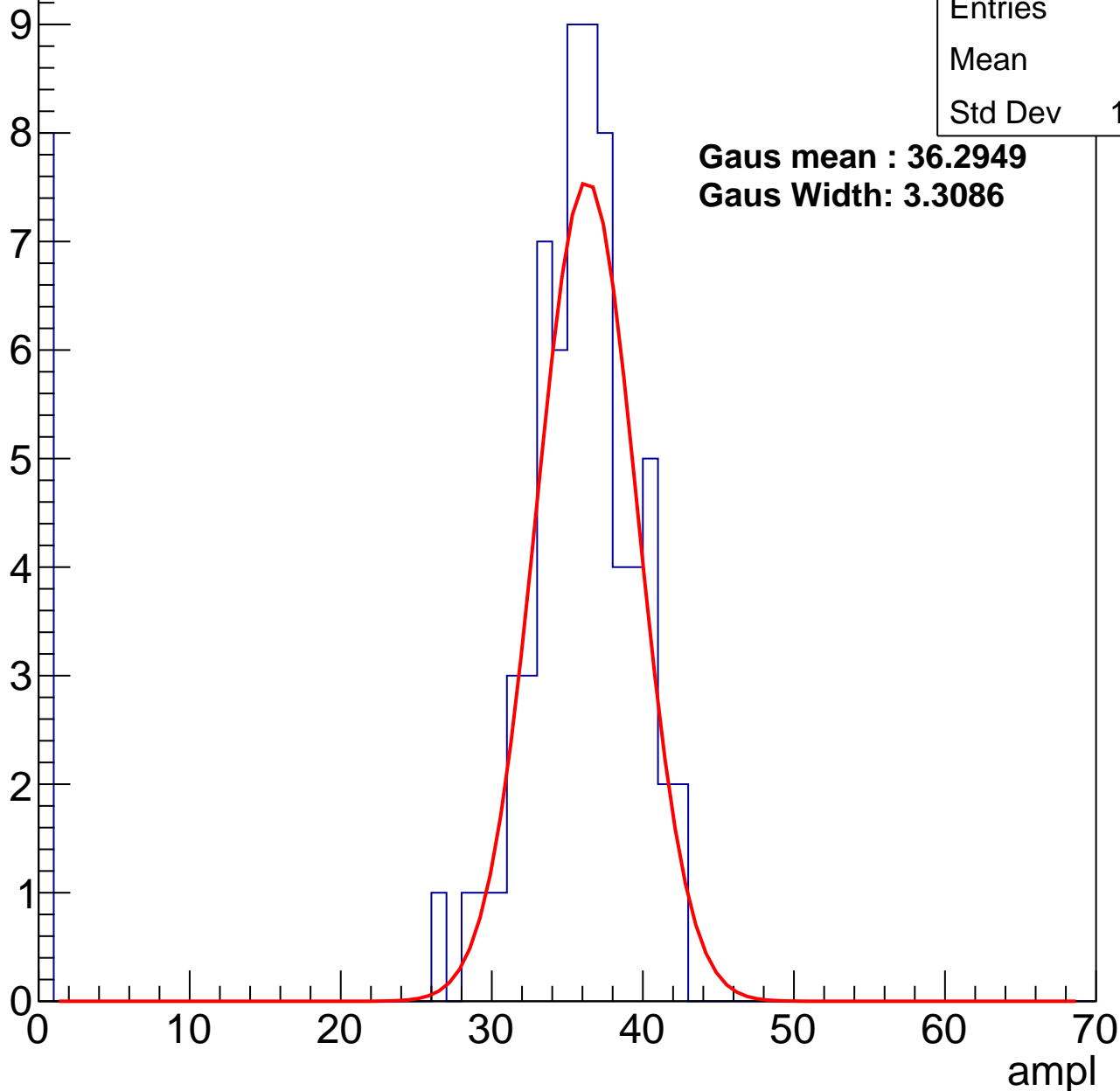
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	31.7
Std Dev	11.47

Gaus mean : 36.2949

Gaus Width: 3.3086



B1L103S, U24-ch77, adc2

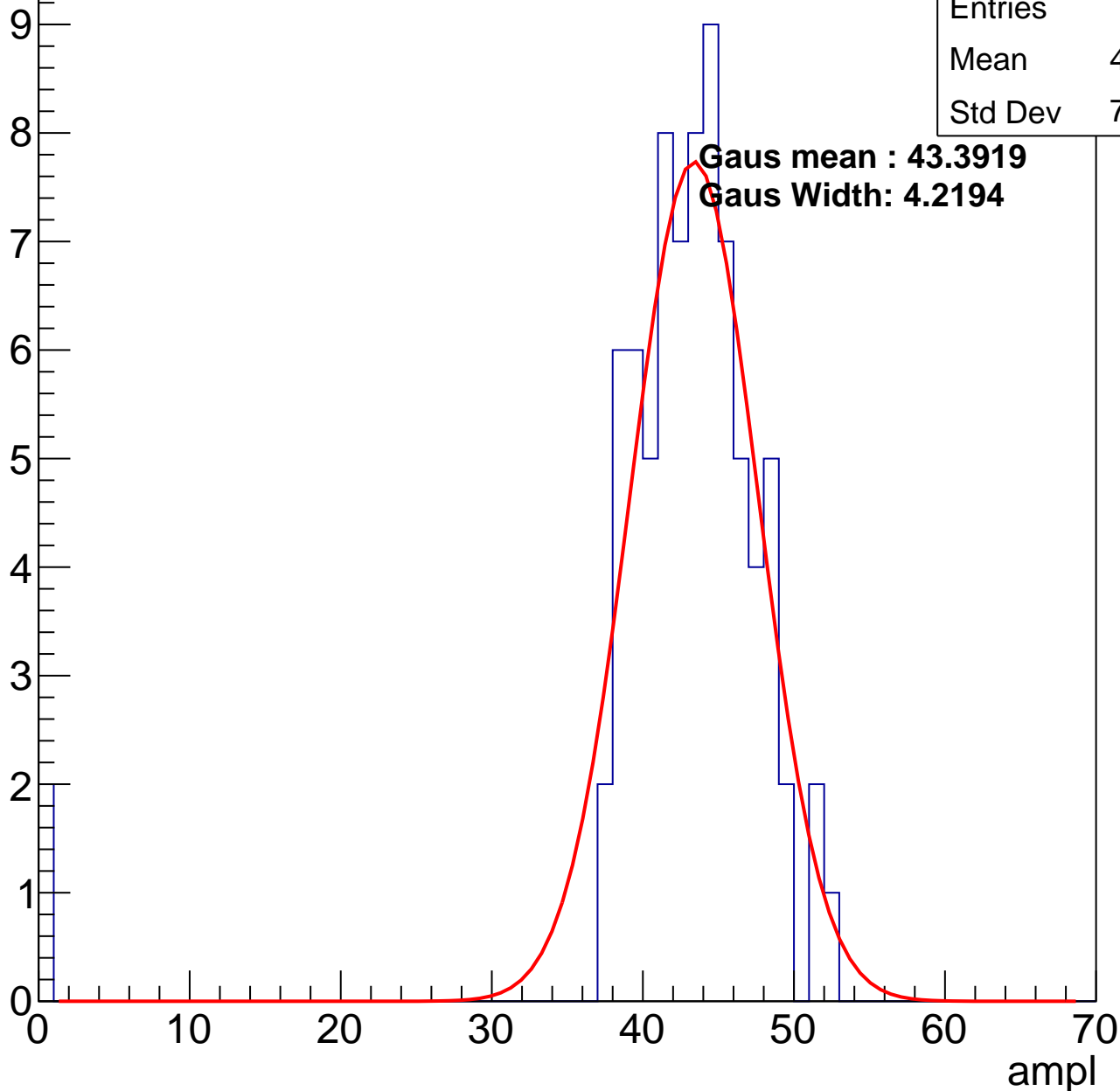
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	42.06
Std Dev	7.616

Gaus mean : 43.3919

Gaus Width: 4.2194

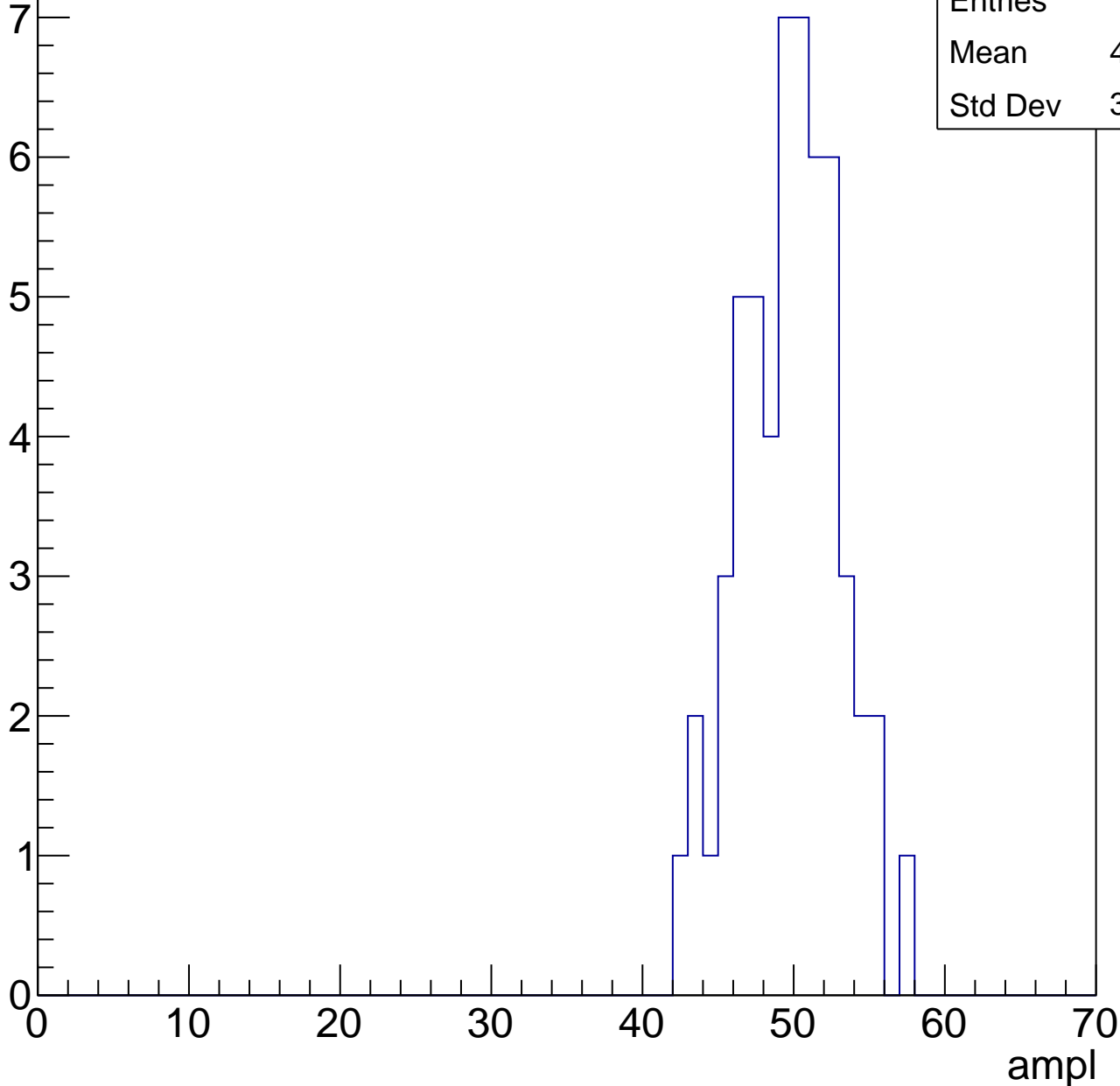


B1L103S, U24-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	49.25
Std Dev	3.243

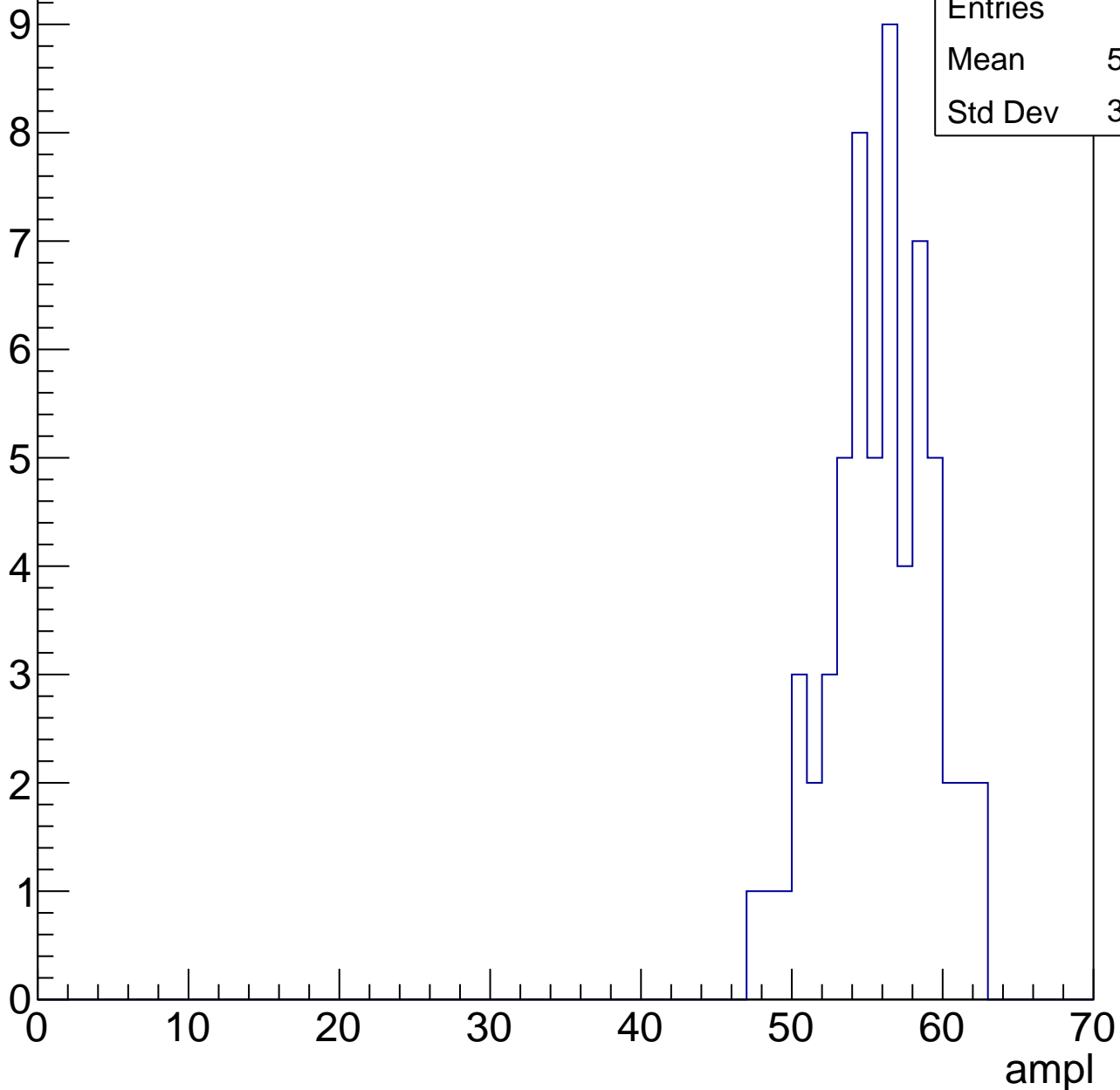


B1L103S, U24-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.38
Std Dev	3.392

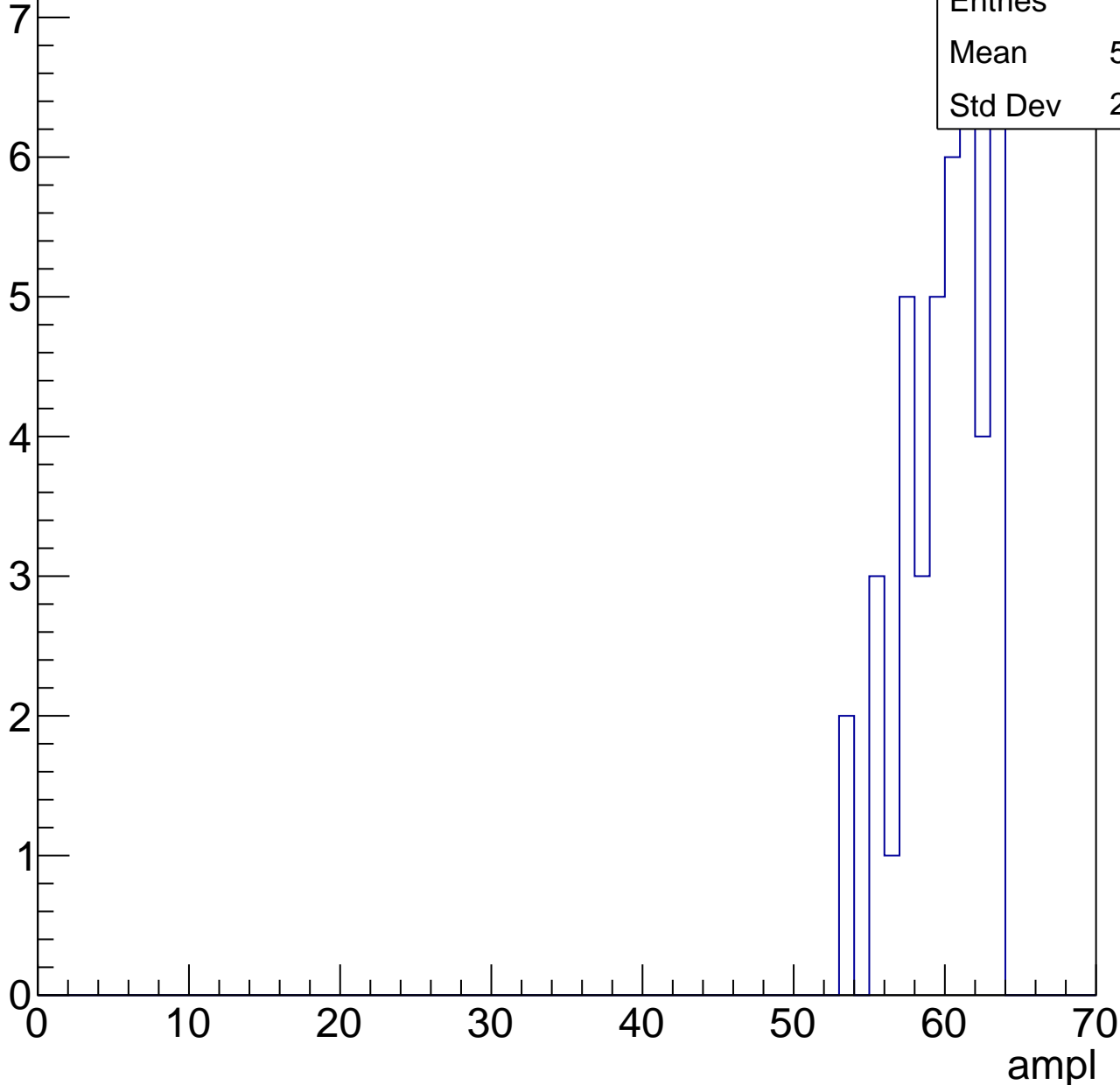


B1L103S, U24-ch77, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

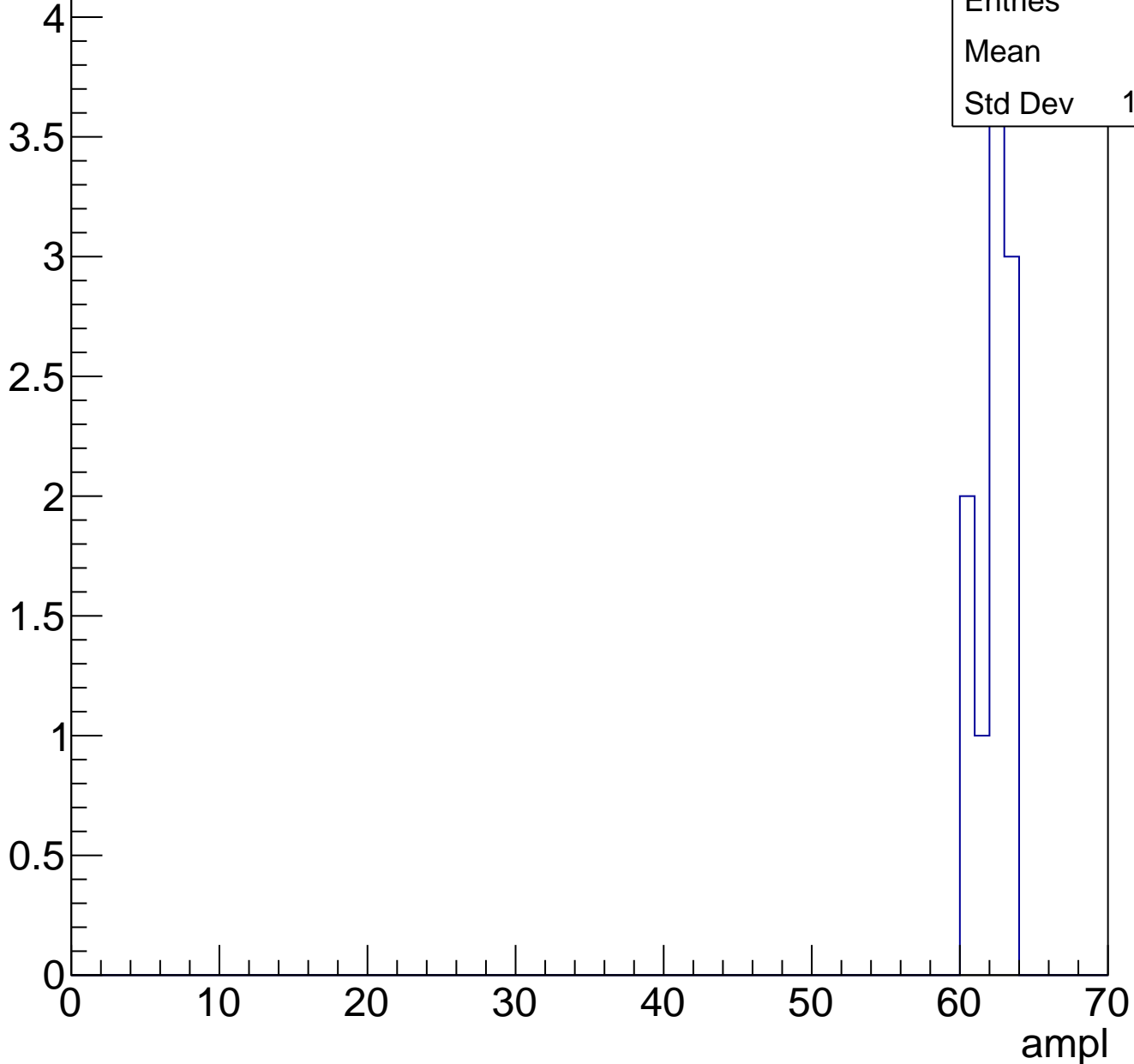
Entries	43
Mean	59.47
Std Dev	2.748



B1L103S, U24-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

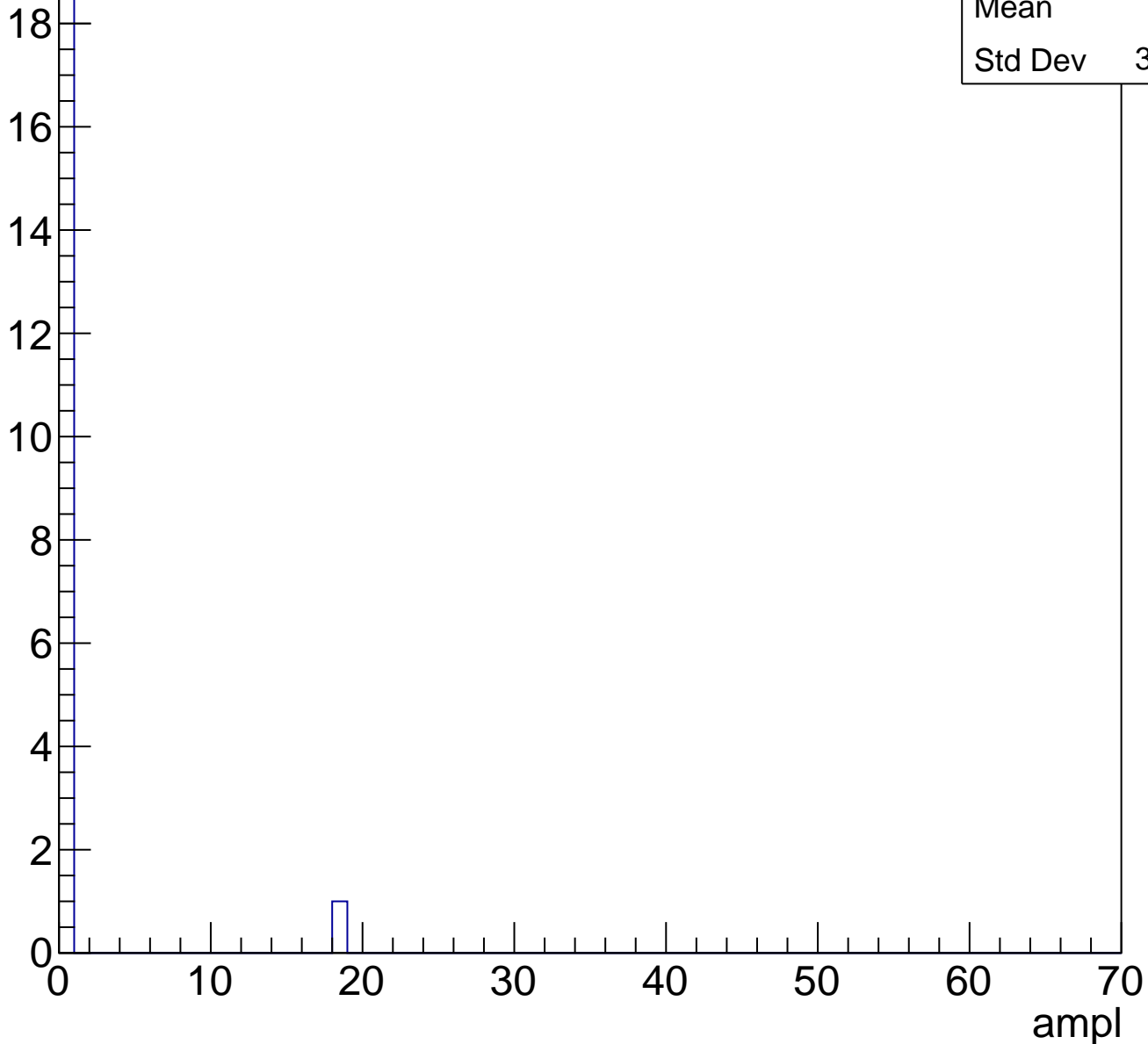


B1L103S, U24-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	0.9
Std Dev	3.923

Entry



B1L103S, U24-ch78, adc0

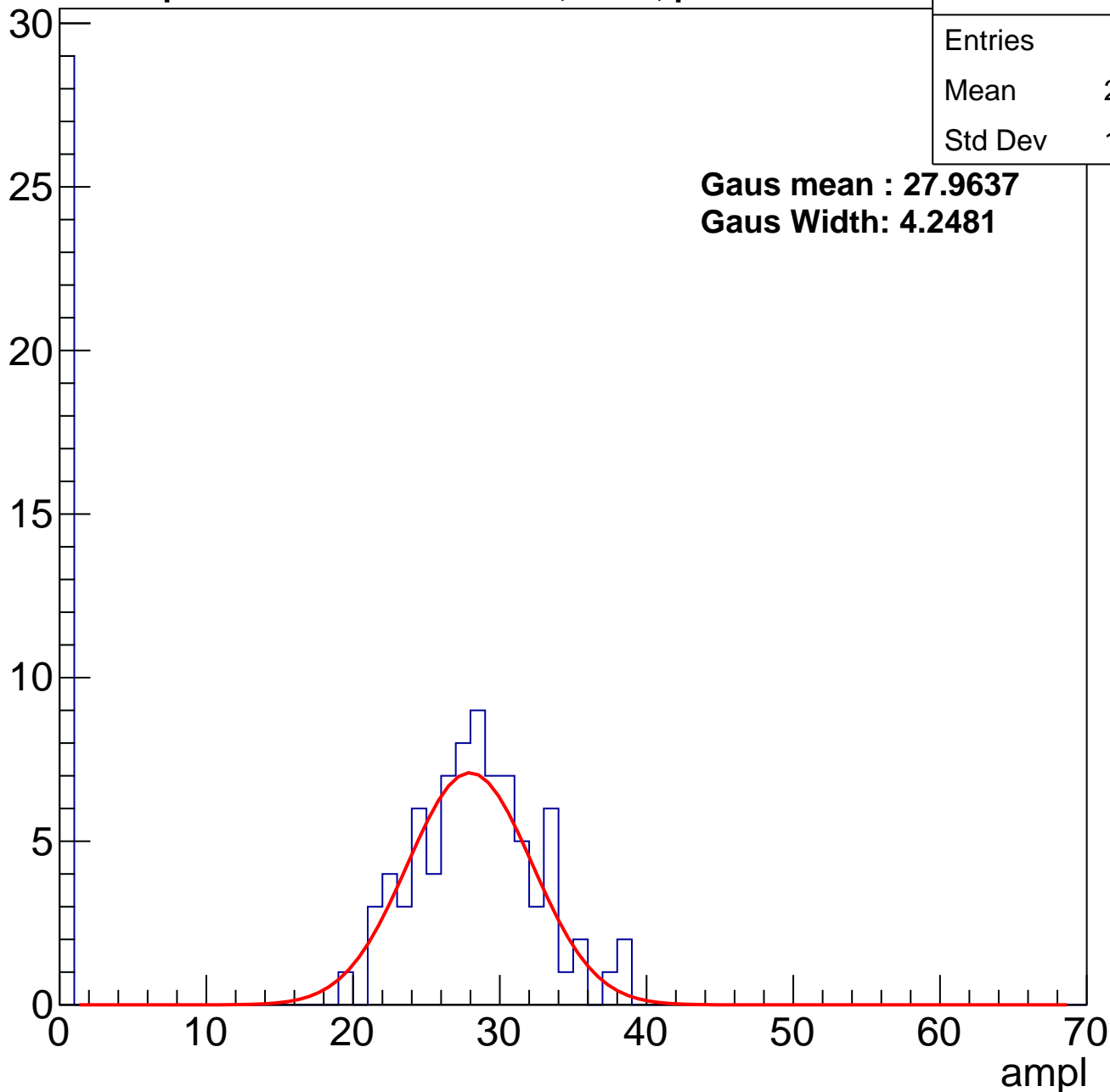
calib_packv5_041523_1651.root, FC#0, port C2

Entries	108
Mean	20.48
Std Dev	12.89

Gaus mean : 27.9637

Gaus Width: 4.2481

Entry



B1L103S, U24-ch78, adc1

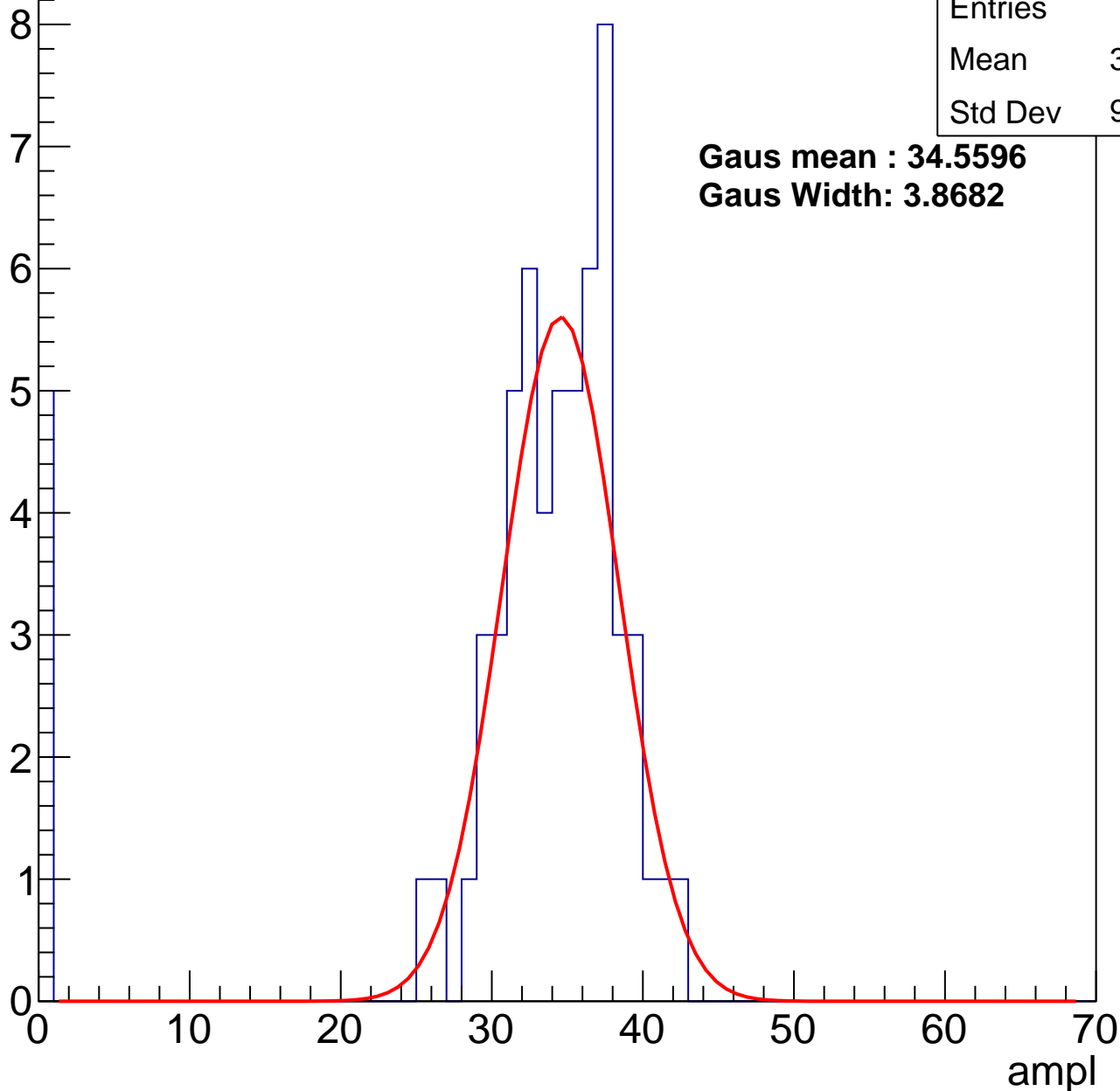
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	31.39
Std Dev	9.924

Gaus mean : 34.5596

Gaus Width: 3.8682



B1L103S, U24-ch78, adc2

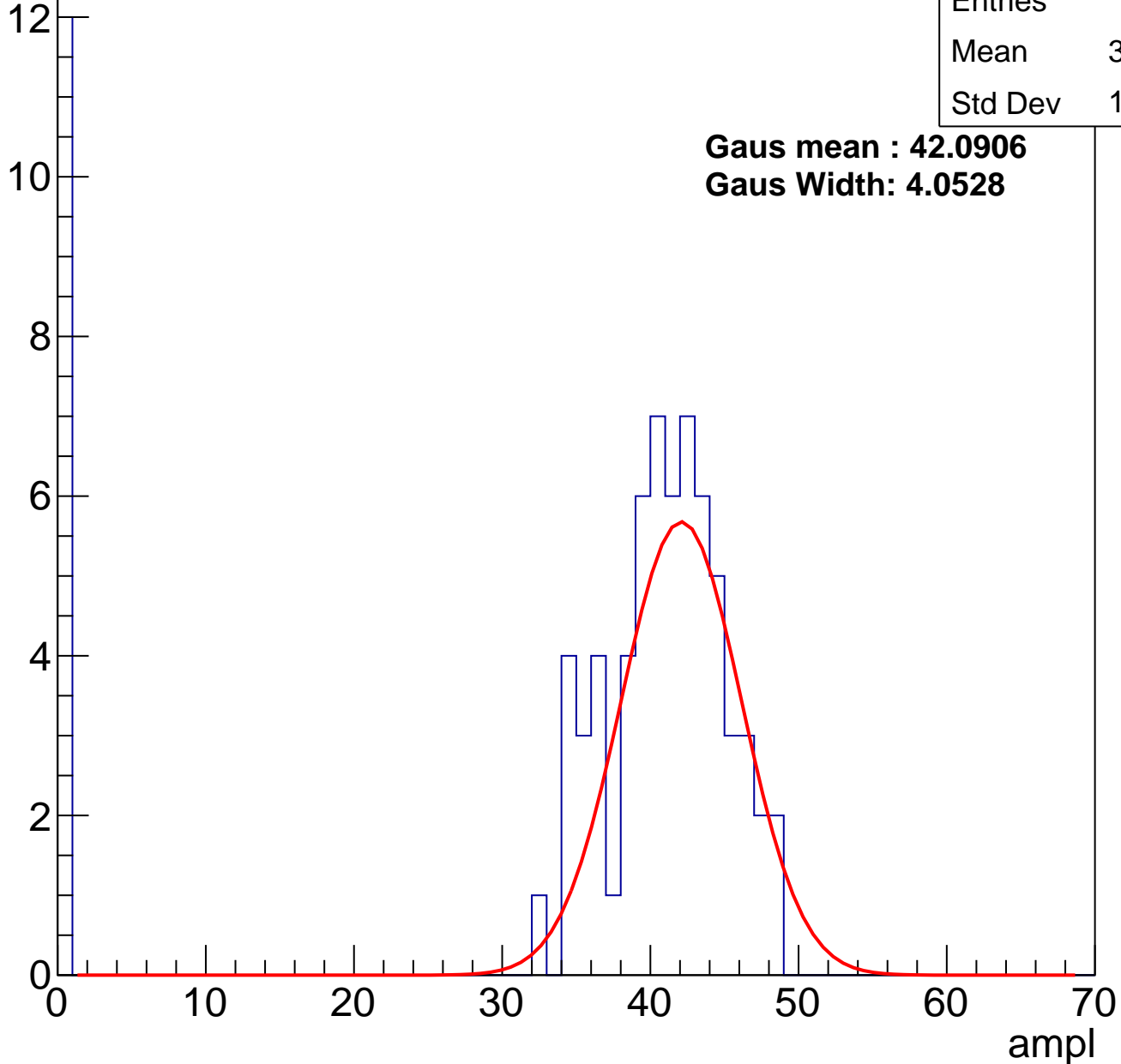
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	34.22
Std Dev	15.23

Gaus mean : 42.0906

Gaus Width: 4.0528

Entry

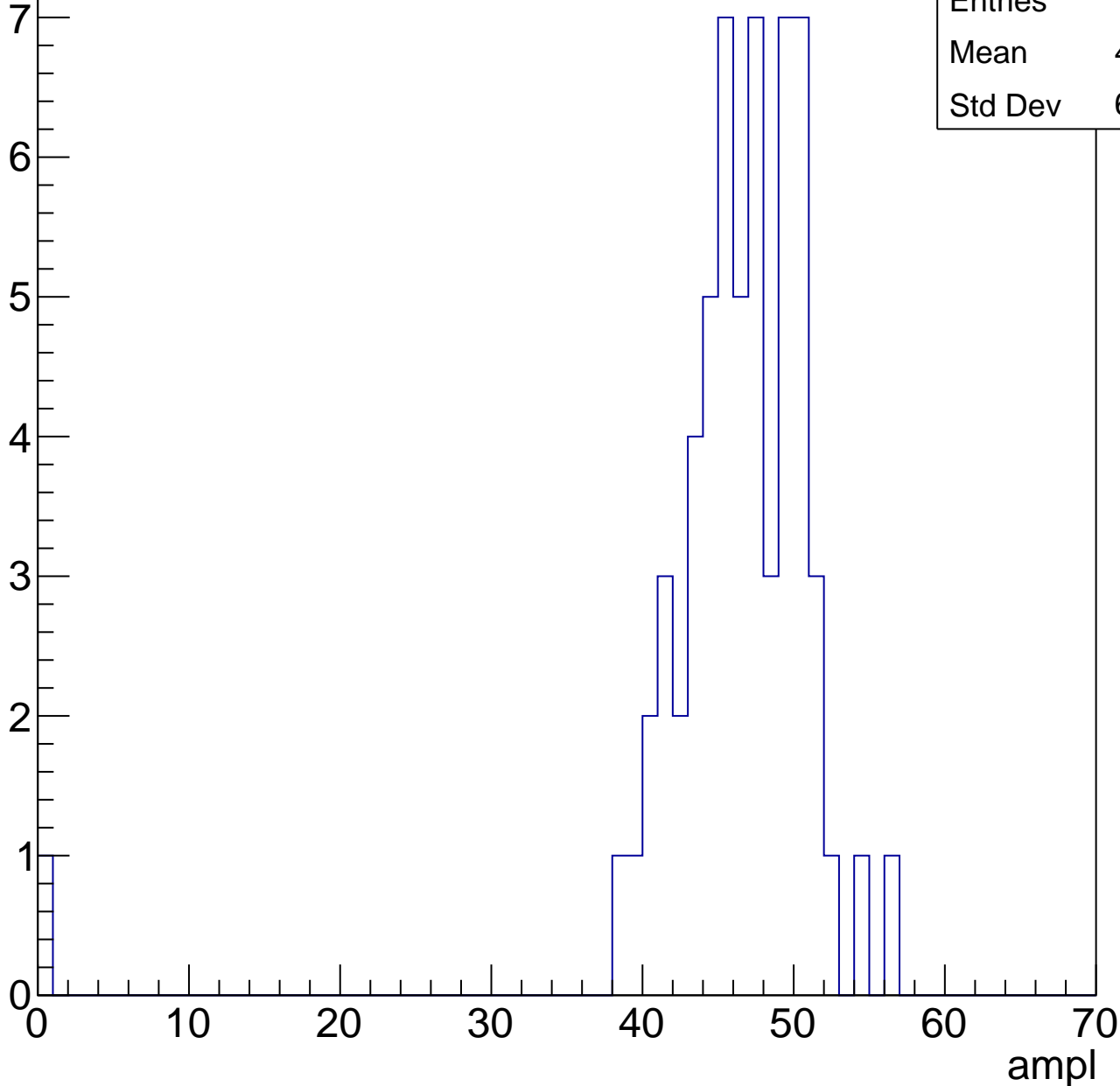


B1L103S, U24-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	45.61
Std Dev	6.931

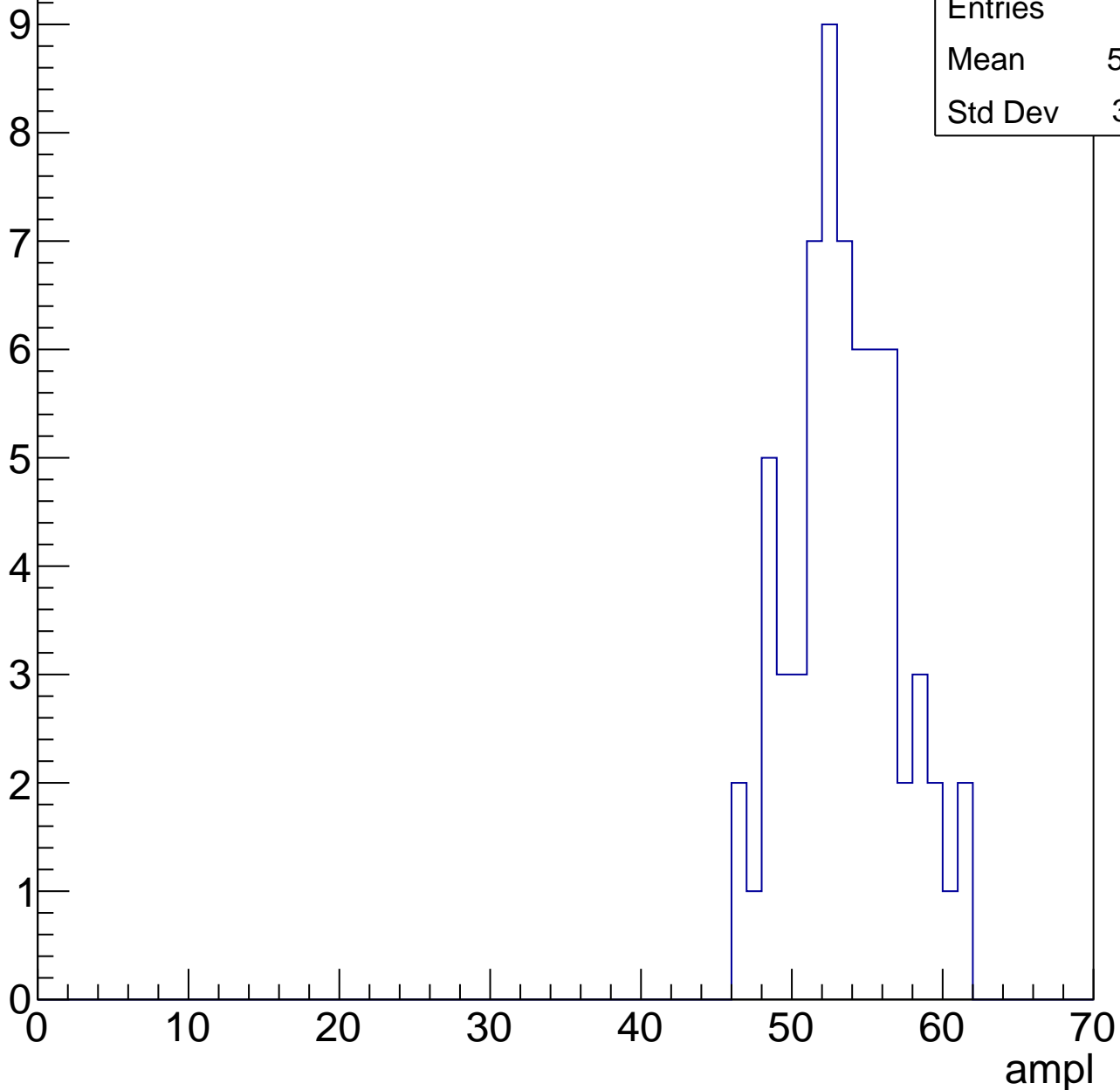


B1L103S, U24-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

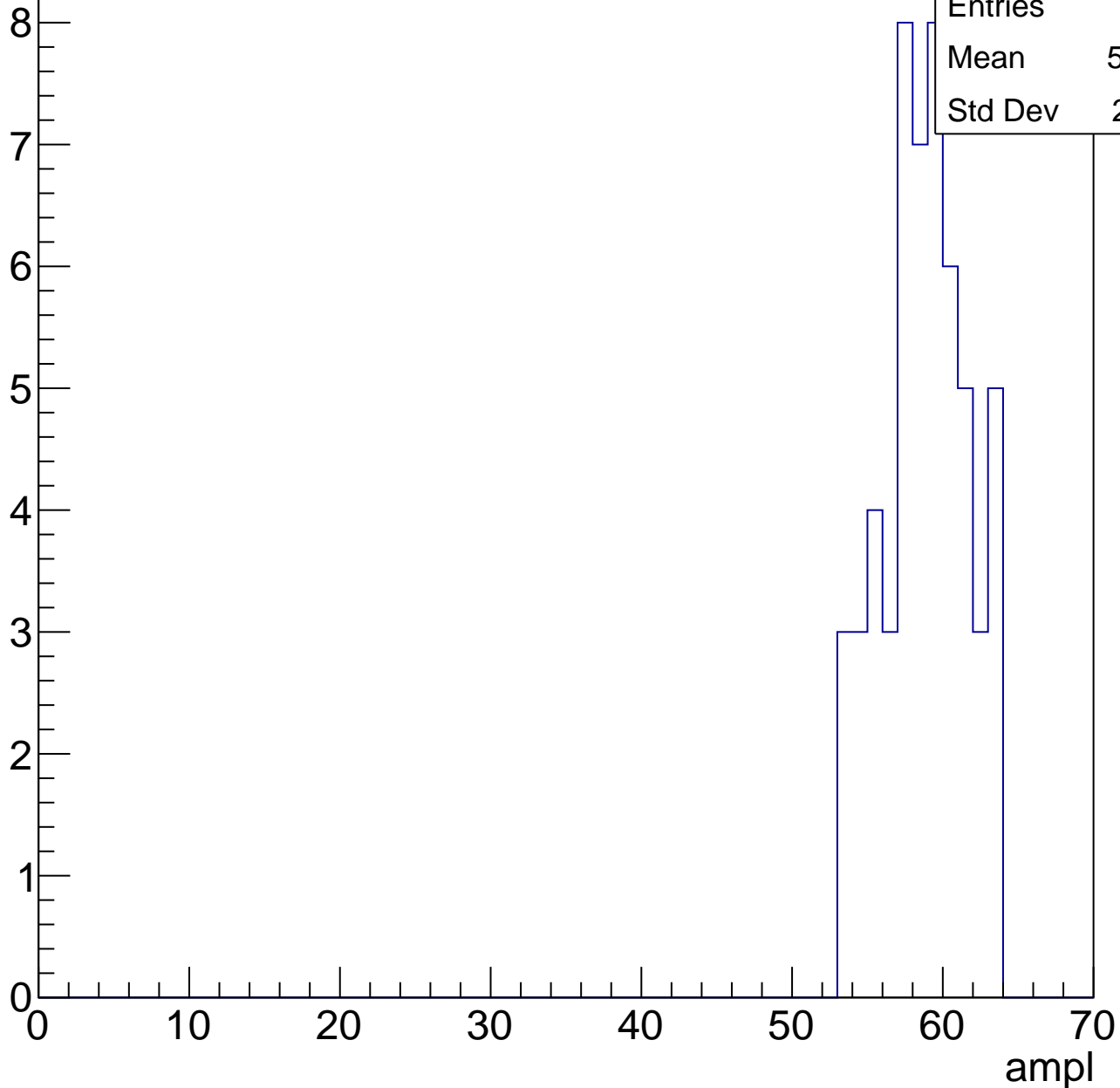
Entries	65
Mean	53.08
Std Dev	3.531



B1L103S, U24-ch78, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

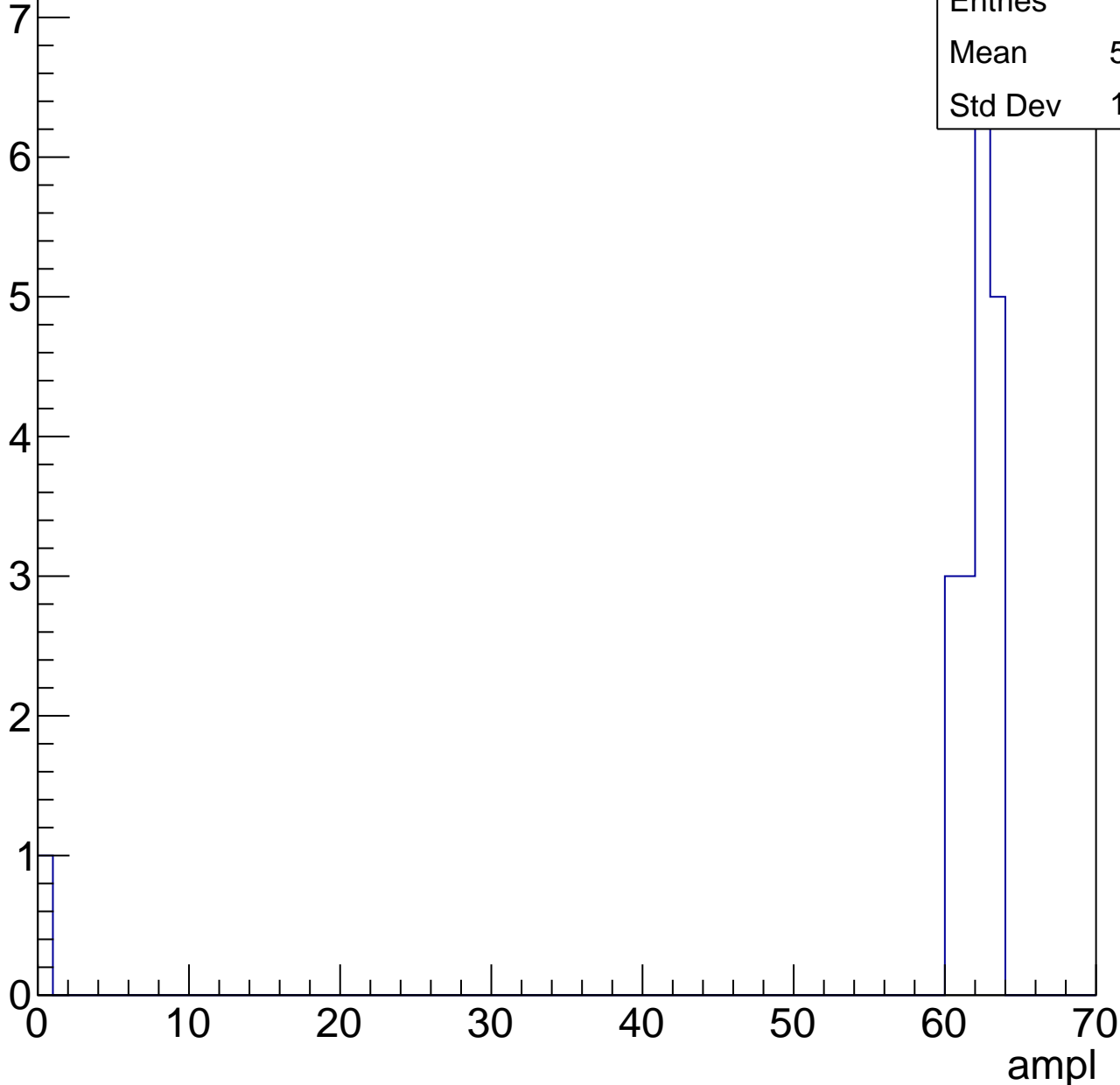


B1L103S, U24-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	58.53
Std Dev	13.83



B1L103S, U24-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch79, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	24.82
Std Dev	9.967

Gaus mean : 28.7294

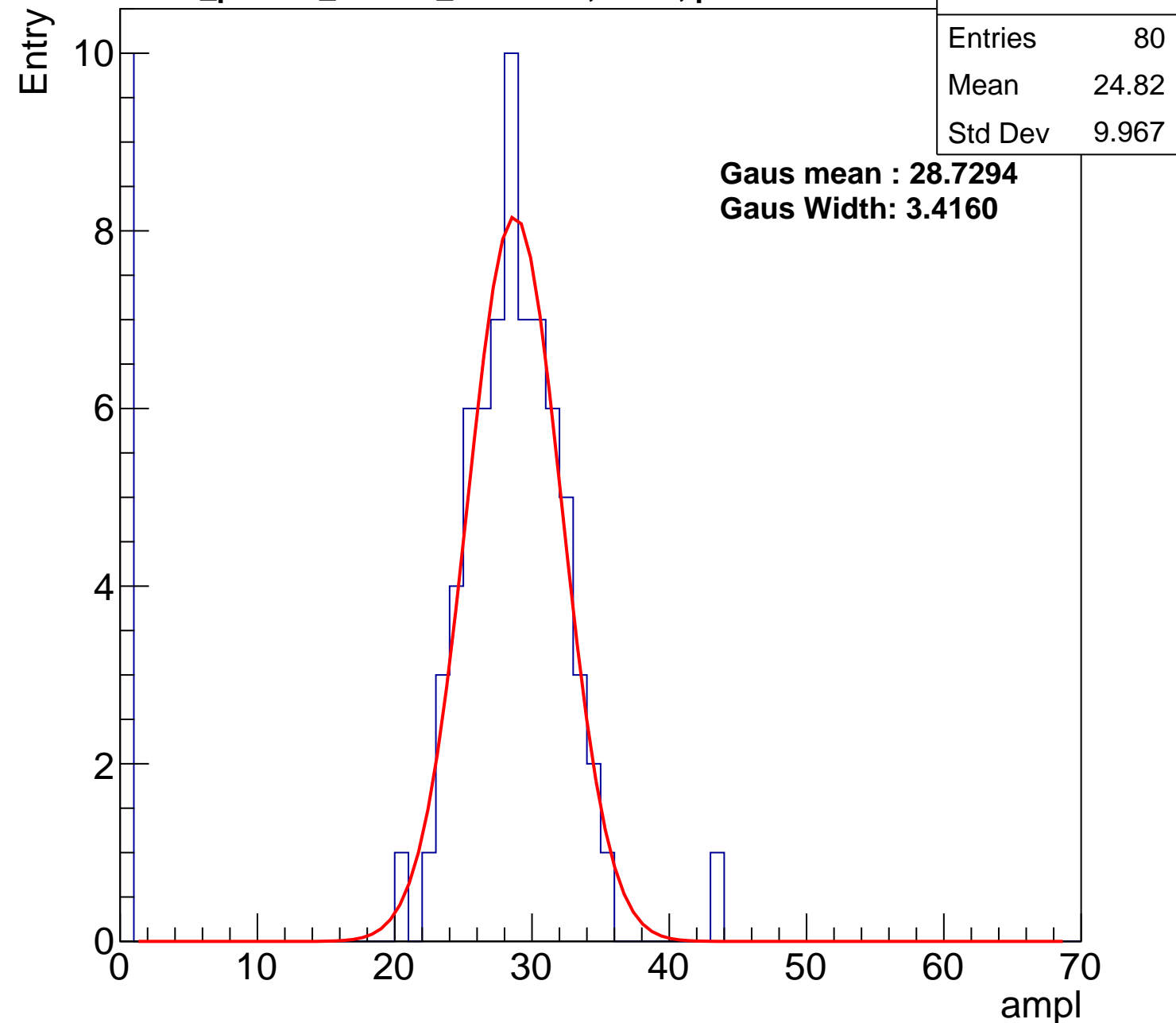
Gaus Width: 3.4160

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch79, adc1

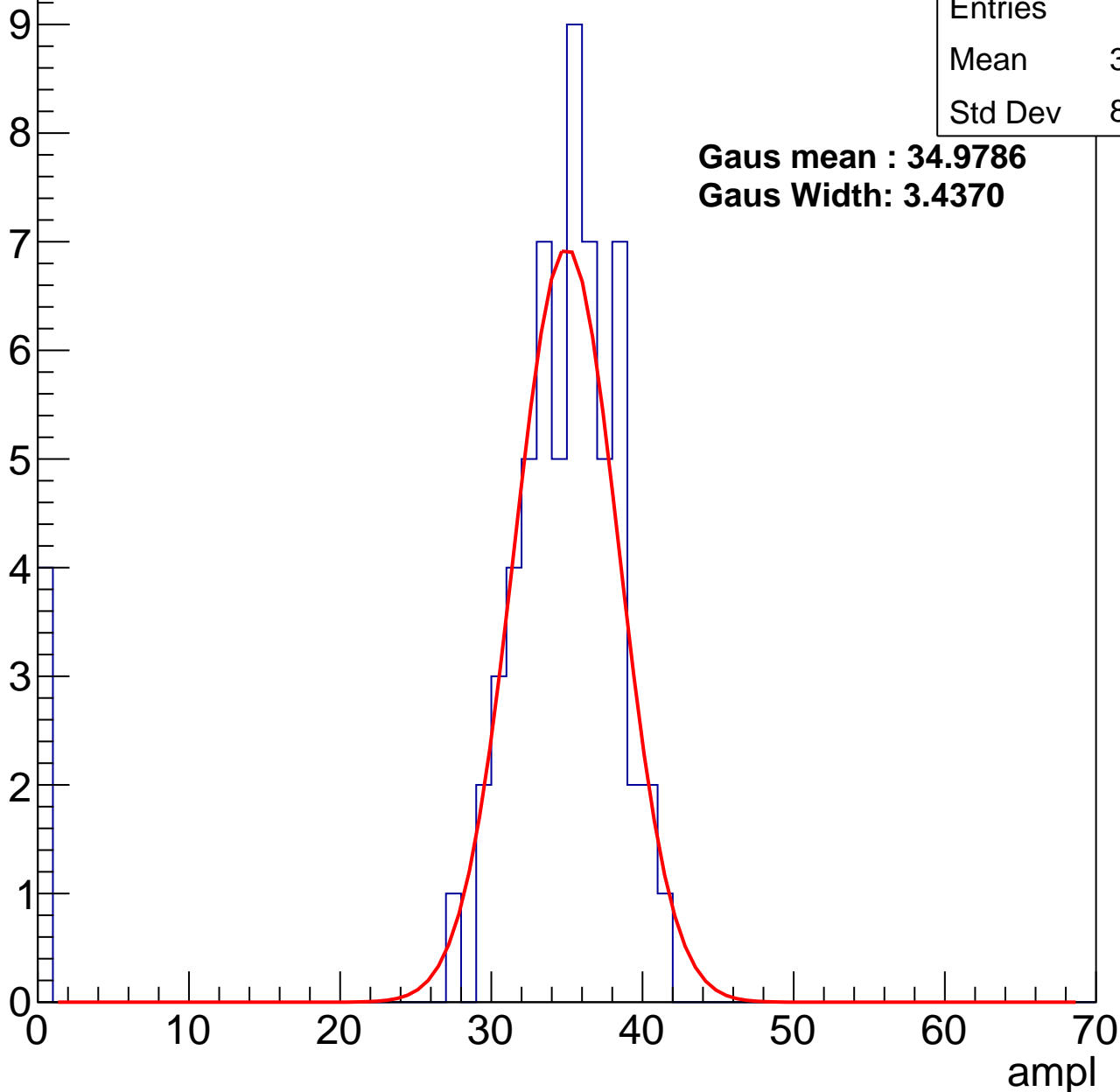
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	32.45
Std Dev	8.879

Gaus mean : 34.9786

Gaus Width: 3.4370



B1L103S, U24-ch79, adc2

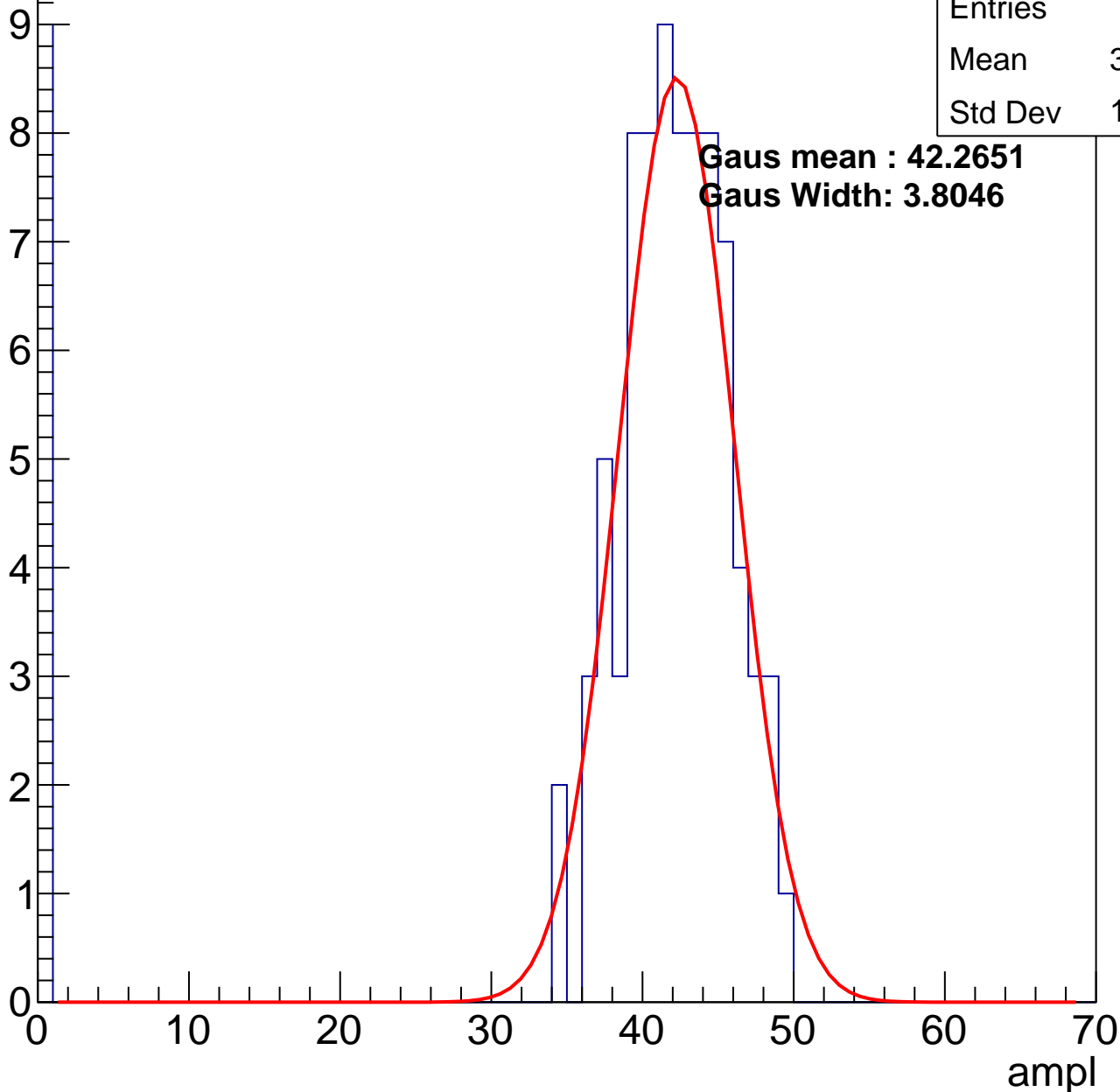
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	37.54
Std Dev	12.99

Gaus mean : 42.2651

Gaus Width: 3.8046

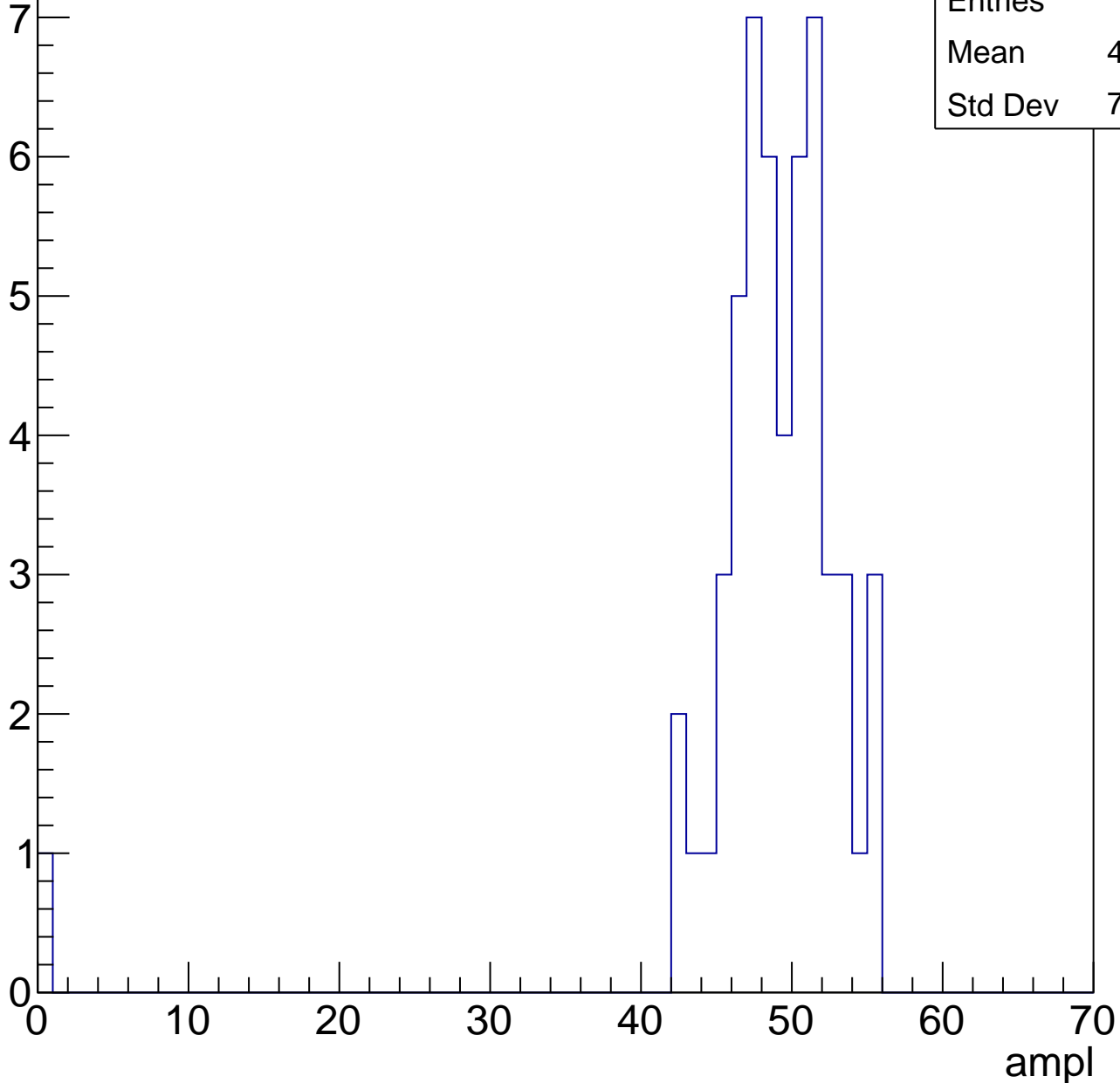


B1L103S, U24-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	47.92
Std Dev	7.356

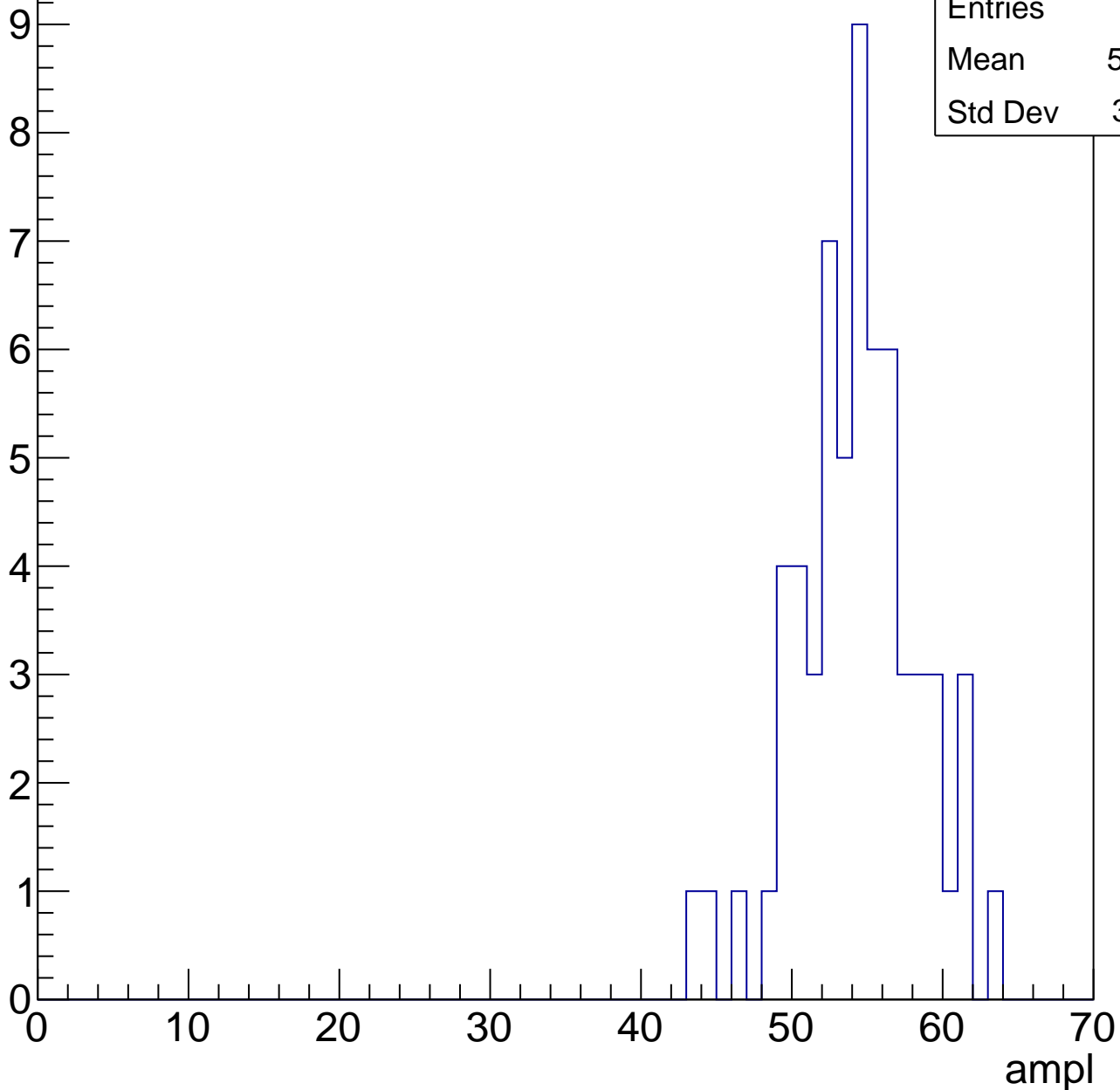


B1L103S, U24-ch79, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	53.85
Std Dev	3.991

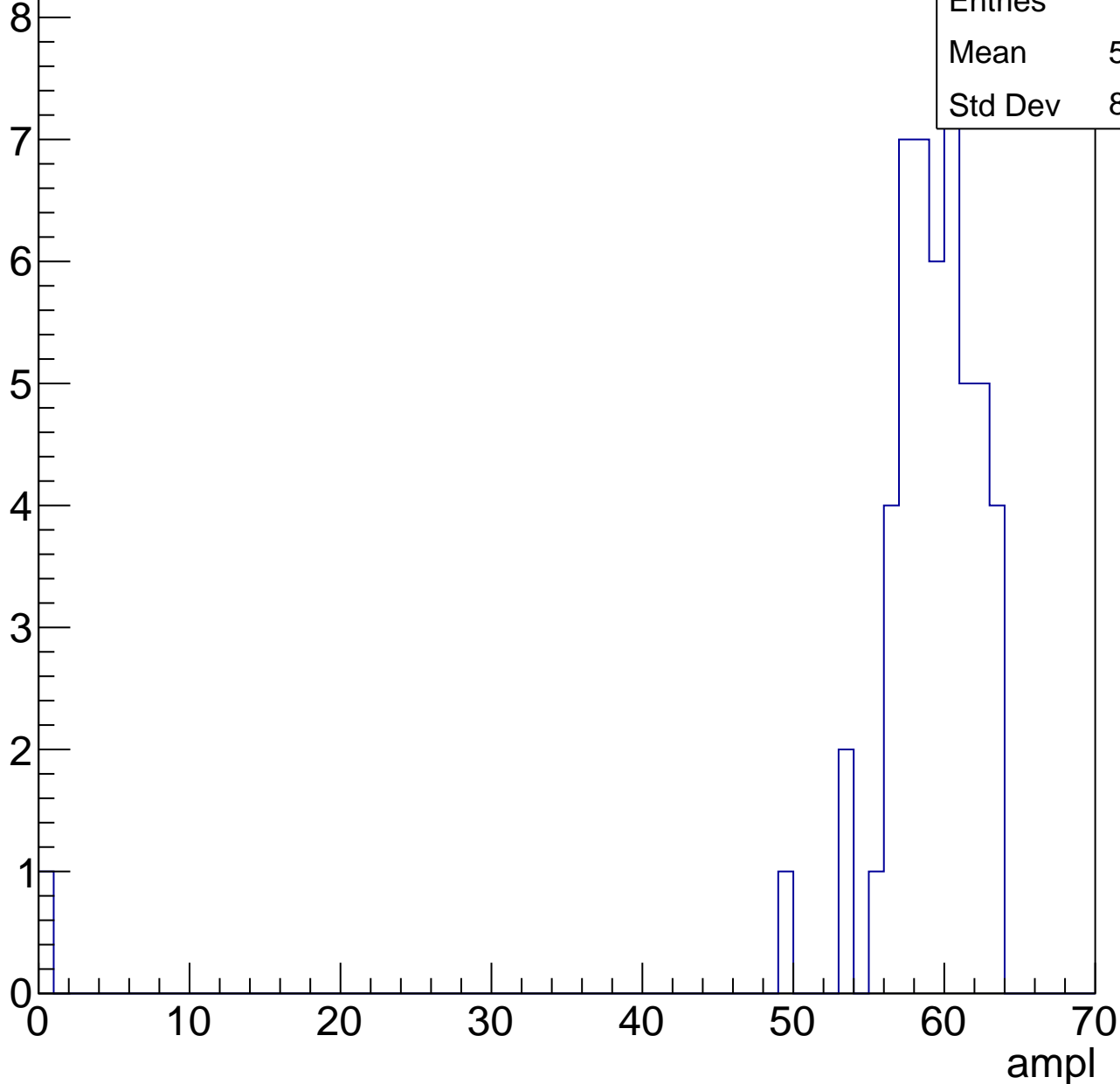


B1L103S, U24-ch79, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.65
Std Dev	8.613

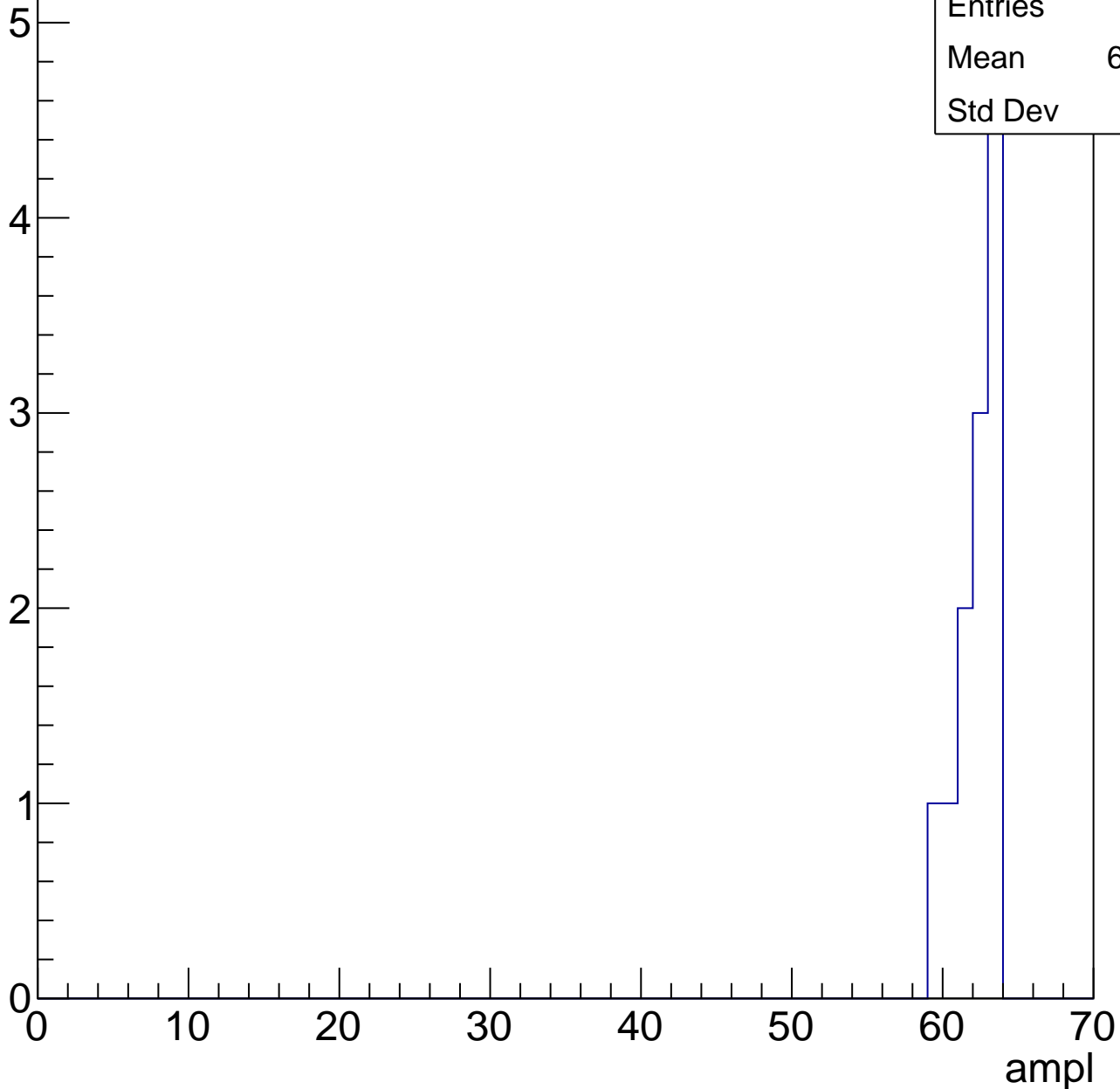


B1L103S, U24-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.83
Std Dev	1.28

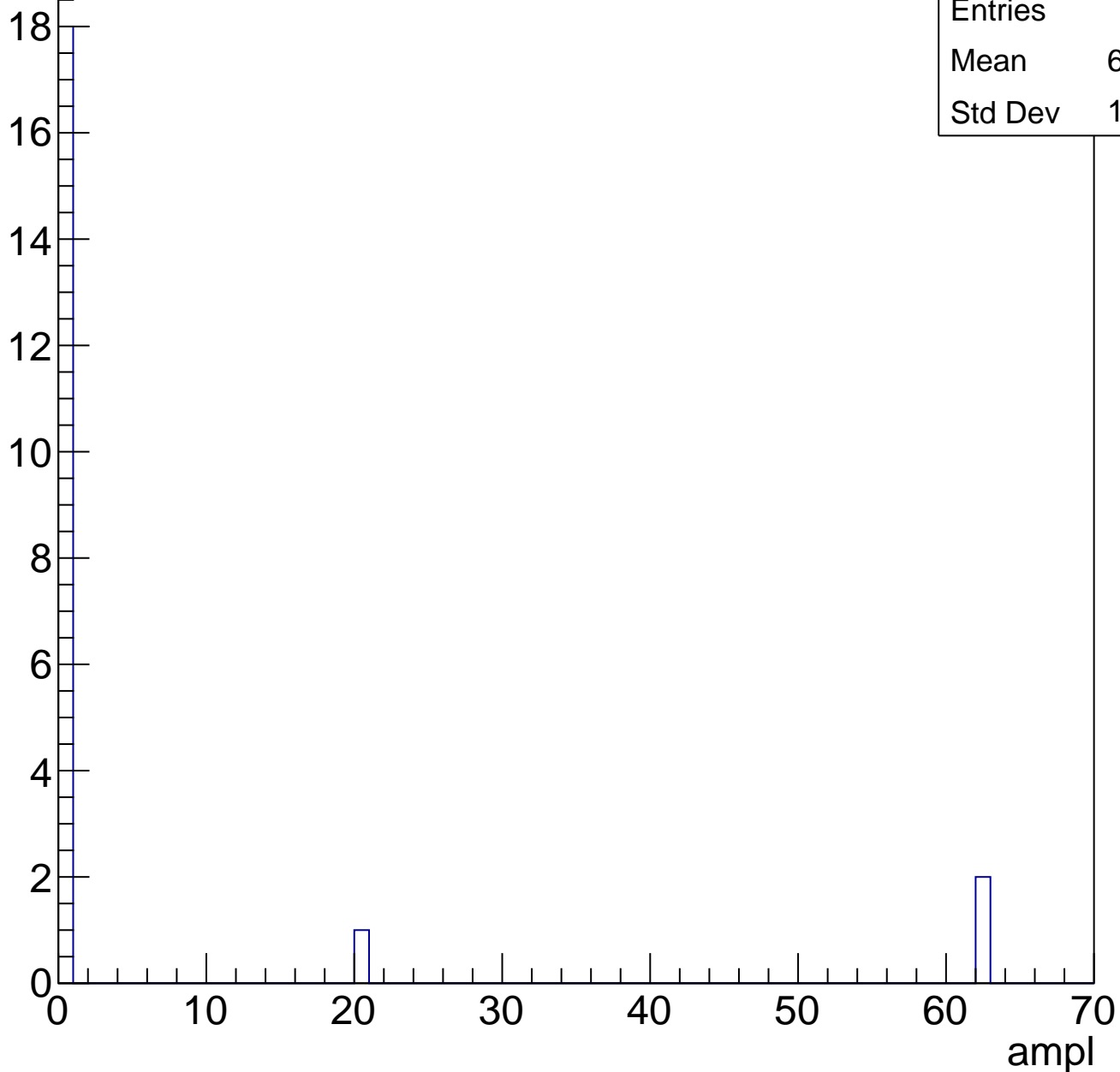


B1L103S, U24-ch79, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6.857
Std Dev	18.39

Entry



B1L103S, U24-ch80, adc0

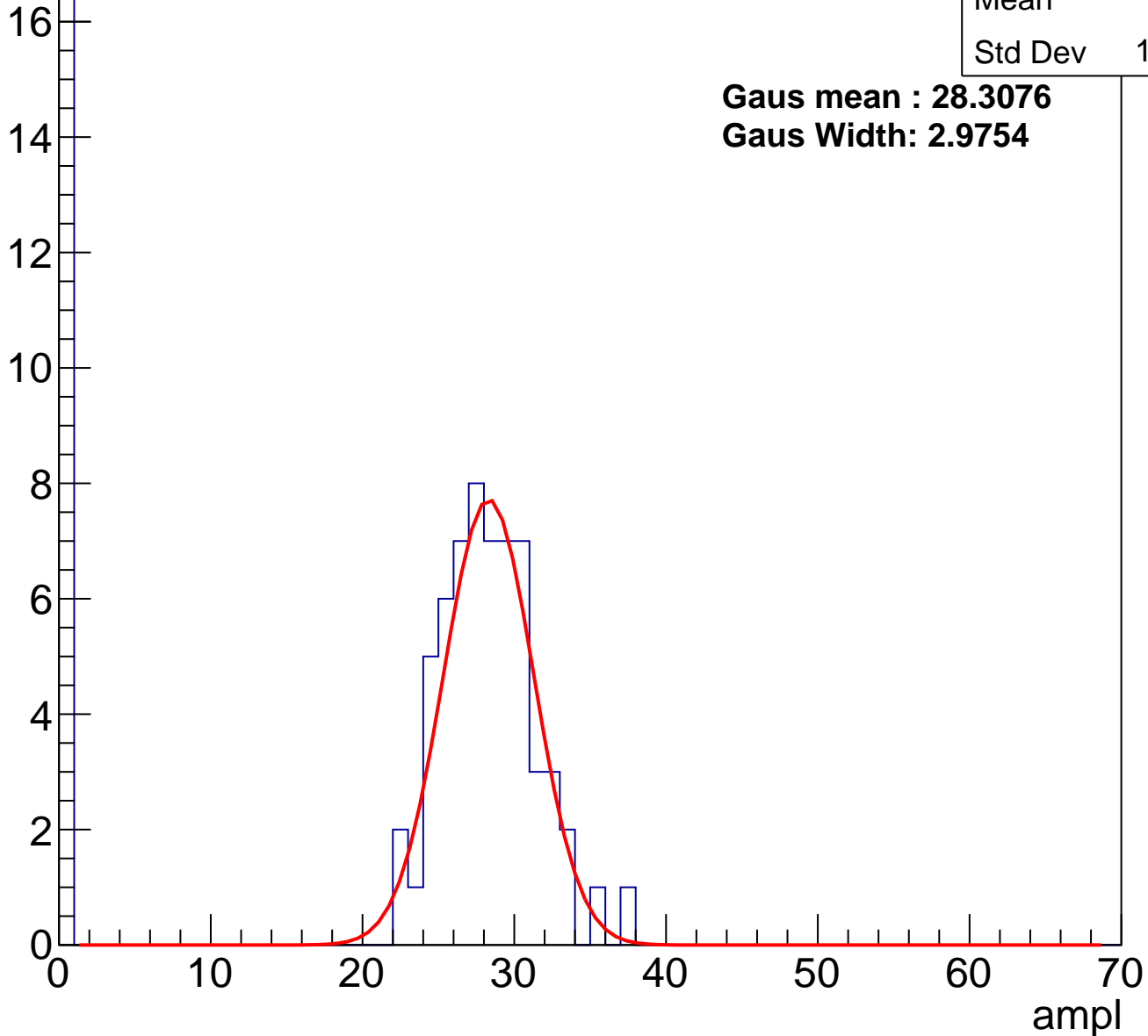
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	21.7
Std Dev	11.86

Gaus mean : 28.3076

Gaus Width: 2.9754

Entry



B1L103S, U24-ch80, adc1

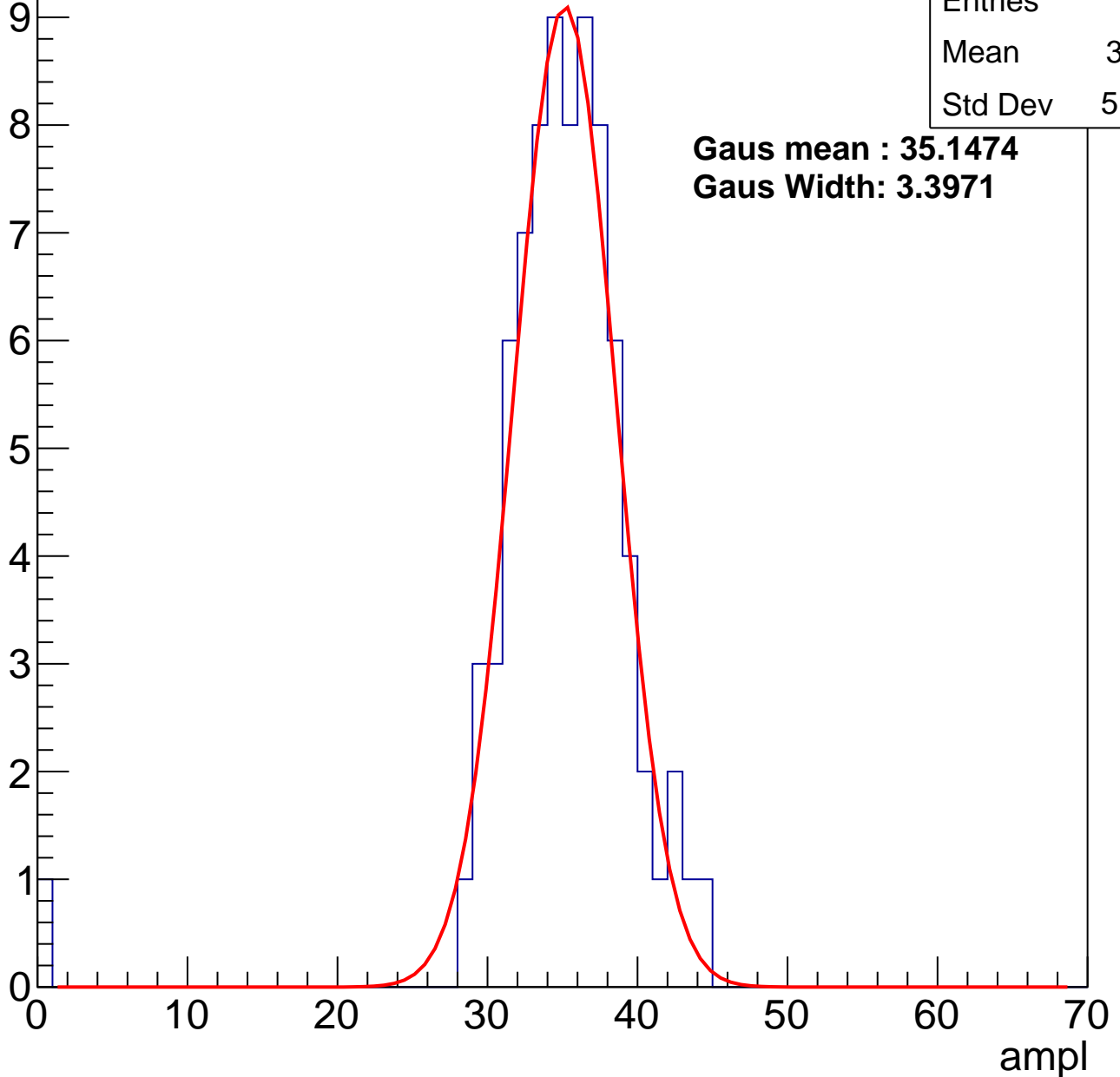
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	34.51
Std Dev	5.155

Gaus mean : 35.1474

Gaus Width: 3.3971



B1L103S, U24-ch80, adc2

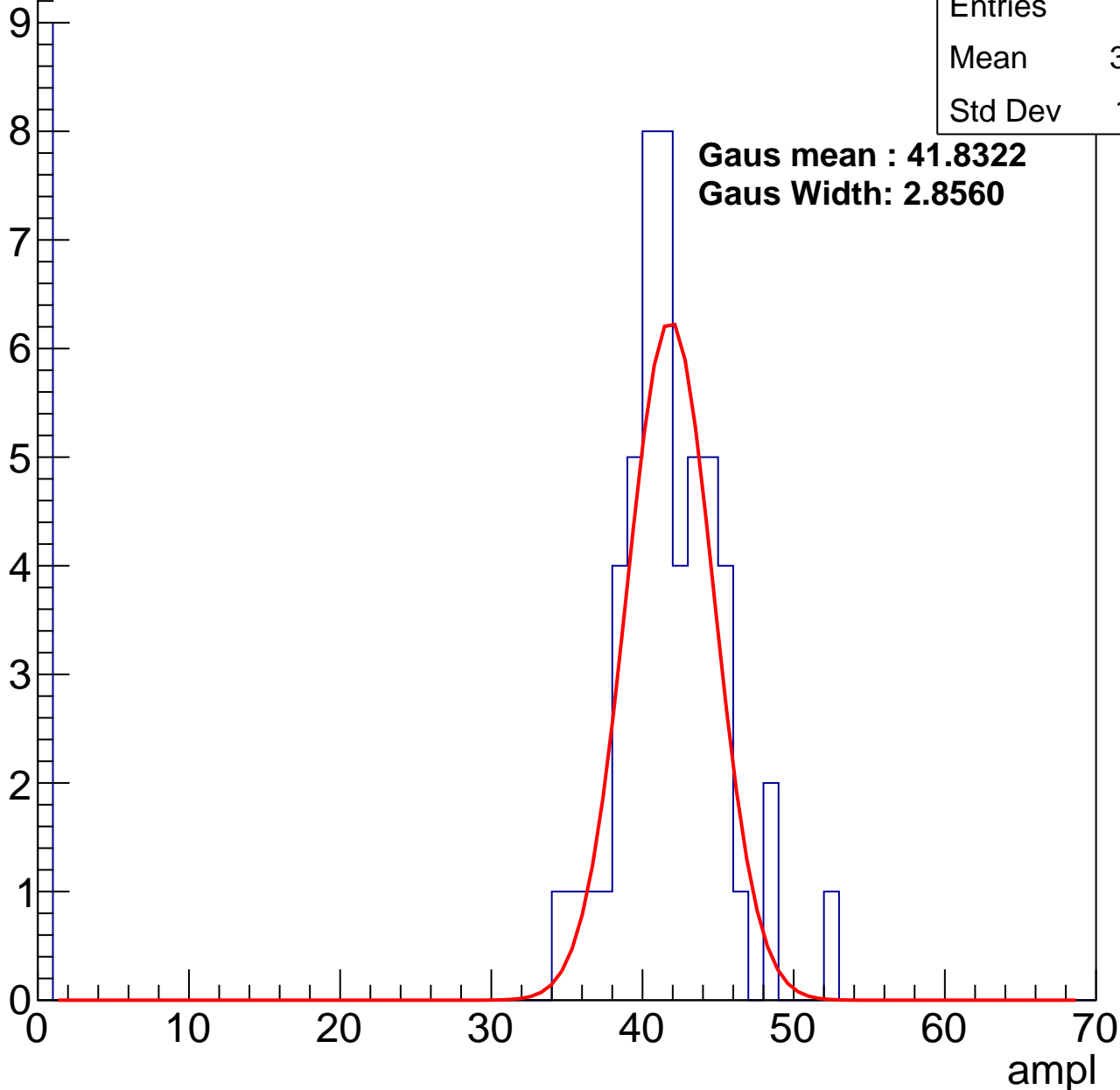
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	35.23
Std Dev	15.11

Gaus mean : 41.8322

Gaus Width: 2.8560

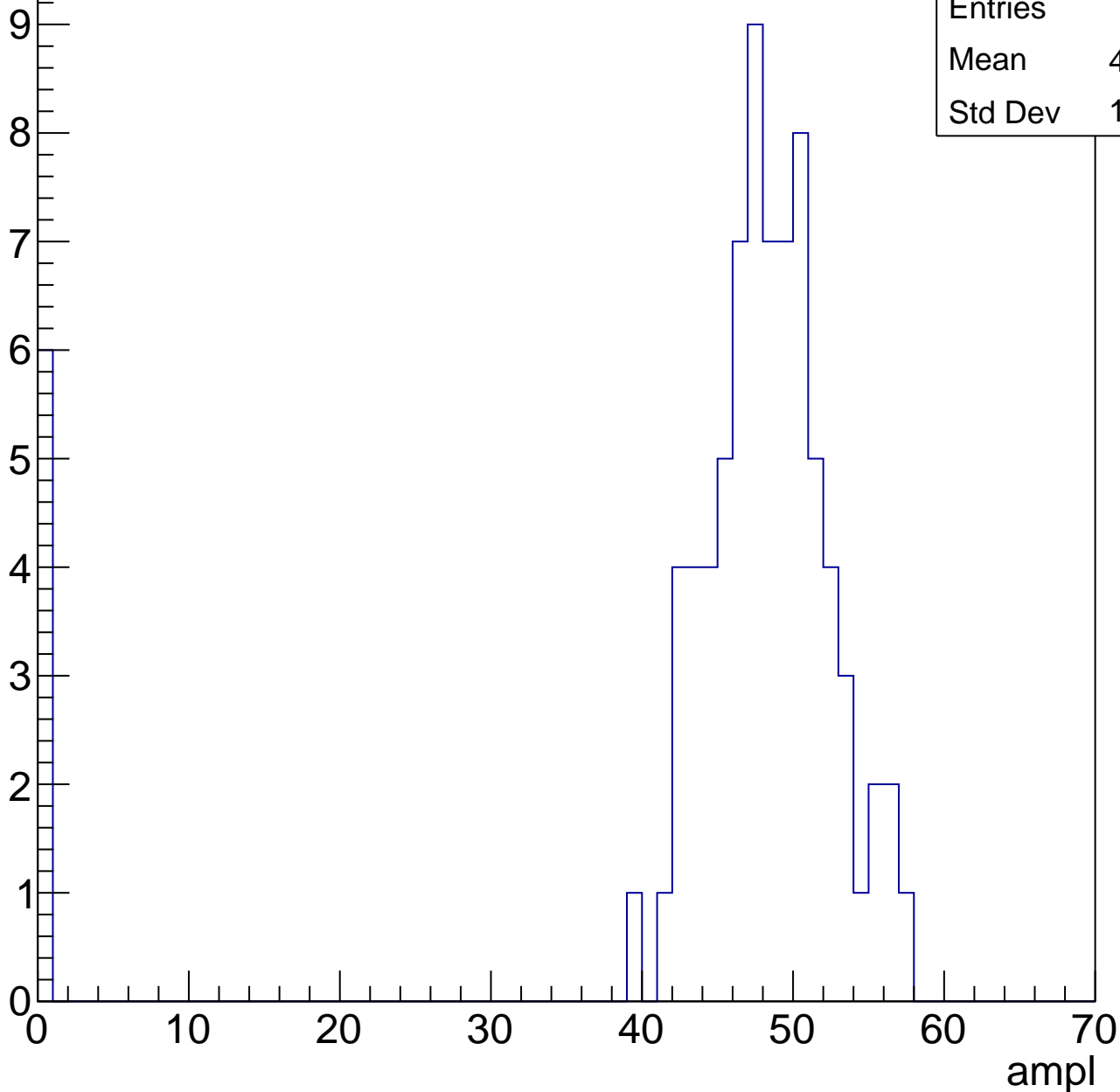


B1L103S, U24-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	44.44
Std Dev	13.09

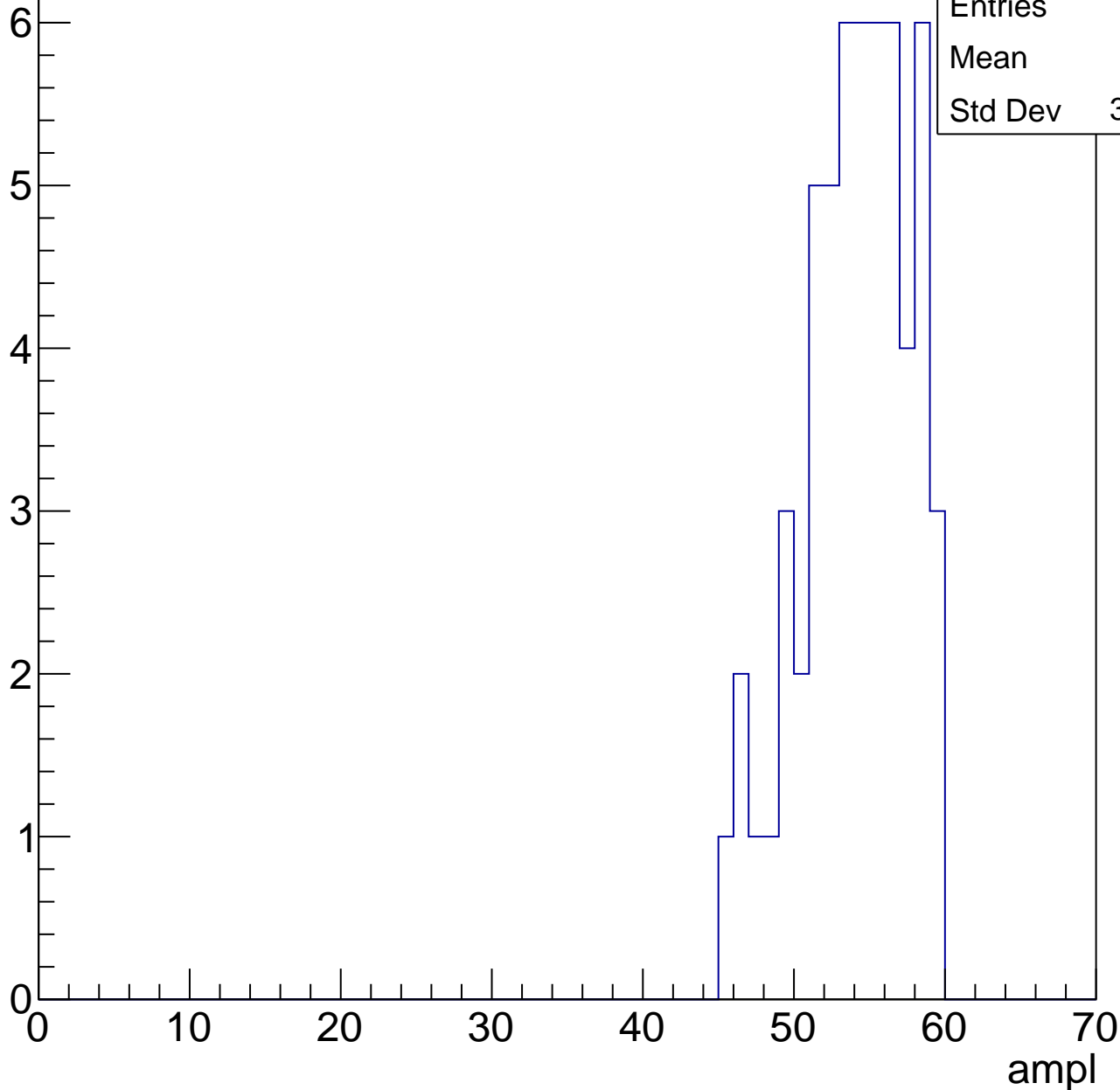


B1L103S, U24-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	53.6
Std Dev	3.504

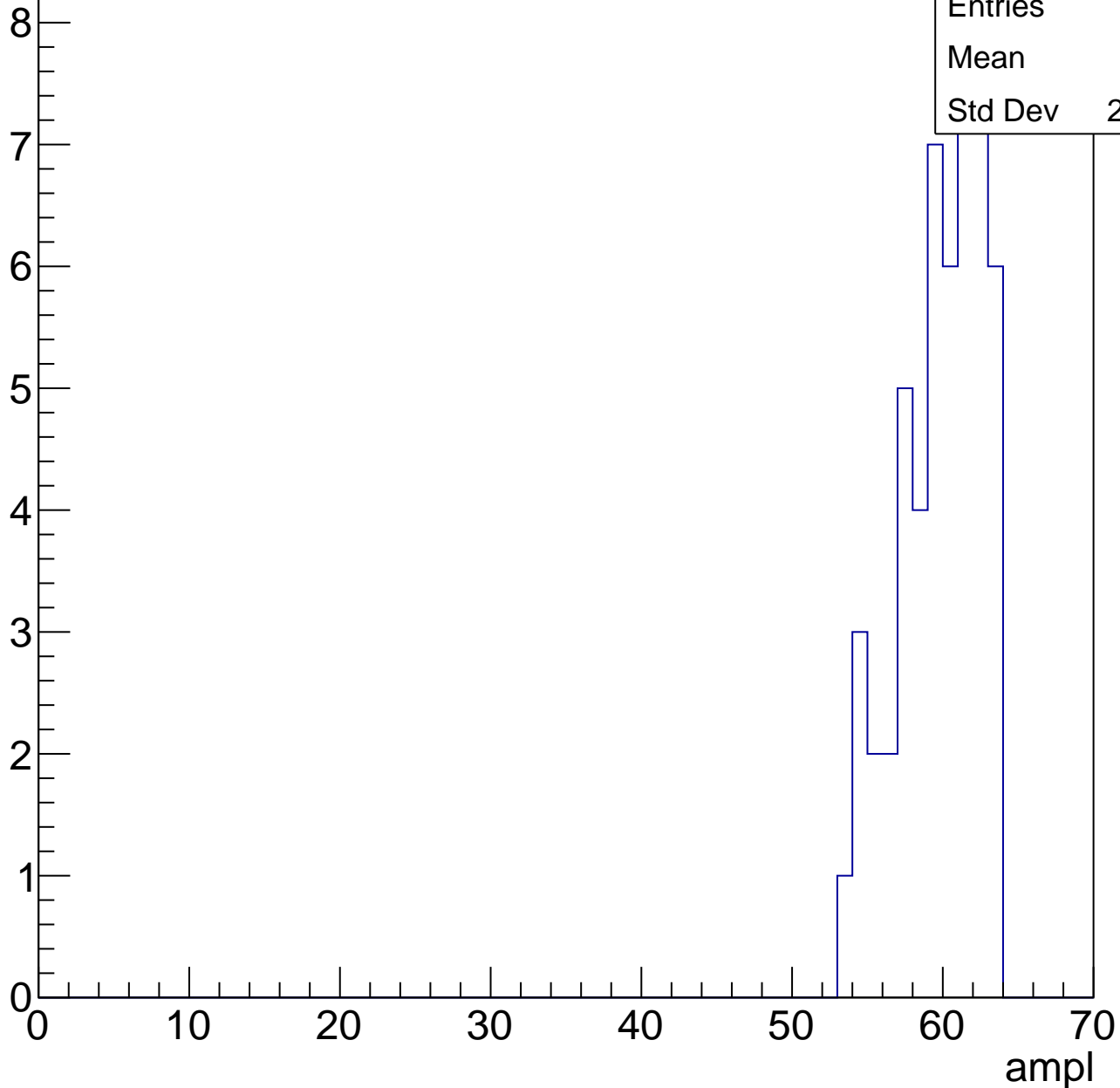


B1L103S, U24-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

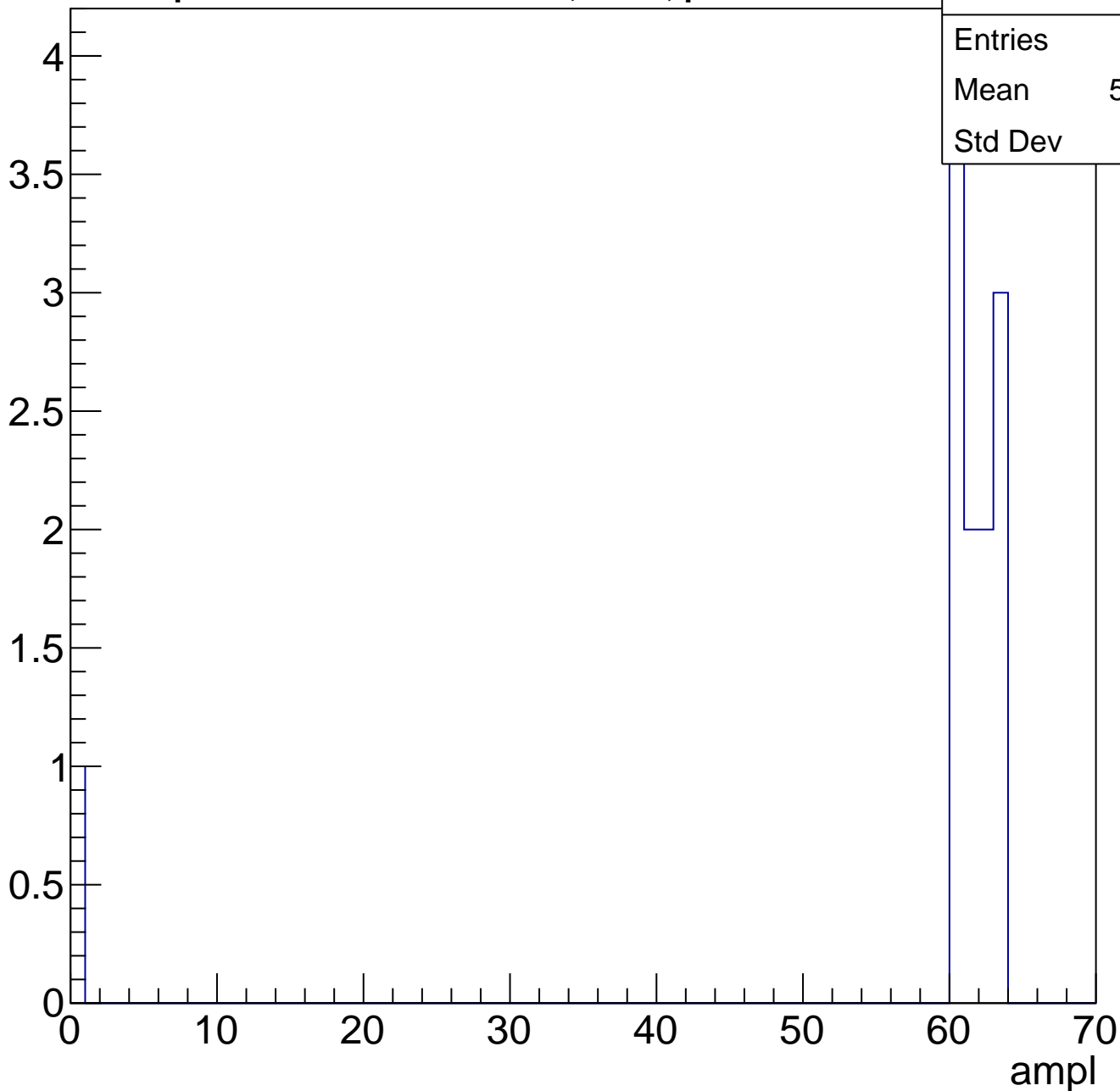
Entries	52
Mean	59.4
Std Dev	2.712



B1L103S, U24-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U24-ch81, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	65
Mean	21.25
Std Dev	11.98

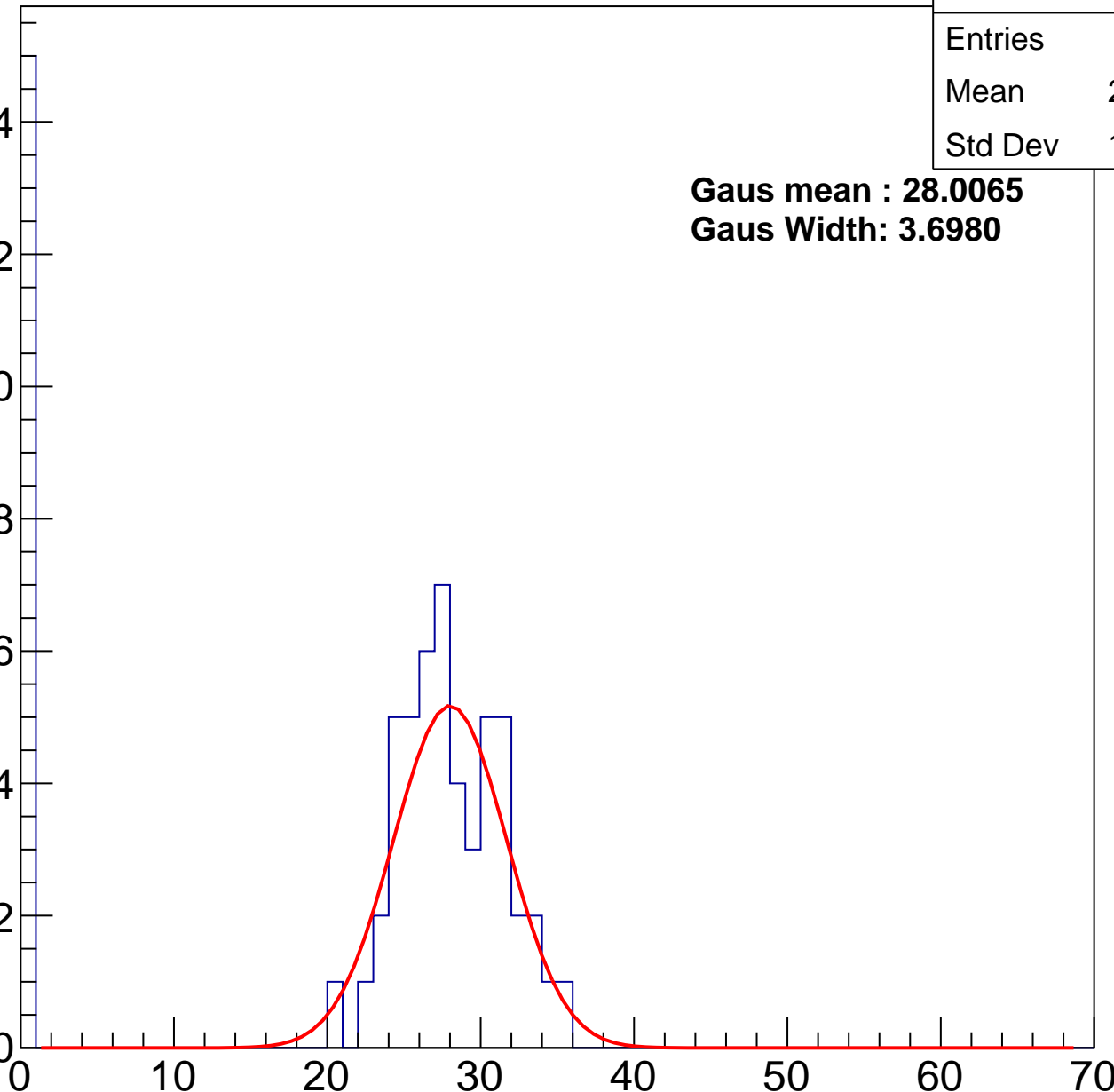
Gaus mean : 28.0065

Gaus Width: 3.6980

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch81, adc1

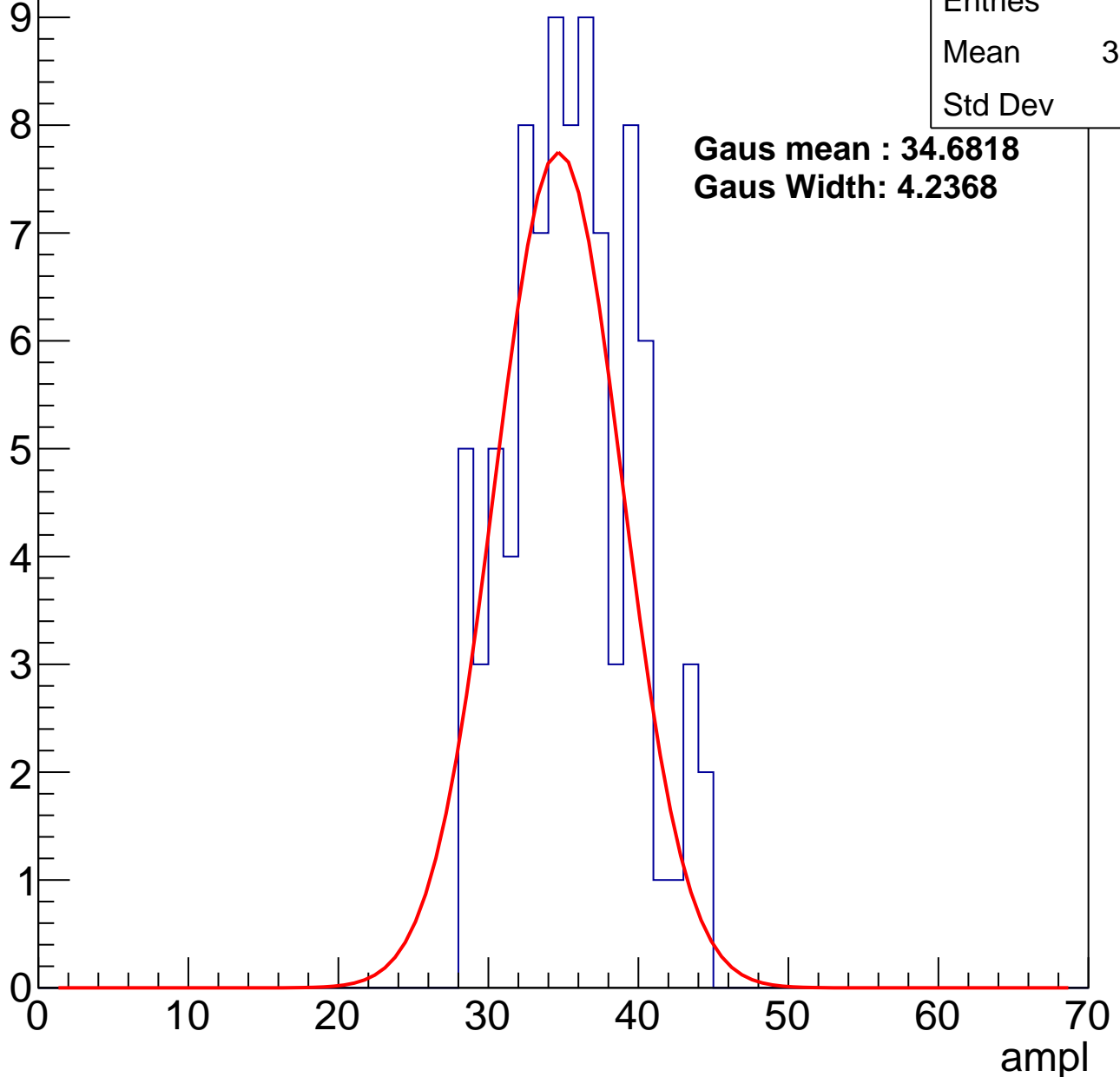
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	89
Mean	35.09
Std Dev	4.01

Gaus mean : 34.6818

Gaus Width: 4.2368



B1L103S, U24-ch81, adc2

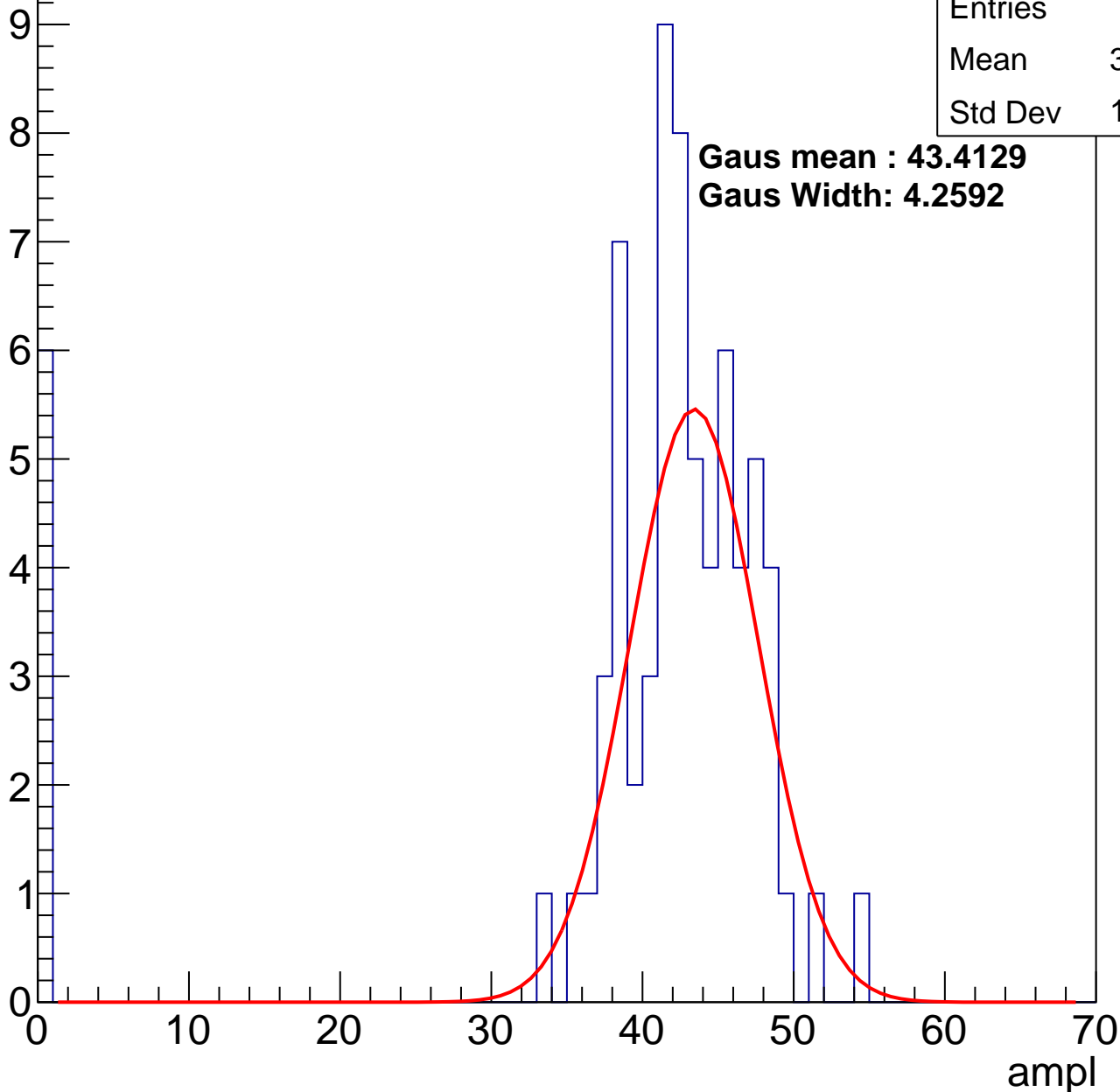
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	39.03
Std Dev	12.37

Gaus mean : 43.4129

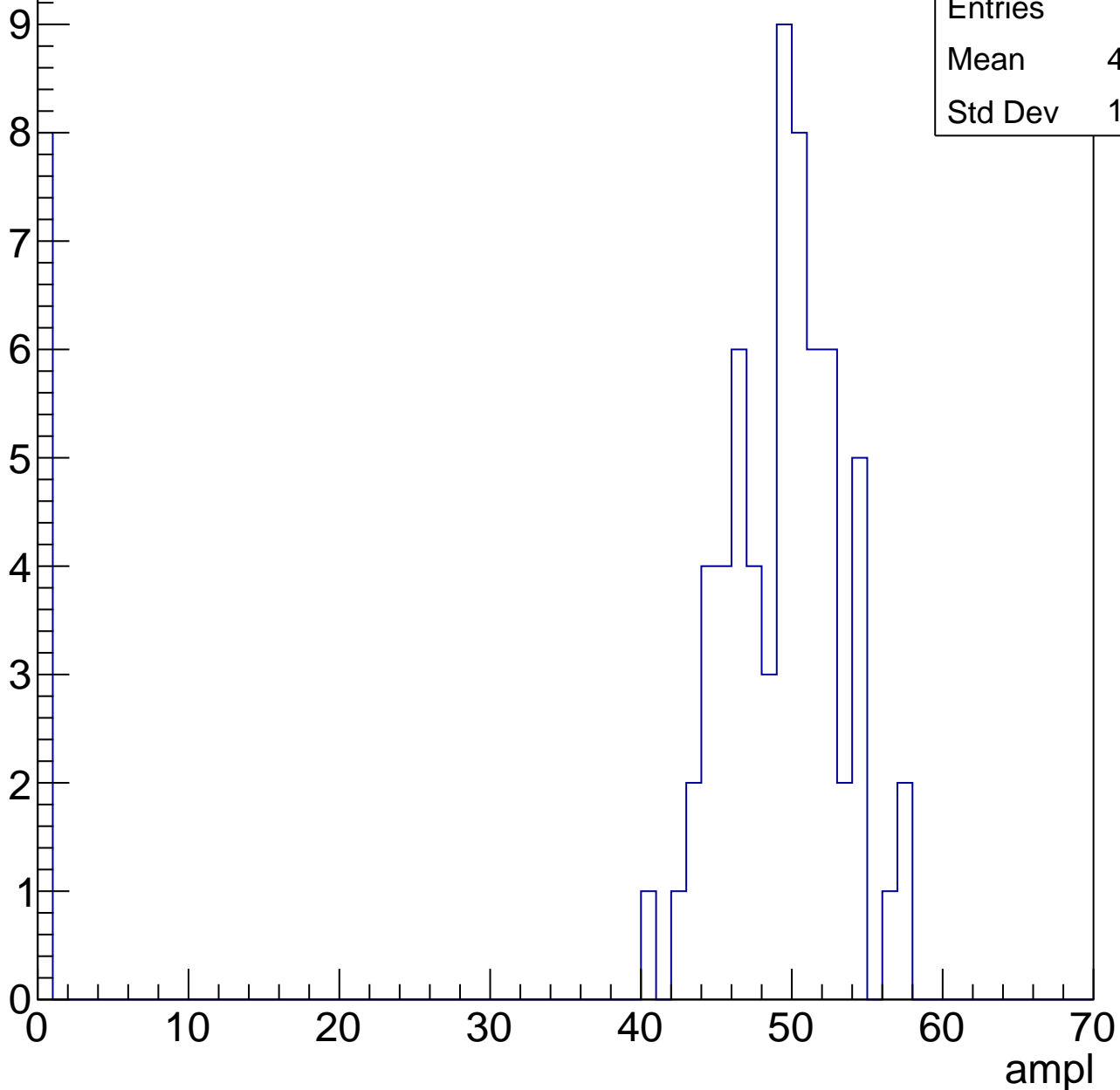
Gaus Width: 4.2592



B1L103S, U24-ch81, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

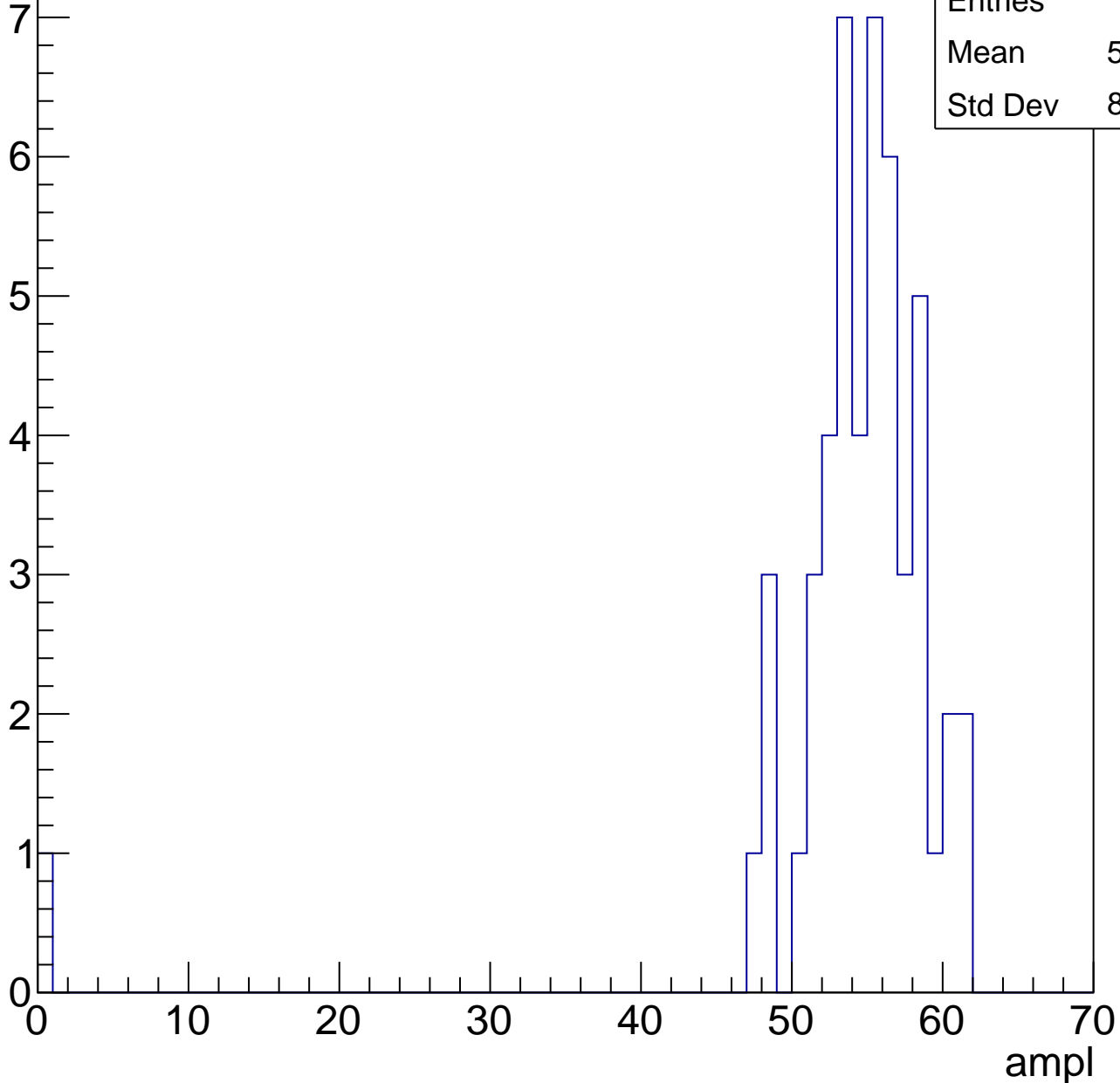


B1L103S, U24-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	53.44
Std Dev	8.314

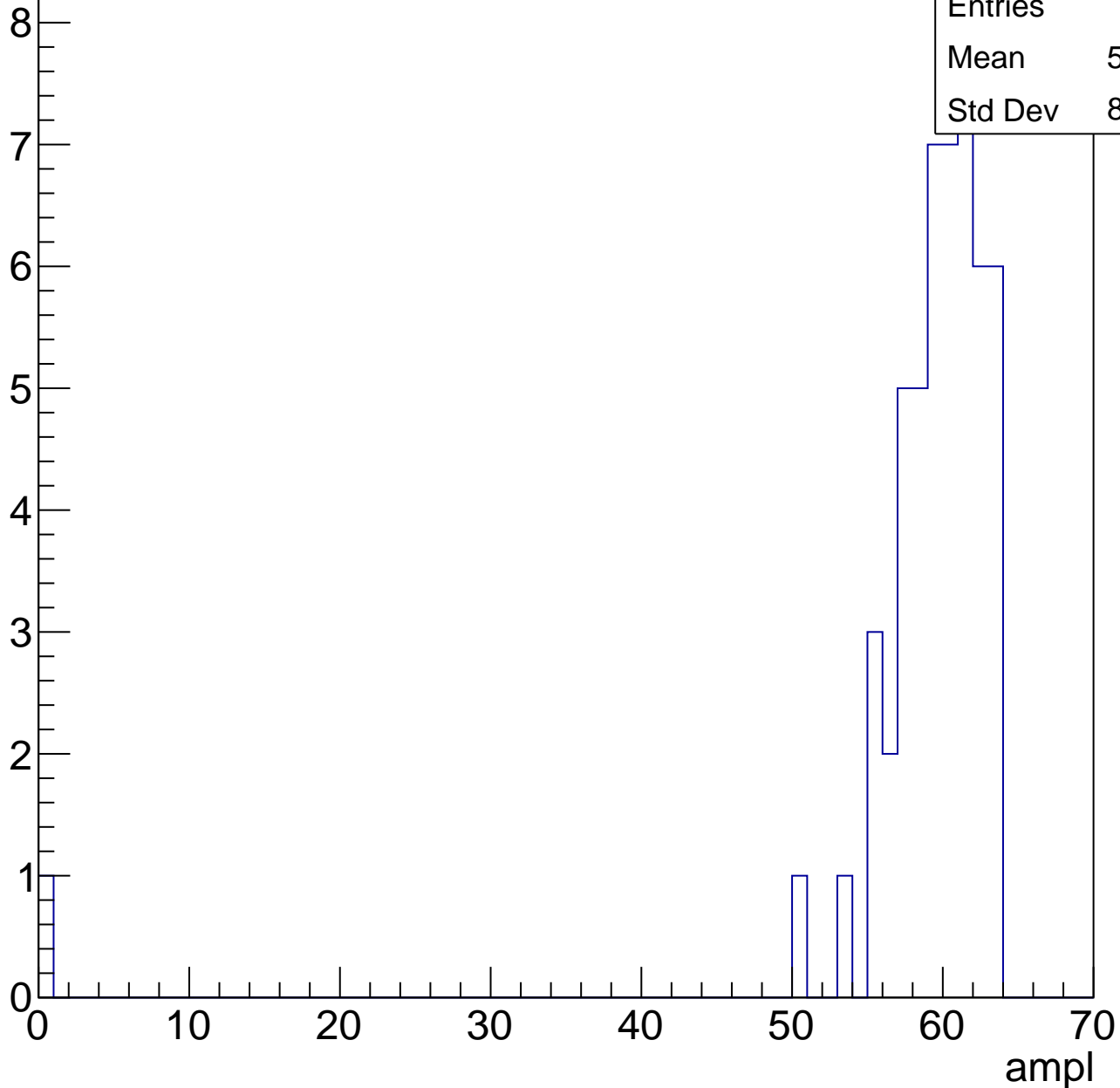


B1L103S, U24-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

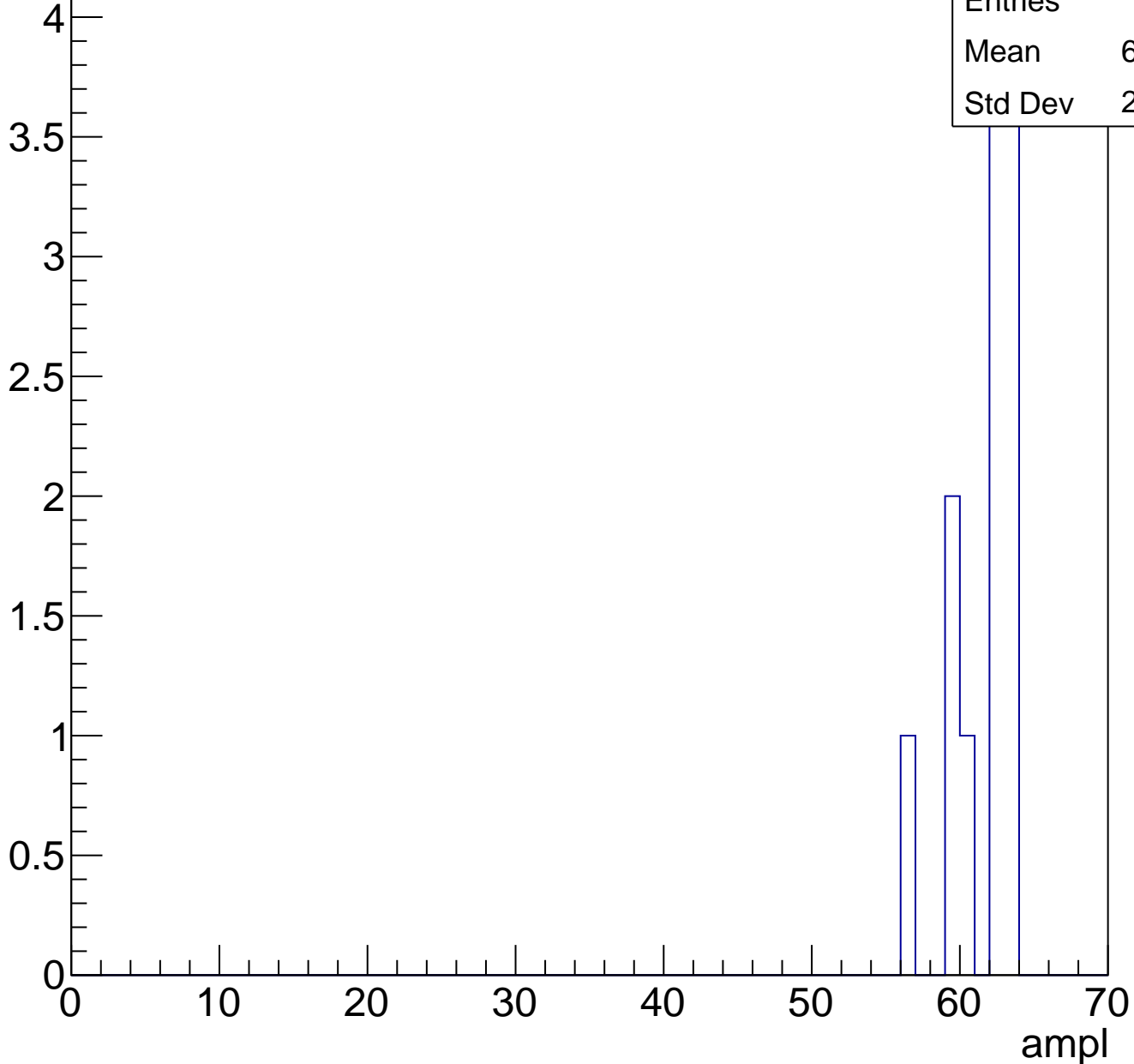
Entries	52
Mean	58.19
Std Dev	8.598



B1L103S, U24-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch82, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	126
Mean	16.44
Std Dev	13.32

Gaus mean : 29.3124

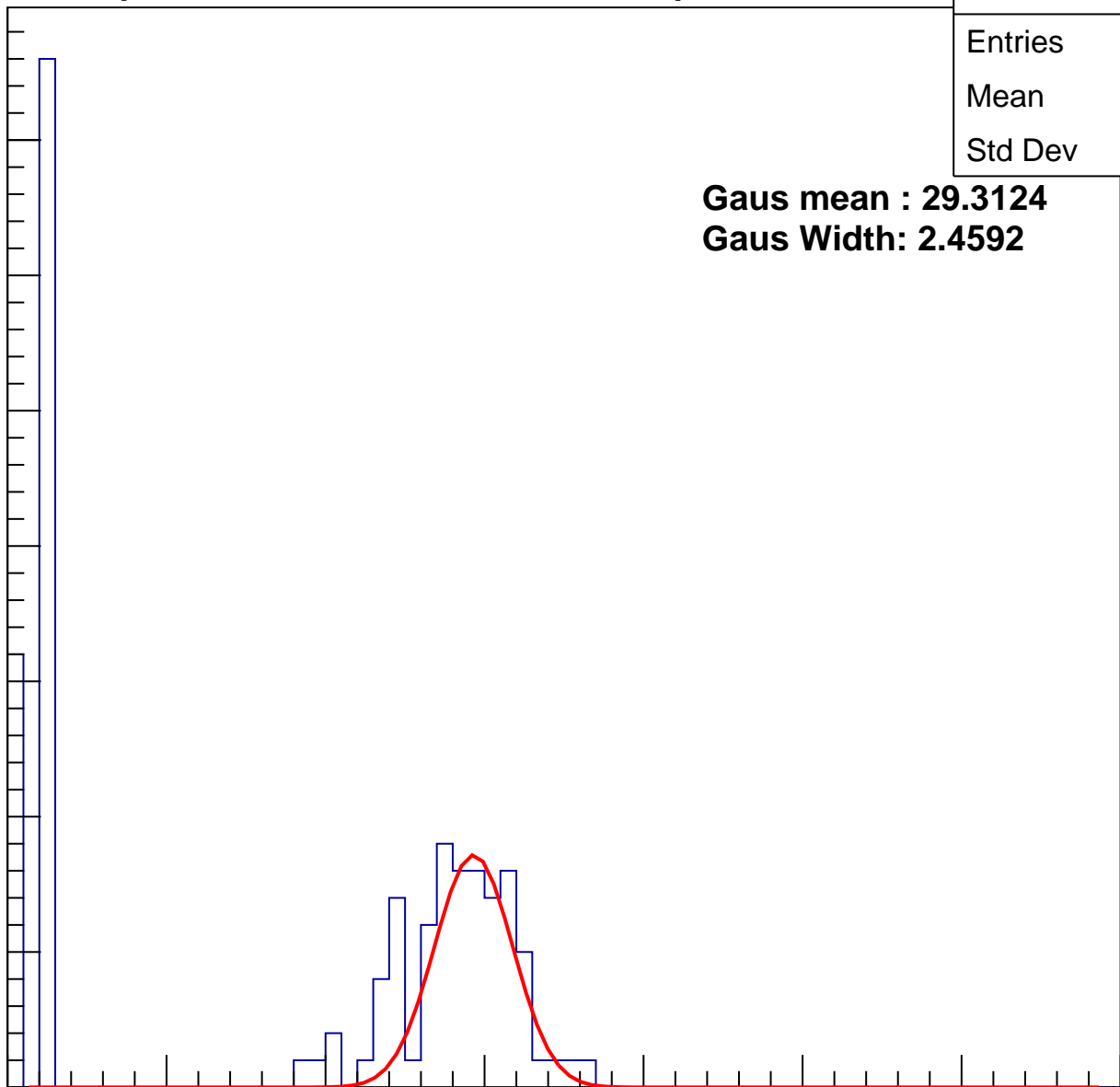
Gaus Width: 2.4592

Entry

35
30
25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch82, adc1

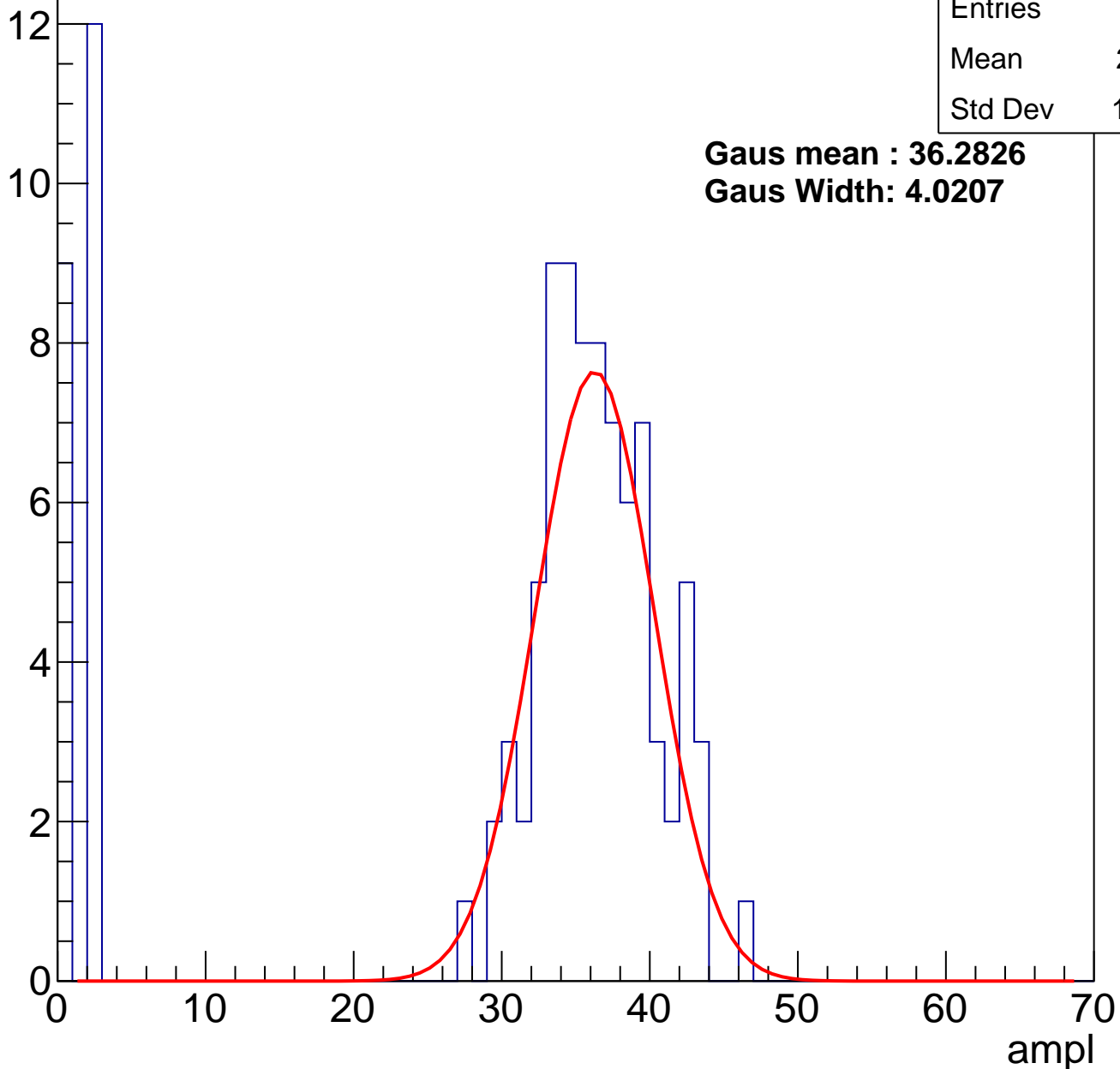
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	28.81
Std Dev	14.49

Gaus mean : 36.2826

Gaus Width: 4.0207

Entry



B1L103S, U24-ch82, adc2

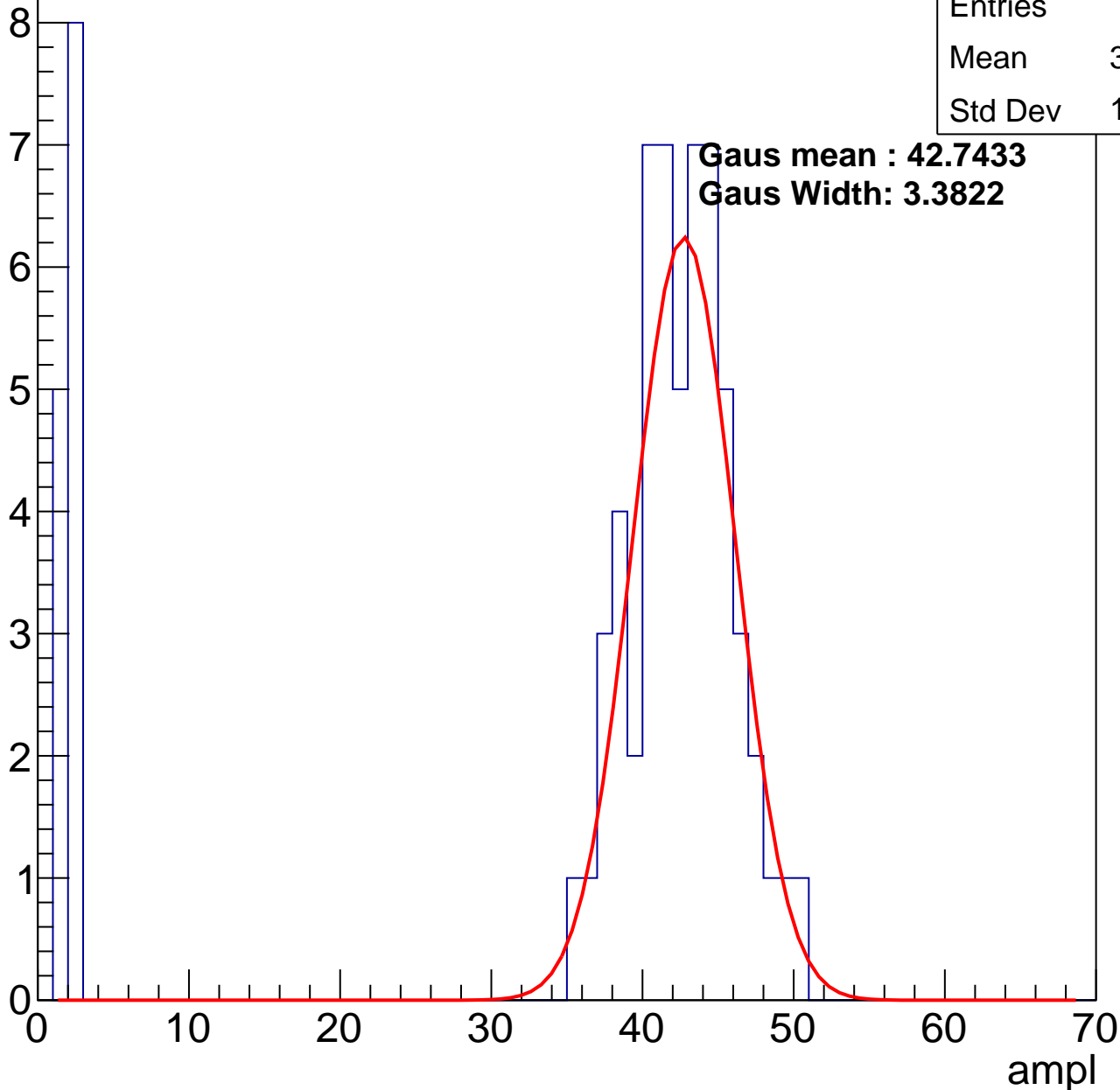
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.54
Std Dev	16.18

Gaus mean : 42.7433

Gaus Width: 3.3822

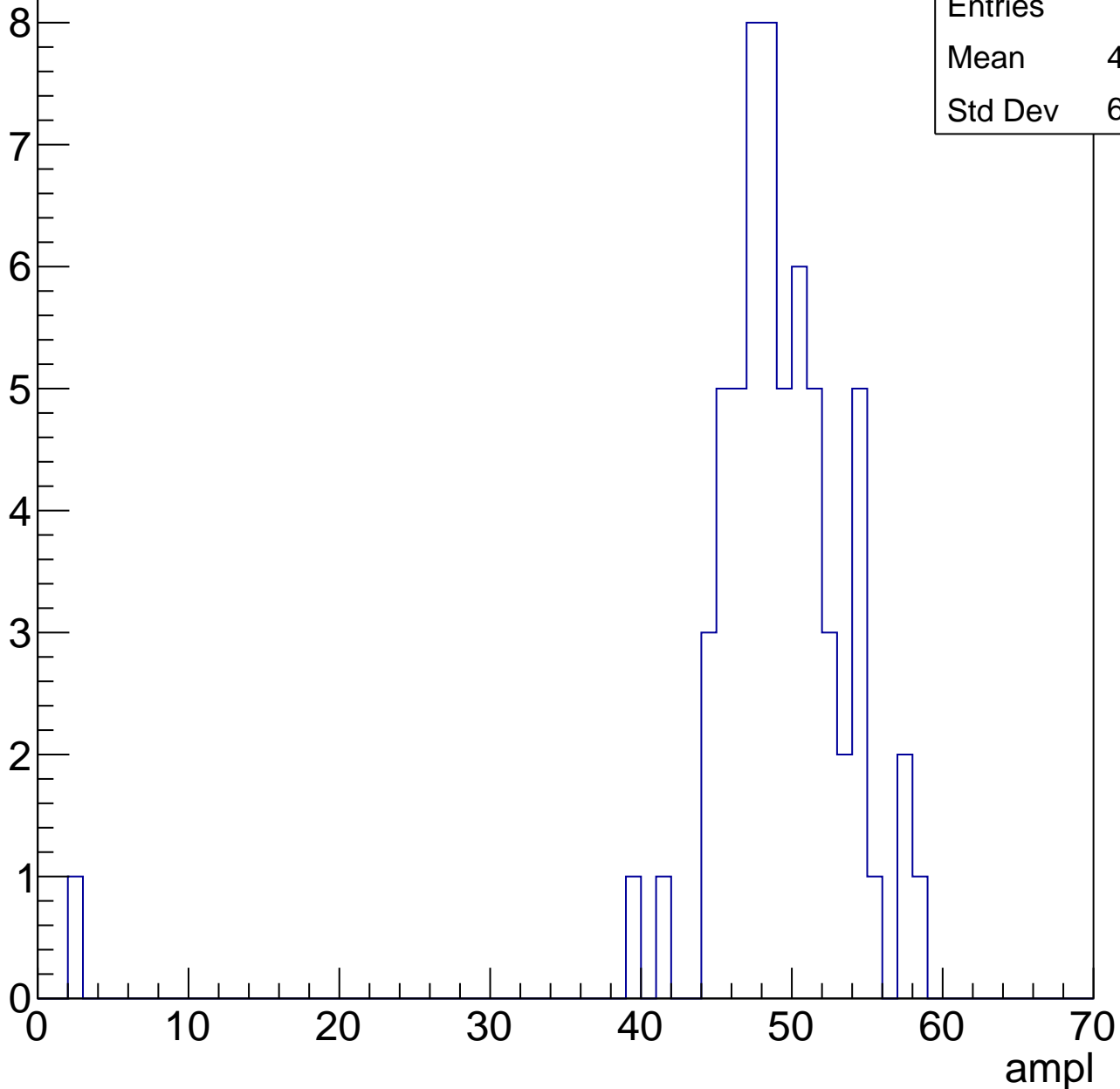


B1L103S, U24-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48.19
Std Dev	6.988

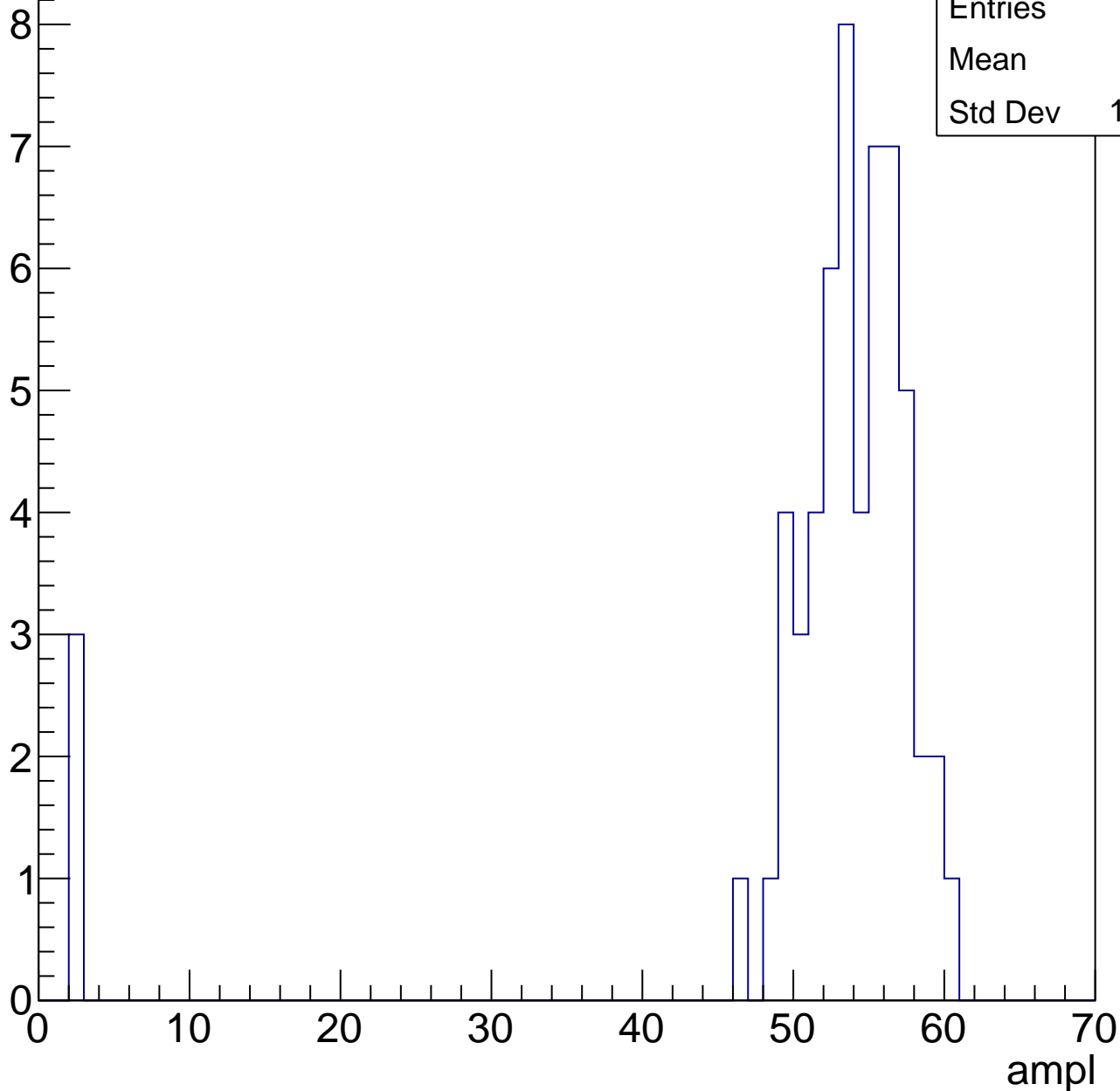


B1L103S, U24-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	51
Std Dev	11.82

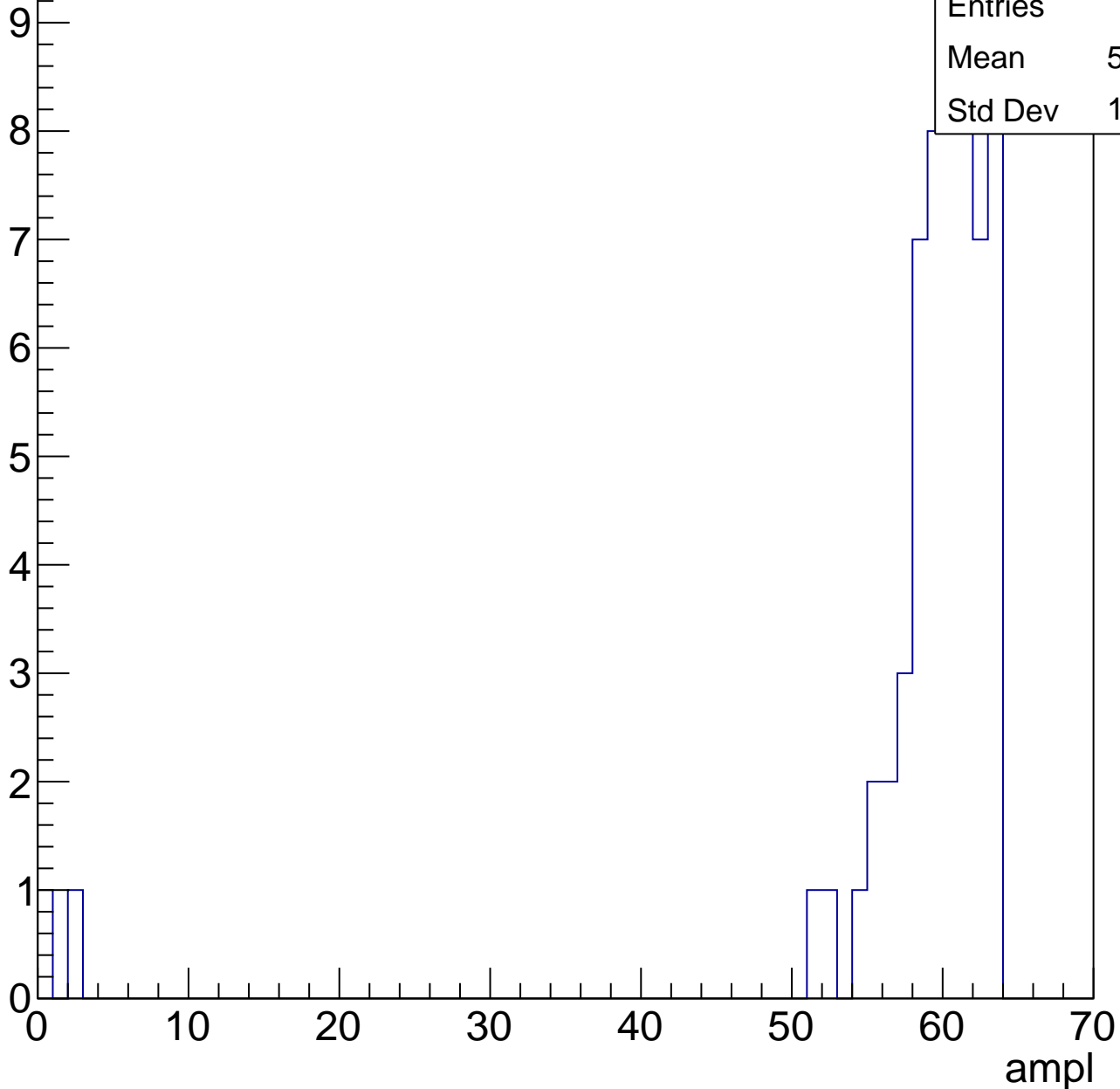


B1L103S, U24-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

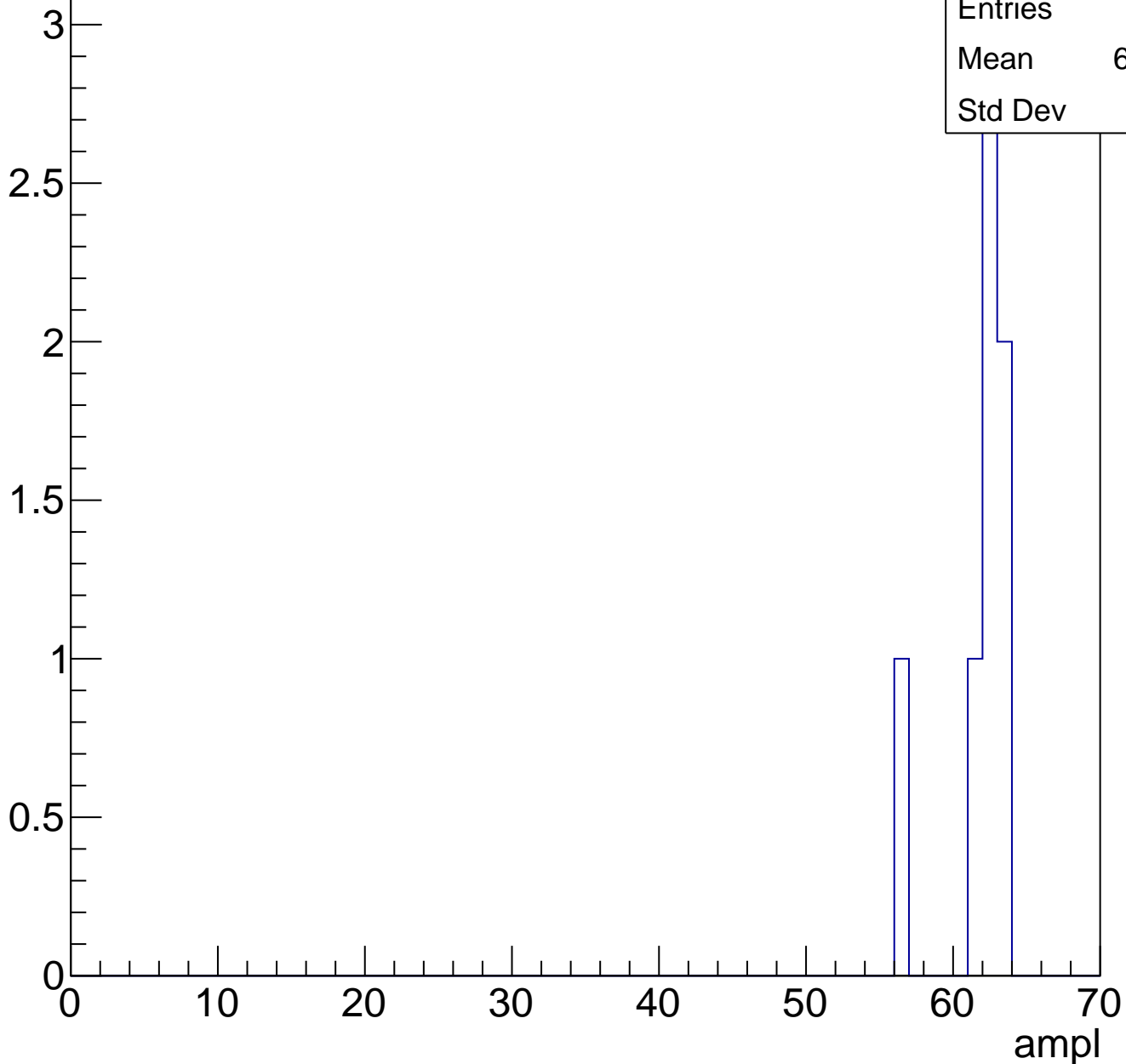
Entries	60
Mean	57.62
Std Dev	10.85



B1L103S, U24-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

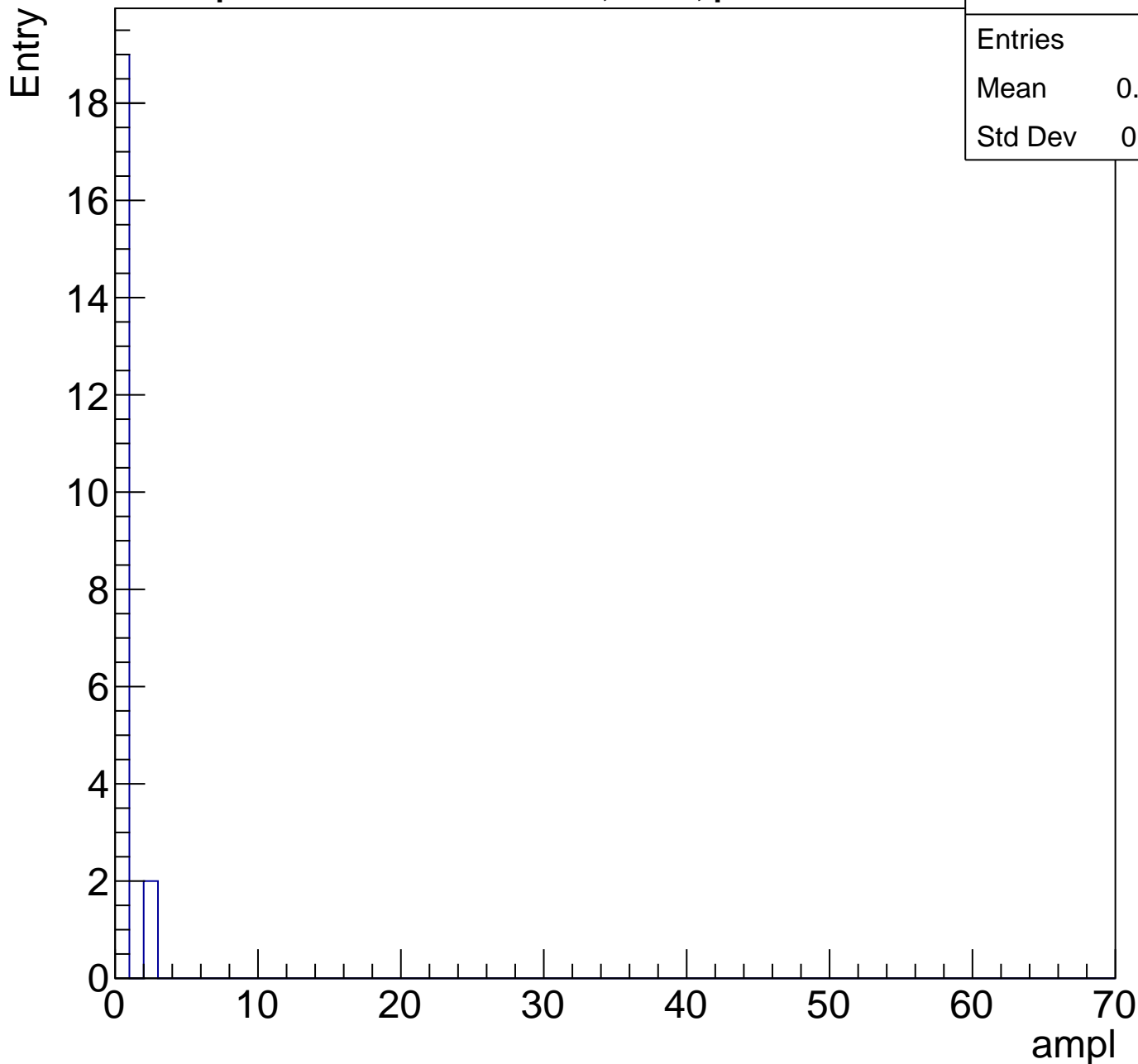
Entry



B1L103S, U24-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	0.1905
Std Dev	0.5871



B1L103S, U24-ch83, adc0

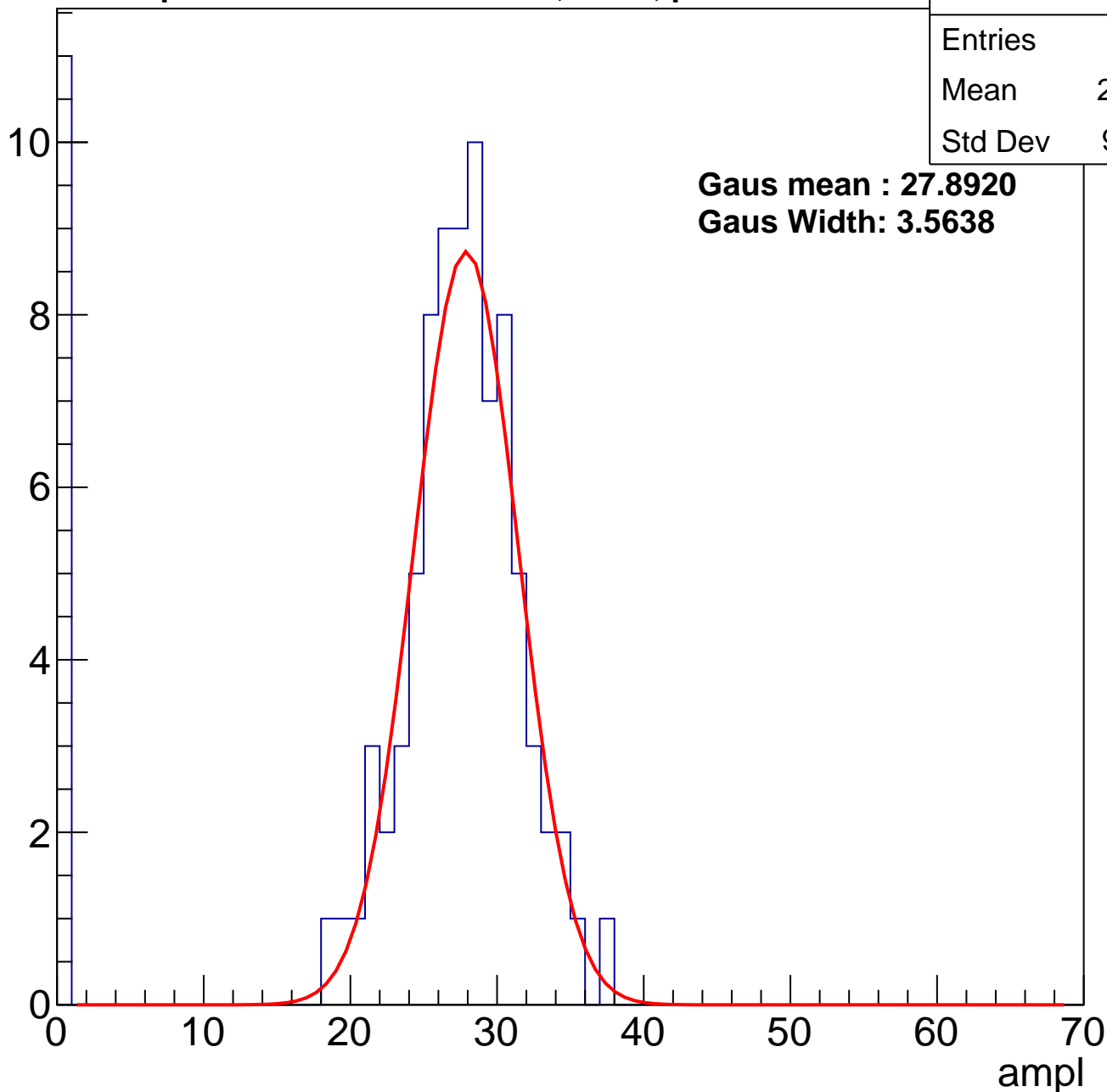
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	24.02
Std Dev	9.491

Gaus mean : 27.8920

Gaus Width: 3.5638

Entry



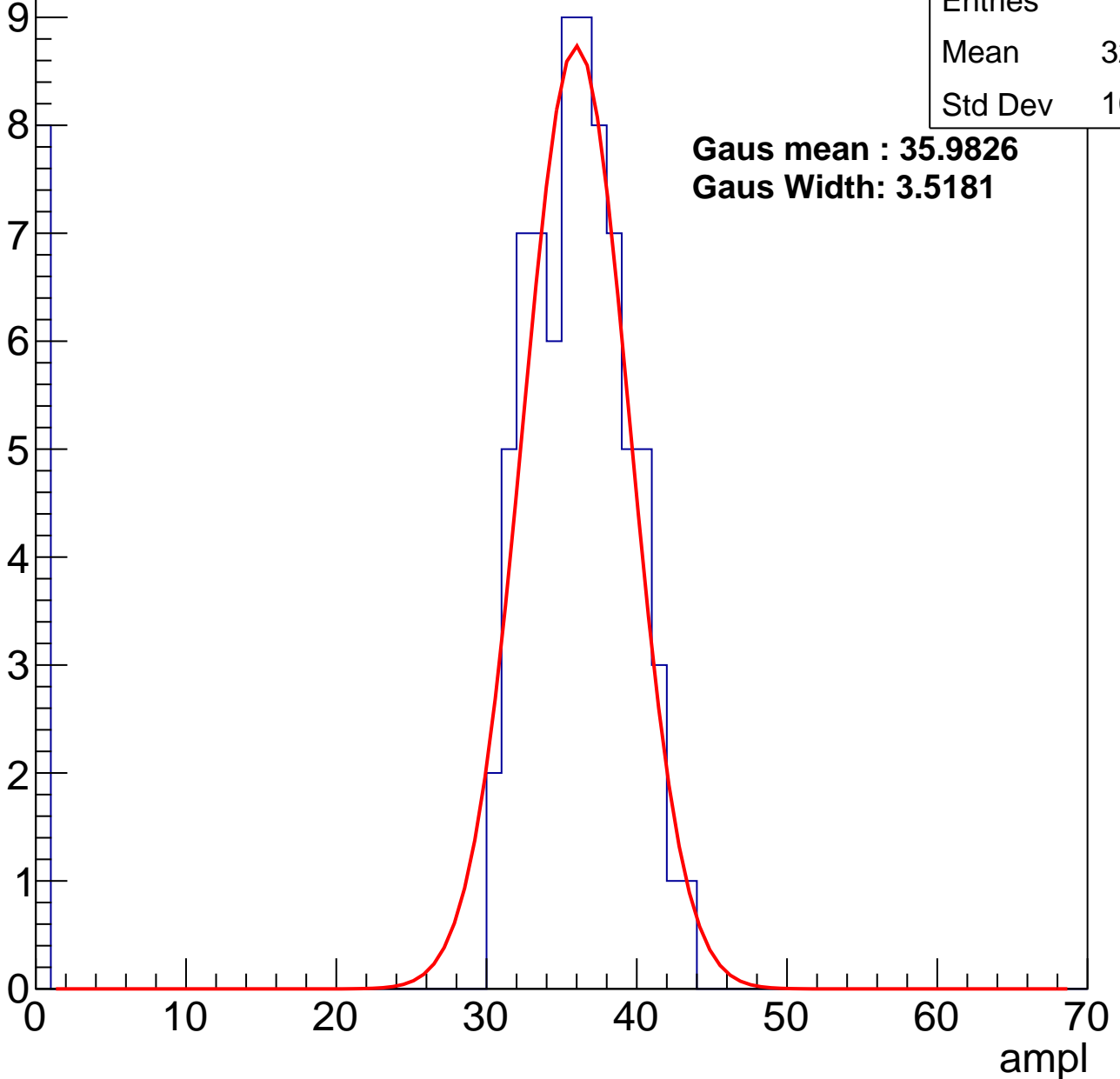
B1L103S, U24-ch83, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	32.27
Std Dev	10.94

Gaus mean : 35.9826
Gaus Width: 3.5181



B1L103S, U24-ch83, adc2

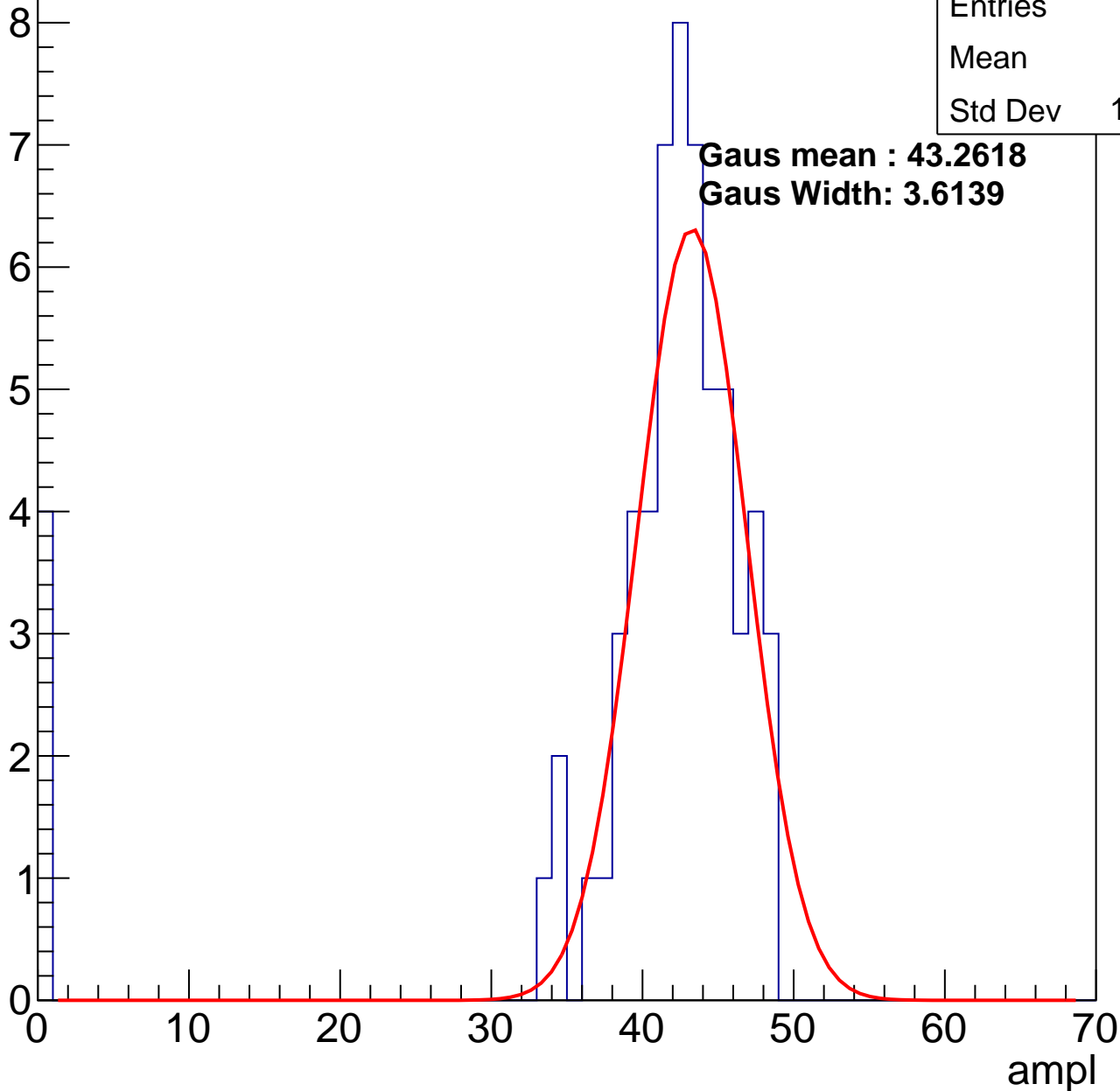
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	39.4
Std Dev	10.88

Gaus mean : 43.2618

Gaus Width: 3.6139

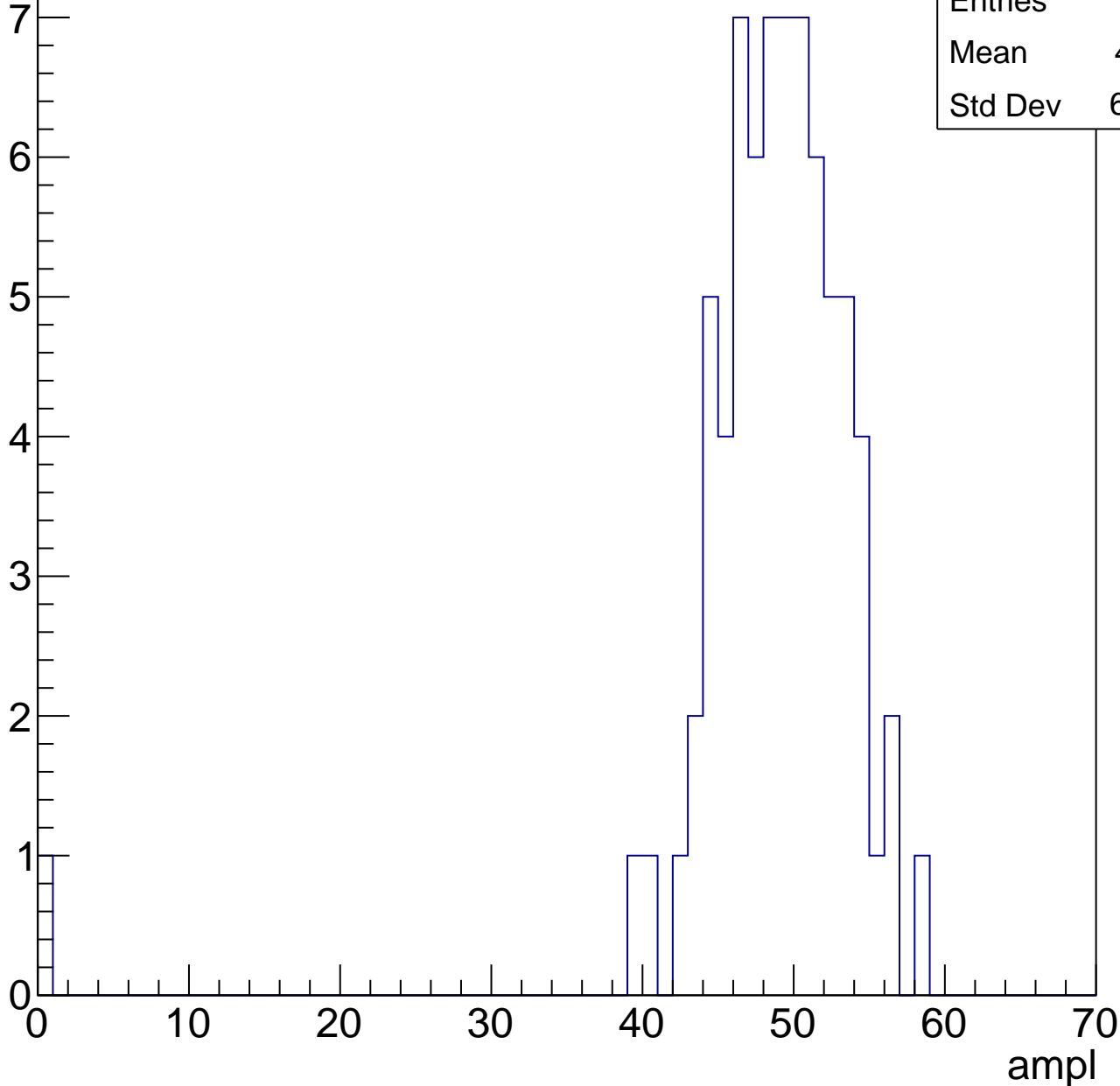


B1L103S, U24-ch83, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	48.11
Std Dev	6.824

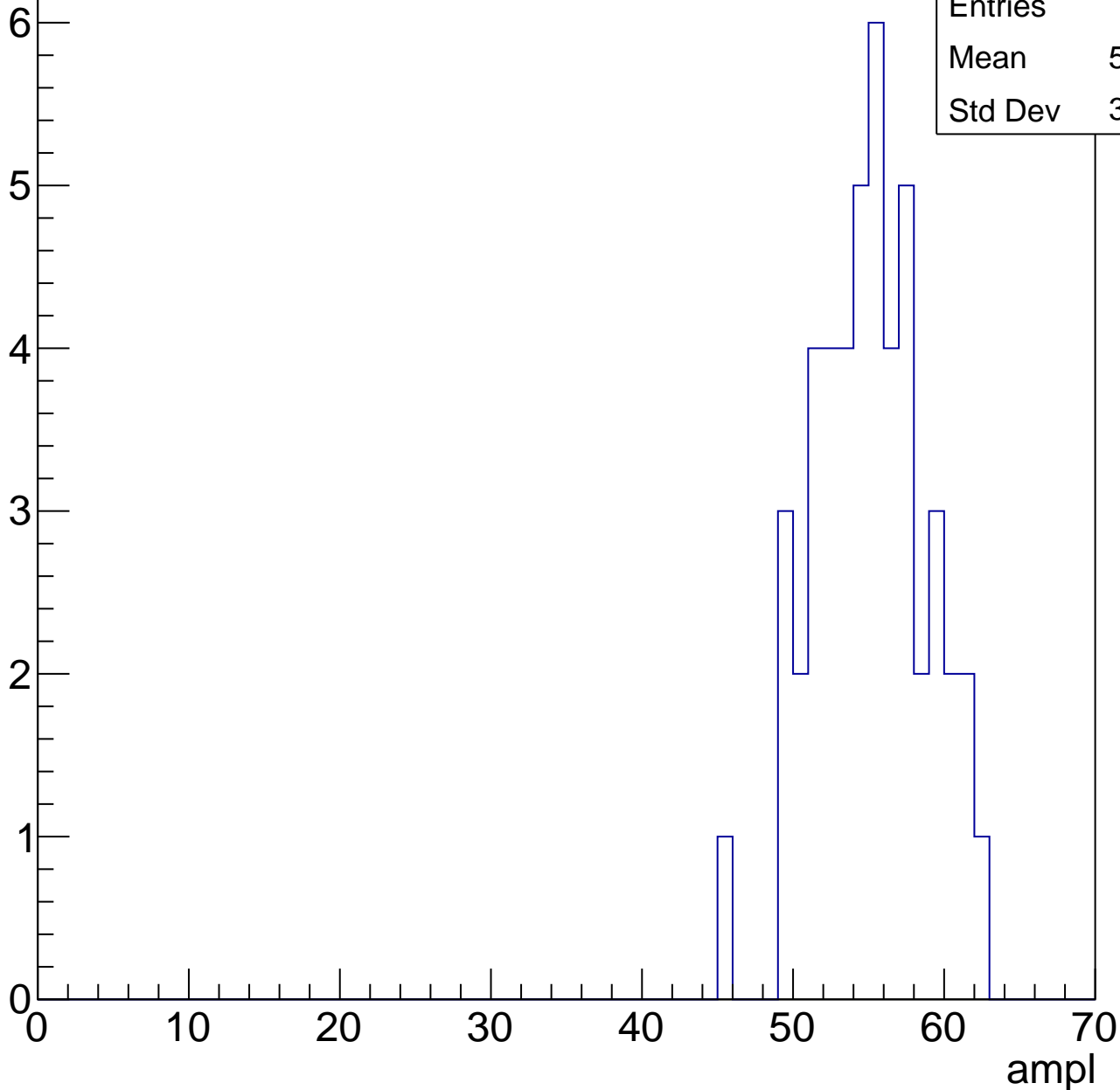


B1L103S, U24-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	54.62
Std Dev	3.632

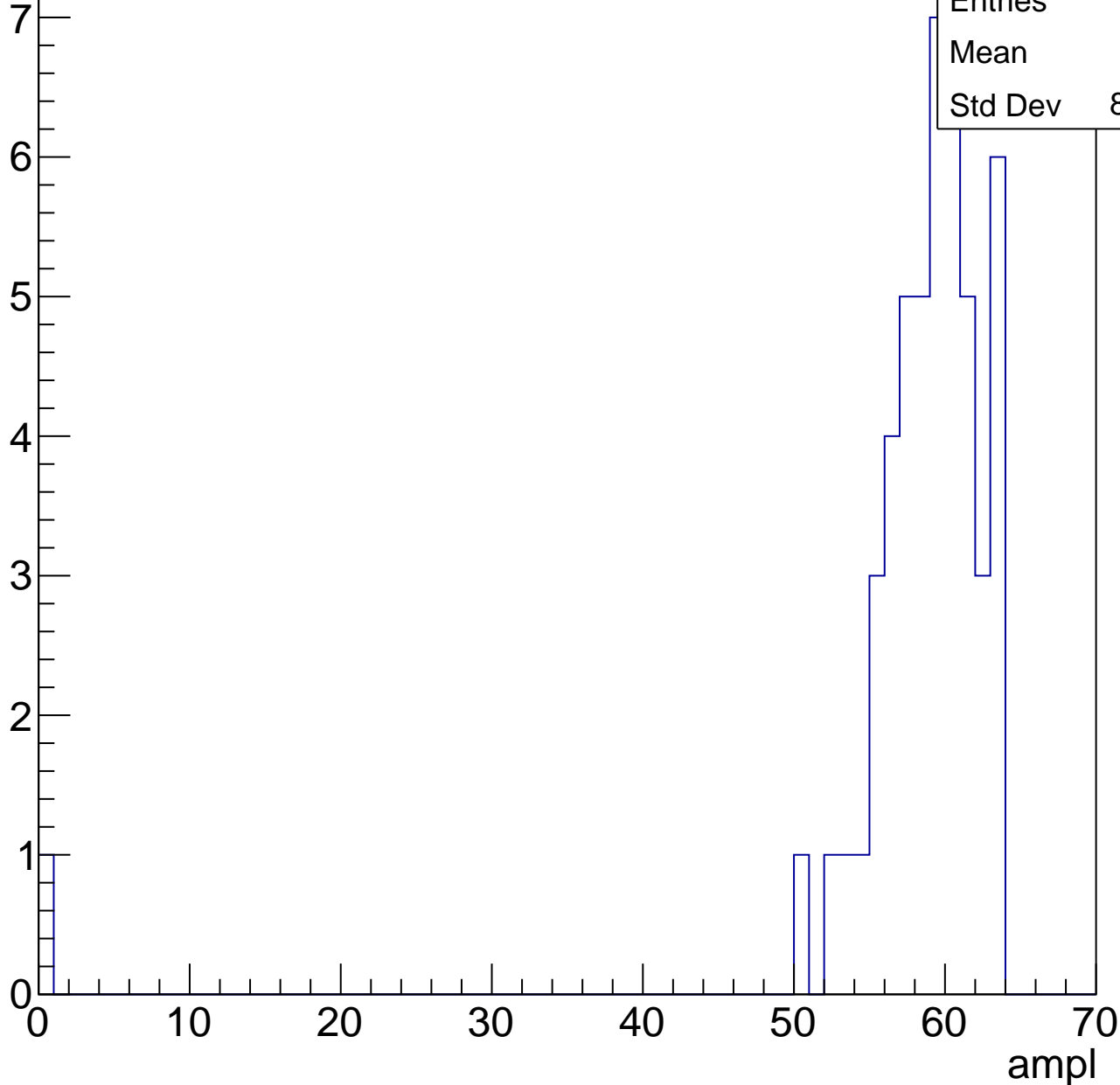


B1L103S, U24-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.5
Std Dev	8.739

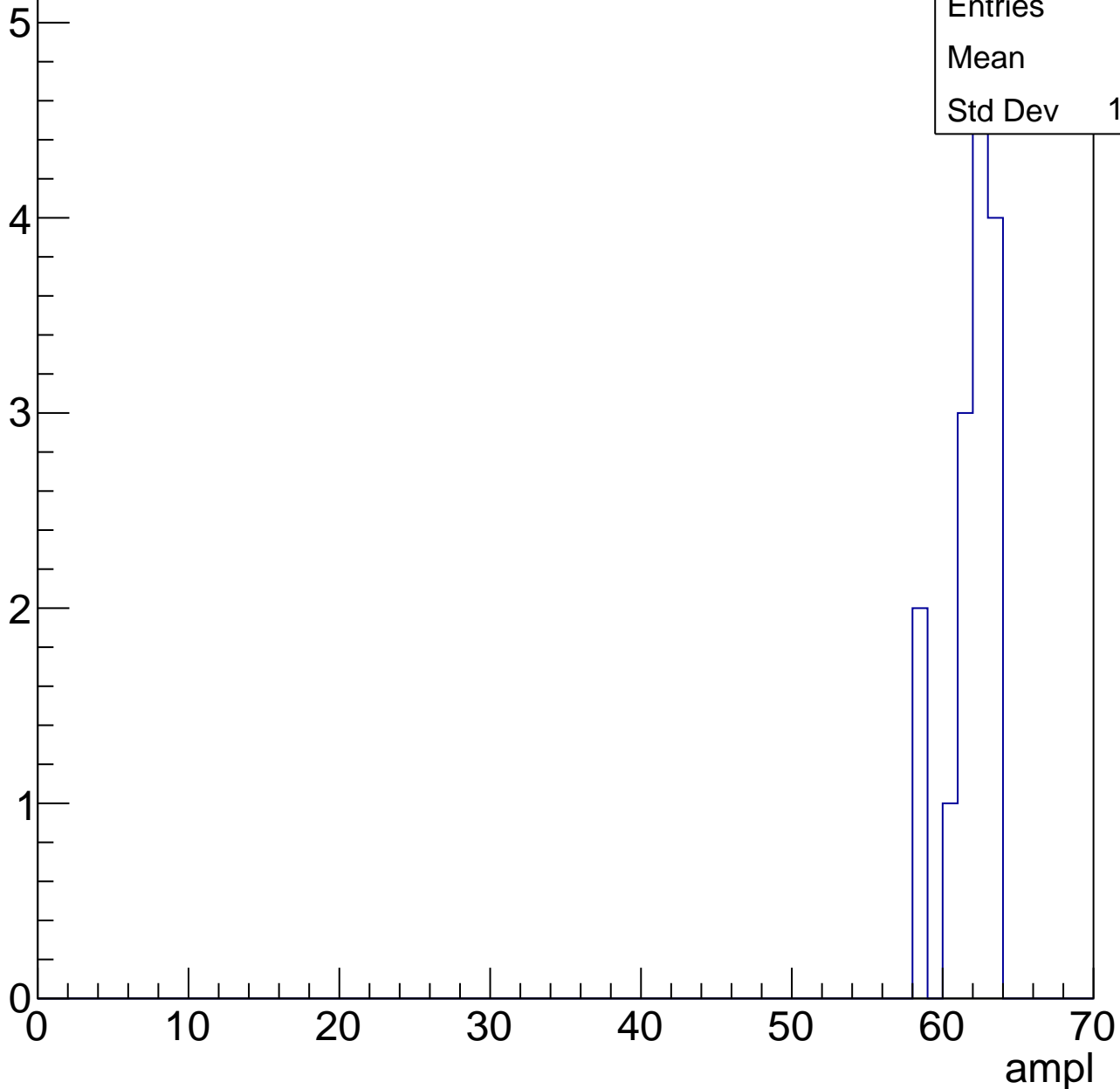


B1L103S, U24-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.4
Std Dev	1.583

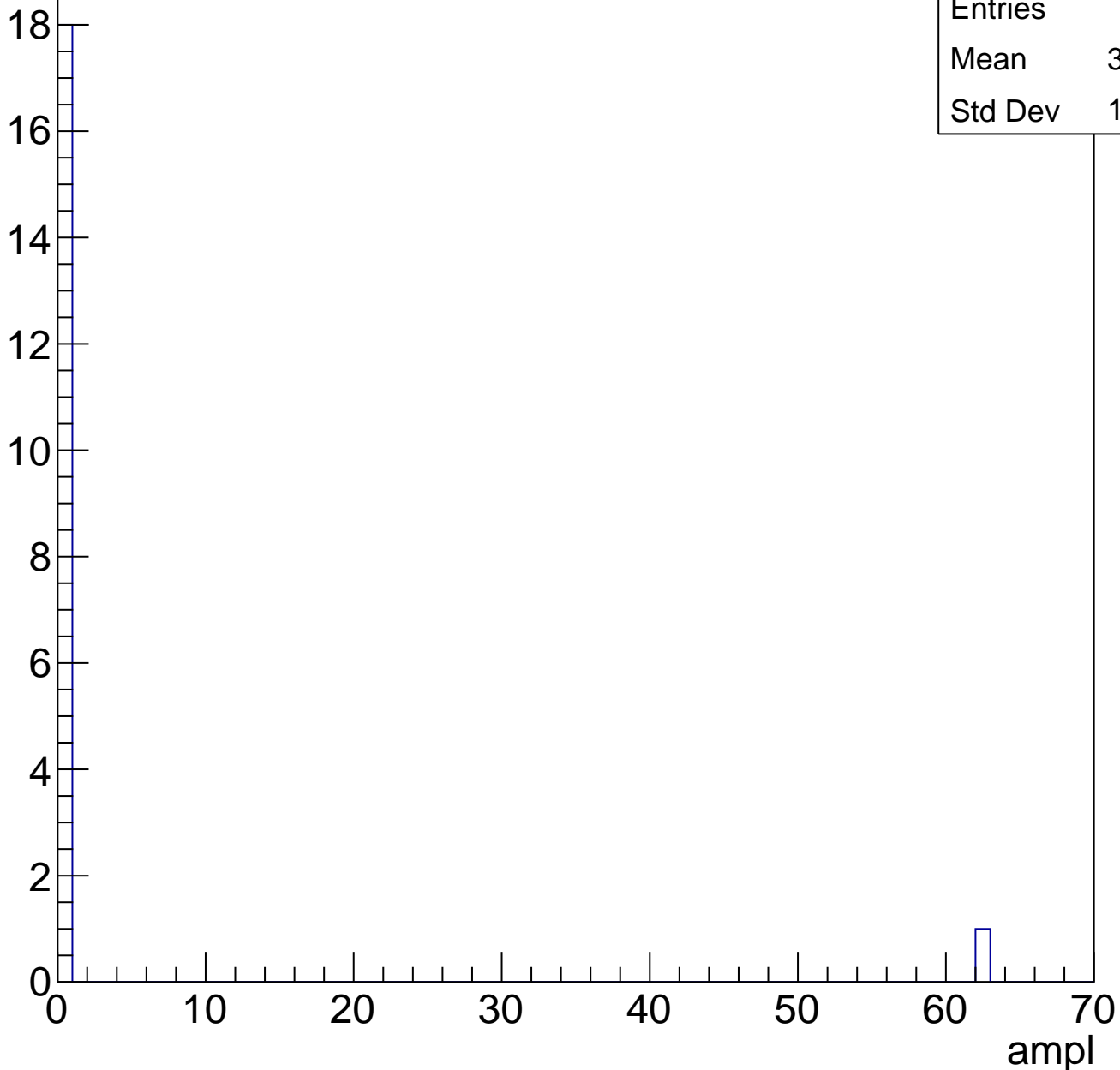


B1L103S, U24-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U24-ch84, adc0

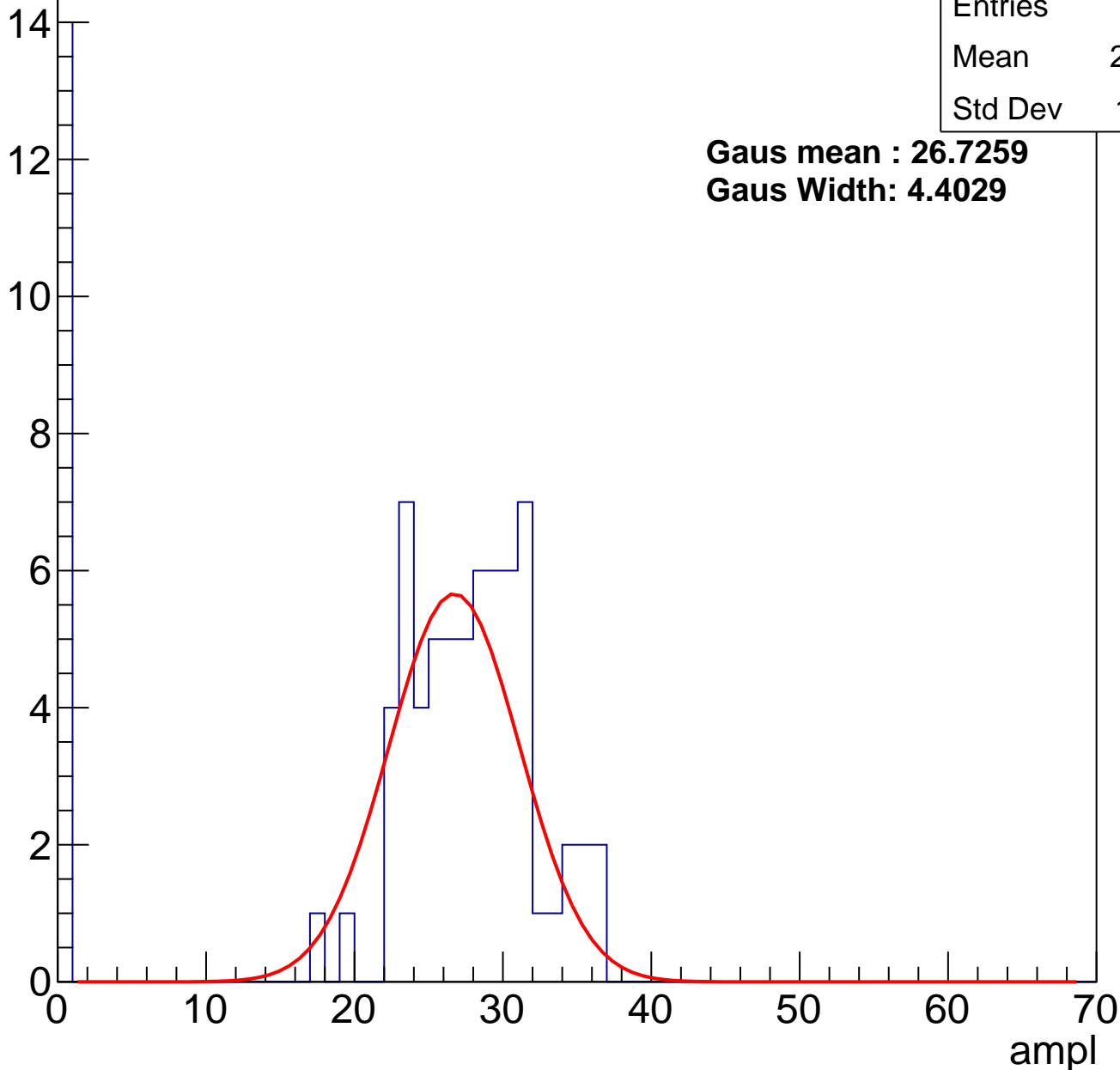
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	22.59
Std Dev	11.11

Gaus mean : 26.7259

Gaus Width: 4.4029

Entry



B1L103S, U24-ch84, adc1

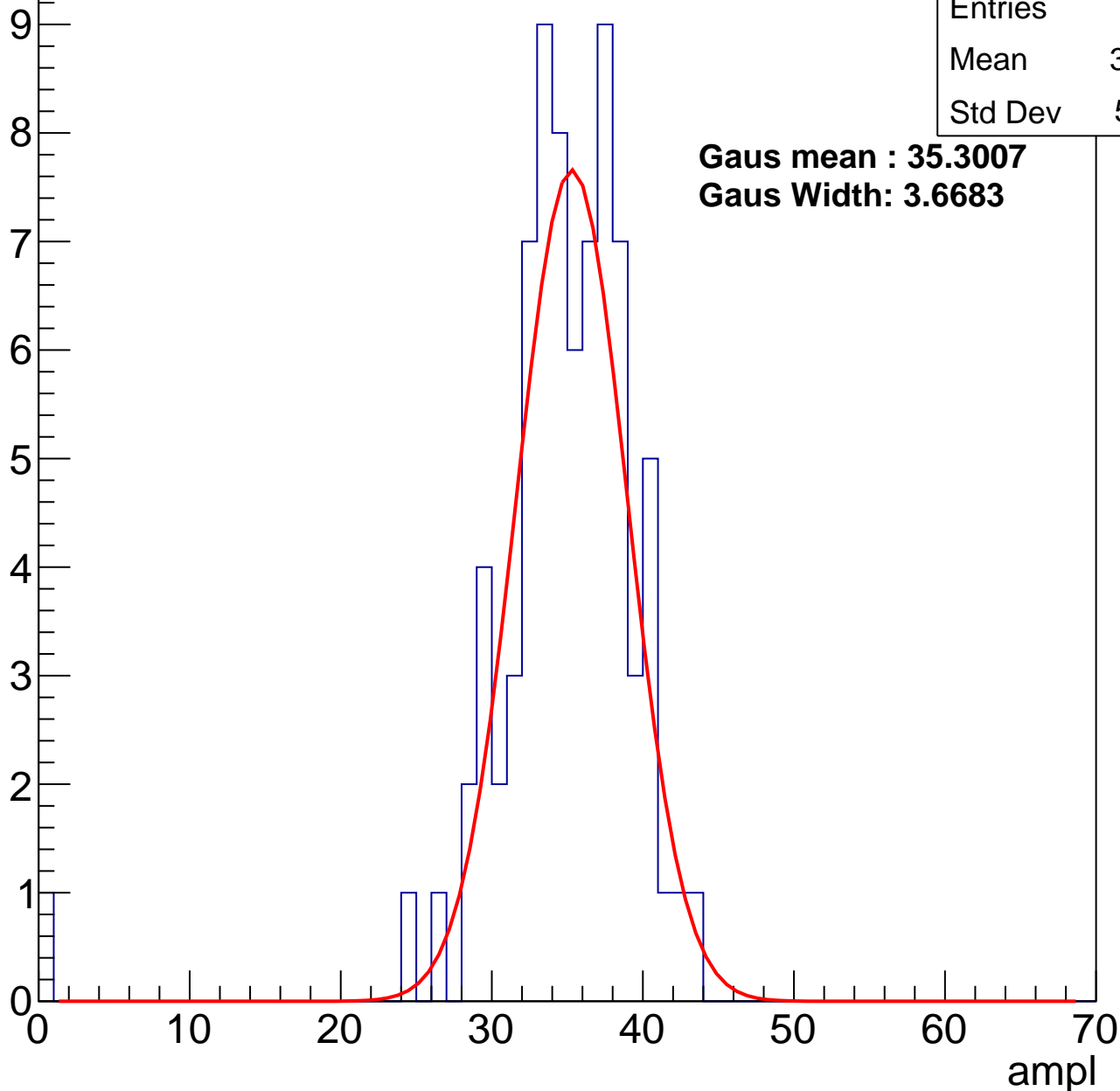
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.26
Std Dev	5.381

Gaus mean : 35.3007

Gaus Width: 3.6683



B1L103S, U24-ch84, adc2

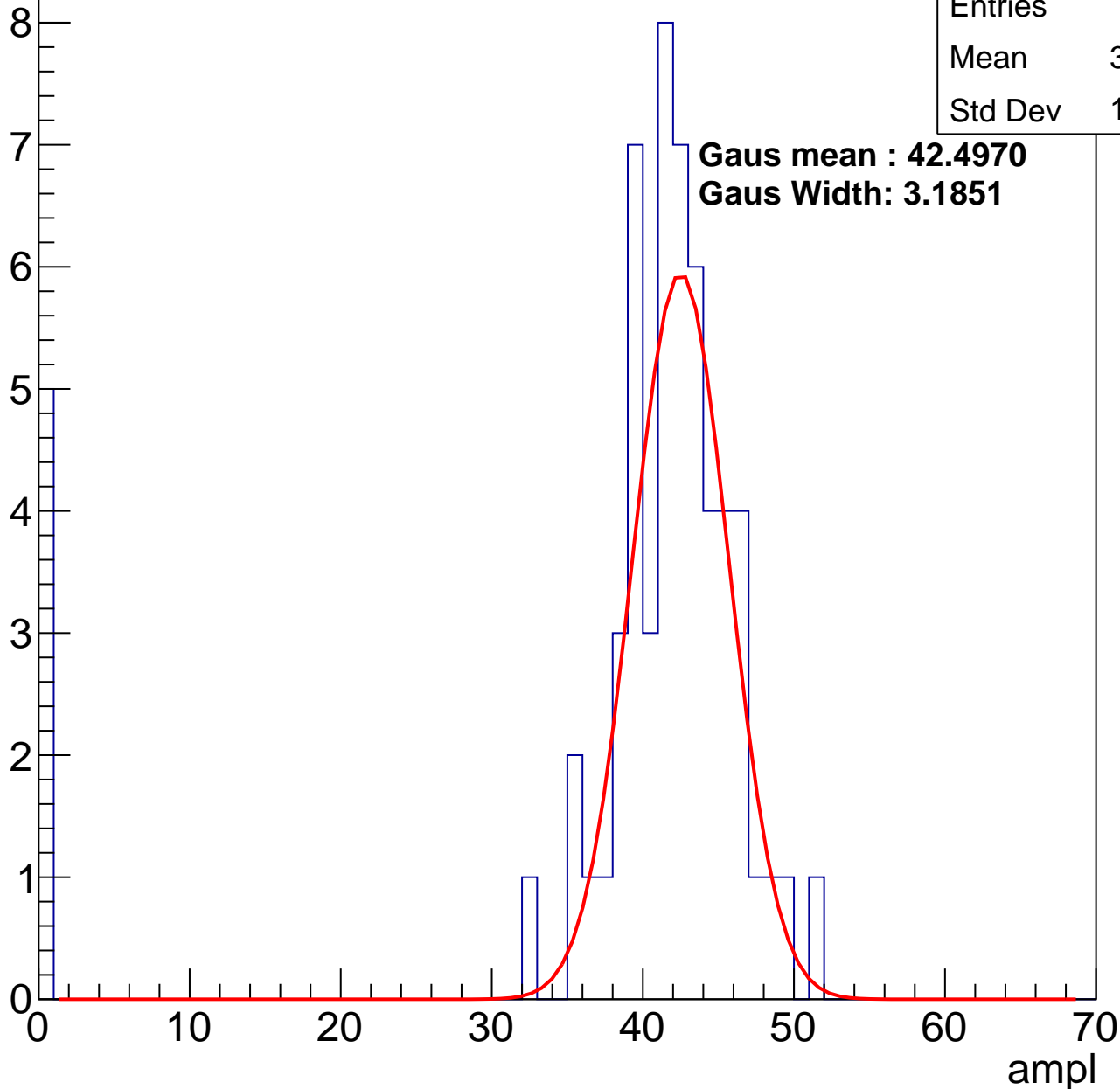
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	38.28
Std Dev	12.04

Gaus mean : 42.4970

Gaus Width: 3.1851

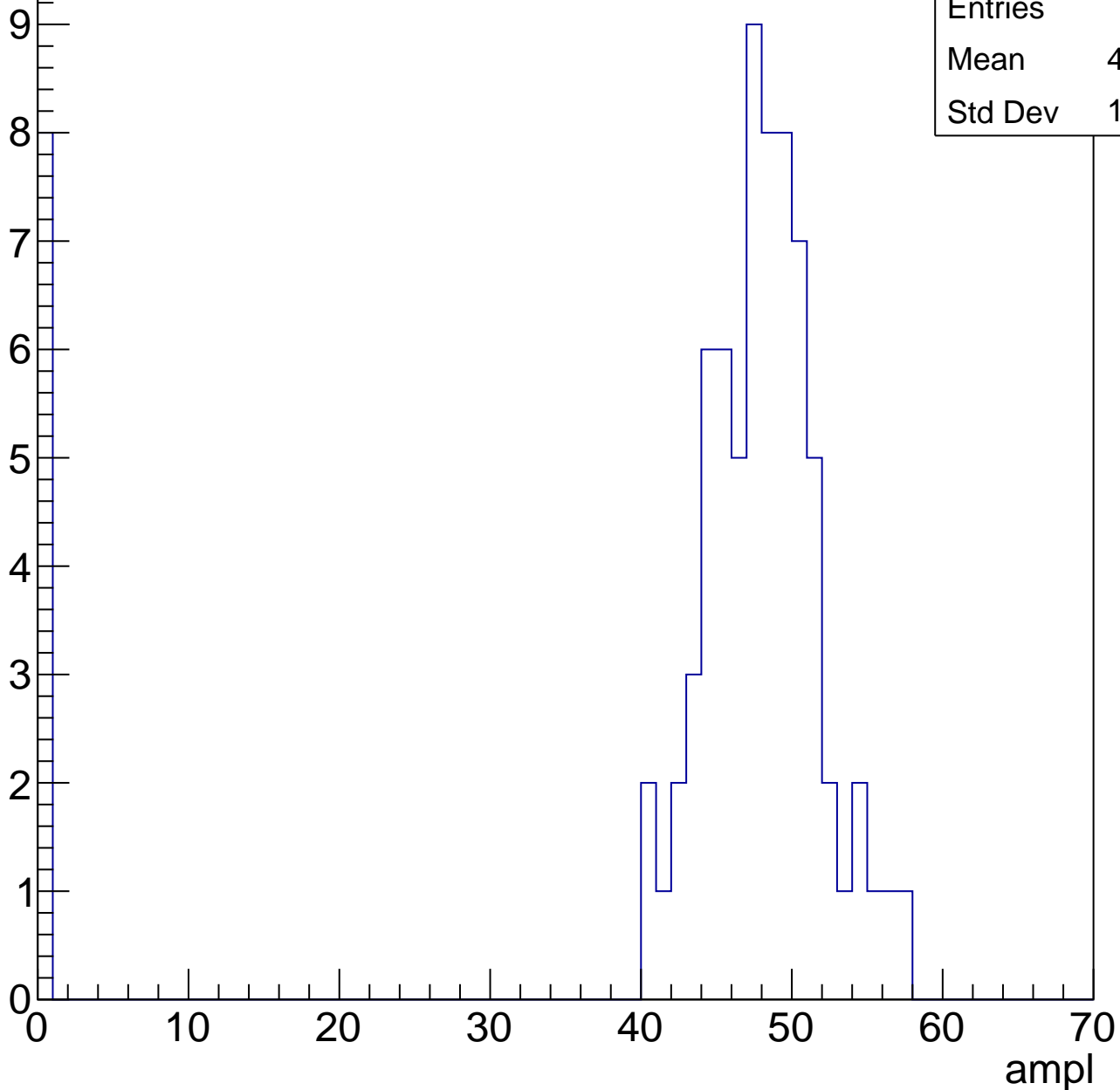


B1L103S, U24-ch84, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	42.76
Std Dev	14.85

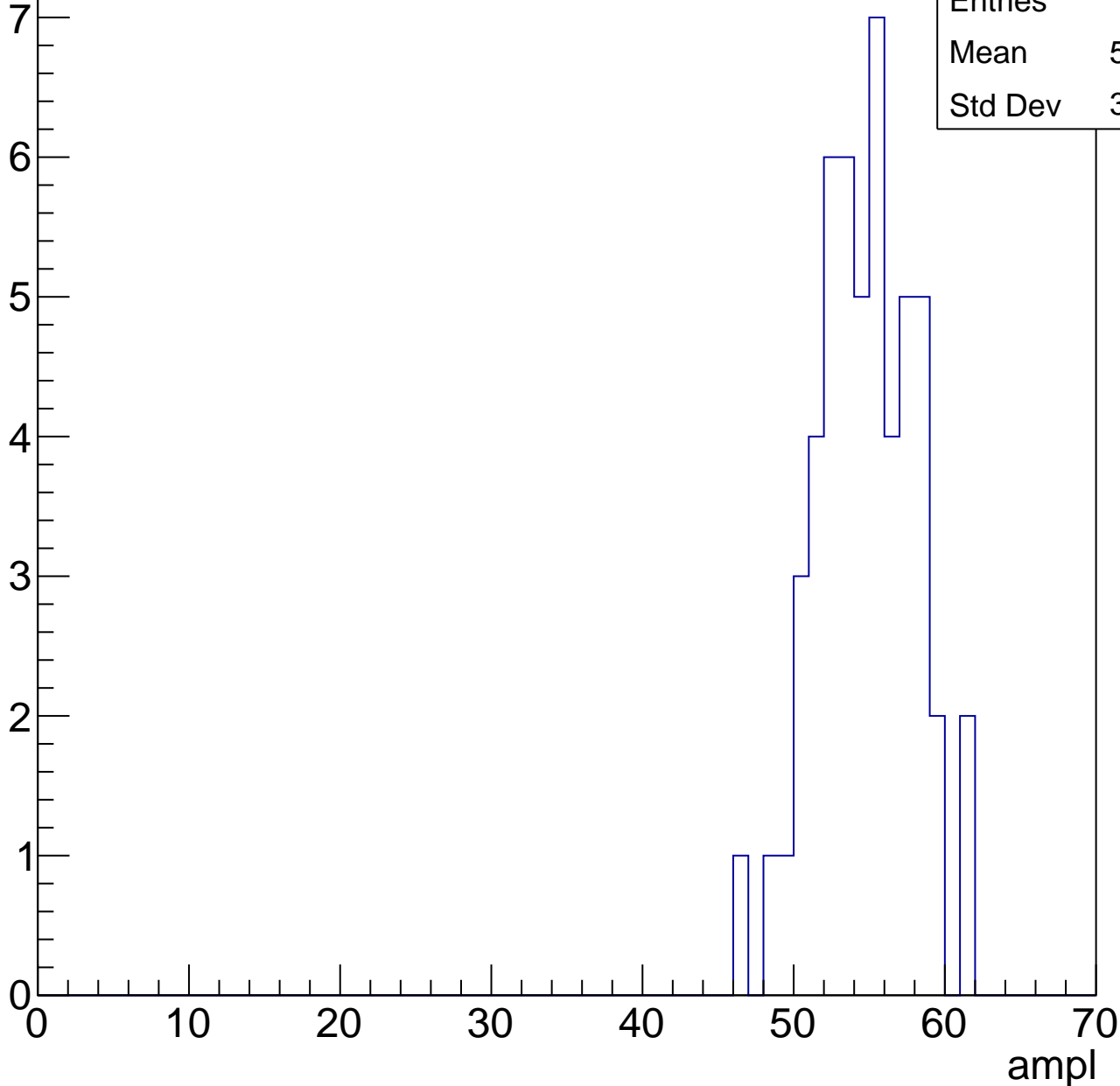


B1L103S, U24-ch84, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

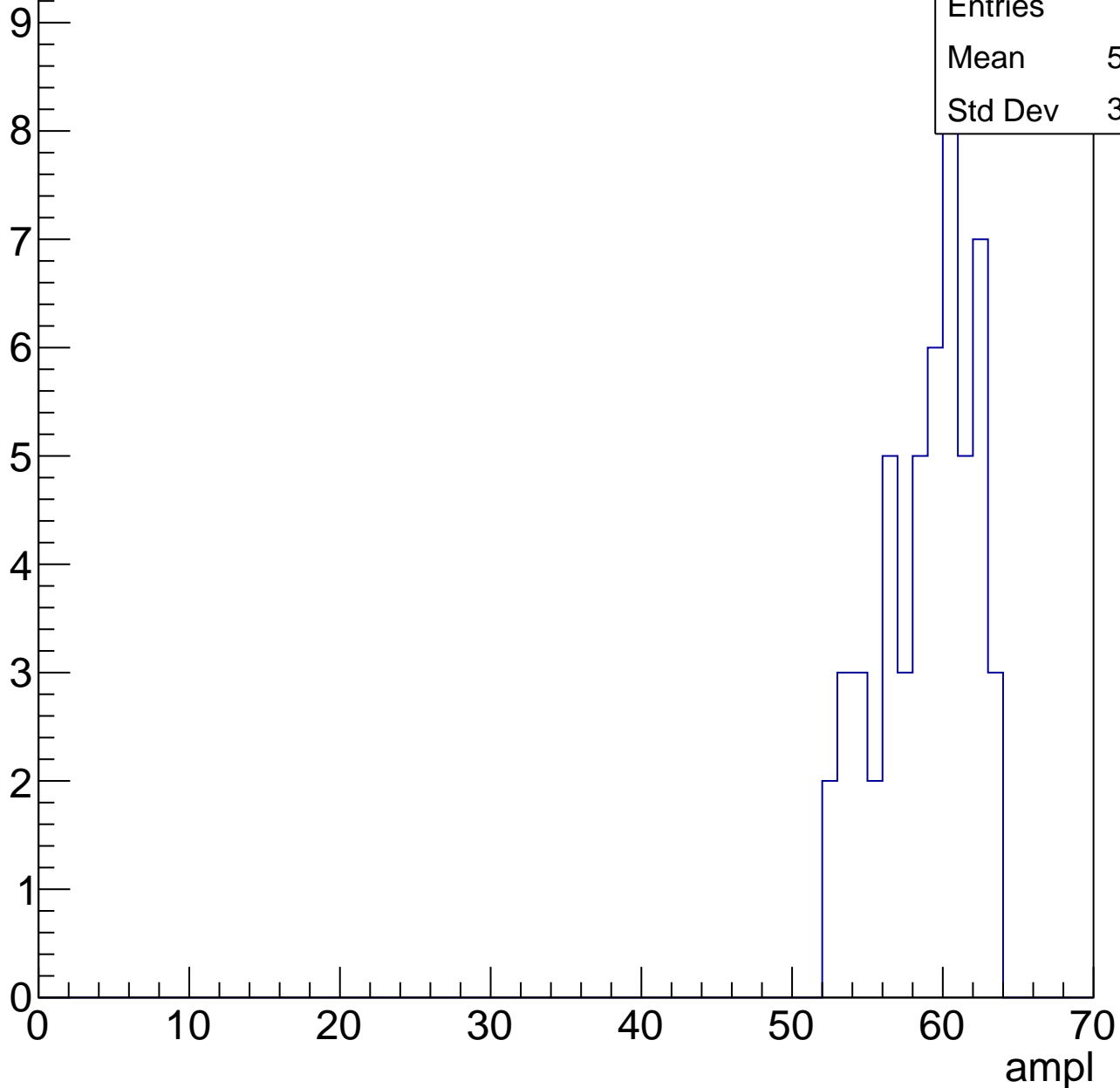
Entries	52
Mean	54.25
Std Dev	3.198



B1L103S, U24-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

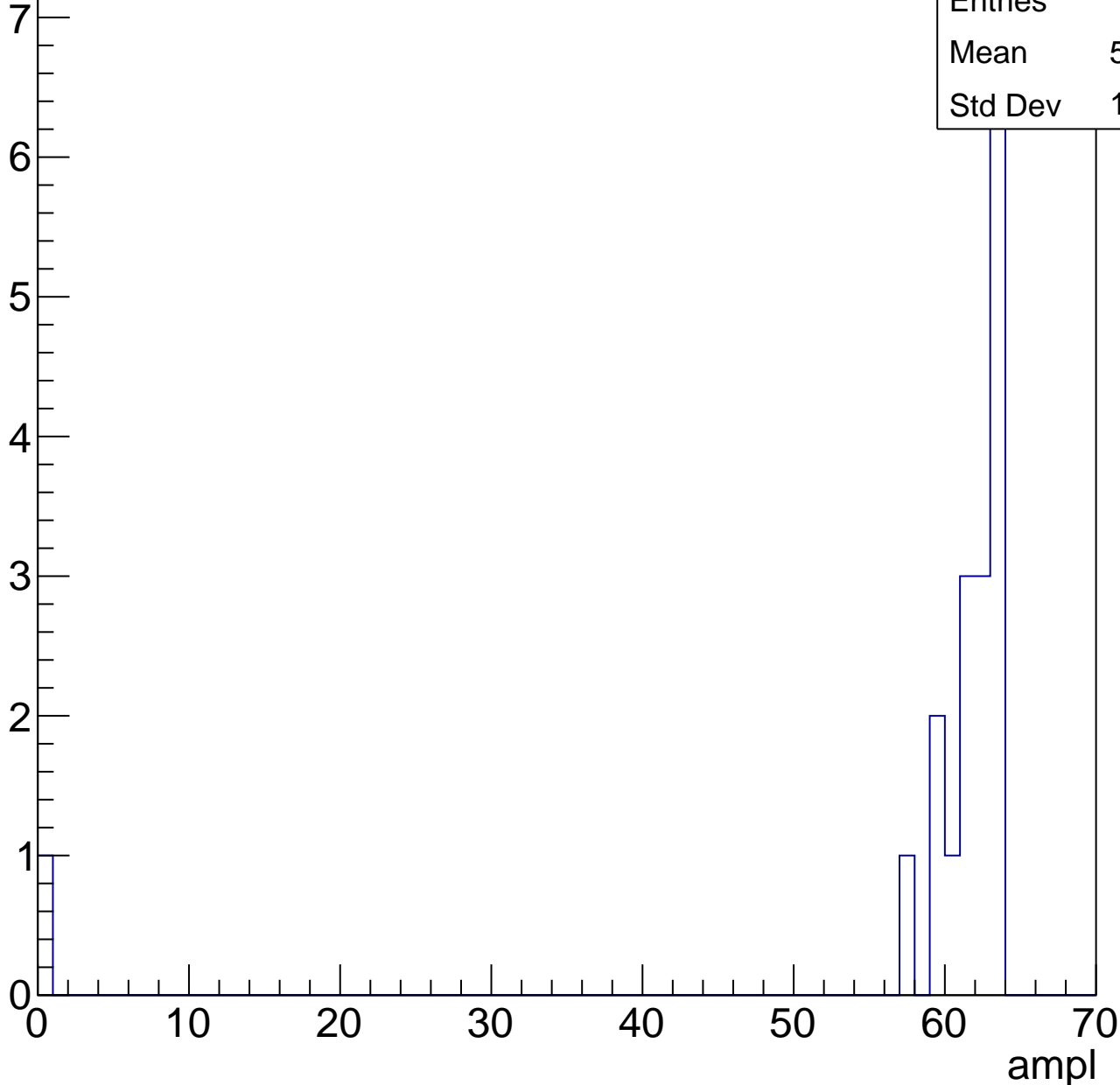


B1L103S, U24-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	58.06
Std Dev	14.18



B1L103S, U24-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

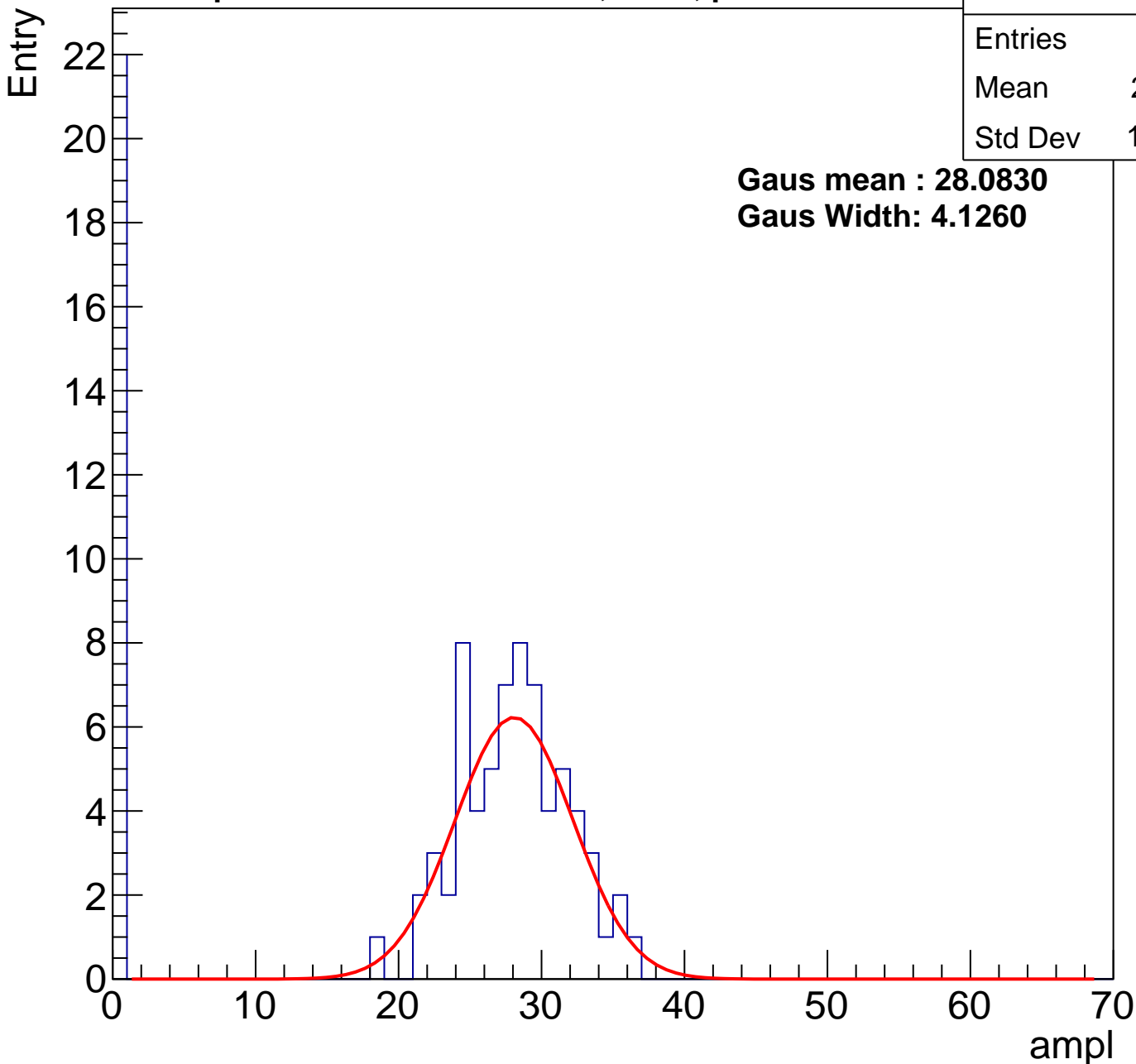
B1L103S, U24-ch85, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	20.81
Std Dev	12.36

Gaus mean : 28.0830

Gaus Width: 4.1260



B1L103S, U24-ch85, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	27.99
Std Dev	13.77

Gaus mean : 34.5109

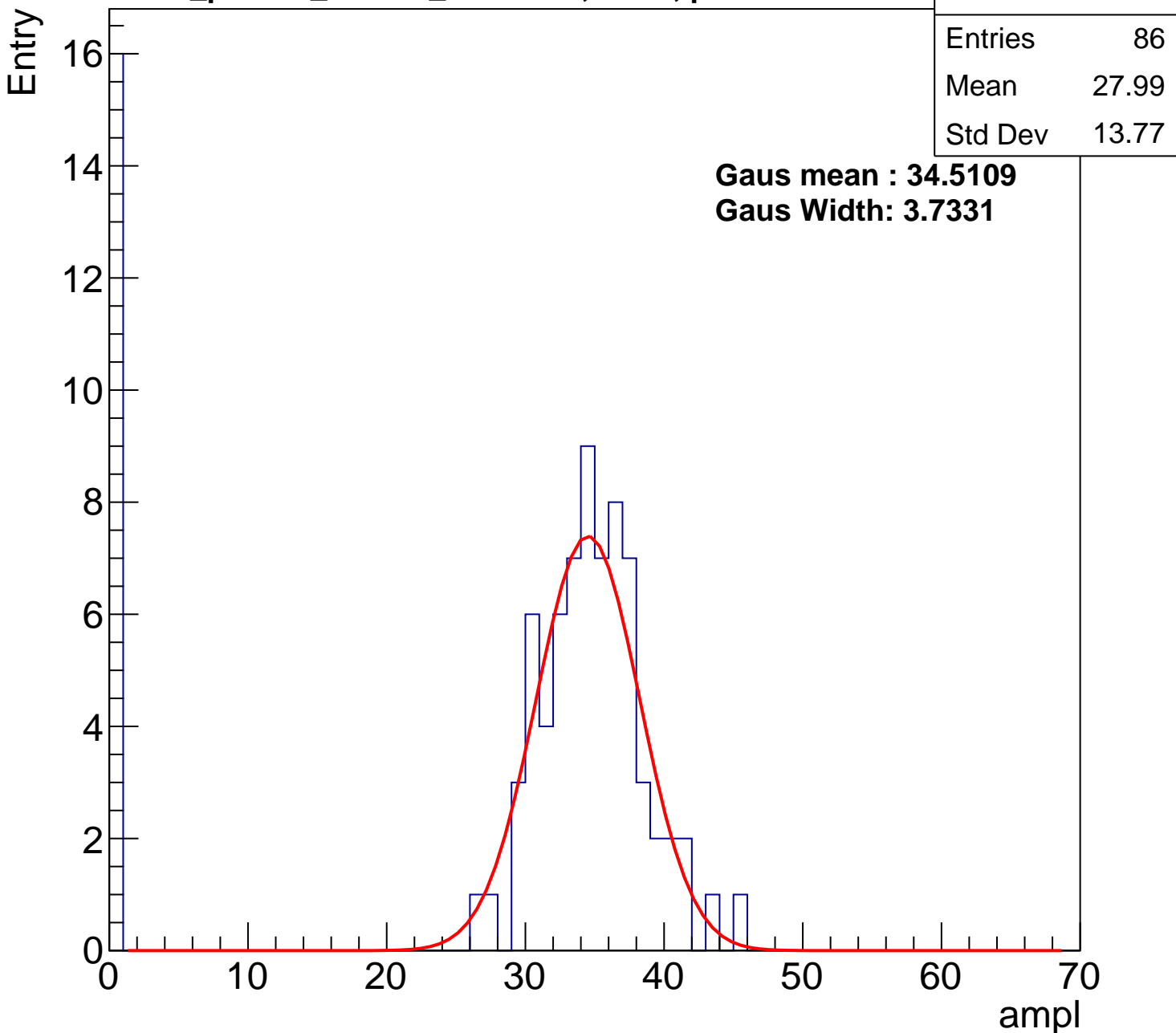
Gaus Width: 3.7331

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch85, adc2

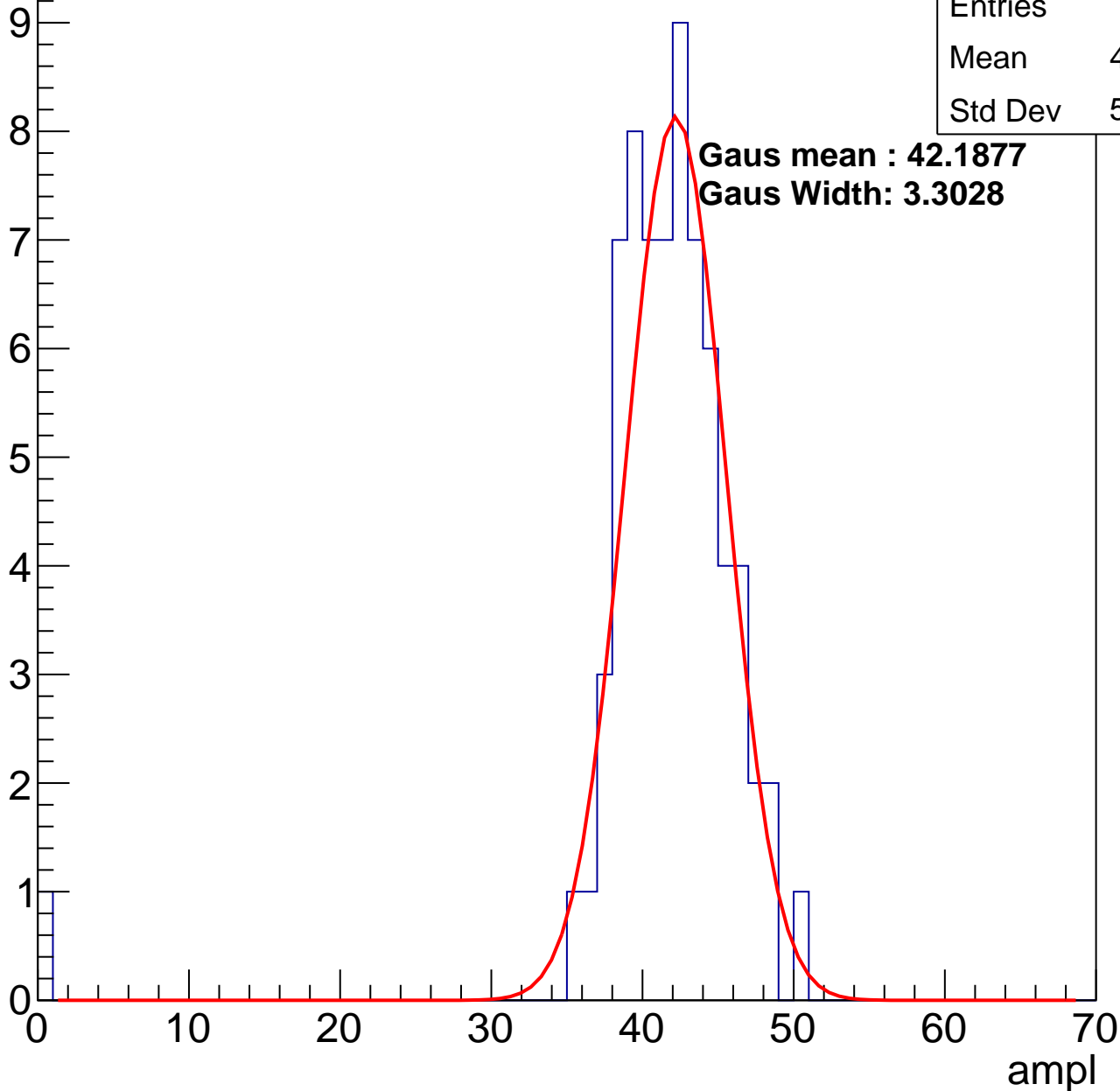
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	41.06
Std Dev	5.853

Gaus mean : 42.1877

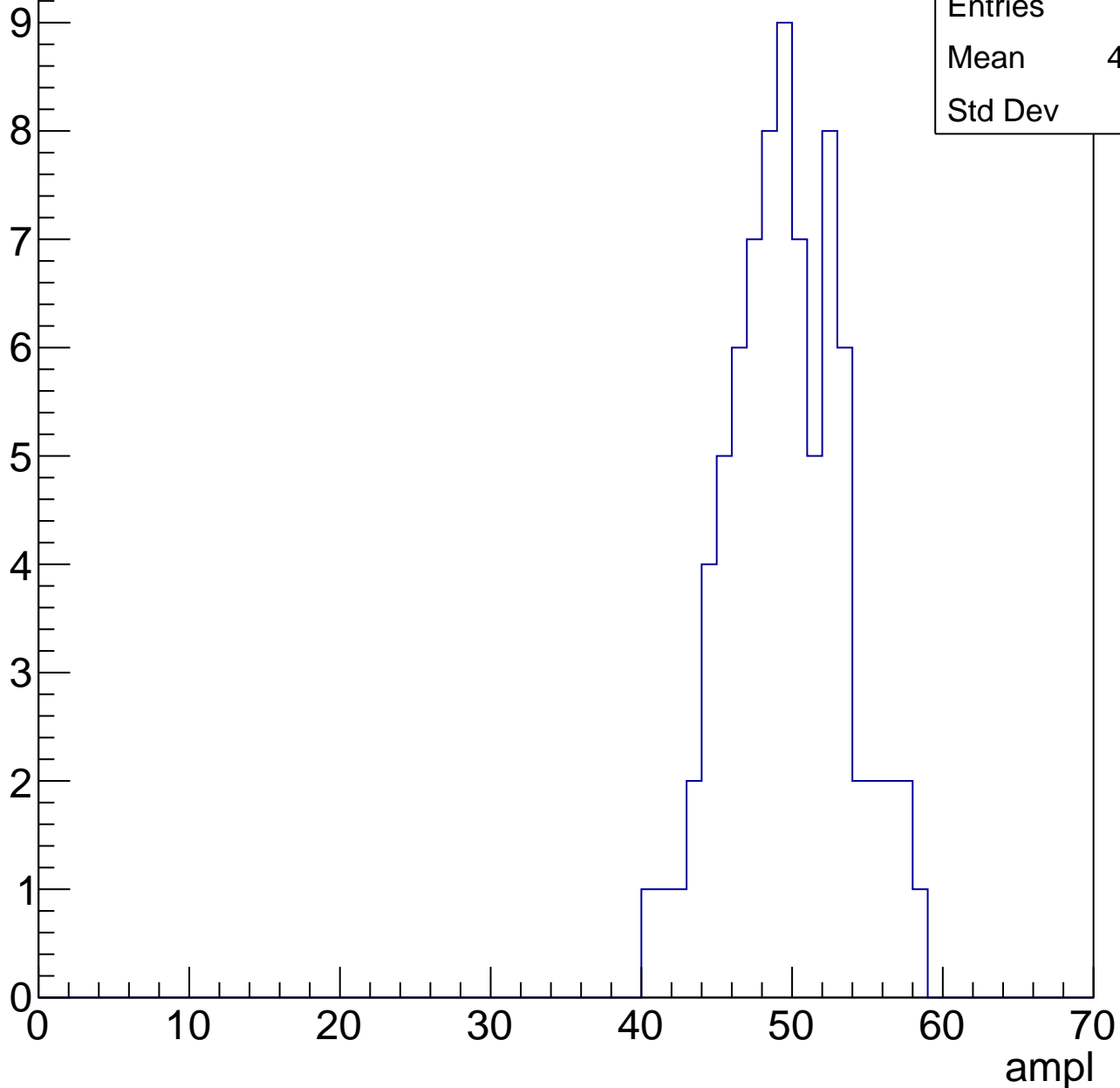
Gaus Width: 3.3028



B1L103S, U24-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

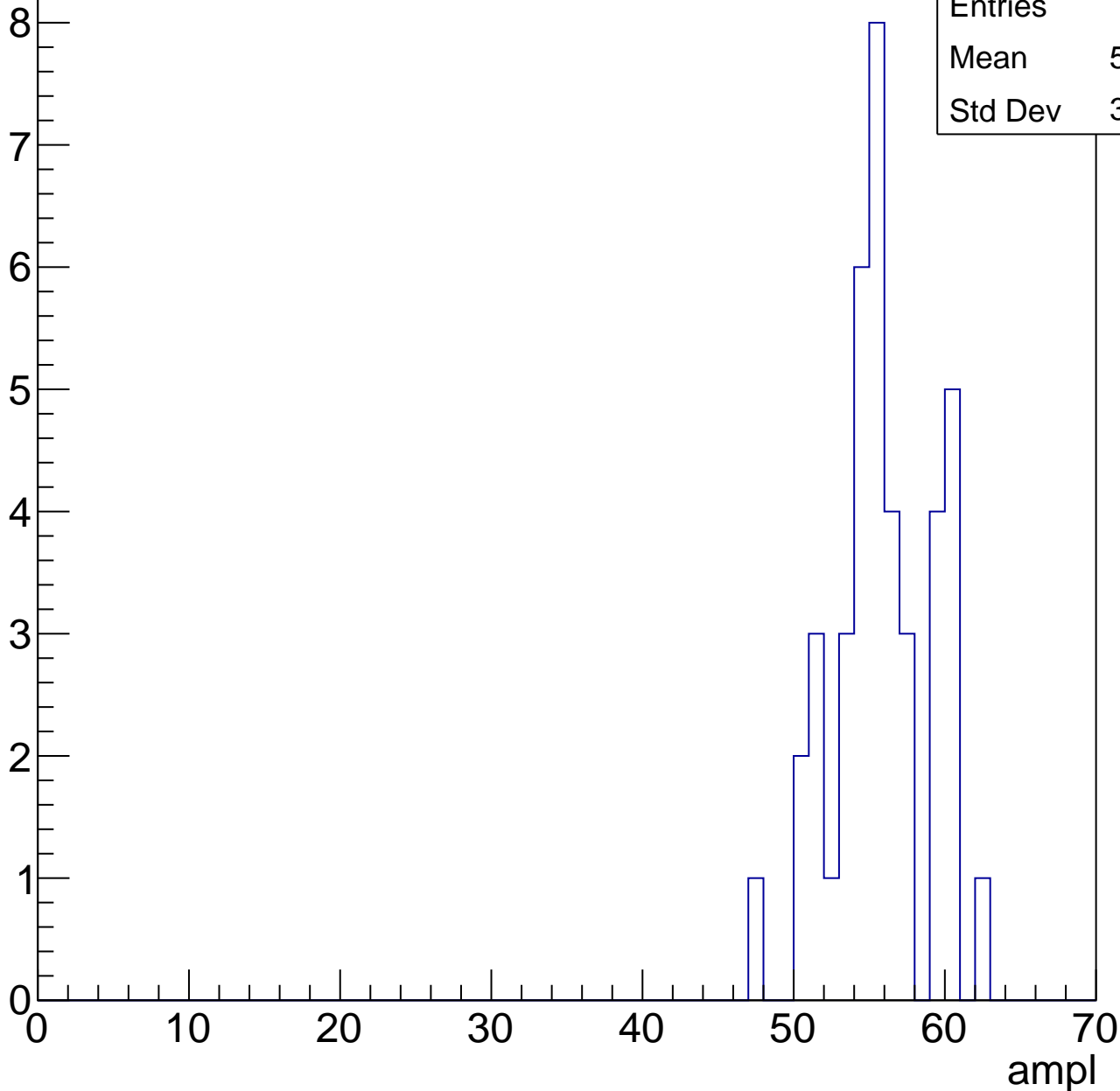


B1L103S, U24-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

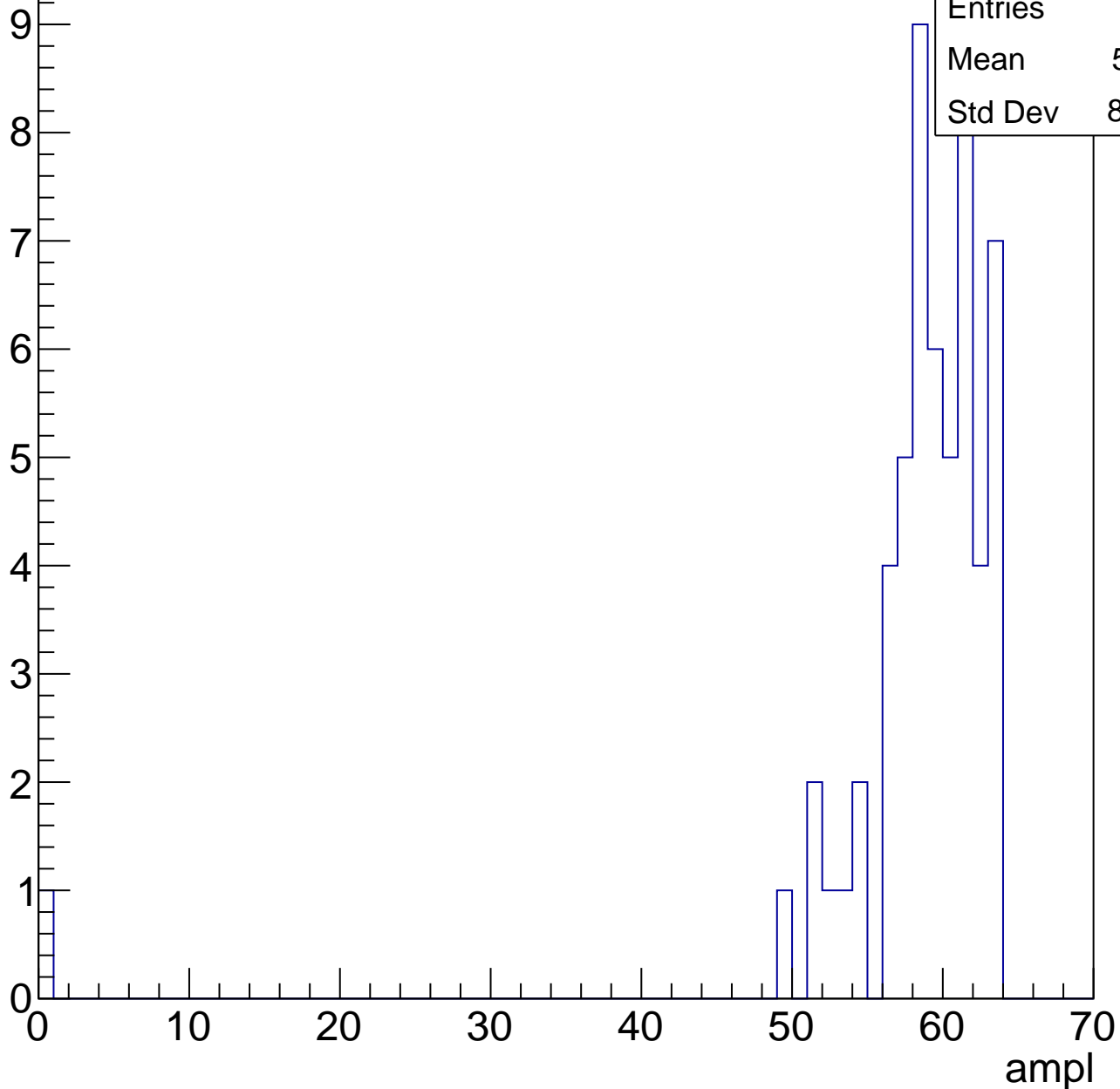
Entries	41
Mean	55.32
Std Dev	3.272



B1L103S, U24-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

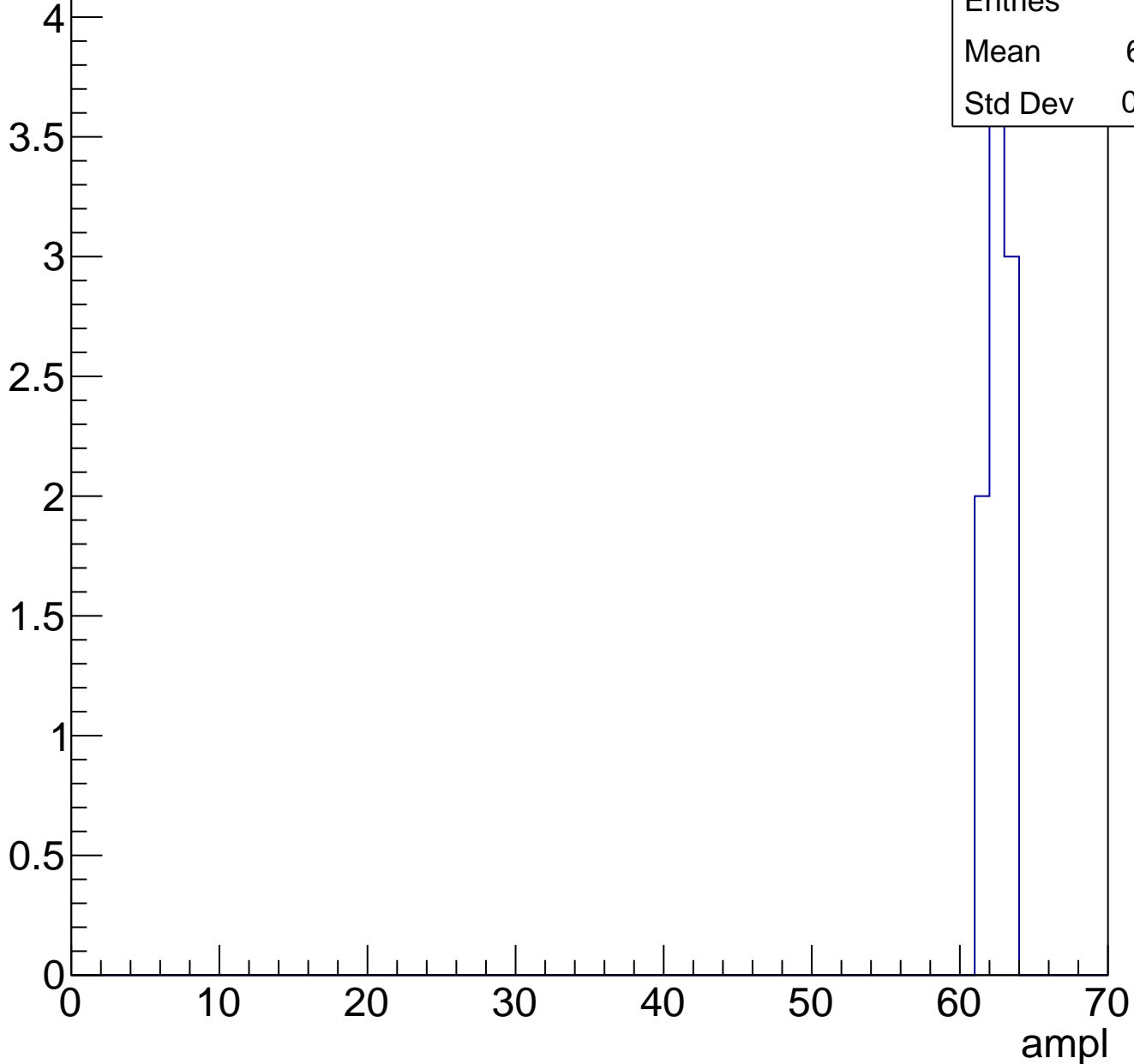
Entry



B1L103S, U24-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U24-ch86, adc0

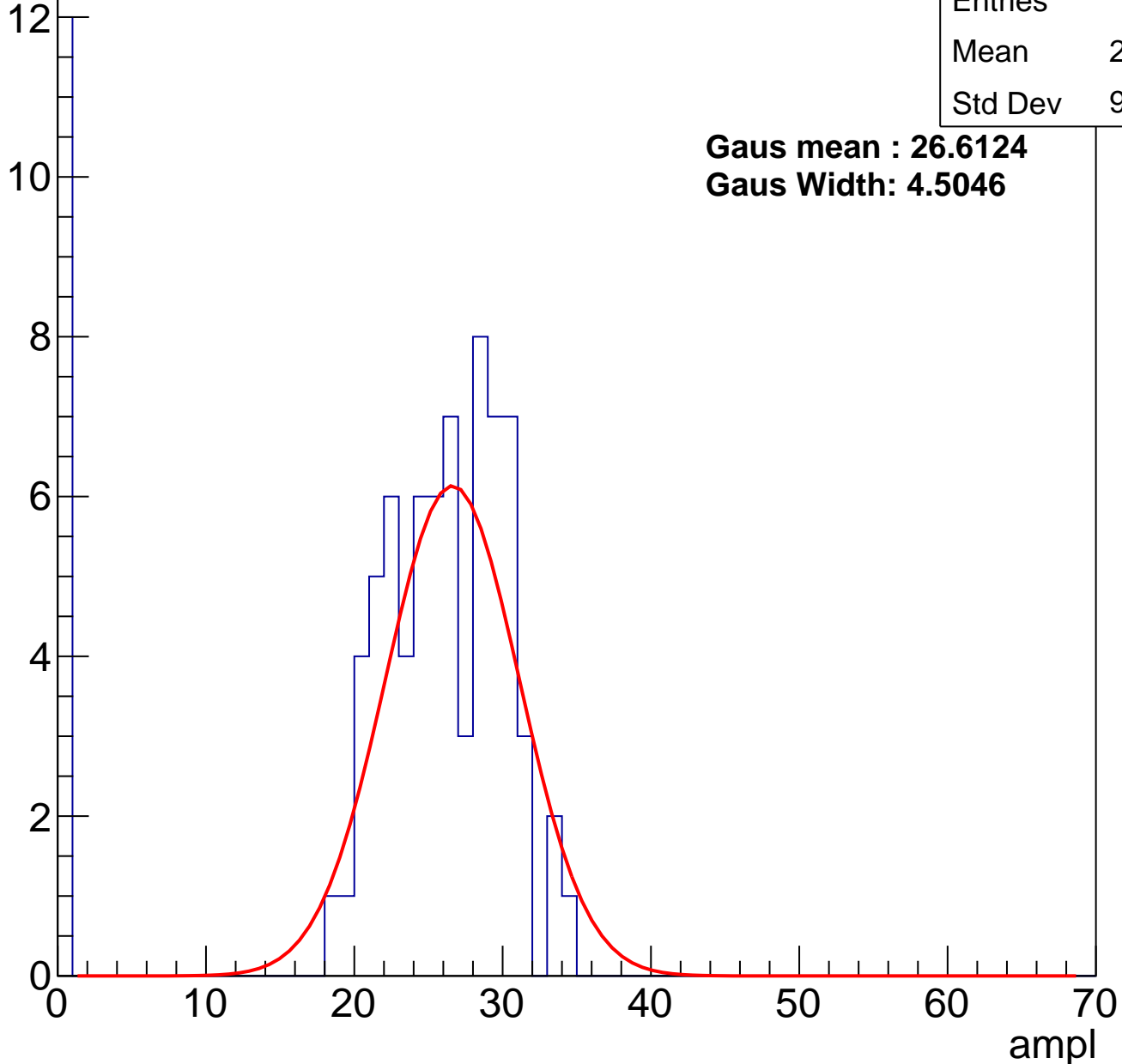
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	22.08
Std Dev	9.713

Gaus mean : 26.6124

Gaus Width: 4.5046

Entry



B1L103S, U24-ch86, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	28.99
Std Dev	11.82

Gaus mean : 34.0195

Gaus Width: 4.1627

Entry

10

8

6

4

2

0

0

10

20

30

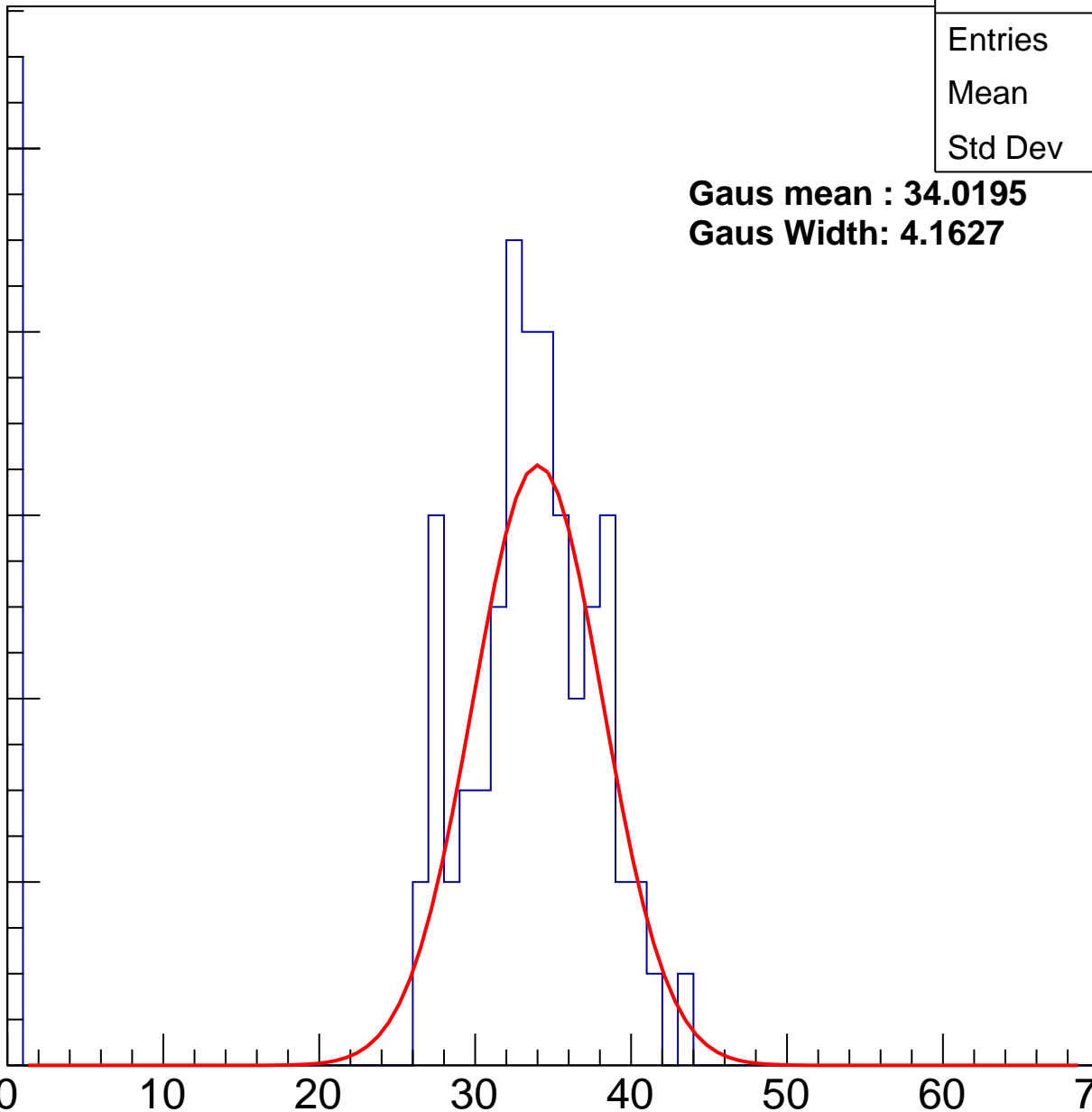
40

50

60

70

ampl



B1L103S, U24-ch86, adc2

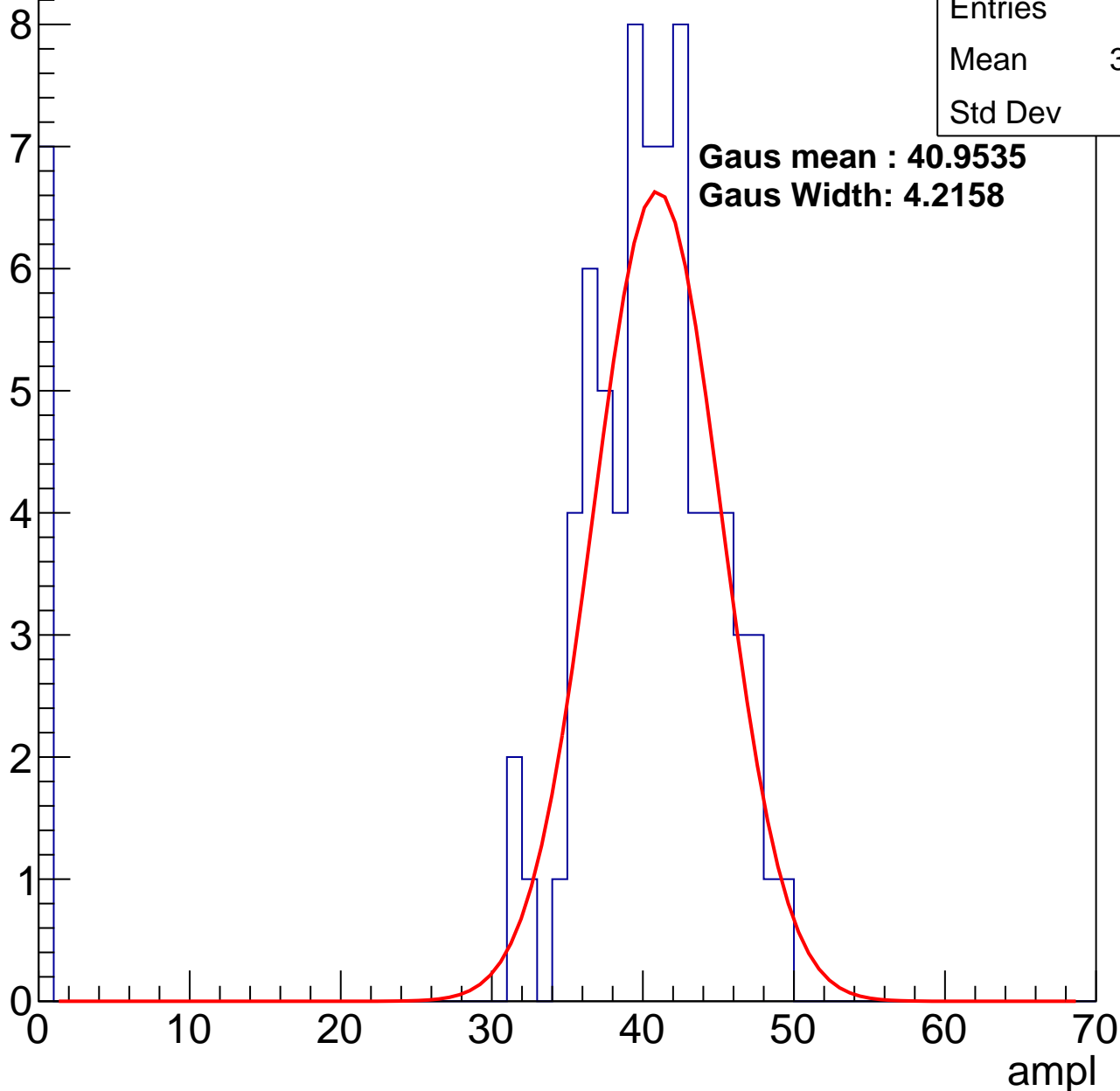
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	36.75
Std Dev	12

Gaus mean : 40.9535

Gaus Width: 4.2158

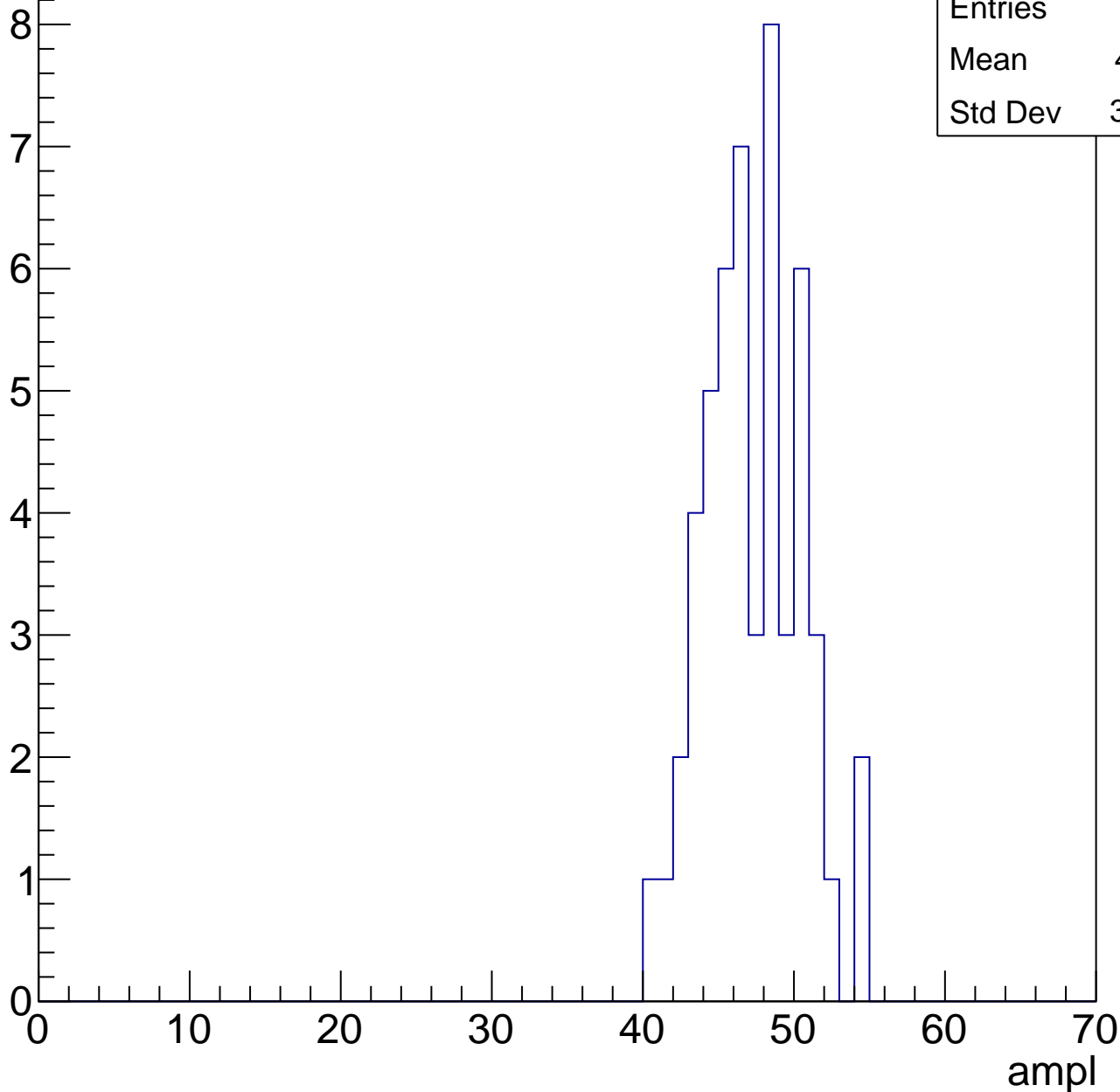


B1L103S, U24-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	46.81
Std Dev	3.156

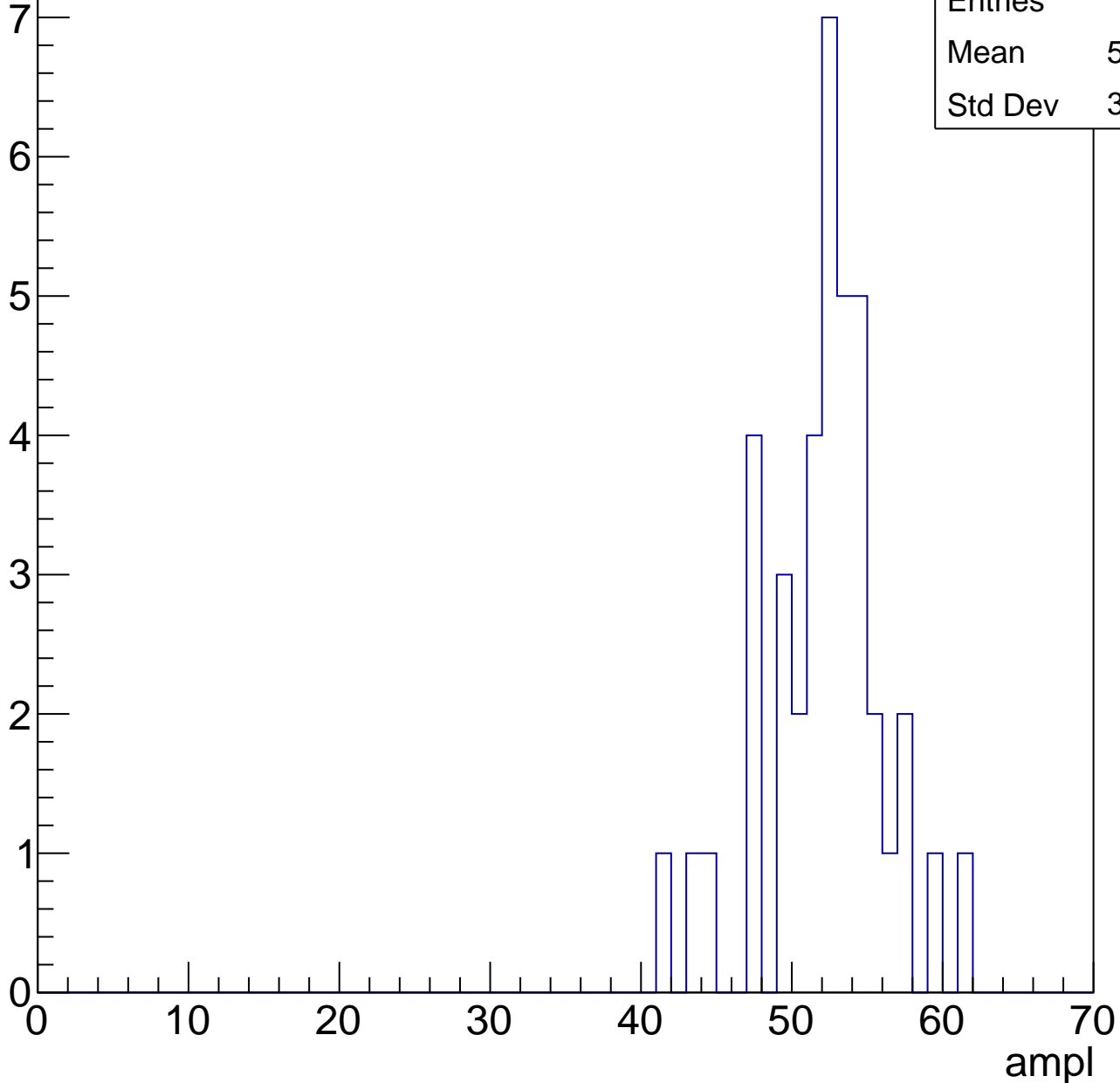


B1L103S, U24-ch86, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	51.65
Std Dev	3.997

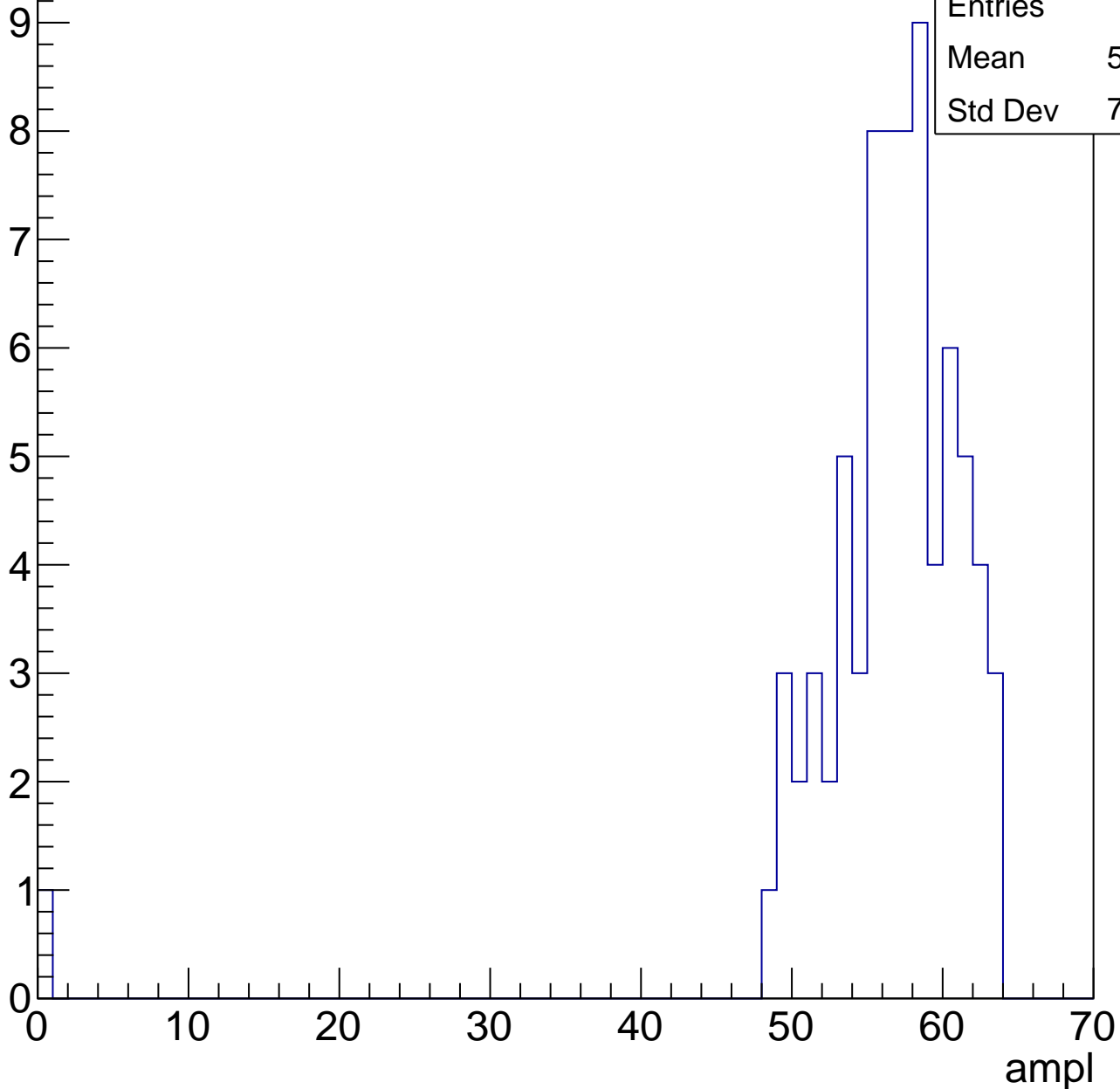


B1L103S, U24-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	55.77
Std Dev	7.465

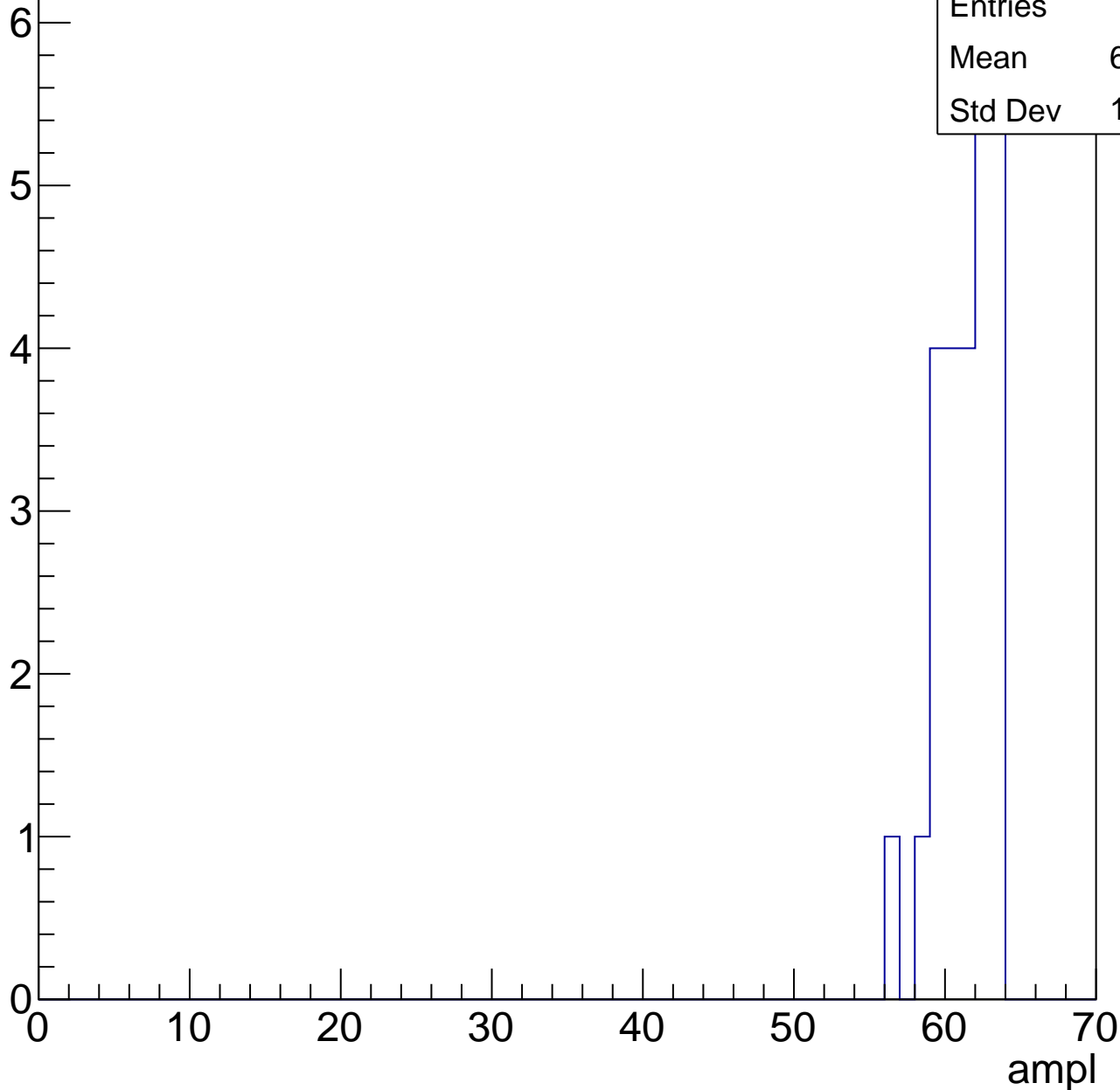


B1L103S, U24-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	60.92
Std Dev	1.796

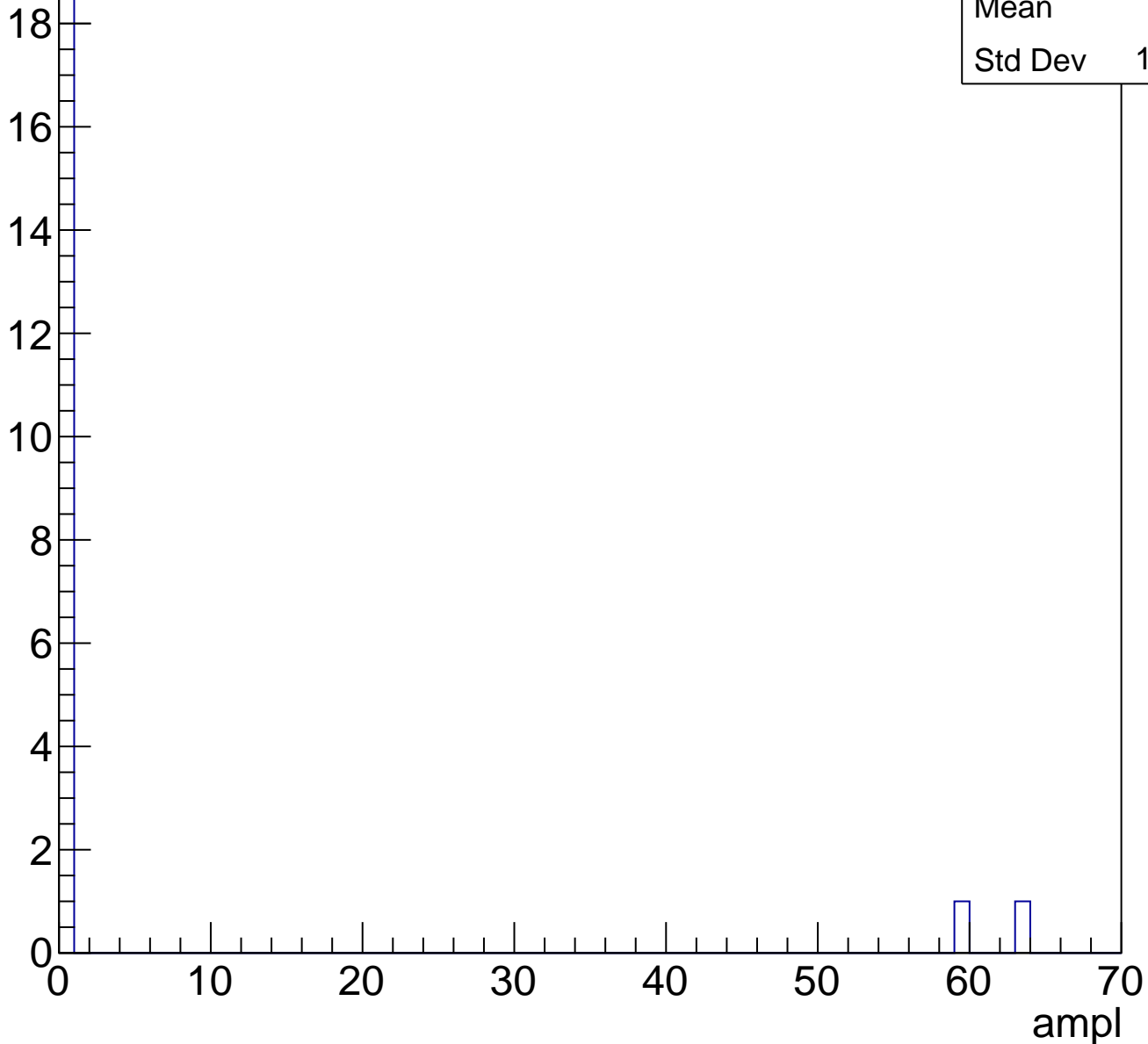


B1L103S, U24-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.92

Entry



B1L103S, U24-ch87, adc0

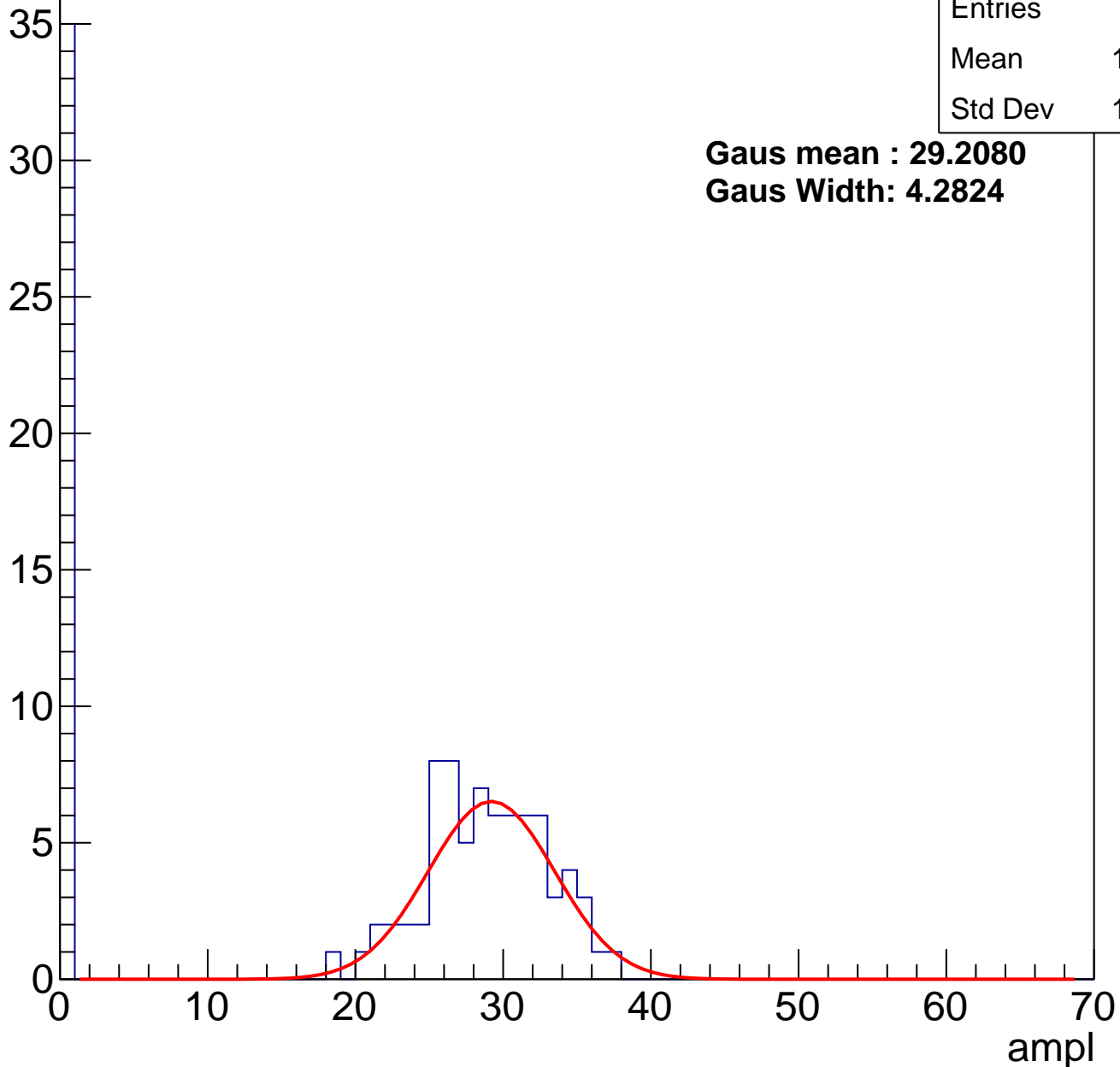
calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	19.28
Std Dev	13.67

Gaus mean : 29.2080

Gaus Width: 4.2824

Entry



B1L103S, U24-ch87, adc1

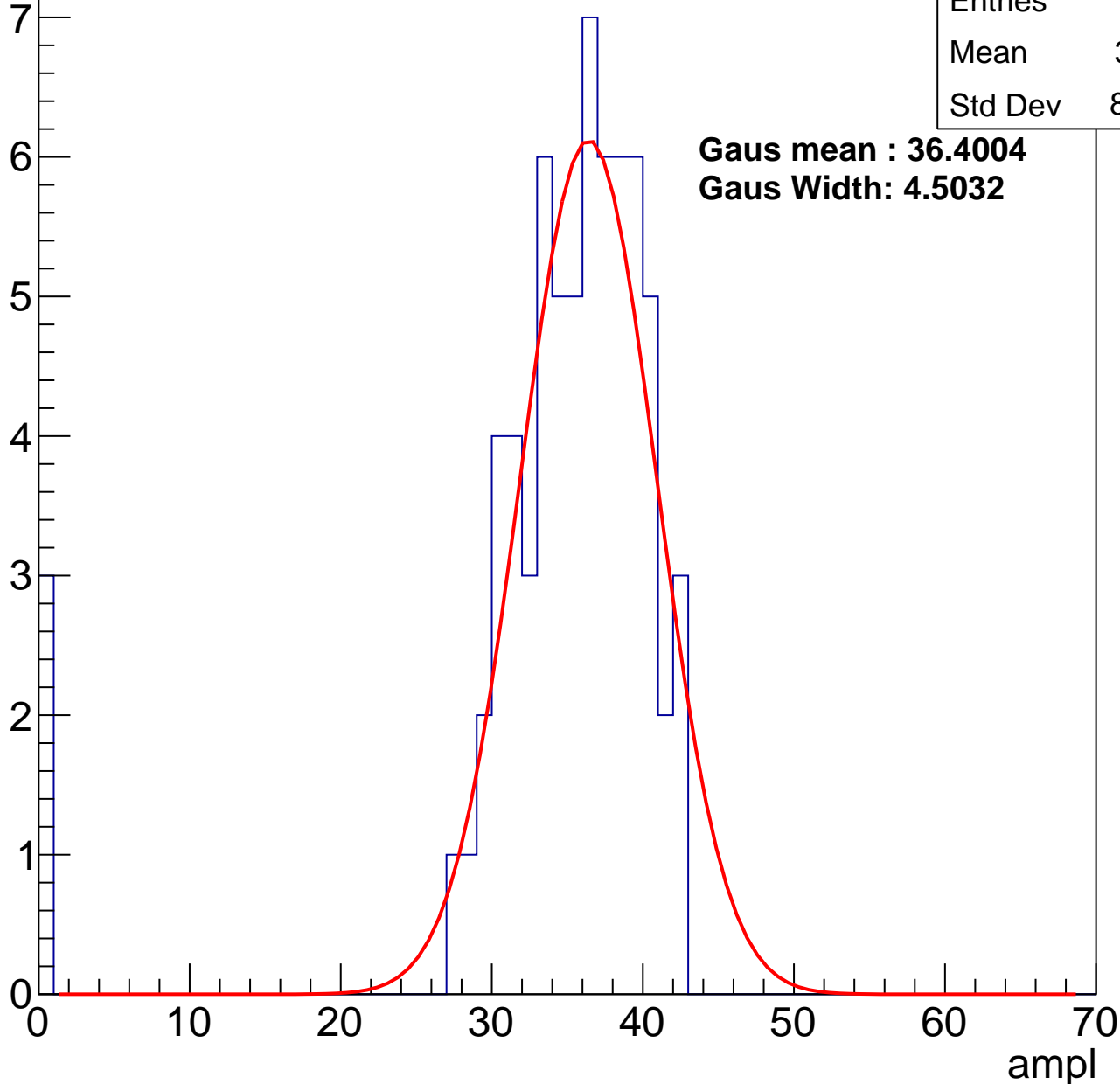
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	33.91
Std Dev	8.097

Gaus mean : 36.4004

Gaus Width: 4.5032



B1L103S, U24-ch87, adc2

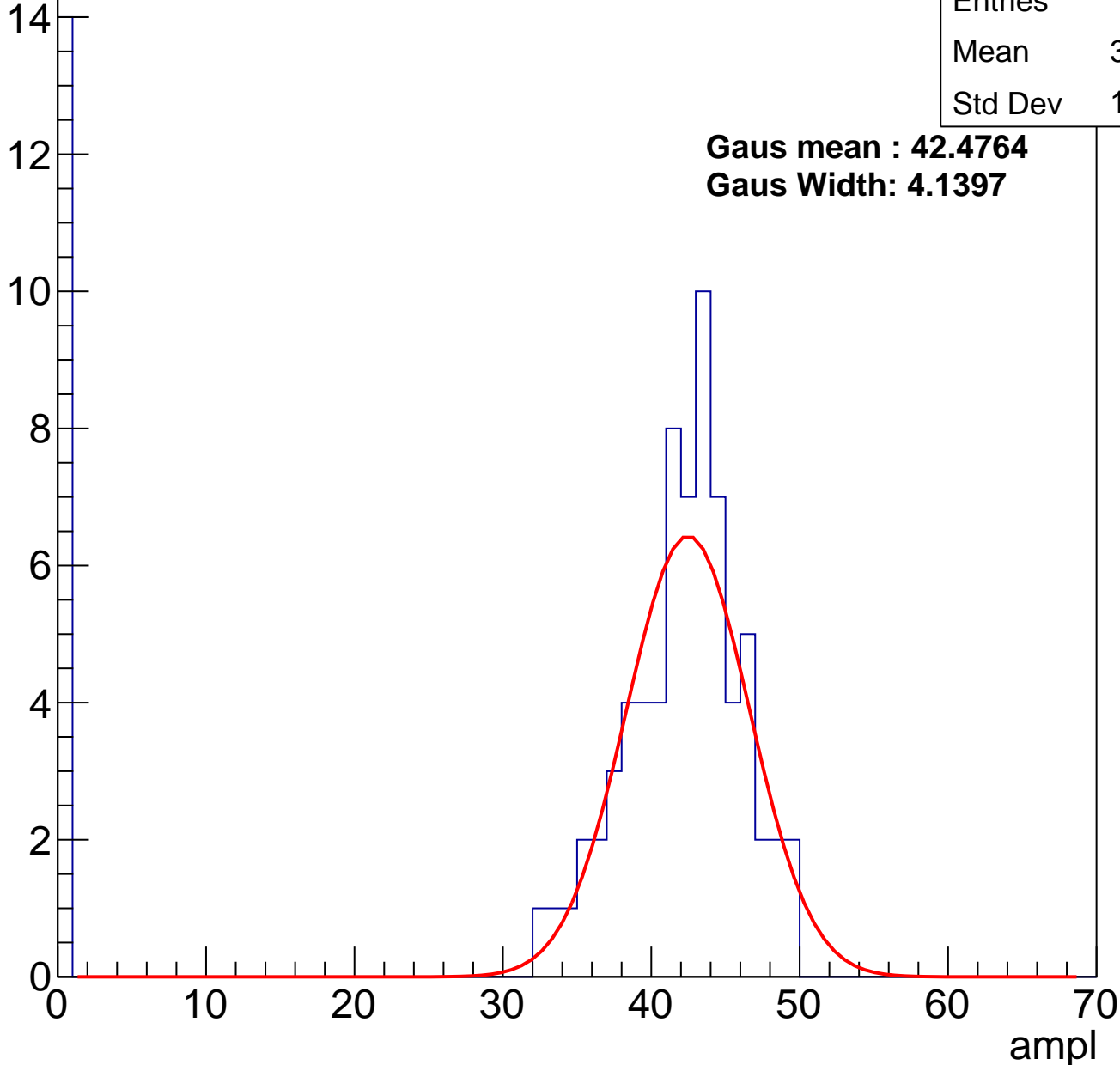
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	34.67
Std Dev	15.99

Gaus mean : 42.4764

Gaus Width: 4.1397

Entry

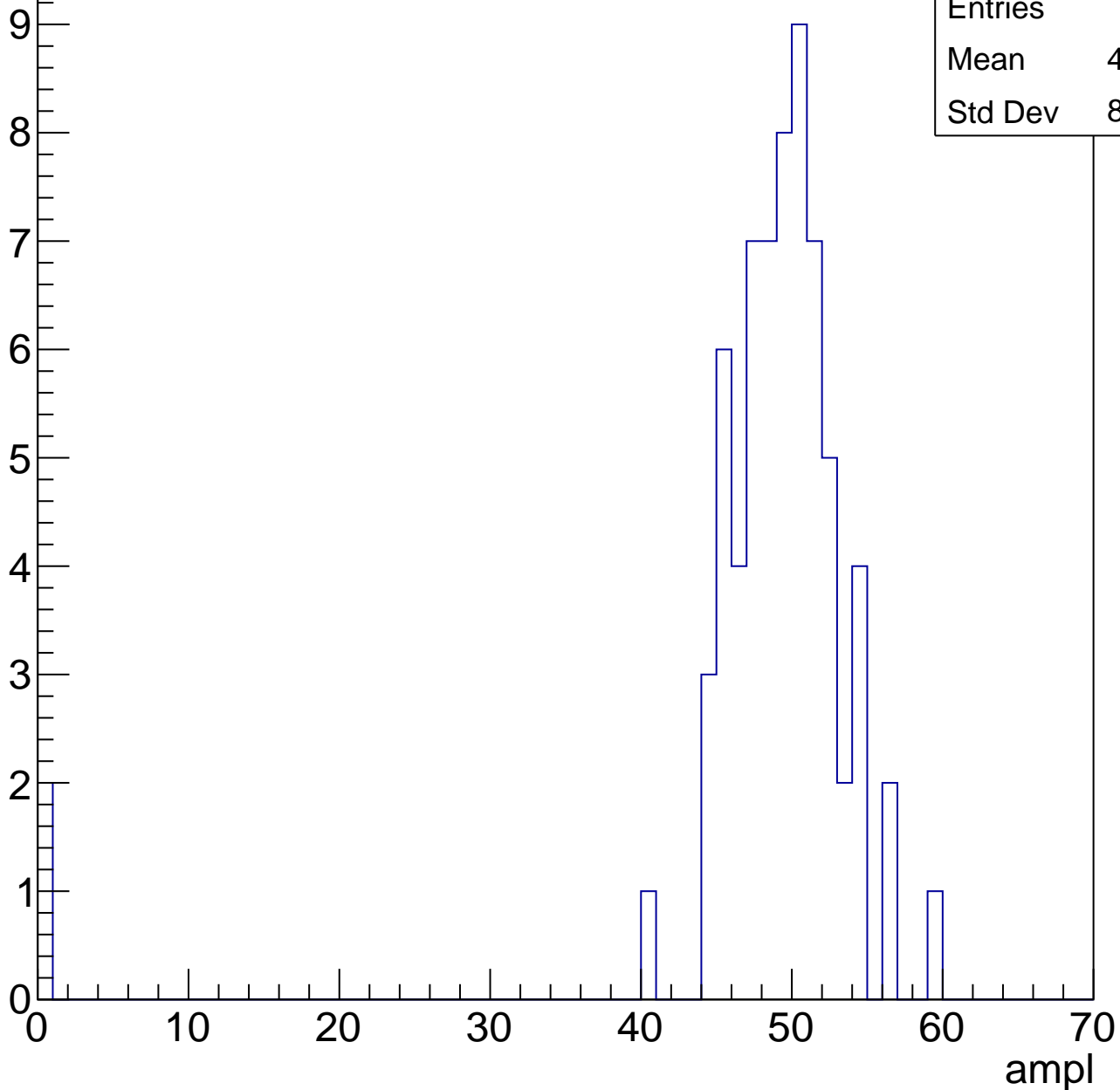


B1L103S, U24-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

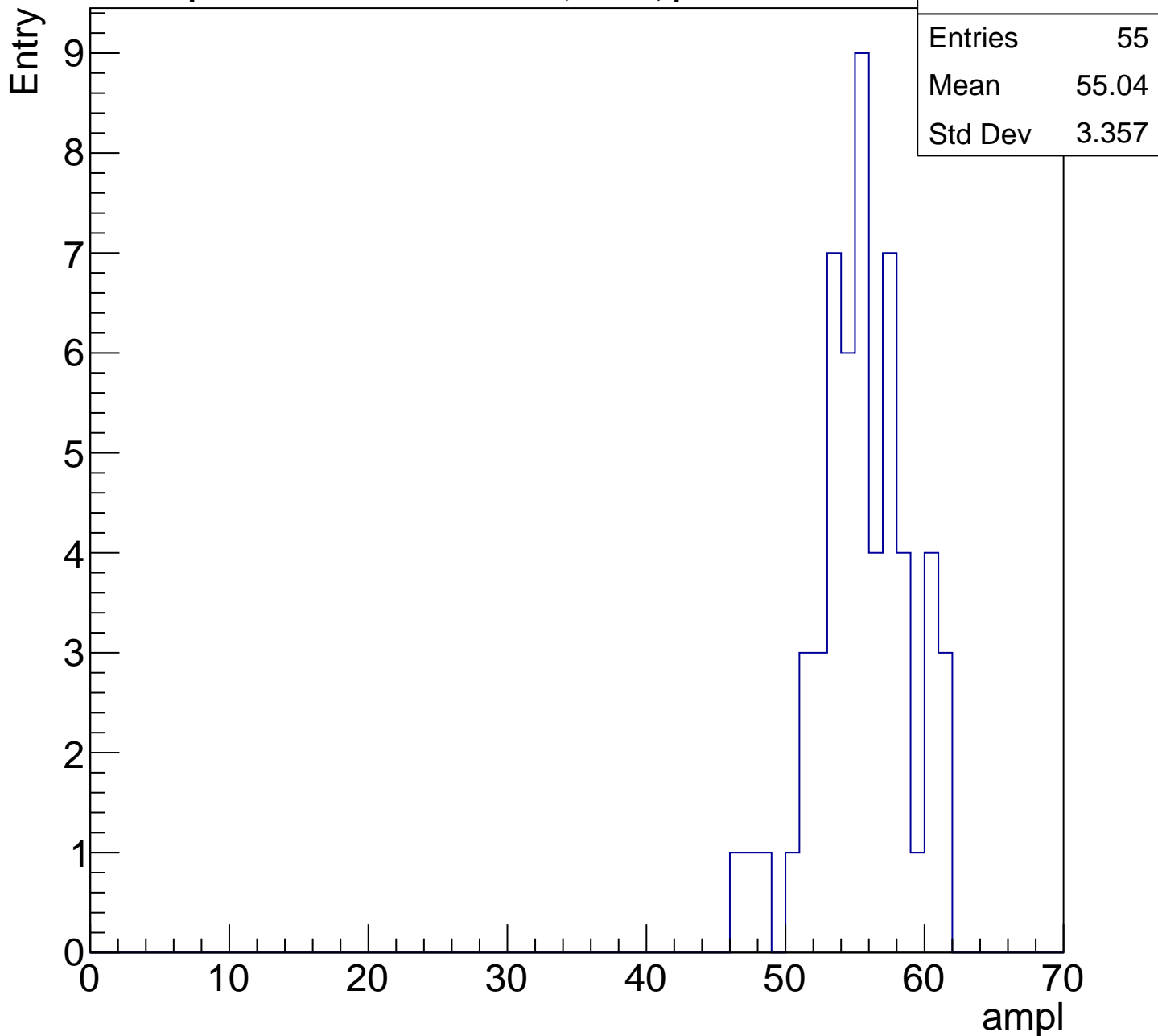
Entry

Entries	68
Mean	47.69
Std Dev	8.928



B1L103S, U24-ch87, adc4

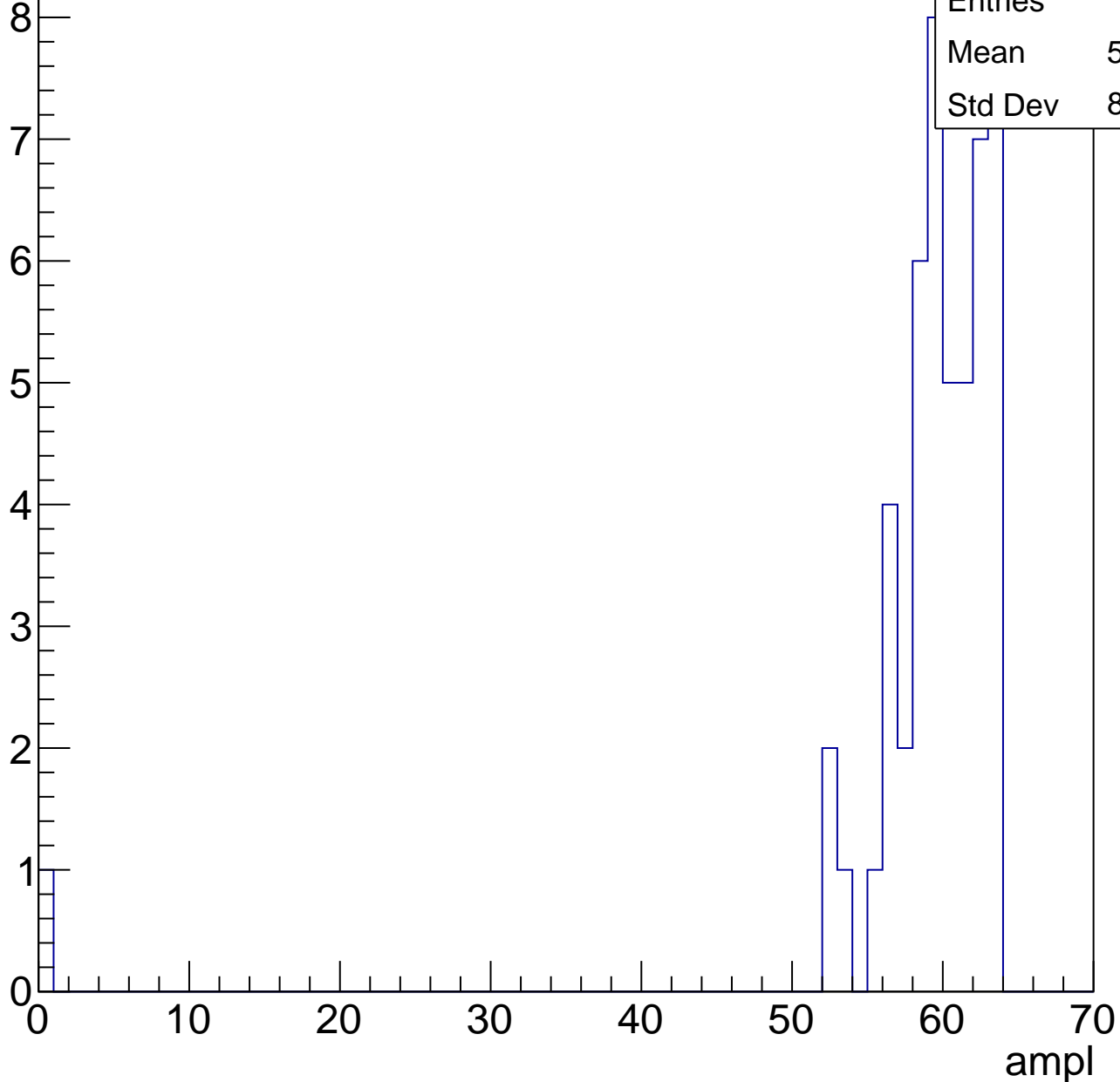
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U24-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

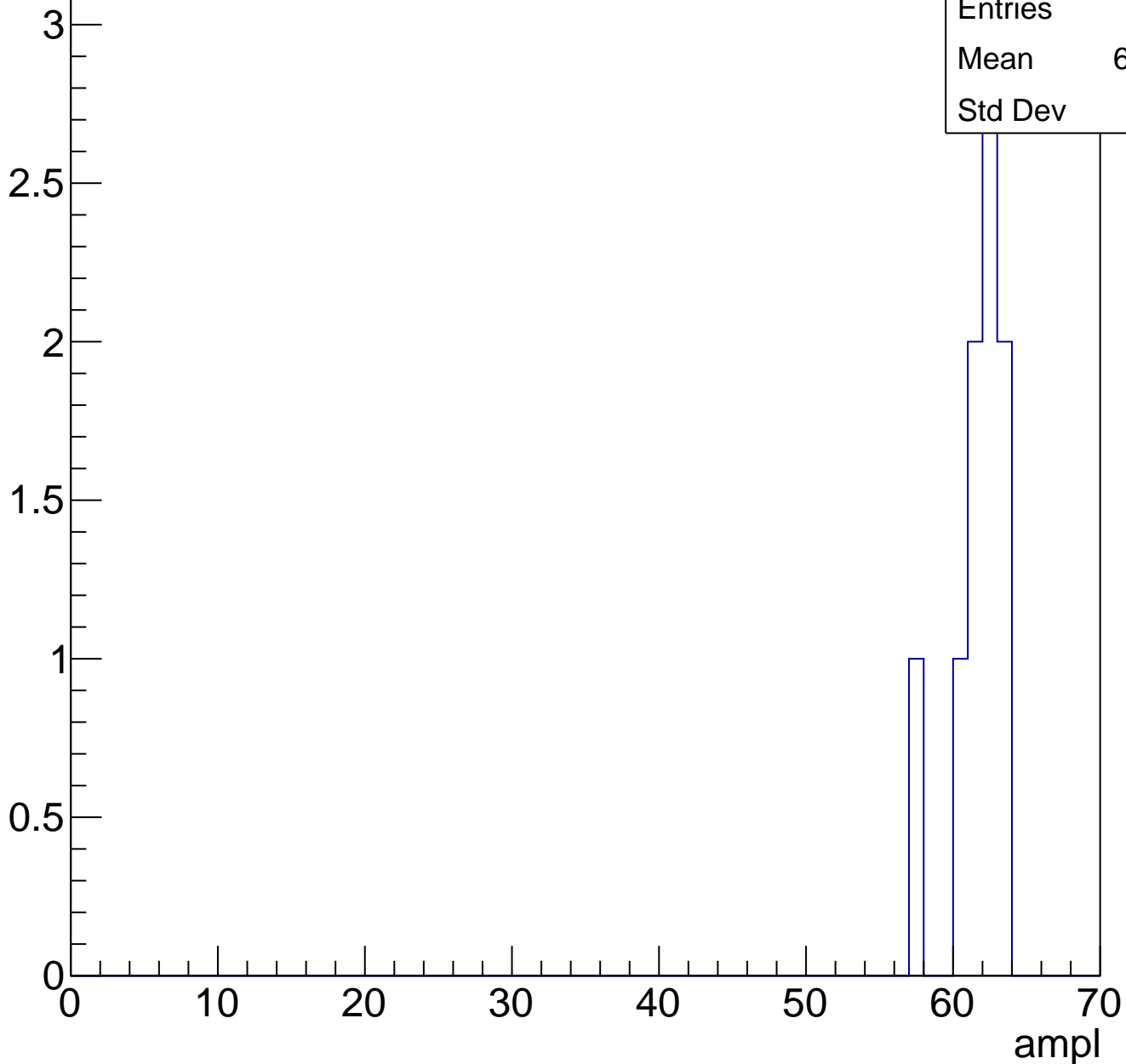
Entry



B1L103S, U24-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch88, adc0

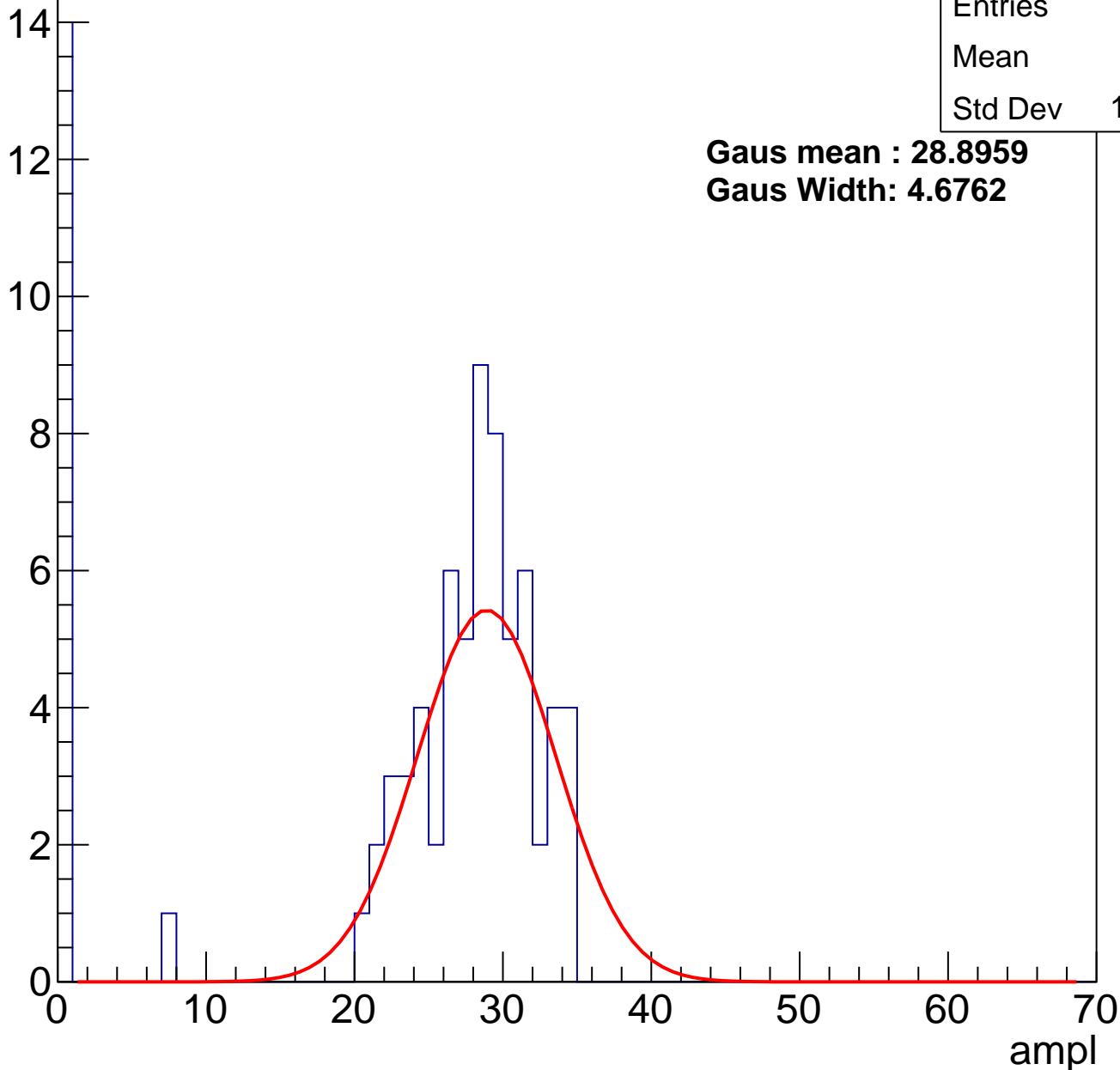
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	22.7
Std Dev	11.26

Gaus mean : 28.8959

Gaus Width: 4.6762

Entry



B1L103S, U24-ch88, adc1

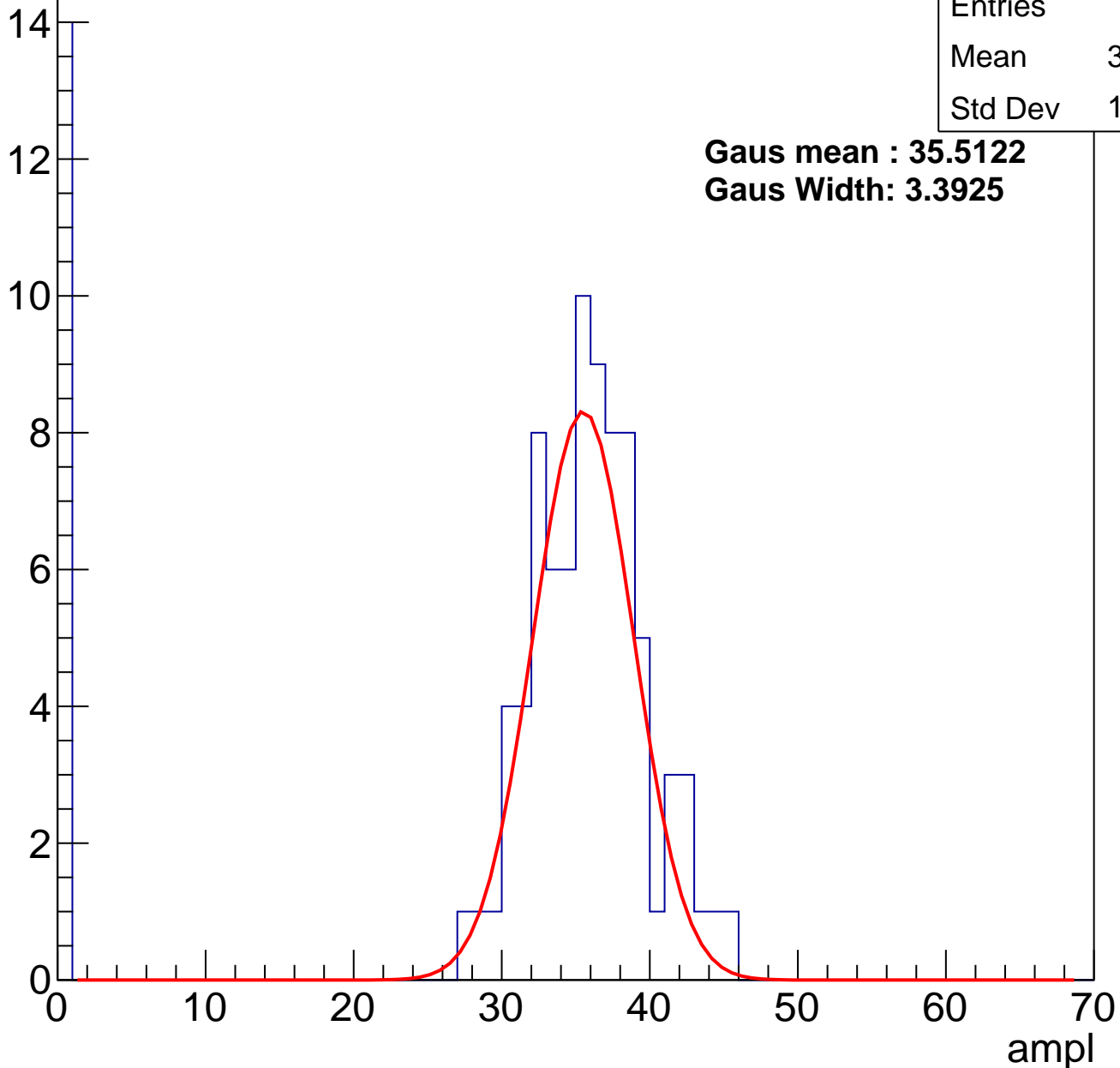
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	30.27
Std Dev	13.04

Gaus mean : 35.5122

Gaus Width: 3.3925

Entry



B1L103S, U24-ch88, adc2

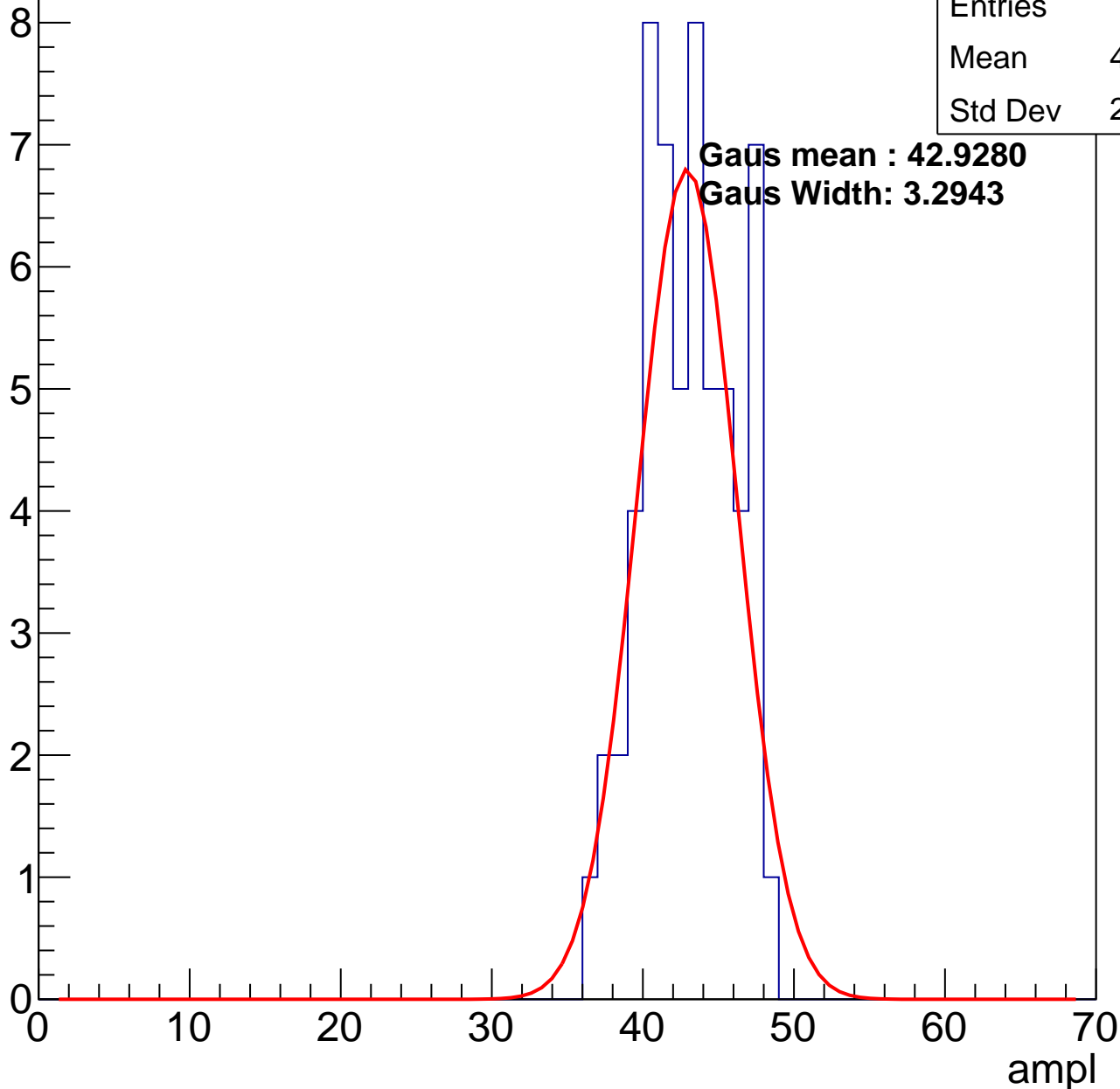
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	42.53
Std Dev	2.982

Gaus mean : 42.9280

Gaus Width: 3.2943

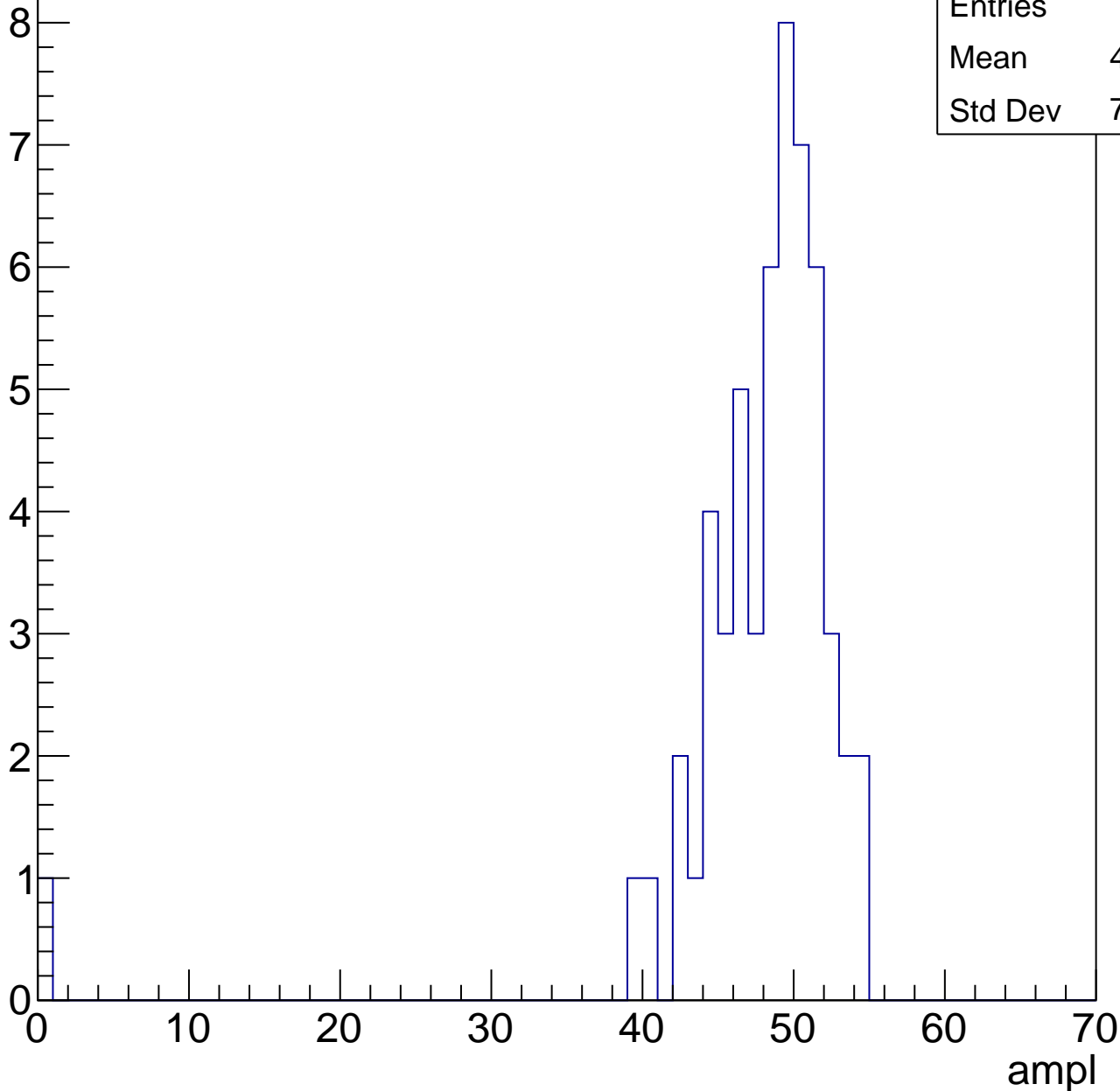


B1L103S, U24-ch88, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	47.16
Std Dev	7.238

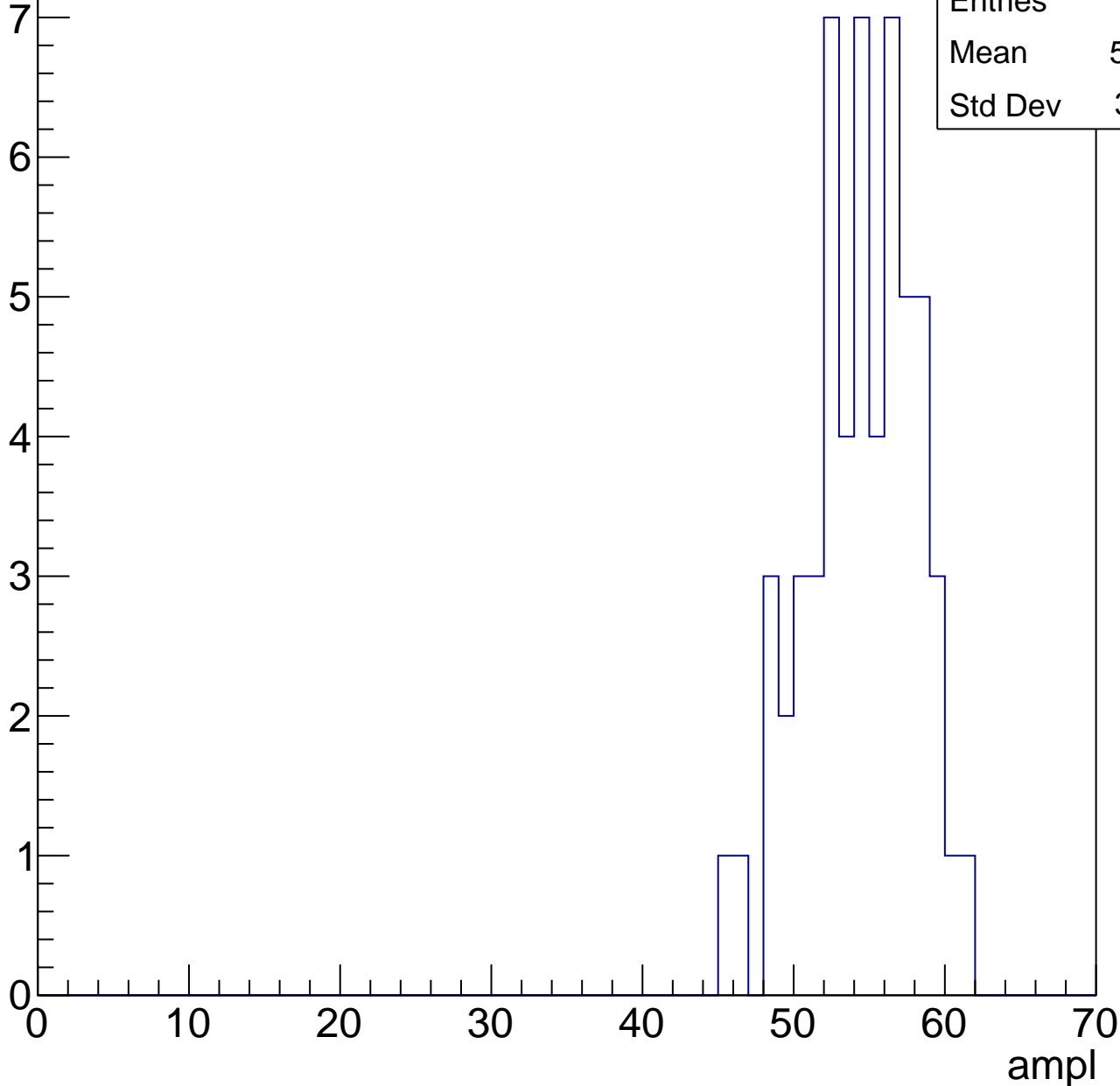


B1L103S, U24-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	53.95
Std Dev	3.571



B1L103S, U24-ch88, adc5

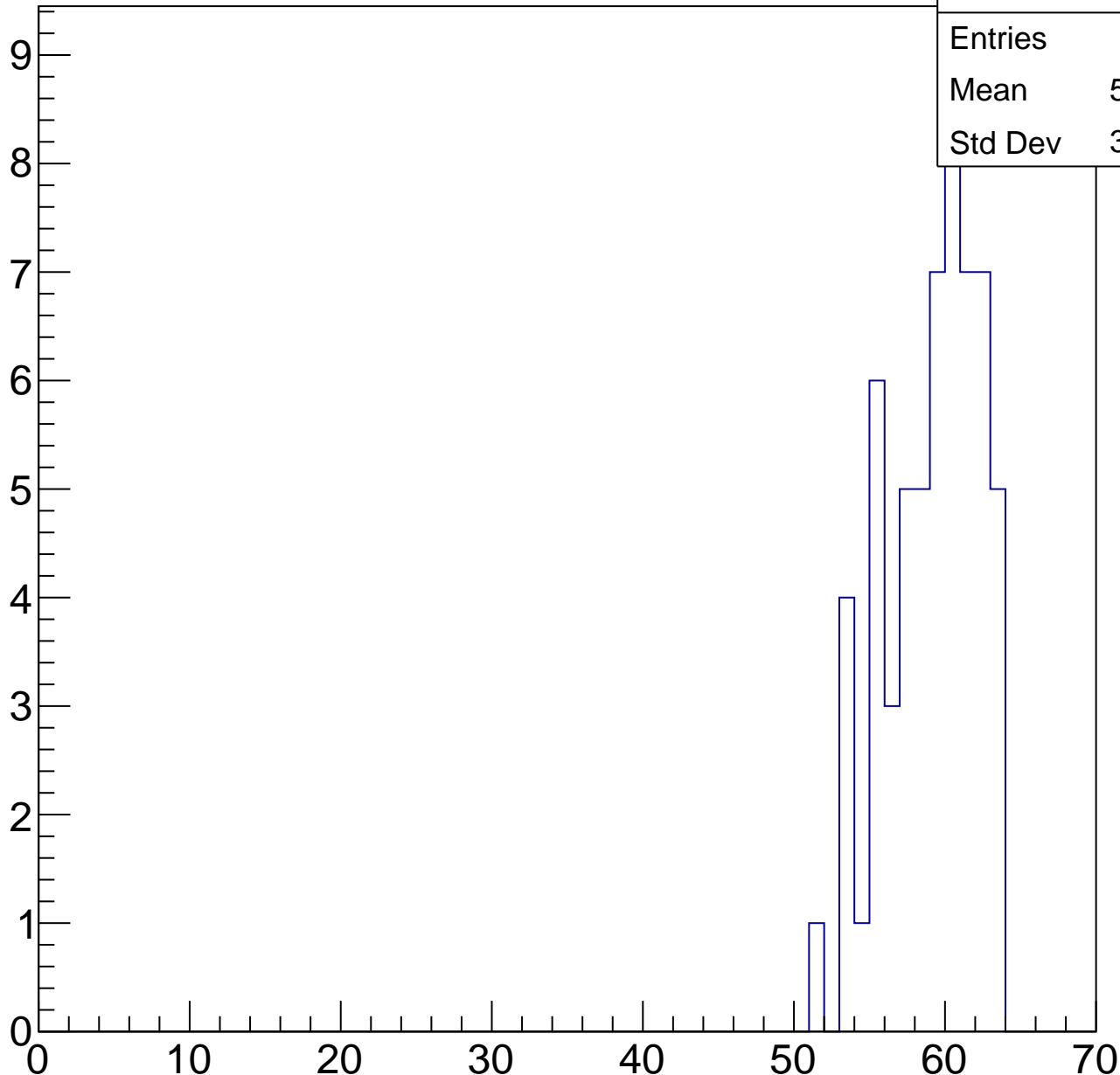
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.65
Std Dev	3.038

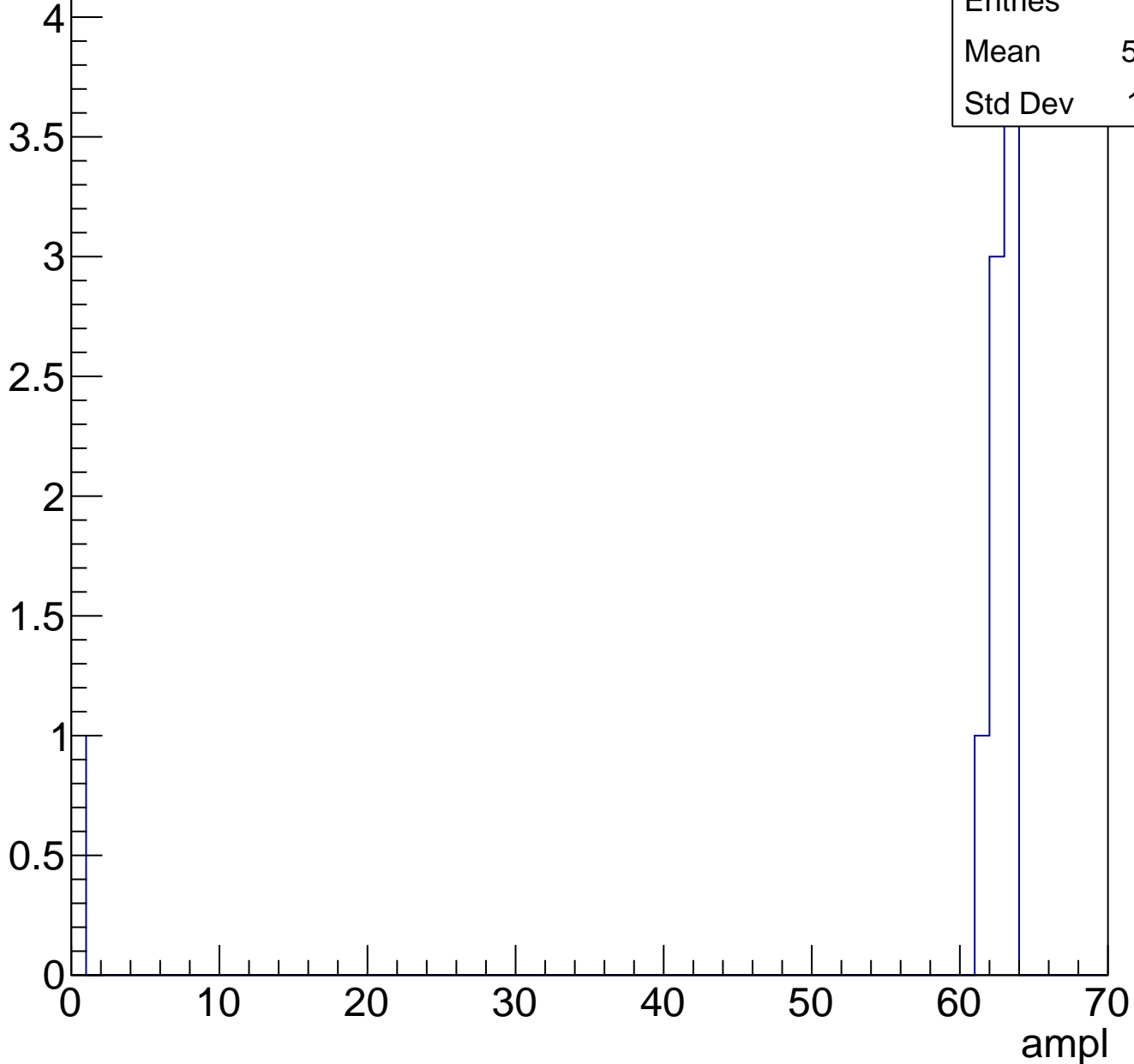
ampl



B1L103S, U24-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U24-ch89, adc0

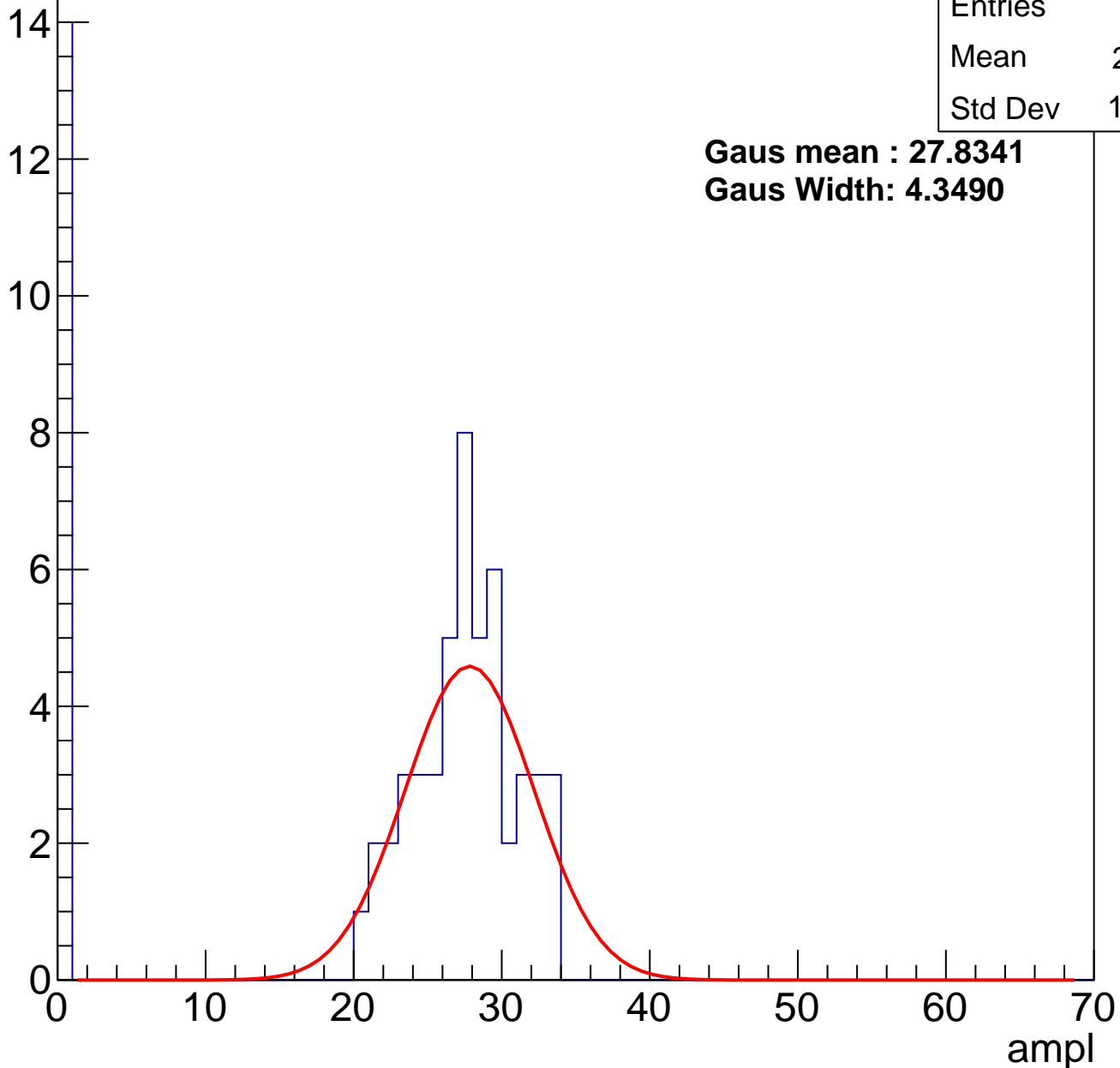
calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	21.11
Std Dev	11.66

Gaus mean : 27.8341

Gaus Width: 4.3490

Entry



B1L103S, U24-ch89, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	32.32
Std Dev	9.342

Gaus mean : 34.5995

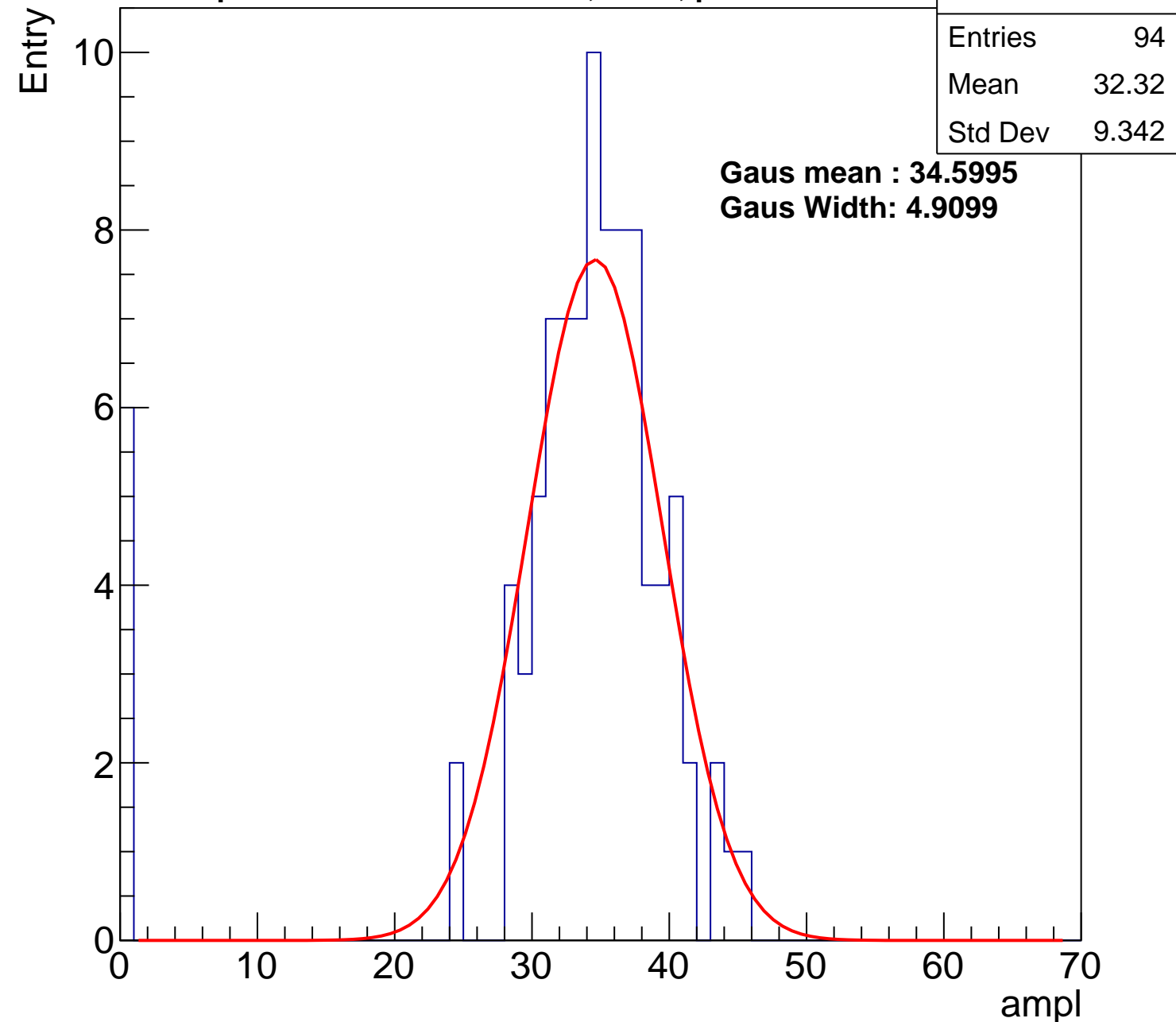
Gaus Width: 4.9099

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch89, adc2

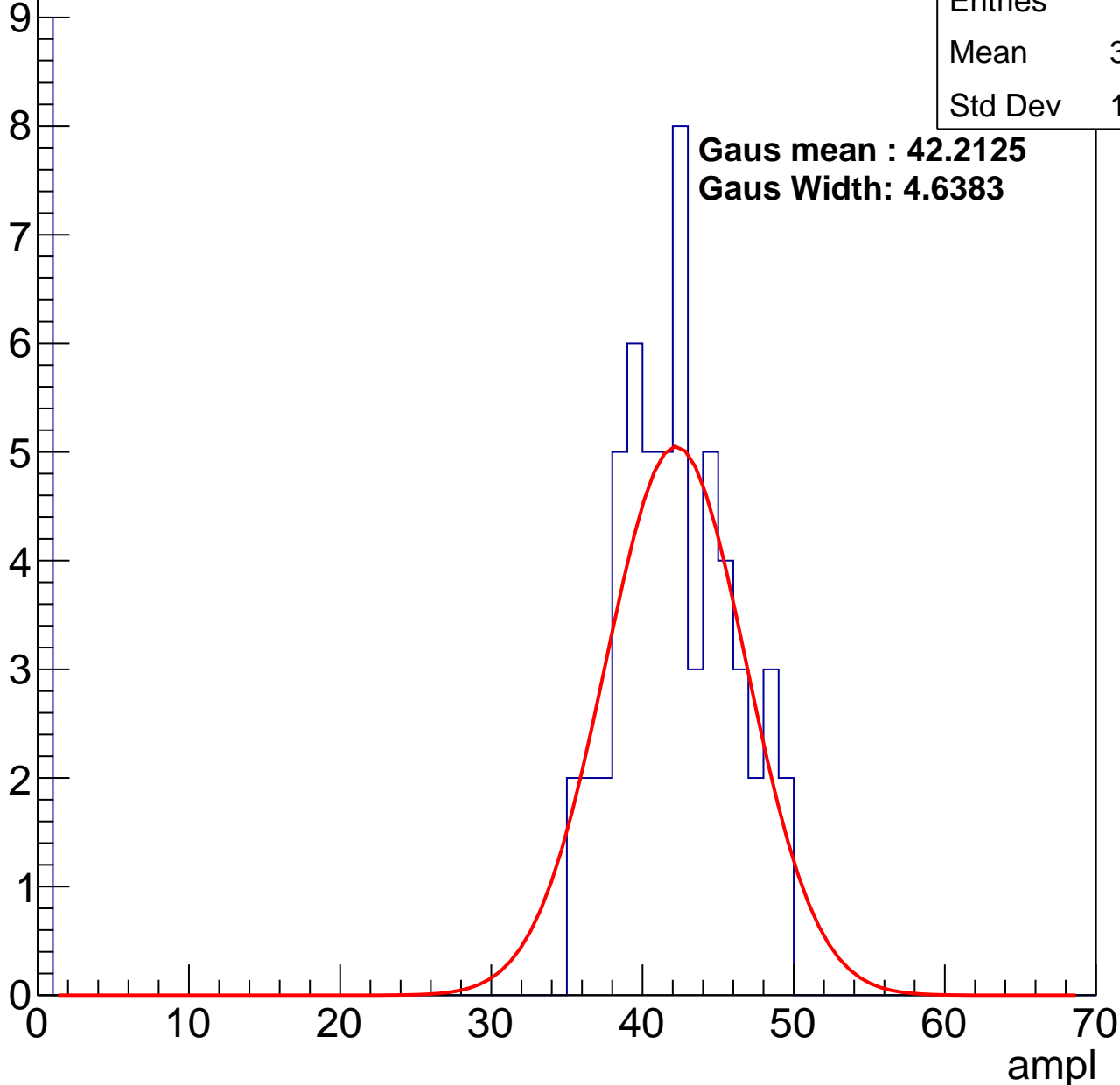
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.12
Std Dev	14.74

Gaus mean : 42.2125

Gaus Width: 4.6383

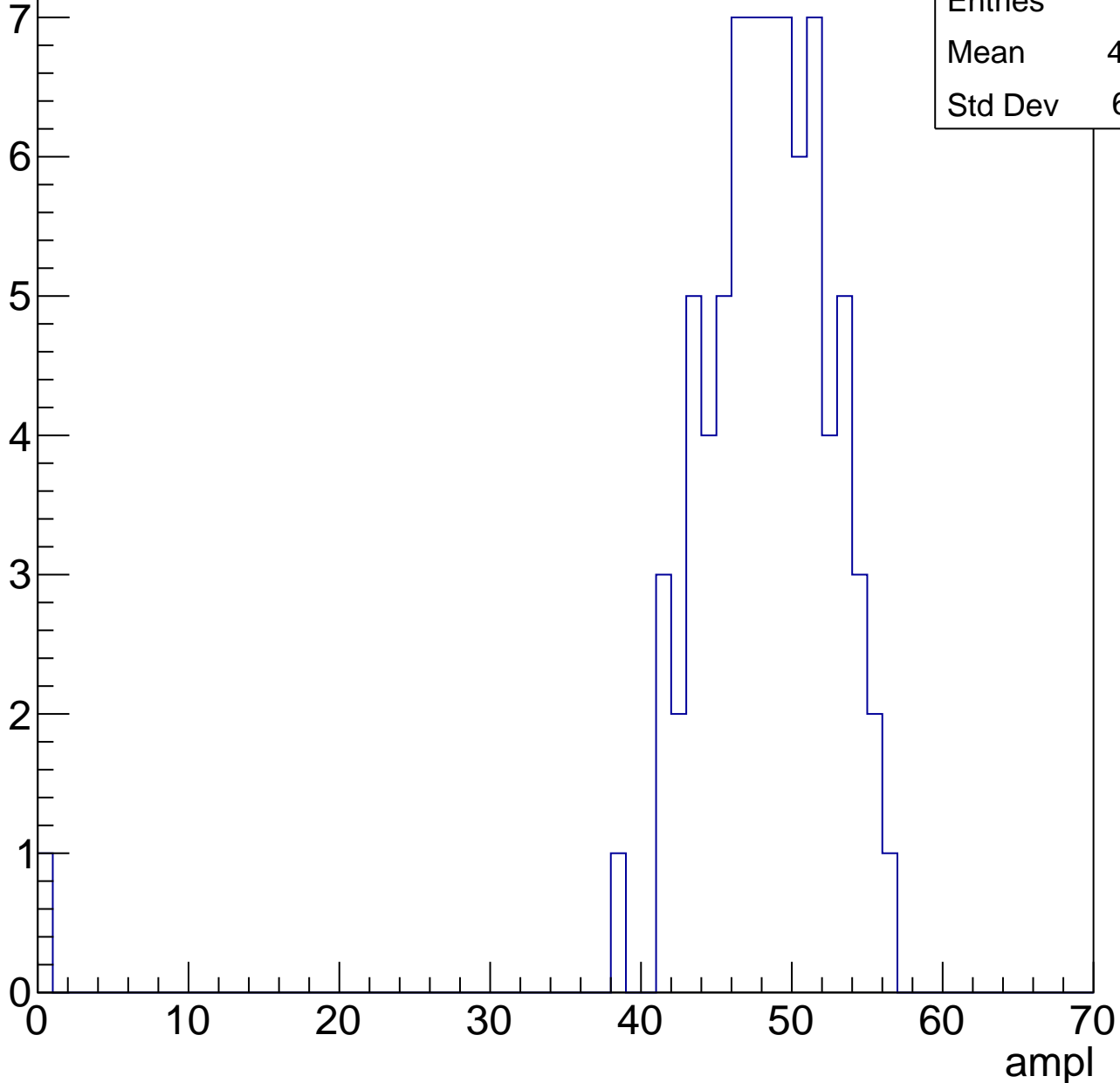


B1L103S, U24-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	47.39
Std Dev	6.661

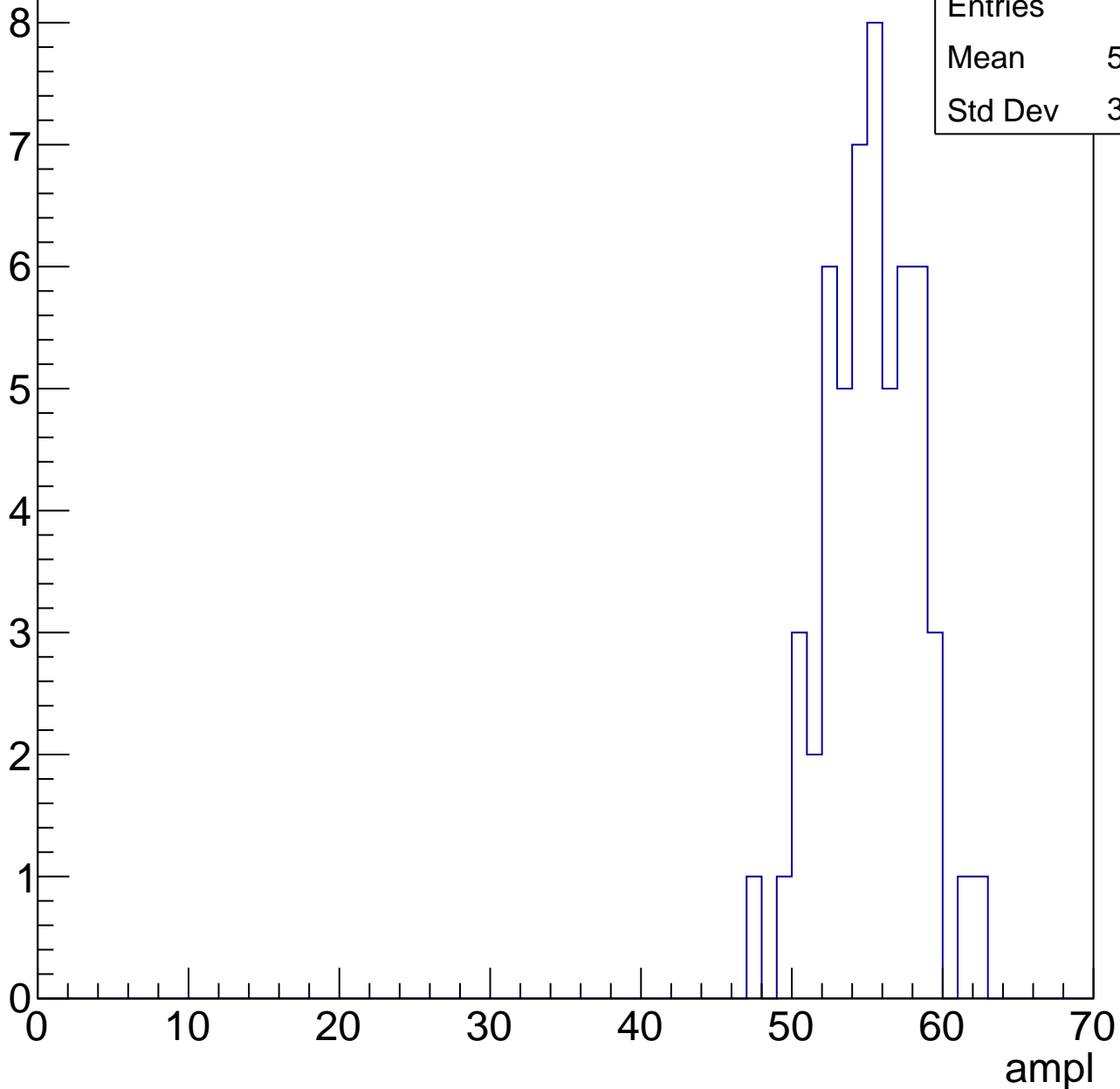


B1L103S, U24-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.78
Std Dev	3.019

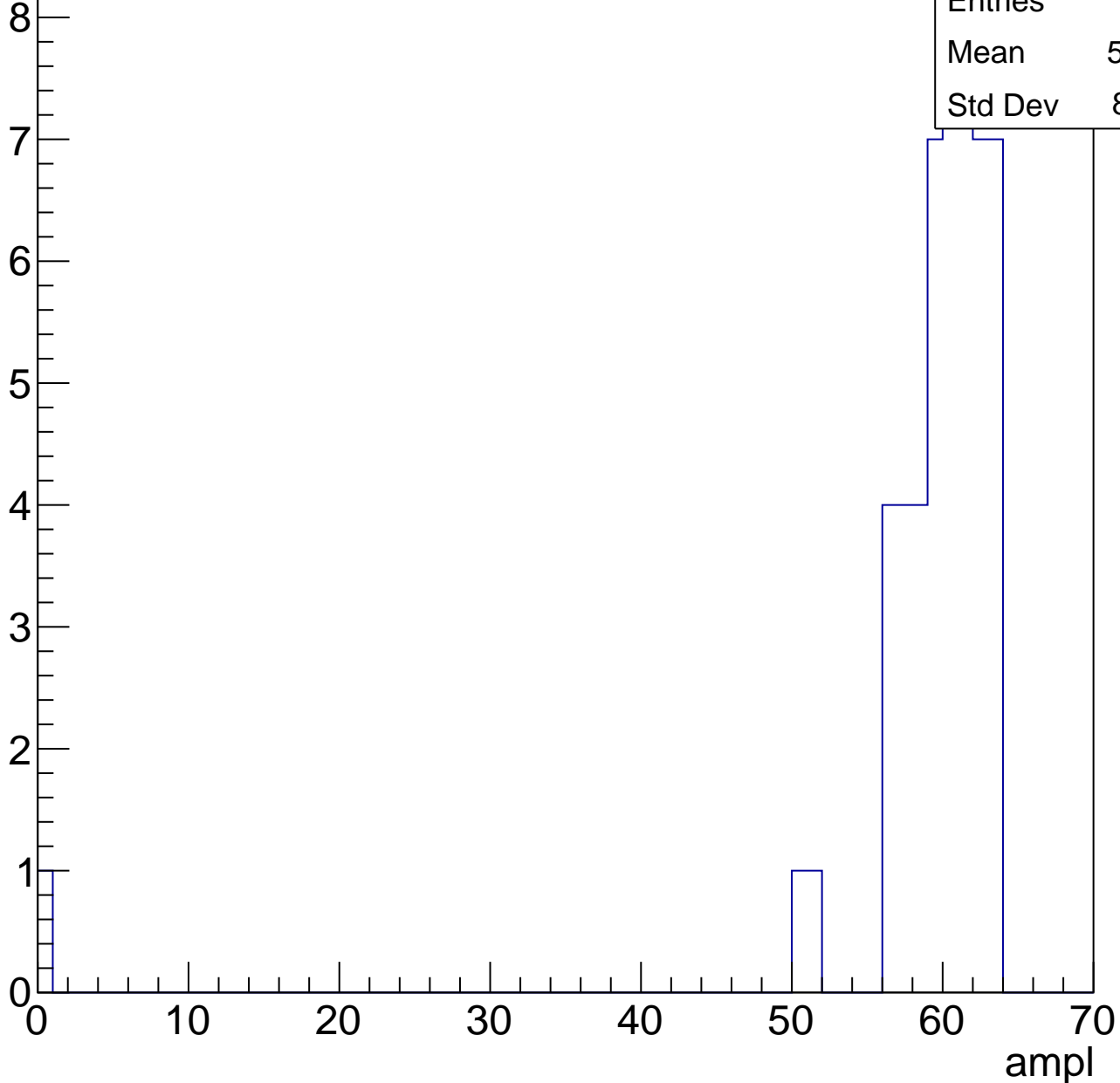


B1L103S, U24-ch89, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.48
Std Dev	8.641



B1L103S, U24-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

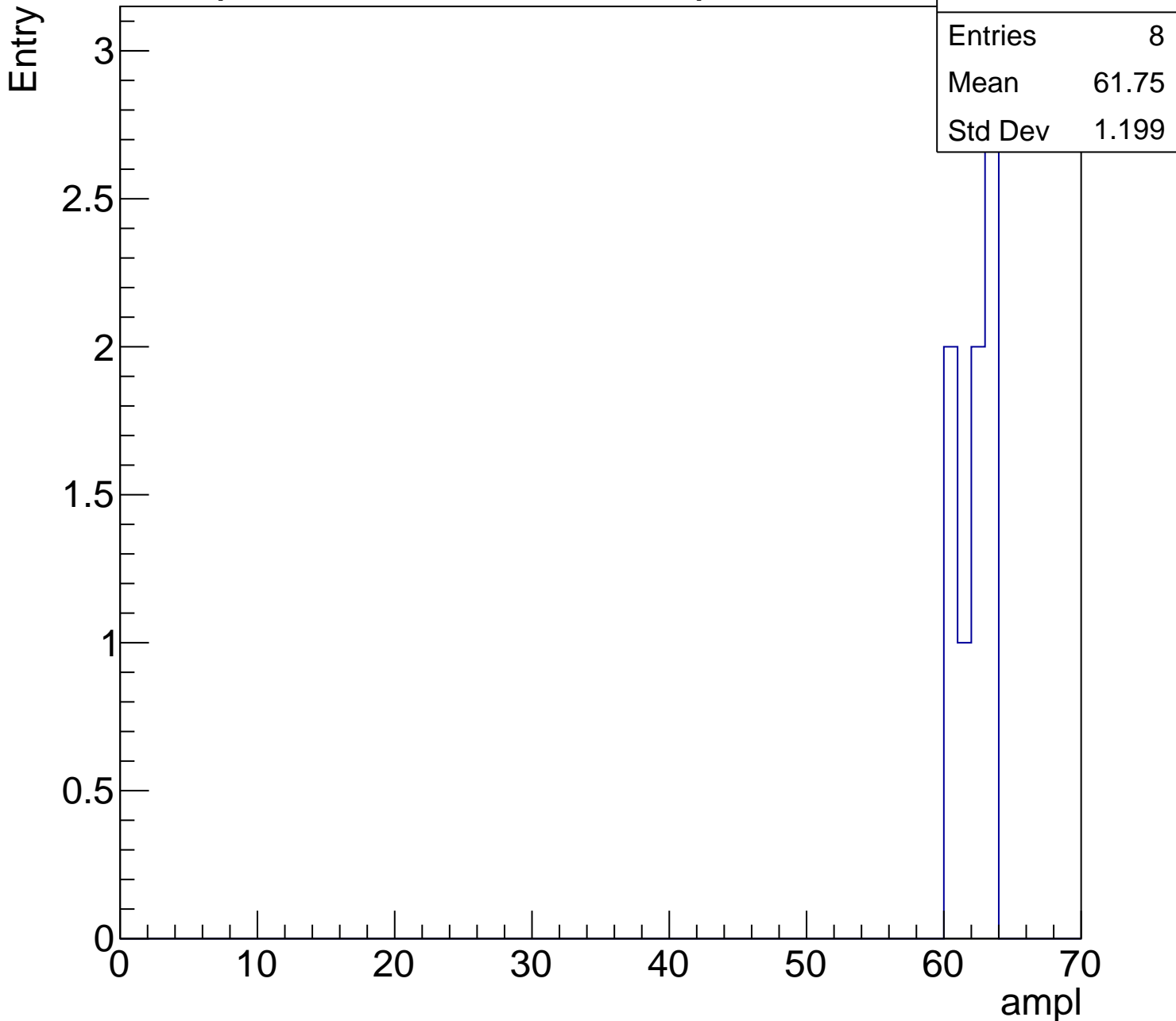
Entry

3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	61.75
Std Dev	1.199

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch89, adc7

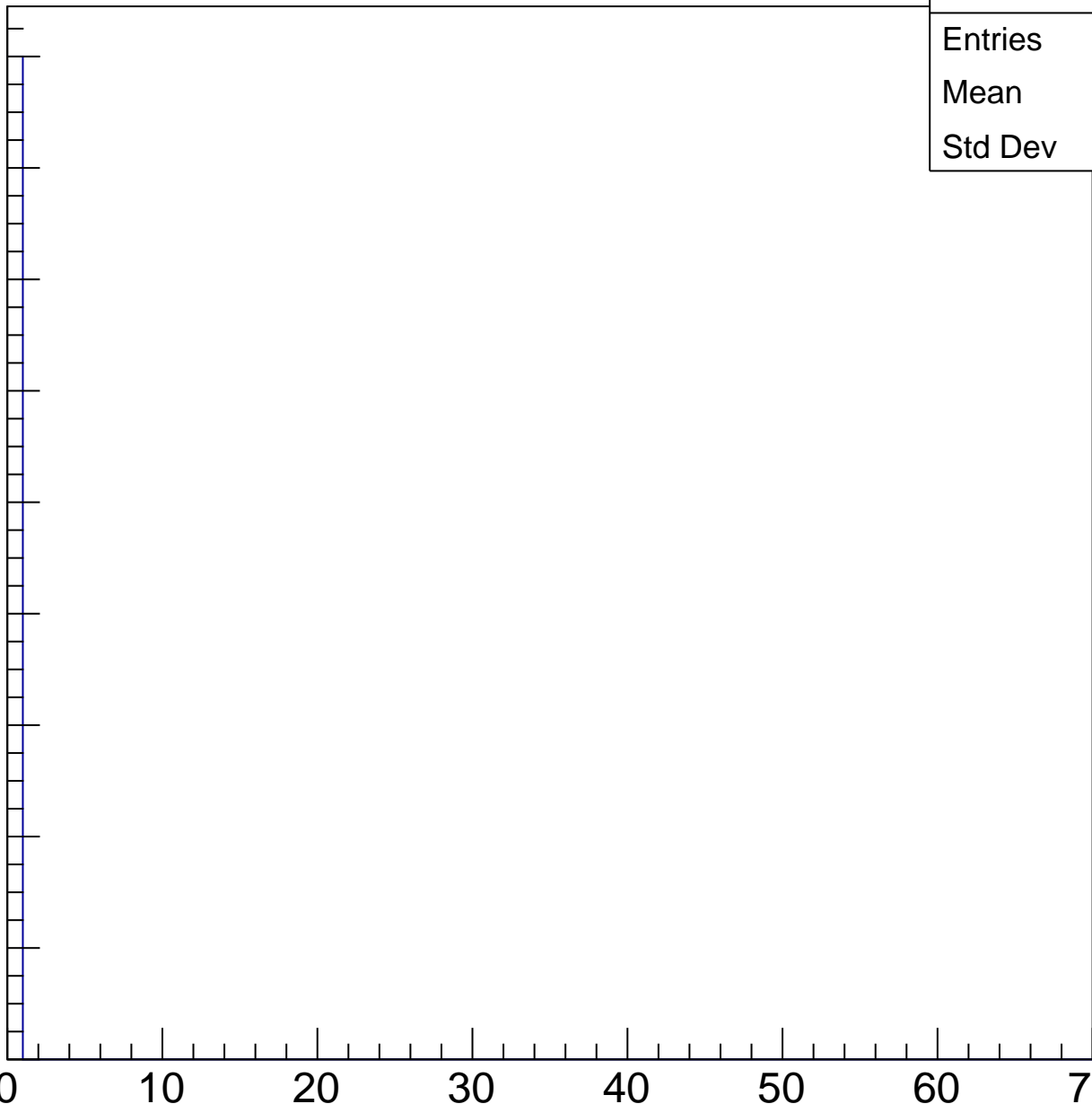
calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	18
Mean	0
Std Dev	0

ampl



B1L103S, U24-ch90, adc0

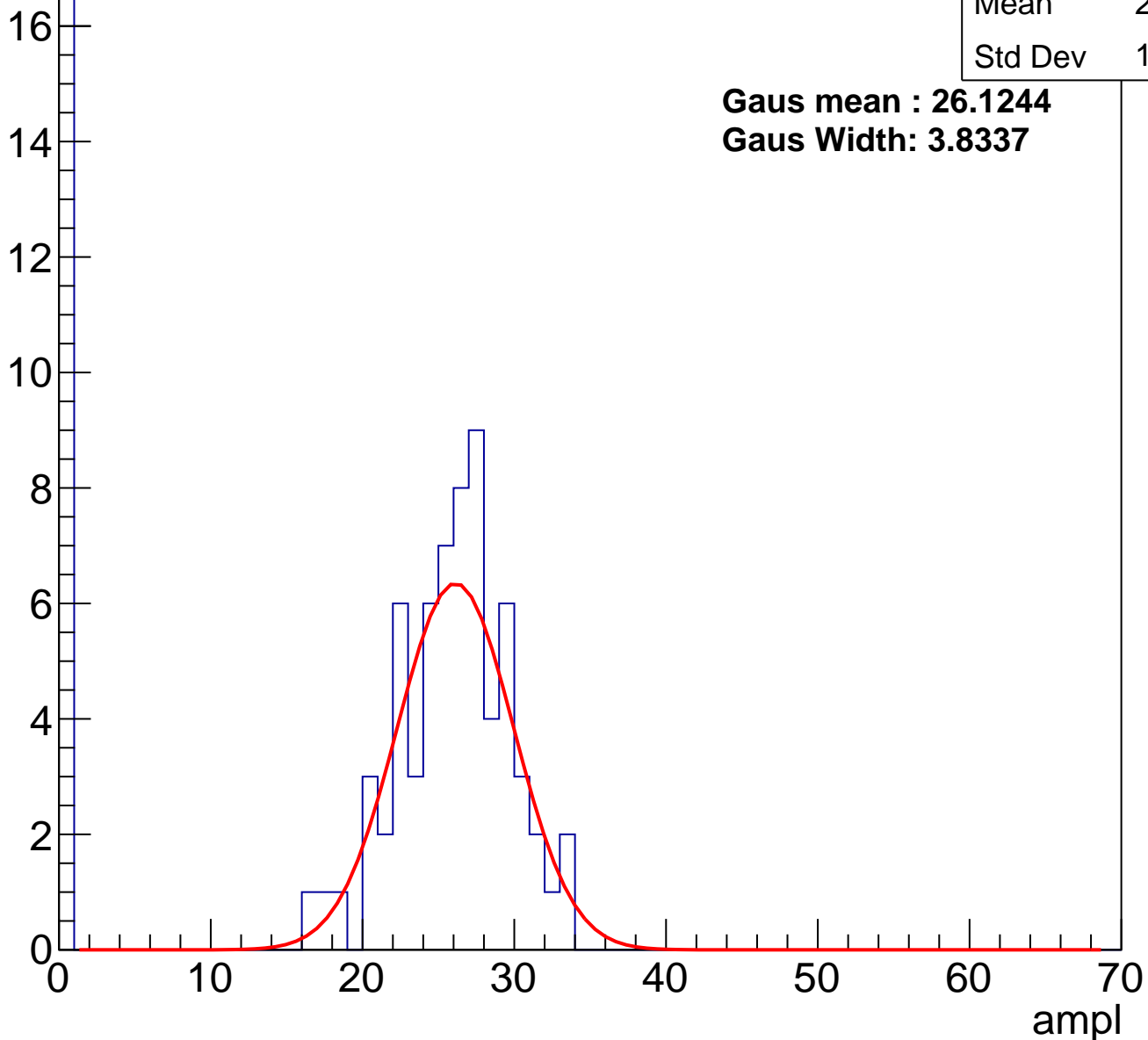
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	20.24
Std Dev	10.84

Gaus mean : 26.1244

Gaus Width: 3.8337

Entry



B1L103S, U24-ch90, adc1

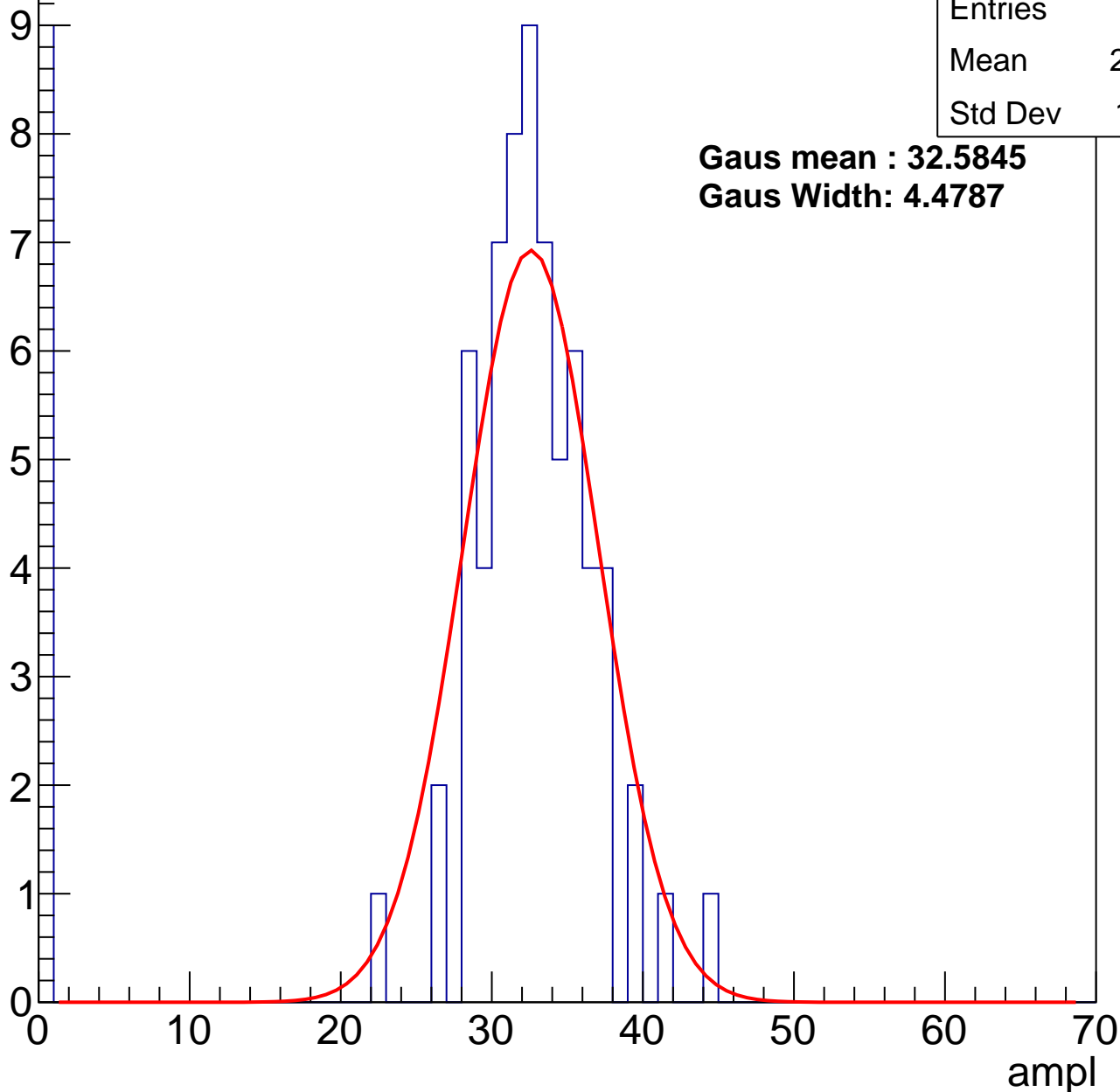
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	28.55
Std Dev	11.01

Gaus mean : 32.5845

Gaus Width: 4.4787



B1L103S, U24-ch90, adc2

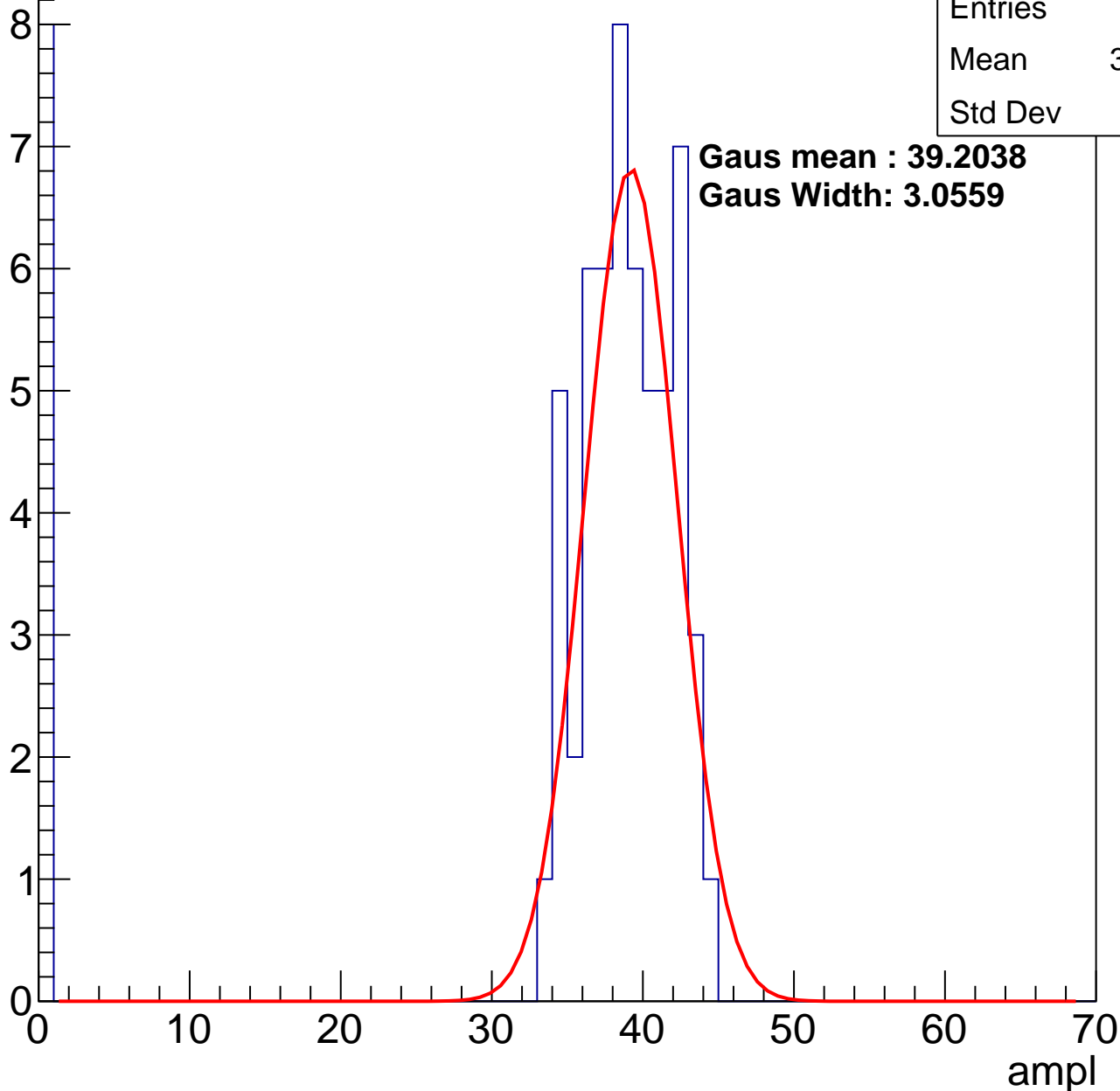
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	33.67
Std Dev	13.1

Gaus mean : 39.2038

Gaus Width: 3.0559

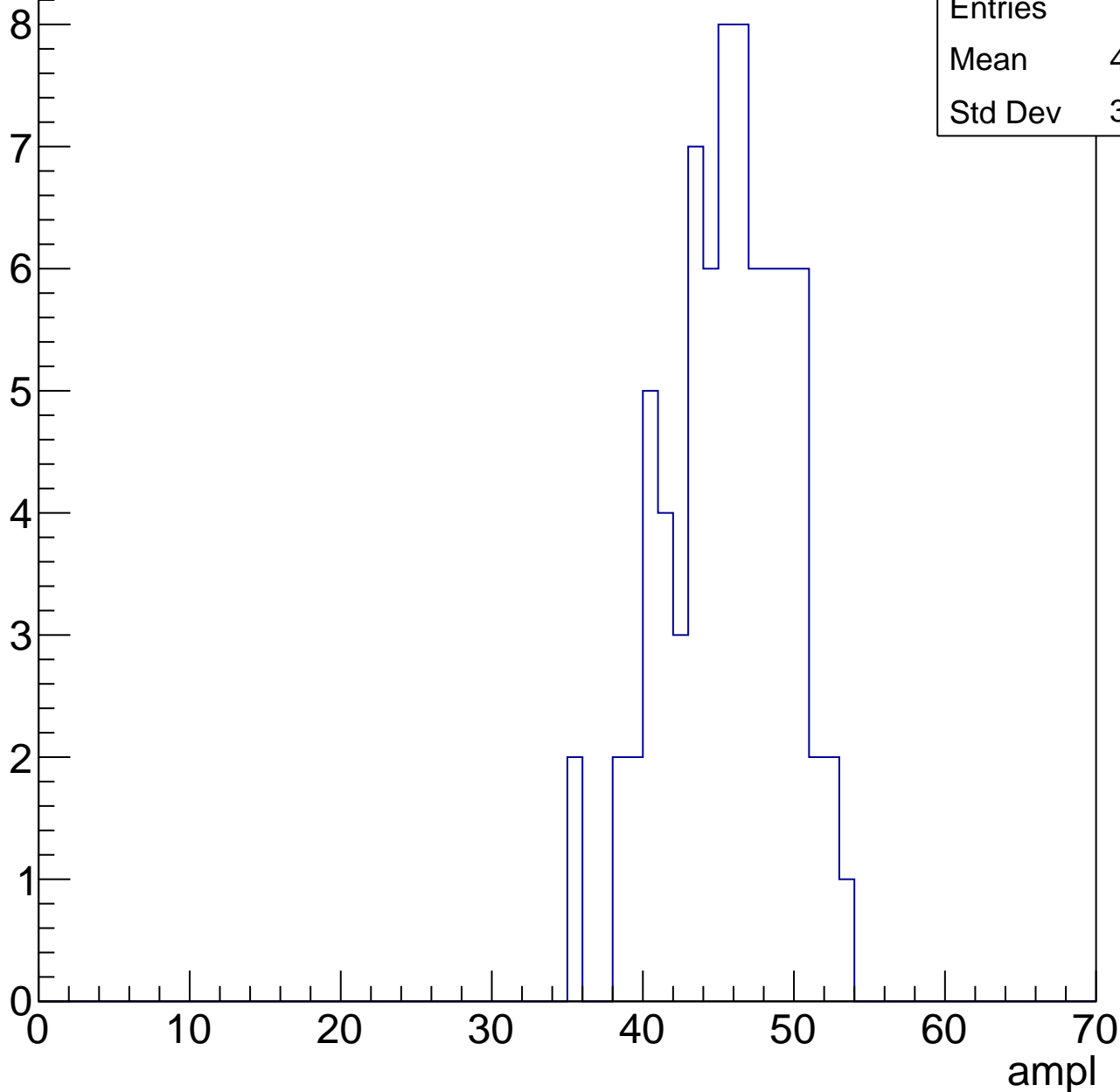


B1L103S, U24-ch90, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	45.13
Std Dev	3.948

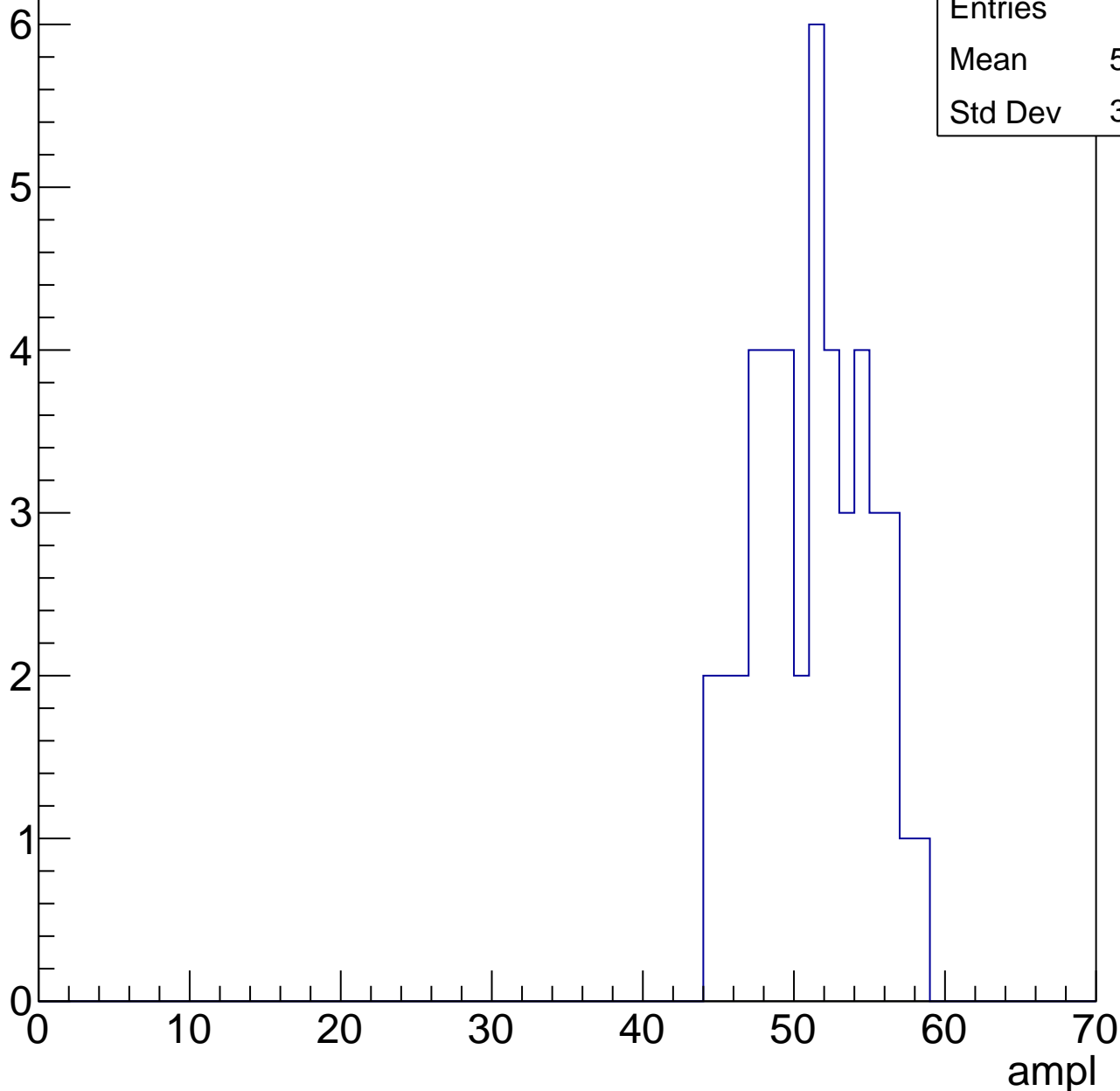


B1L103S, U24-ch90, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

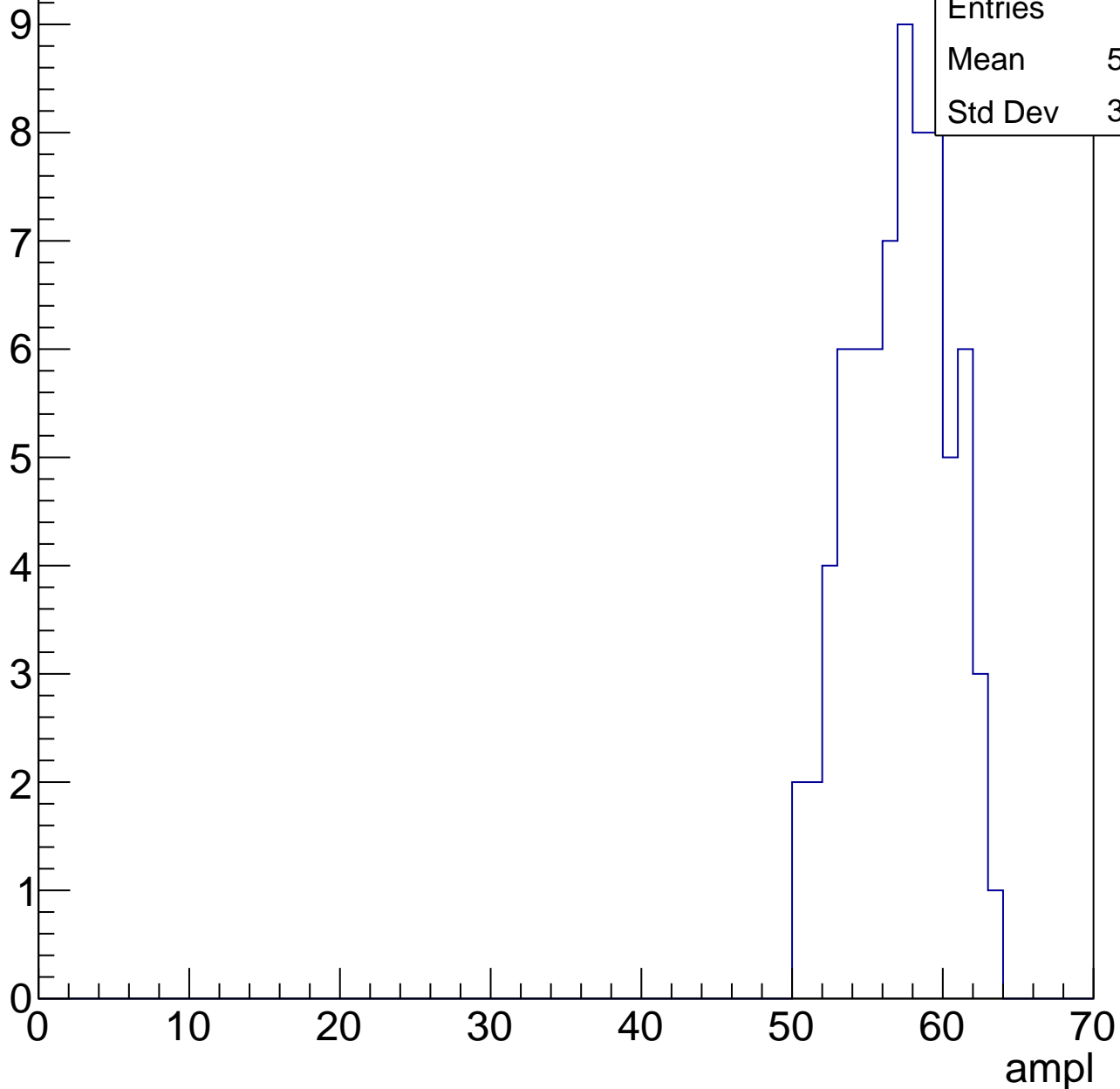
Entries	45
Mean	50.73
Std Dev	3.636



B1L103S, U24-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

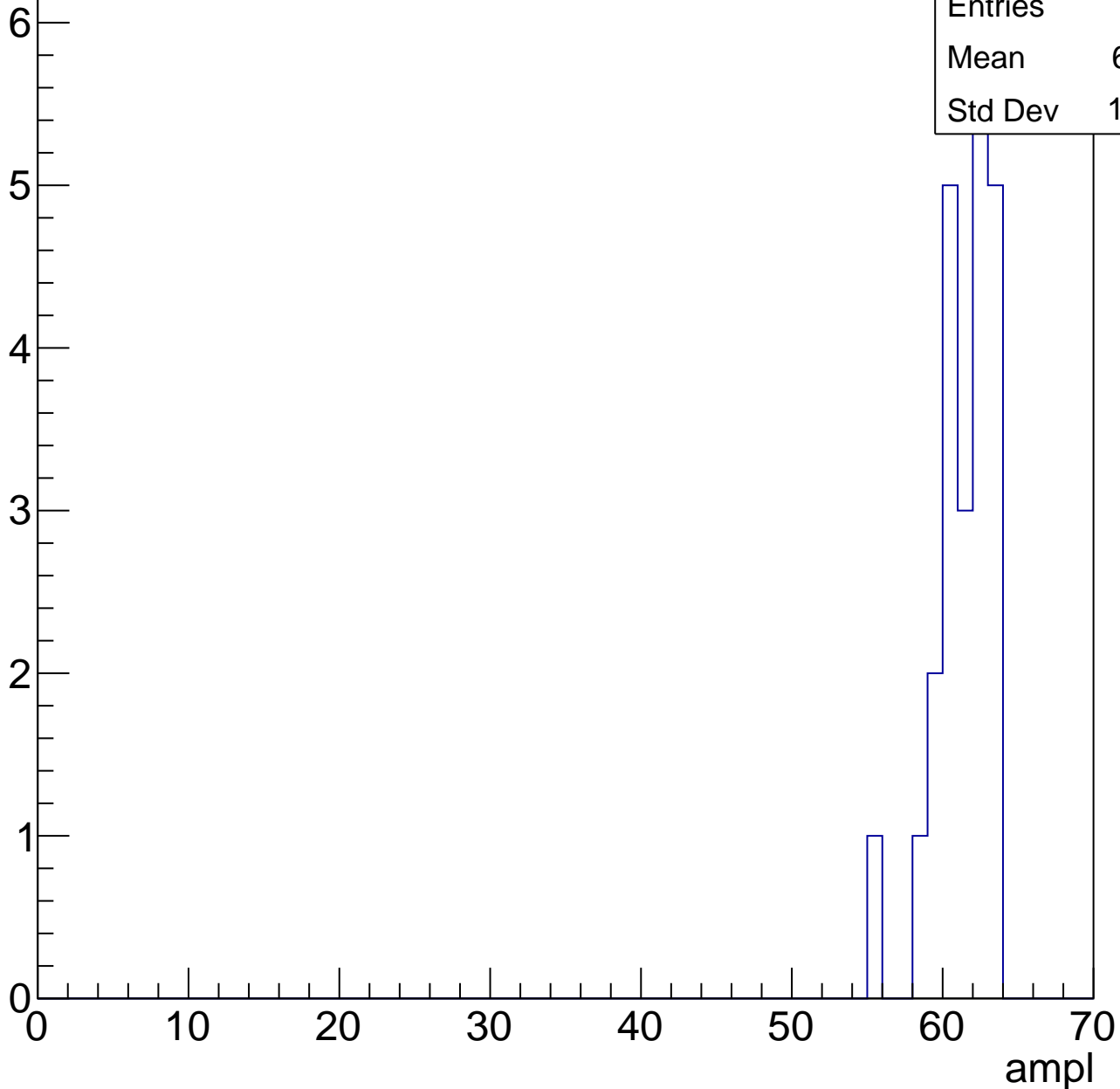


B1L103S, U24-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

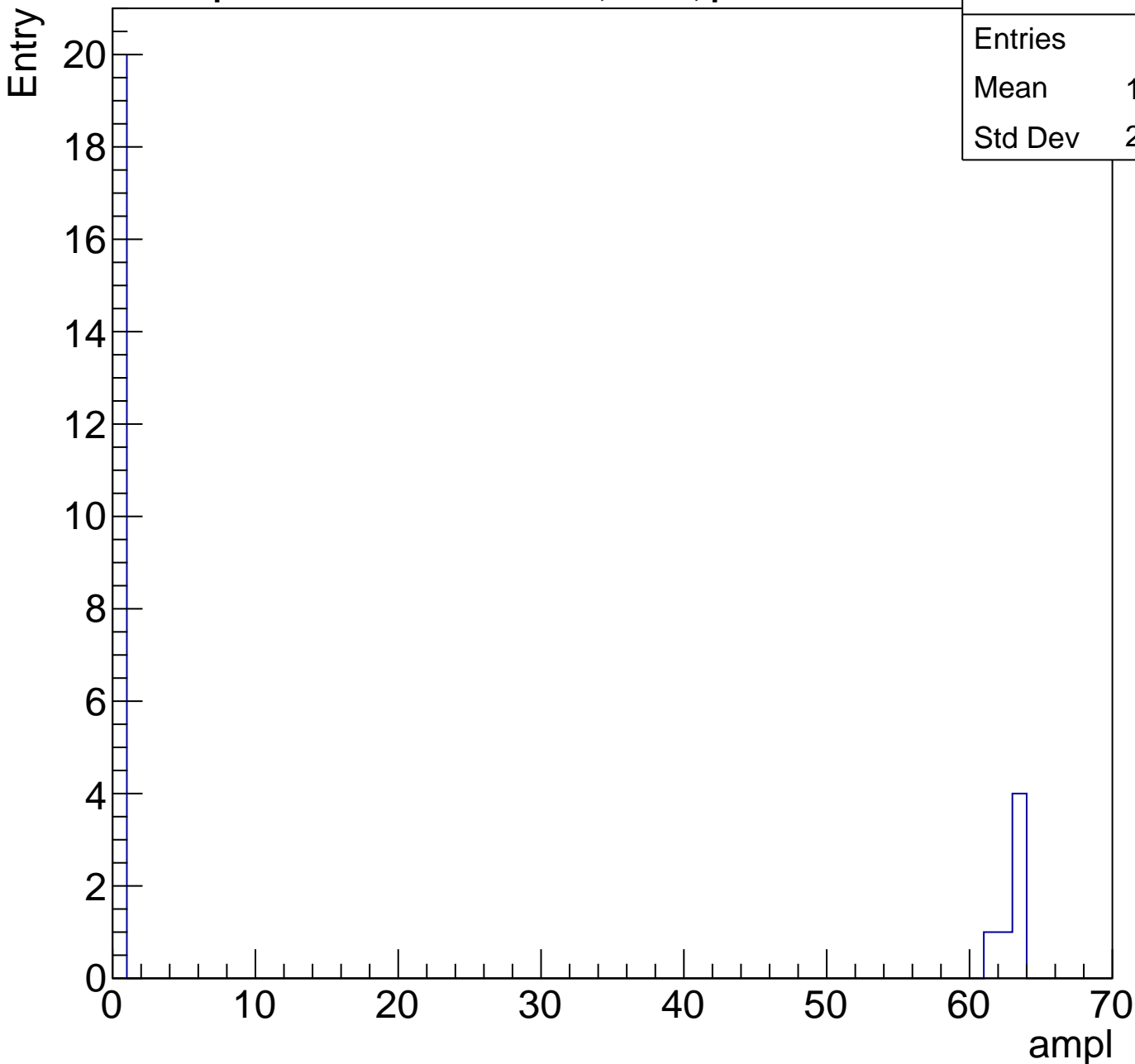
Entries	23
Mean	60.91
Std Dev	1.909



B1L103S, U24-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	26
Mean	14.42
Std Dev	26.34



B1L103S, U24-ch91, adc0

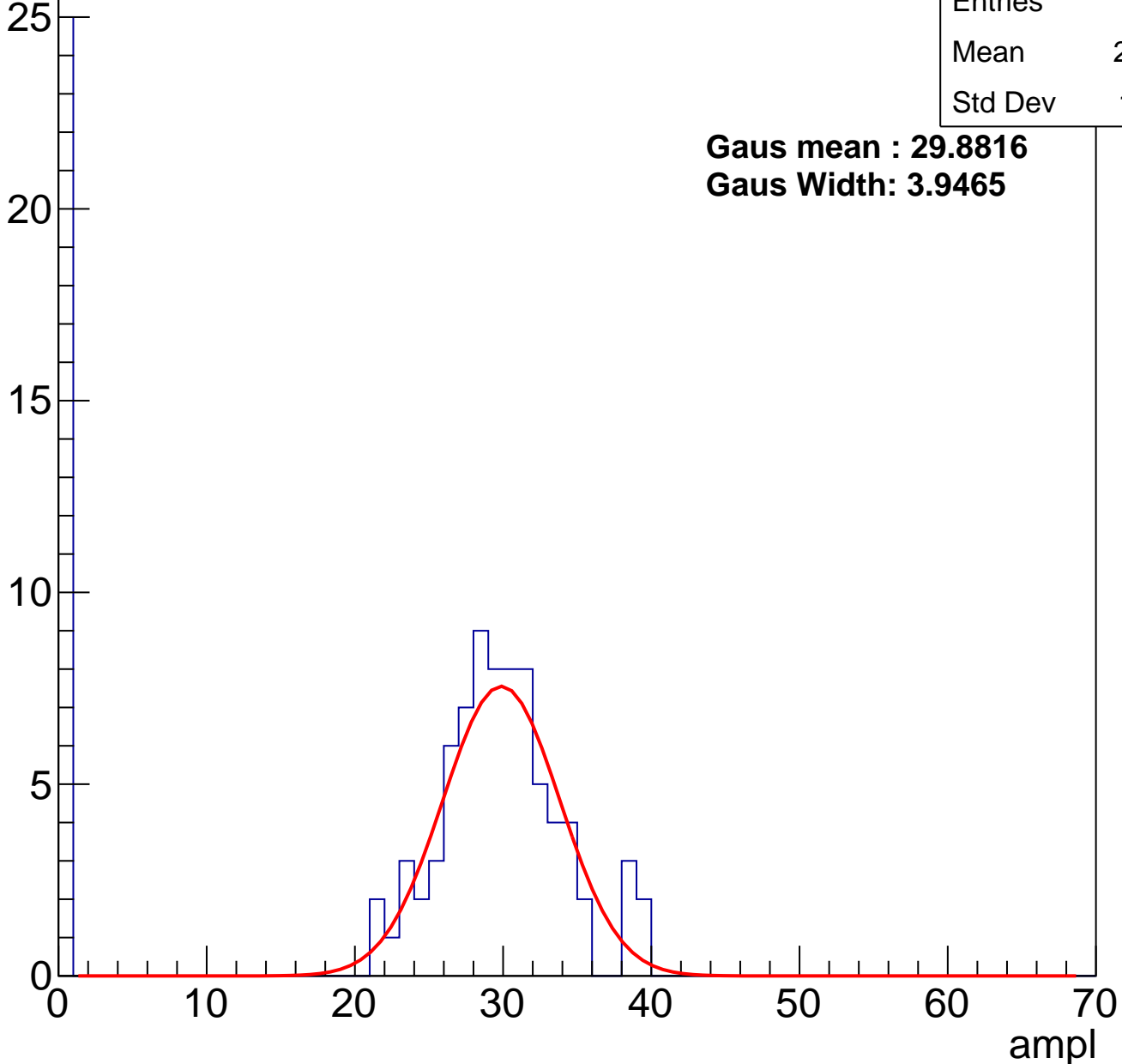
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	22.19
Std Dev	13.11

Gaus mean : 29.8816

Gaus Width: 3.9465

Entry



B1L103S, U24-ch91, adc1

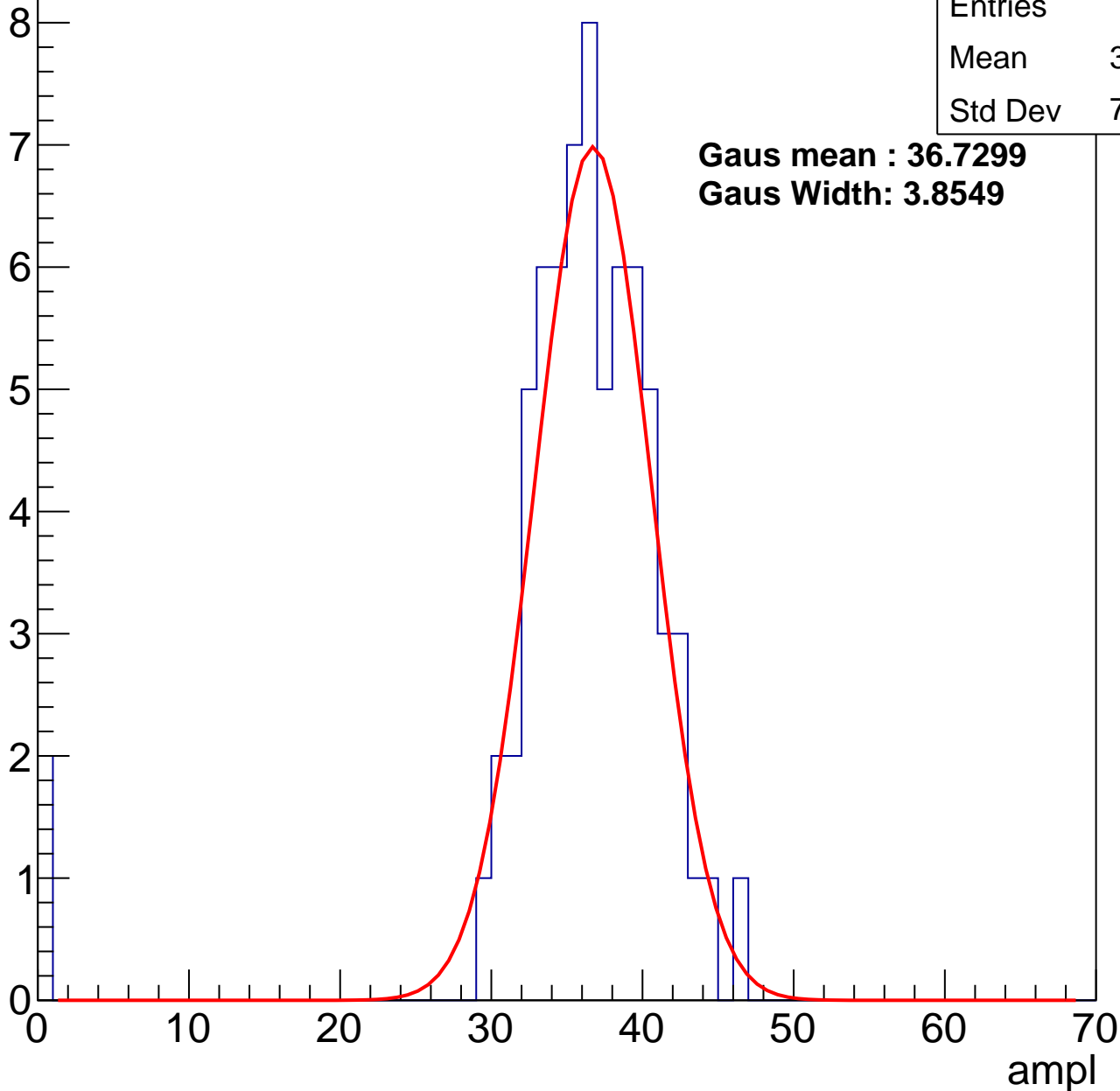
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	35.36
Std Dev	7.029

Gaus mean : 36.7299

Gaus Width: 3.8549



B1L103S, U24-ch91, adc2

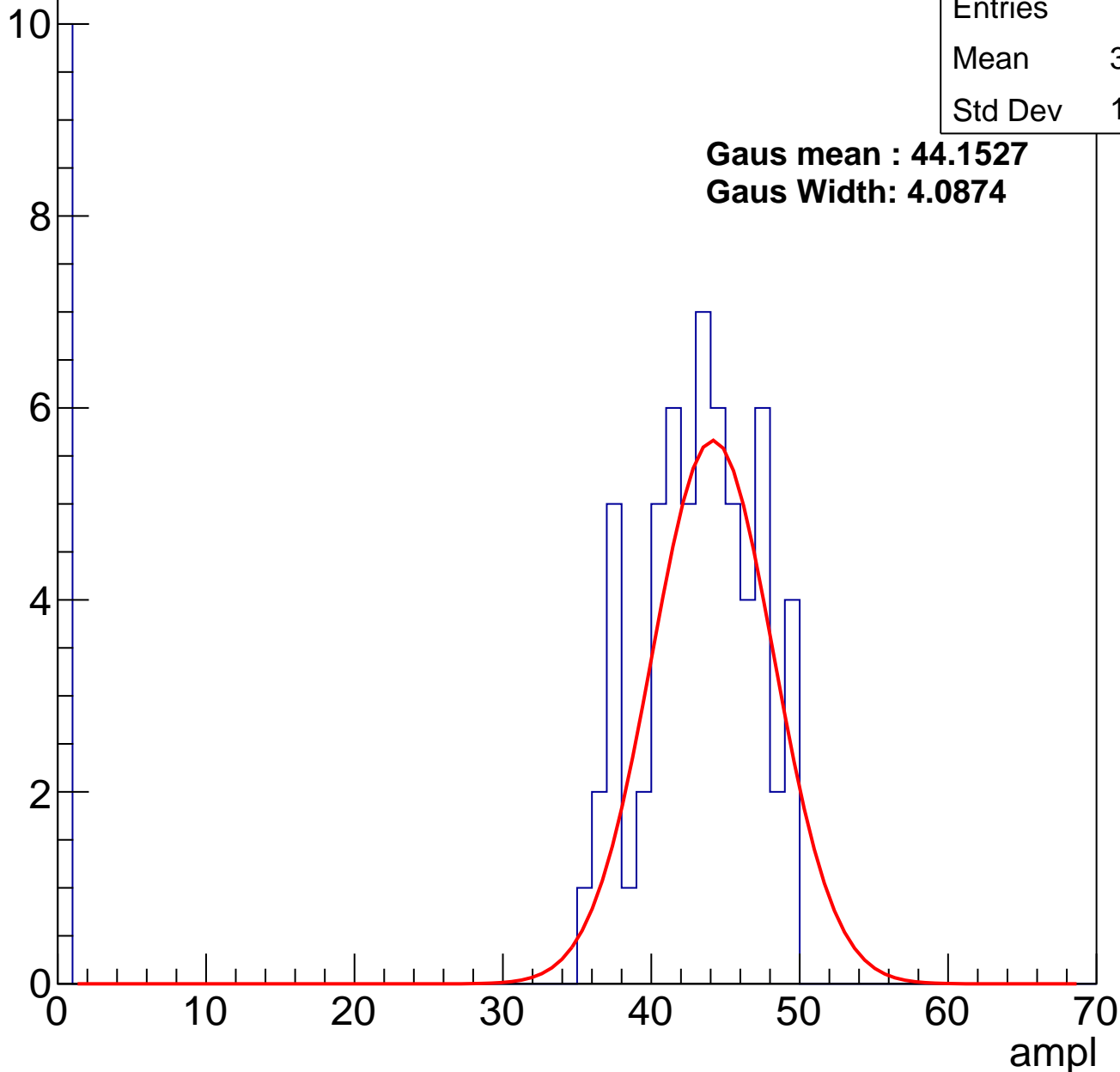
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	36.79
Std Dev	15.28

Gaus mean : 44.1527

Gaus Width: 4.0874

Entry

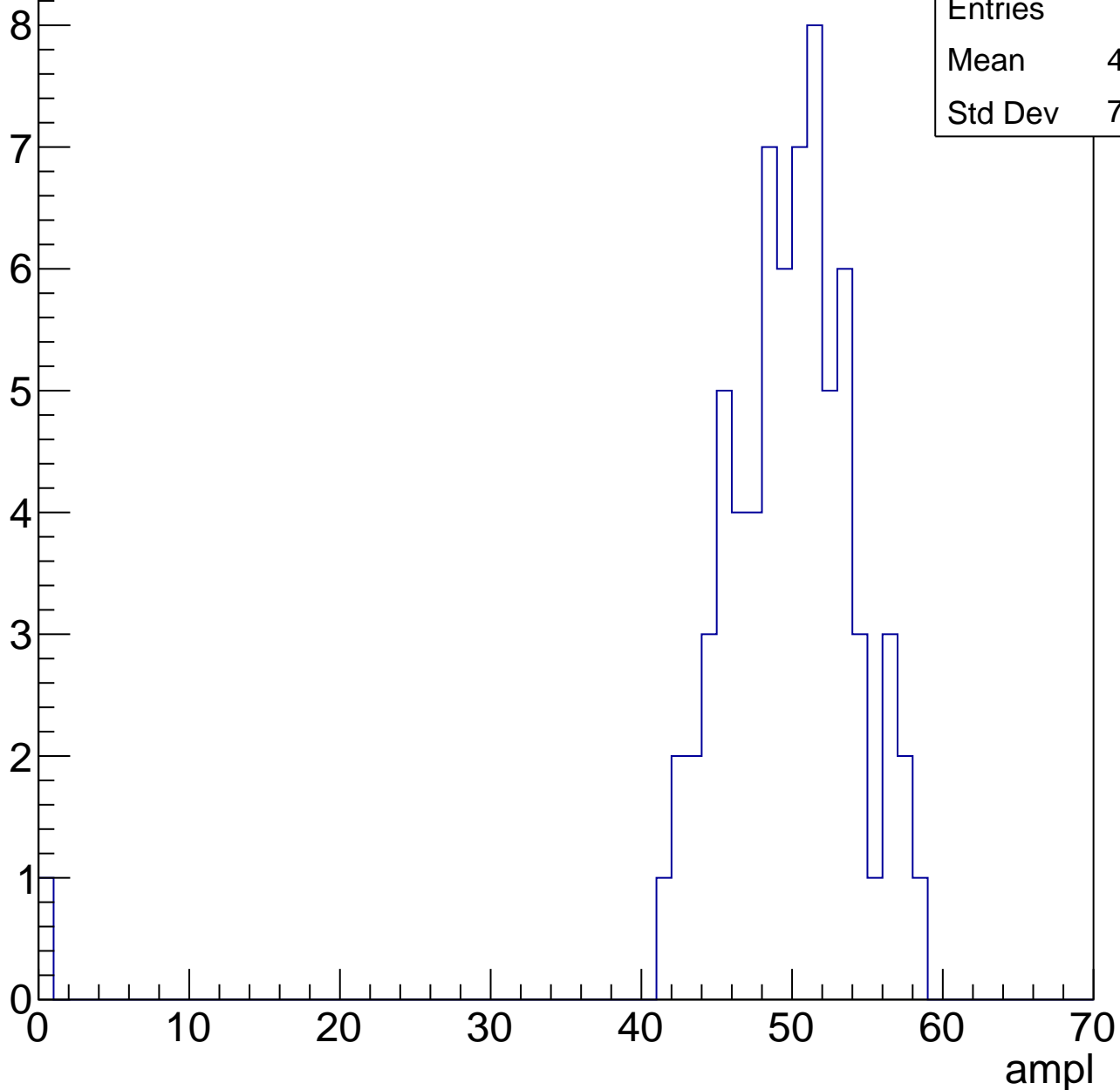


B1L103S, U24-ch91, adc3

calib_packv5_041523_1651.root, FC#0, port C2

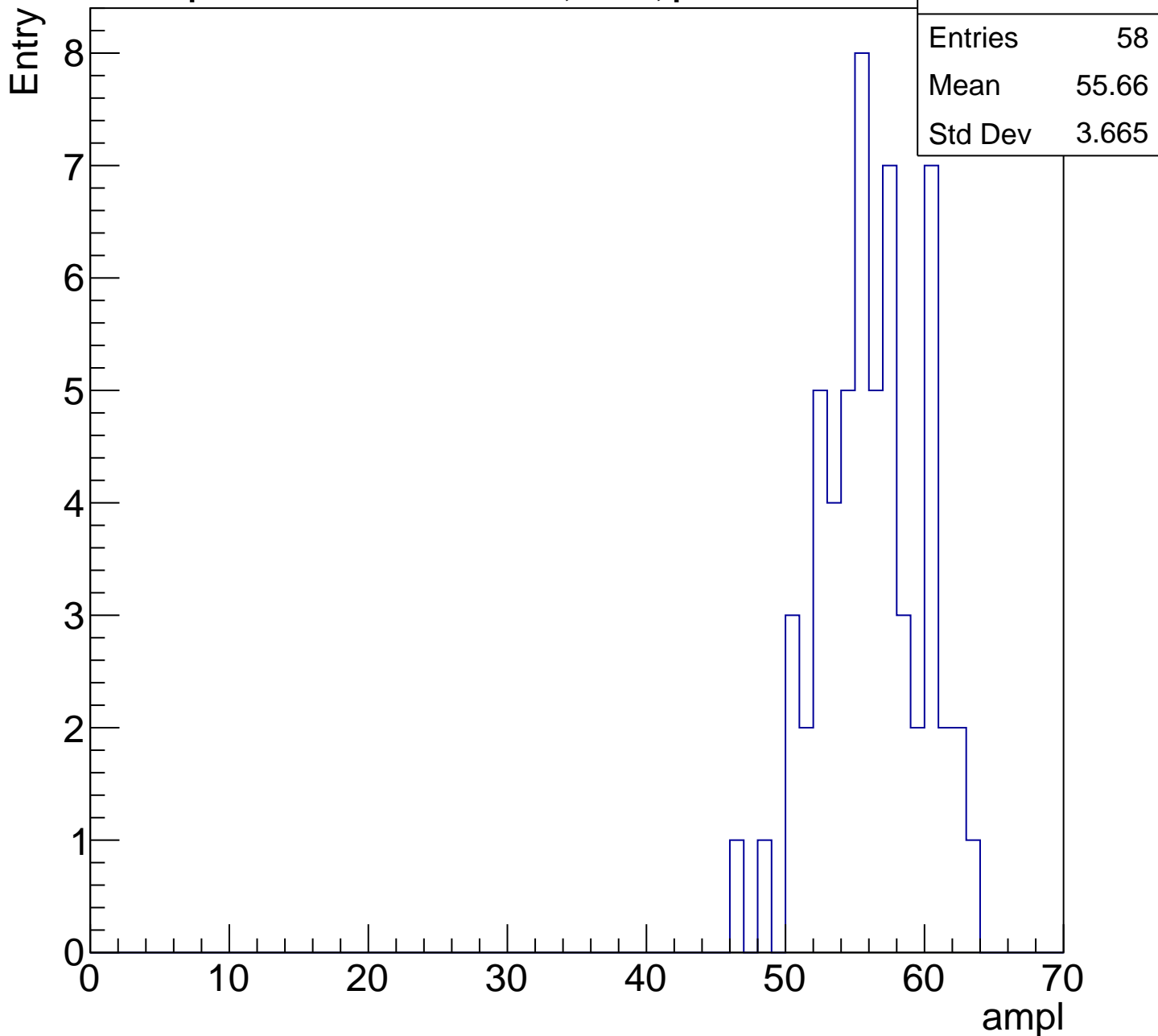
Entry

Entries	71
Mean	48.77
Std Dev	7.013



B1L103S, U24-ch91, adc4

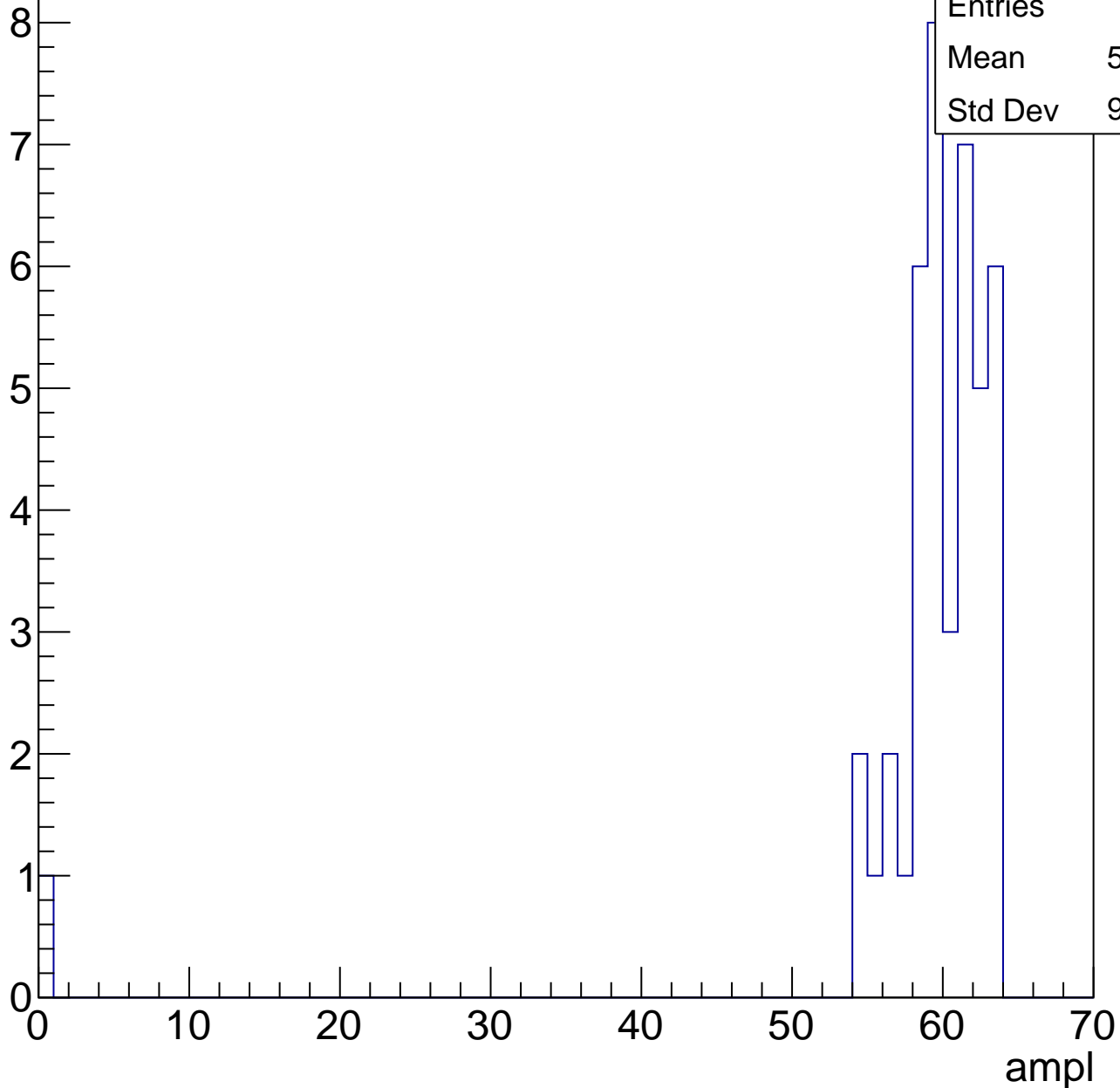
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U24-ch91, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch92, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	18.88
Std Dev	14.15

Gaus mean : 30.2164

Gaus Width: 3.2459

Entry

25

20

15

10

5

0

0

10

20

30

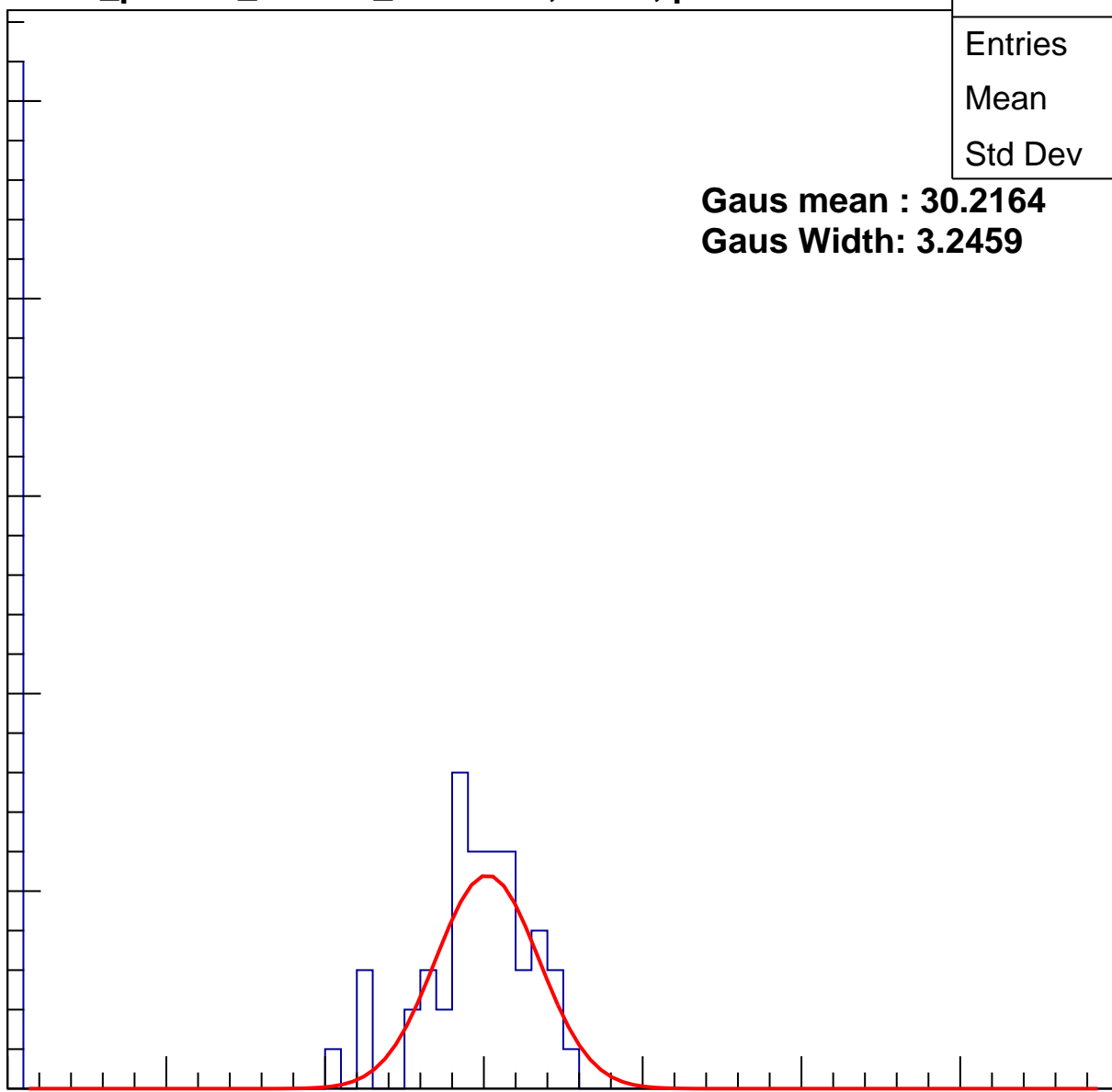
40

50

60

70

ampl



B1L103S, U24-ch92, adc1

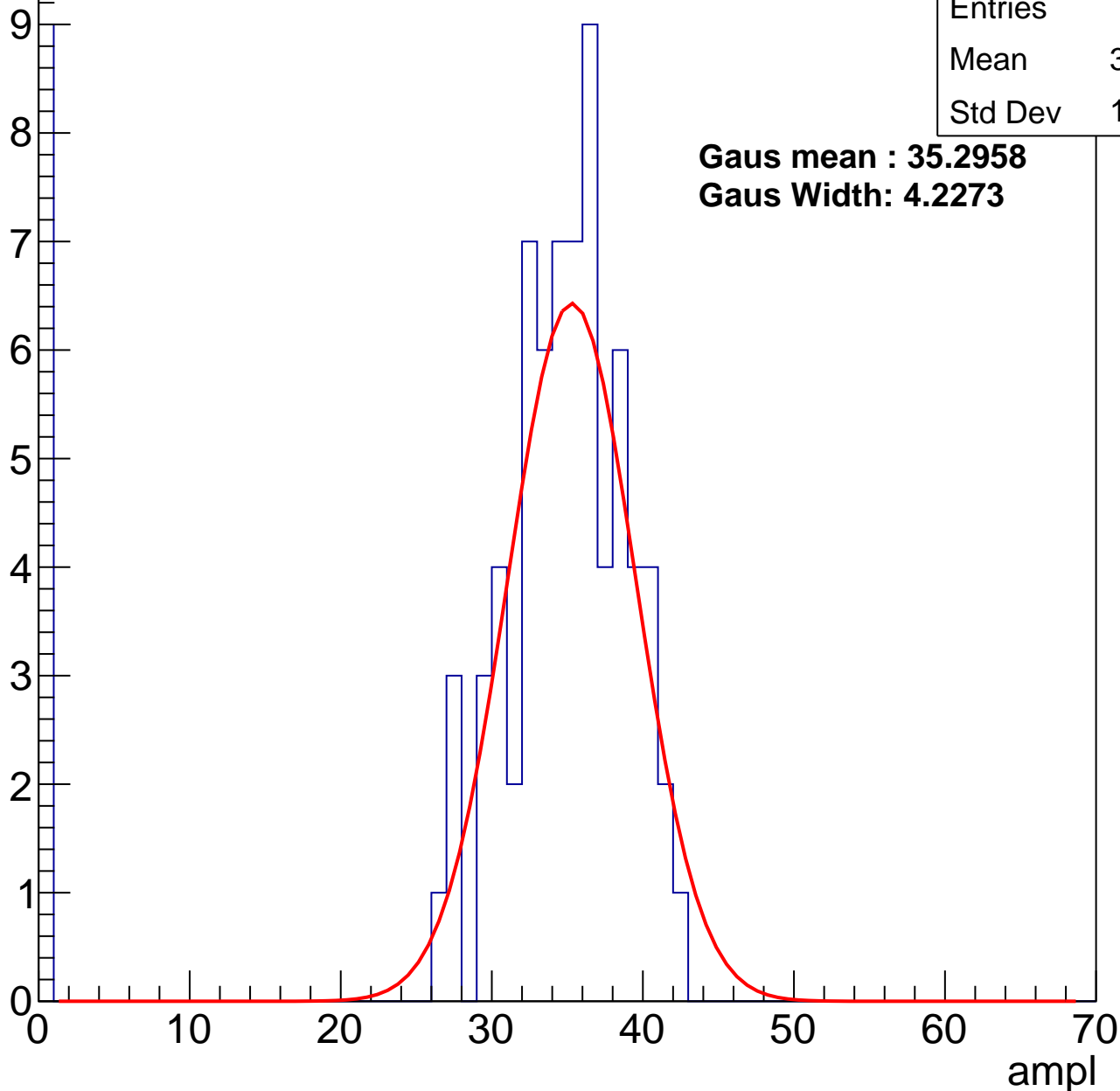
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	30.65
Std Dev	11.53

Gaus mean : 35.2958

Gaus Width: 4.2273



B1L103S, U24-ch92, adc2

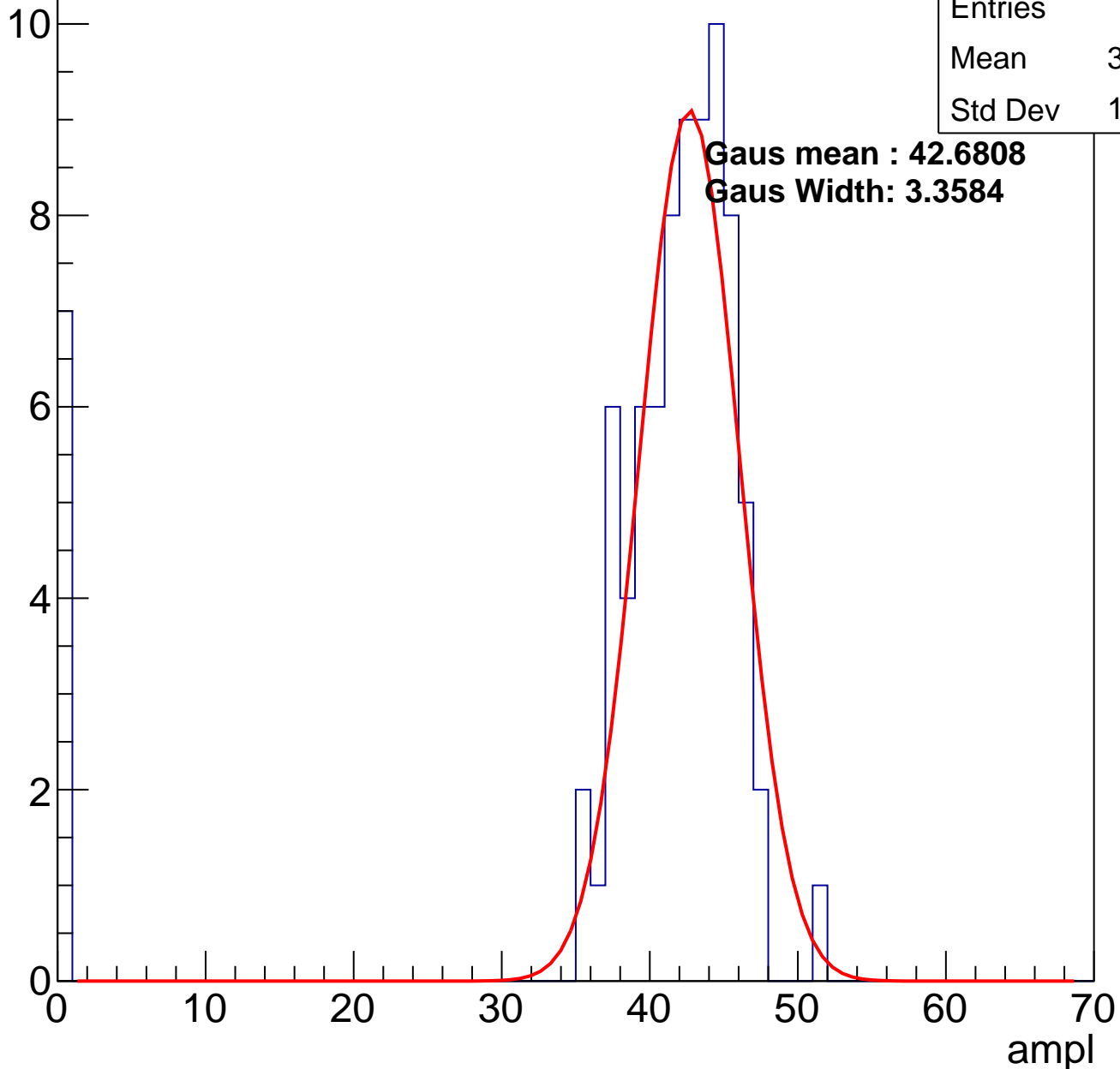
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	38.36
Std Dev	11.95

Gaus mean : 42.6808

Gaus Width: 3.3584

Entry

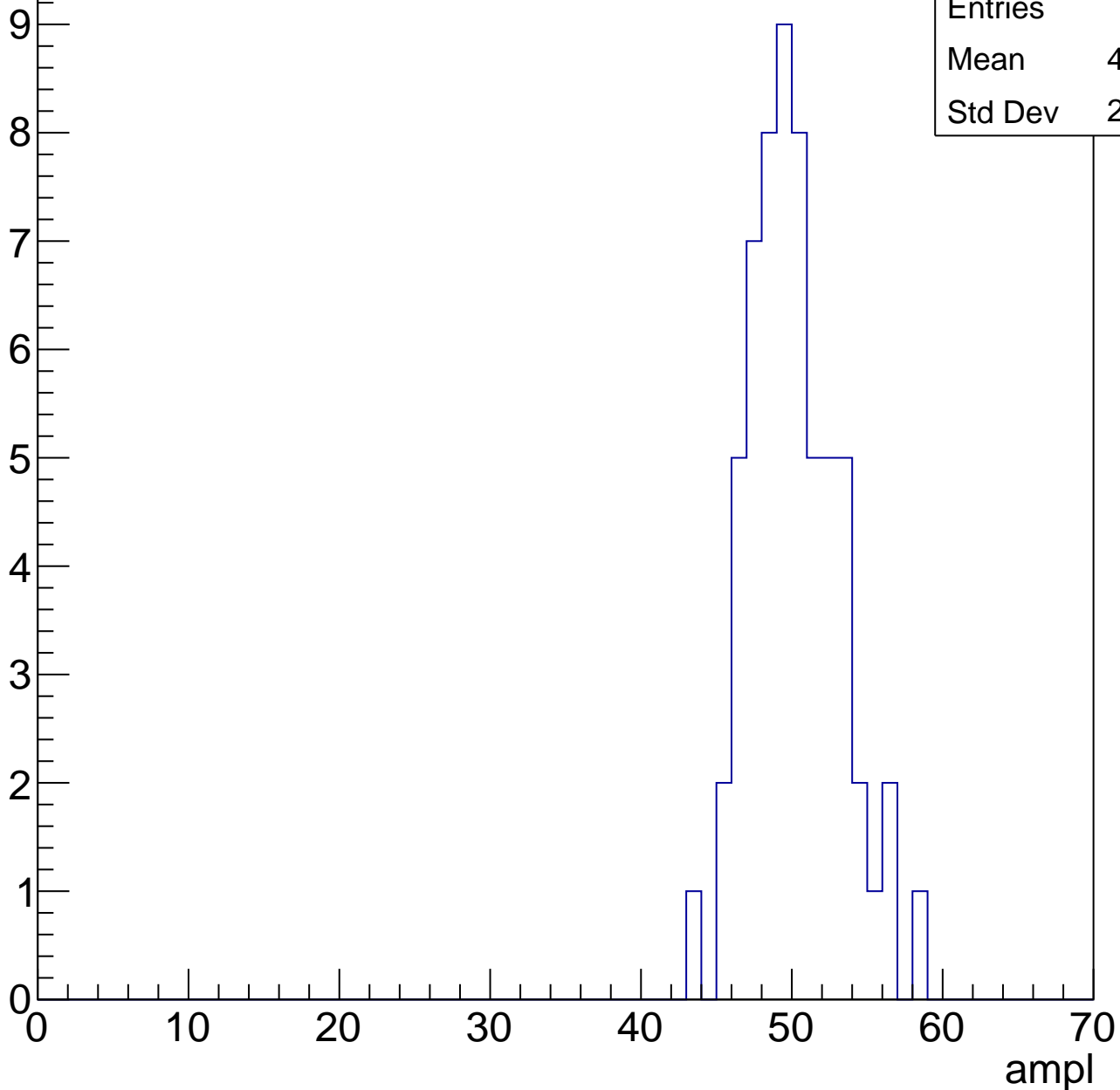


B1L103S, U24-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	49.67
Std Dev	2.974

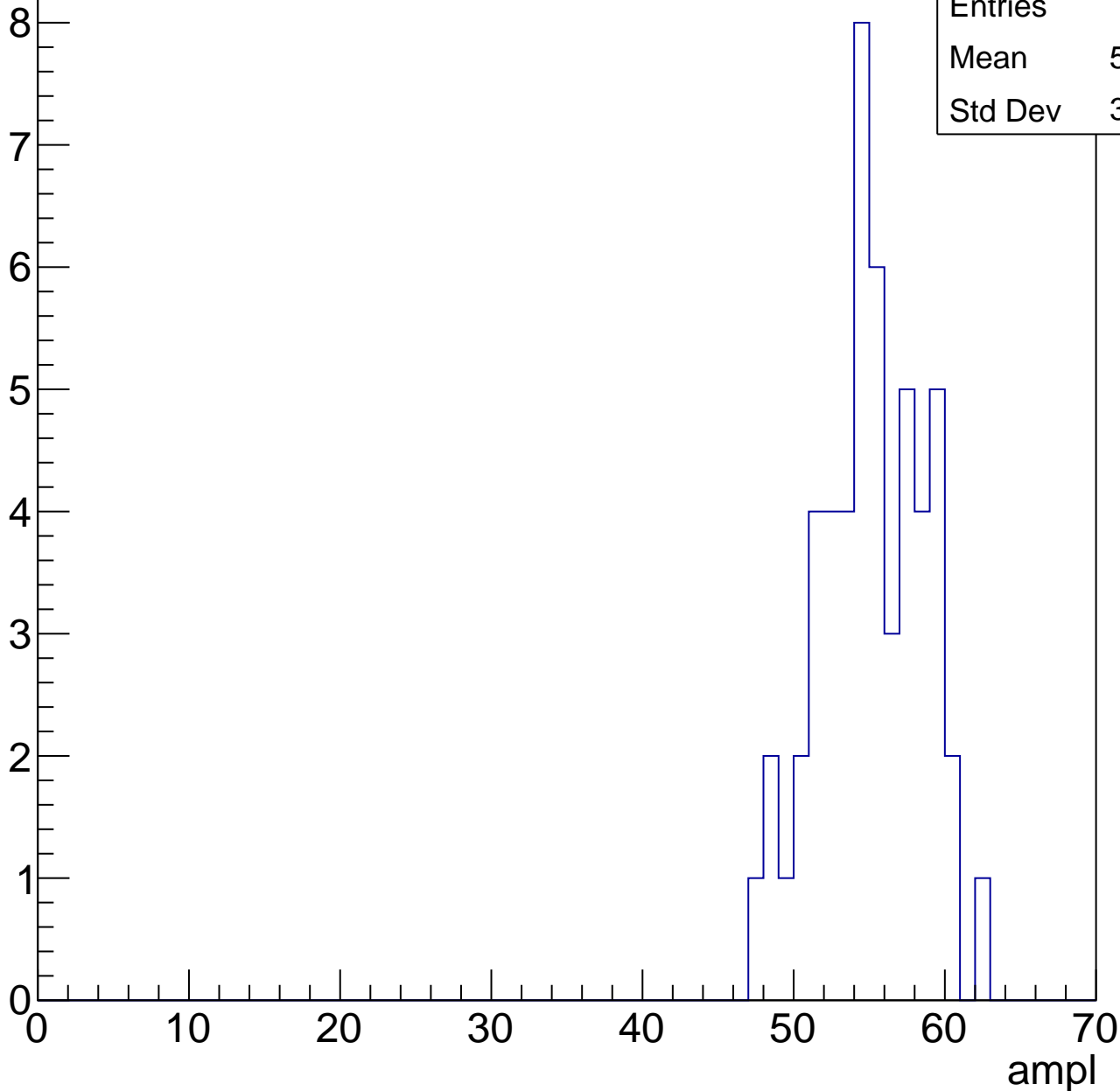


B1L103S, U24-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	54.62
Std Dev	3.403

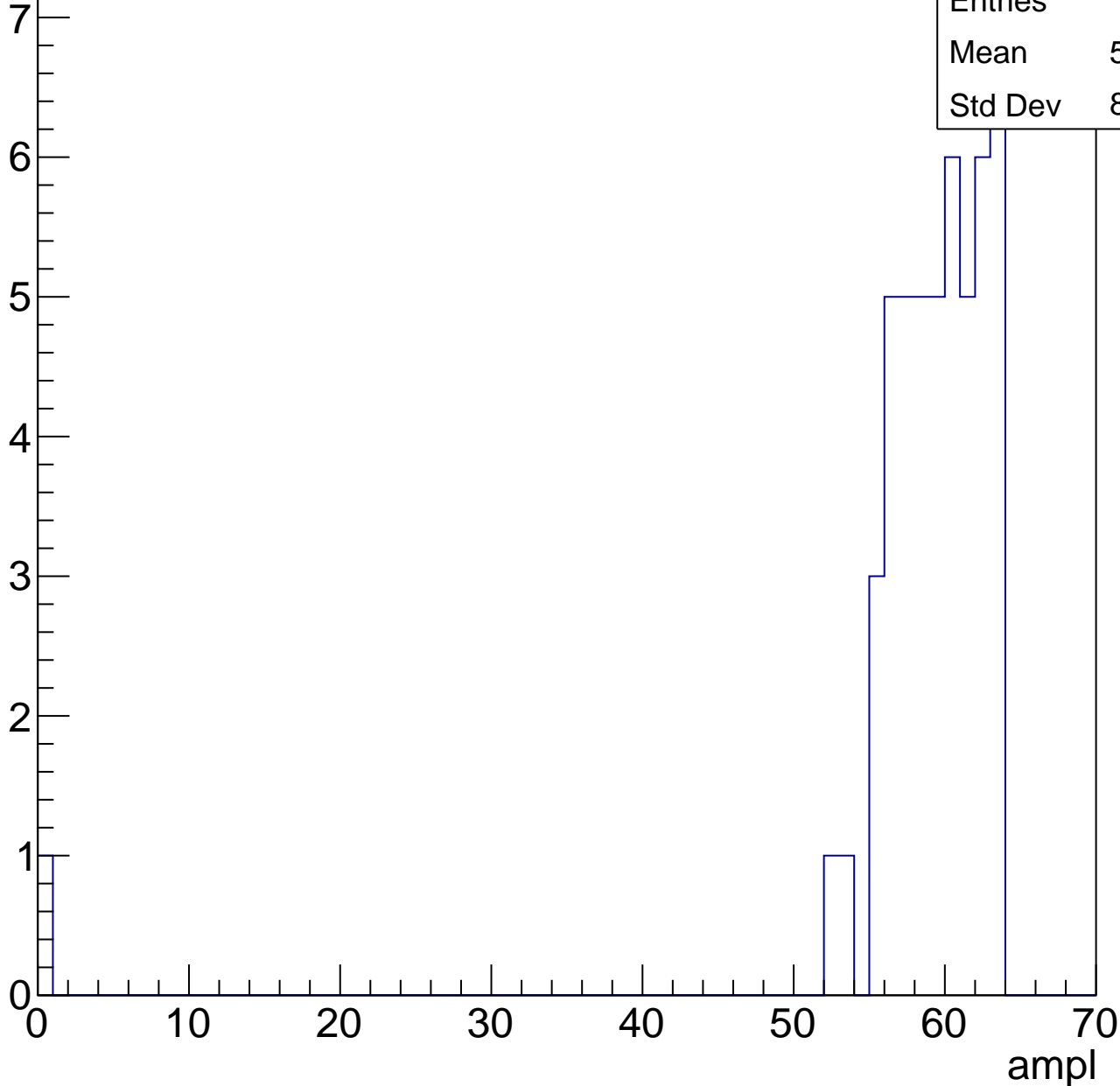


B1L103S, U24-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.96
Std Dev	8.743

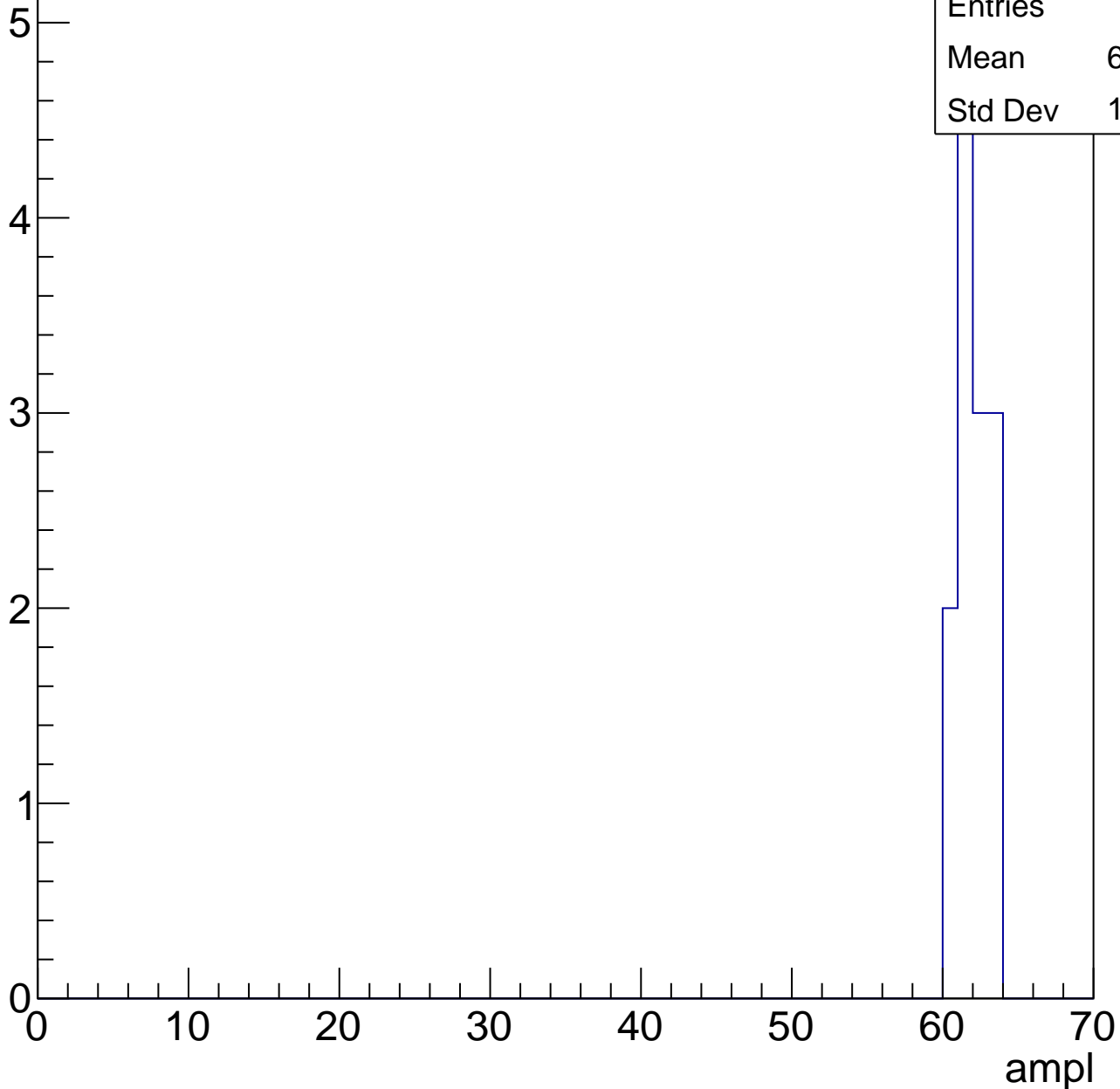


B1L103S, U24-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.54
Std Dev	1.009

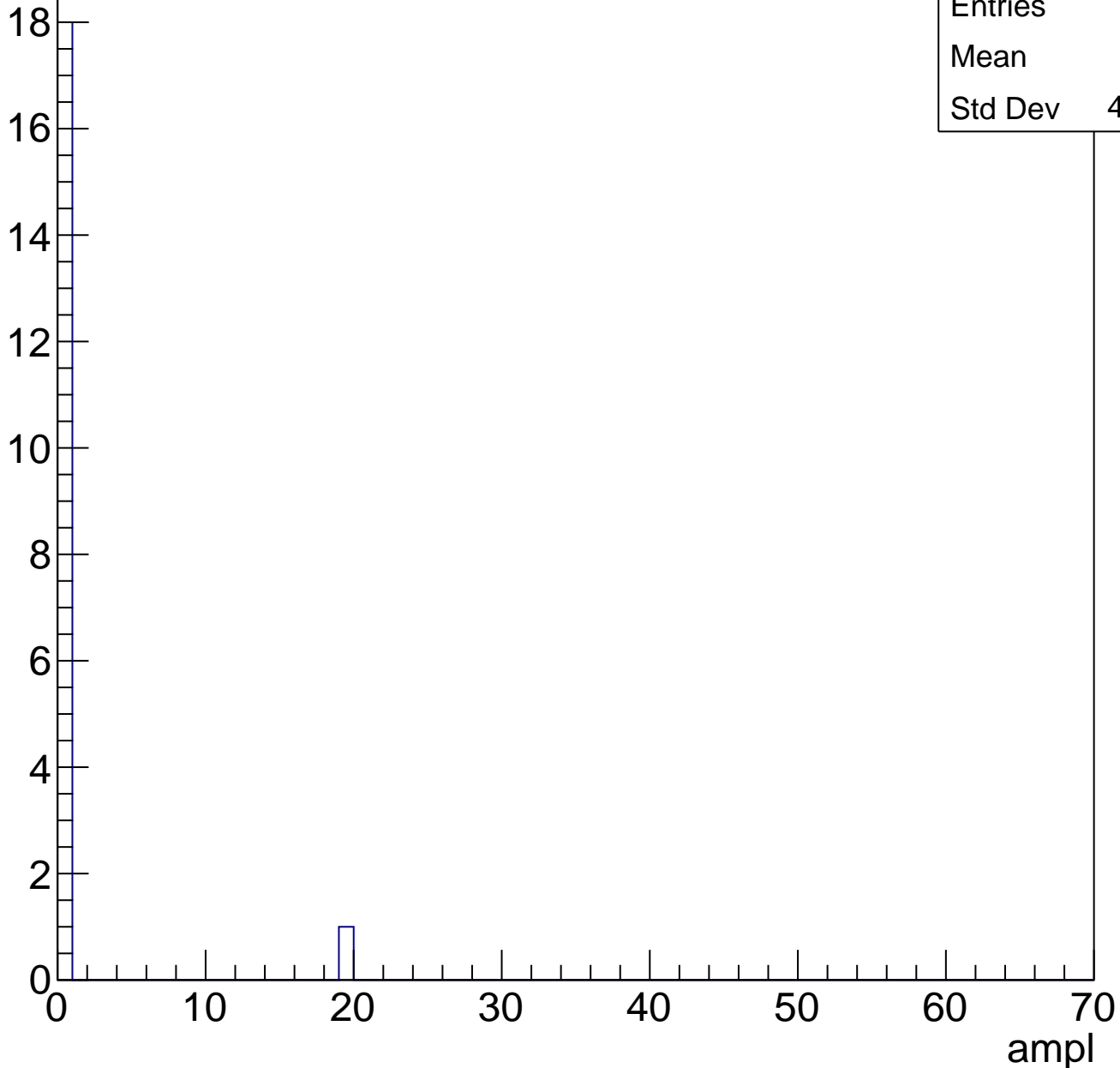


B1L103S, U24-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1
Std Dev	4.243

Entry



B1L103S, U24-ch93, adc0

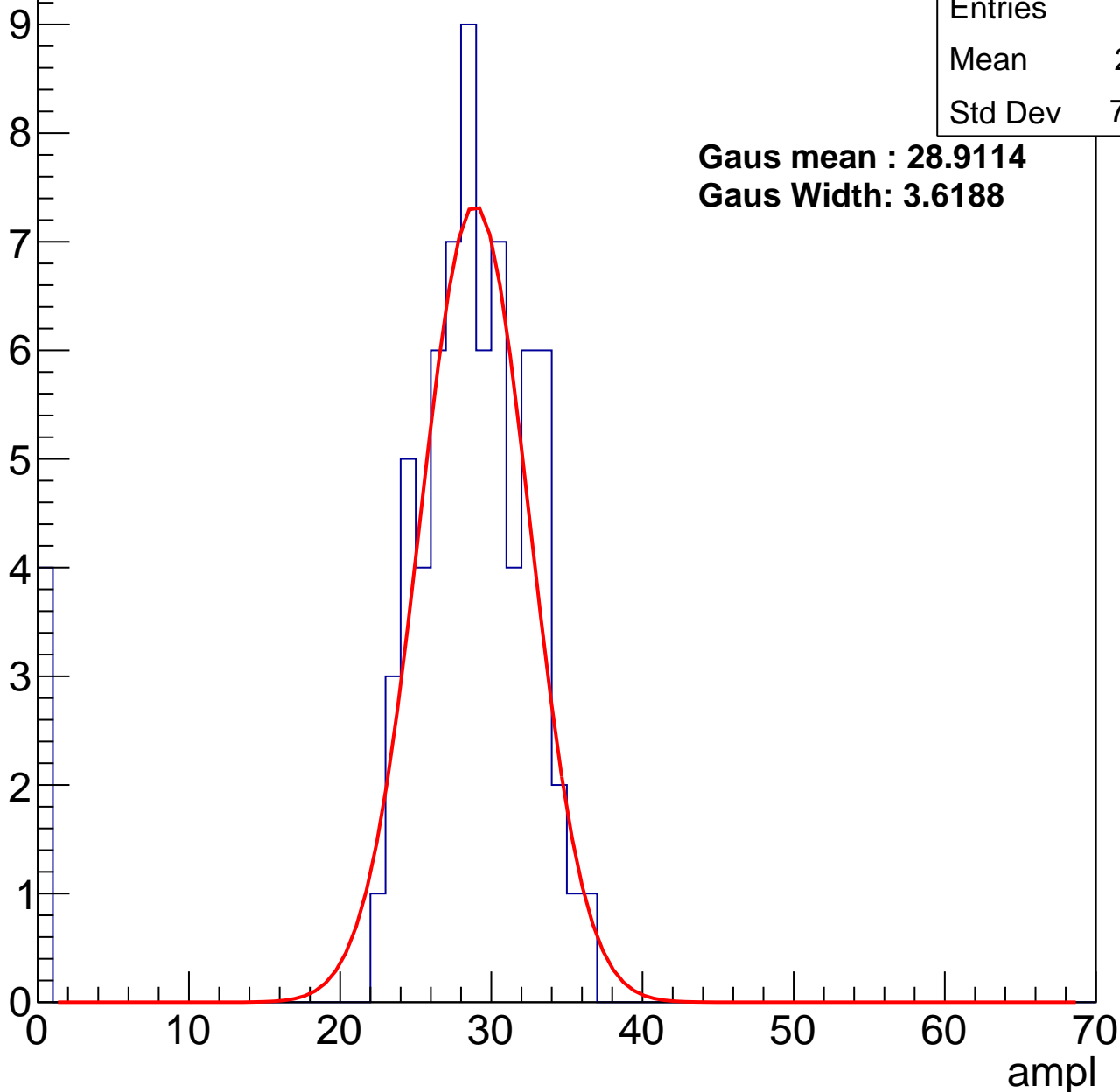
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	27.01
Std Dev	7.289

Gaus mean : 28.9114

Gaus Width: 3.6188



B1L103S, U24-ch93, adc1

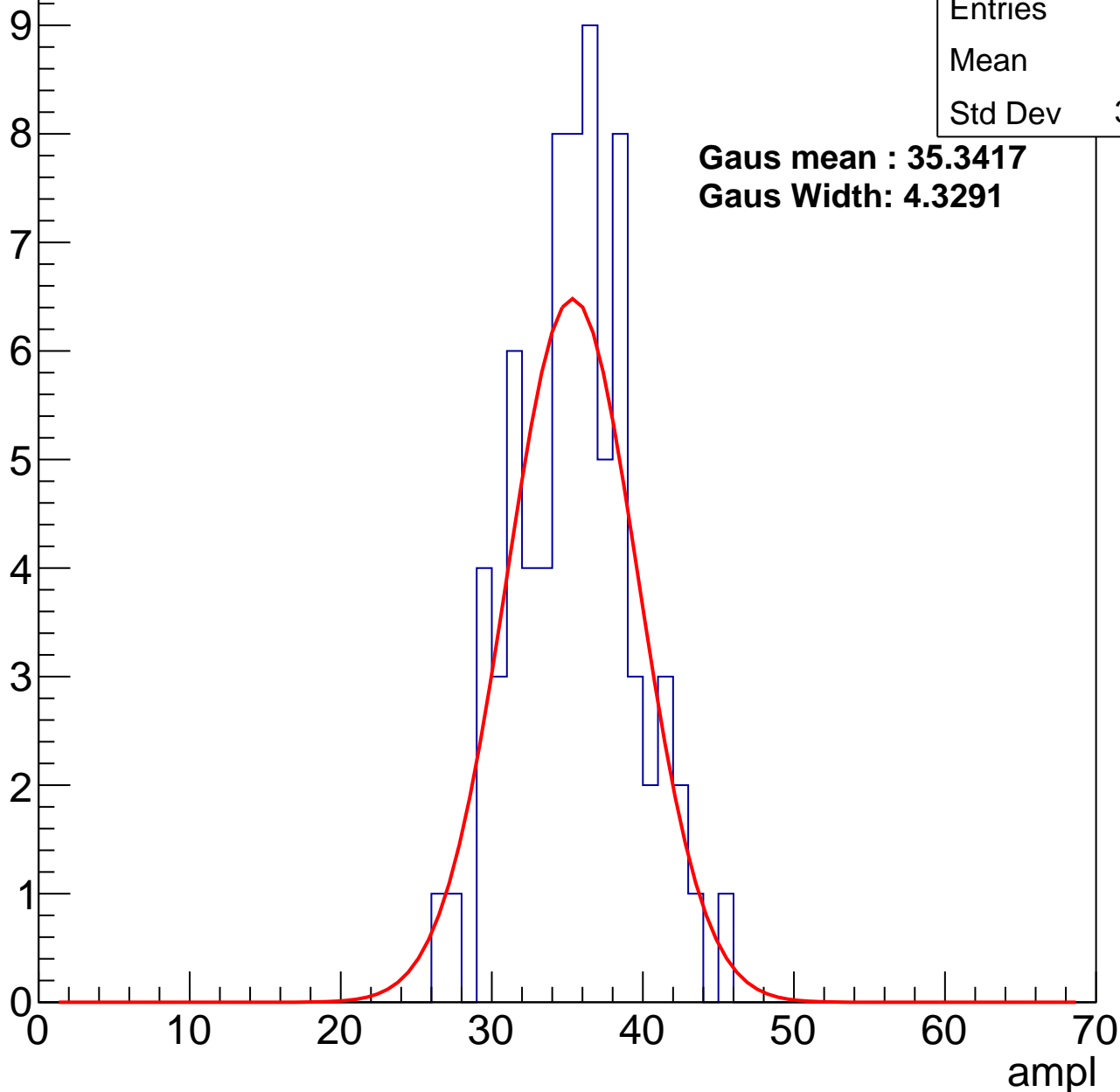
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35.1
Std Dev	3.861

Gaus mean : 35.3417

Gaus Width: 4.3291



B1L103S, U24-ch93, adc2

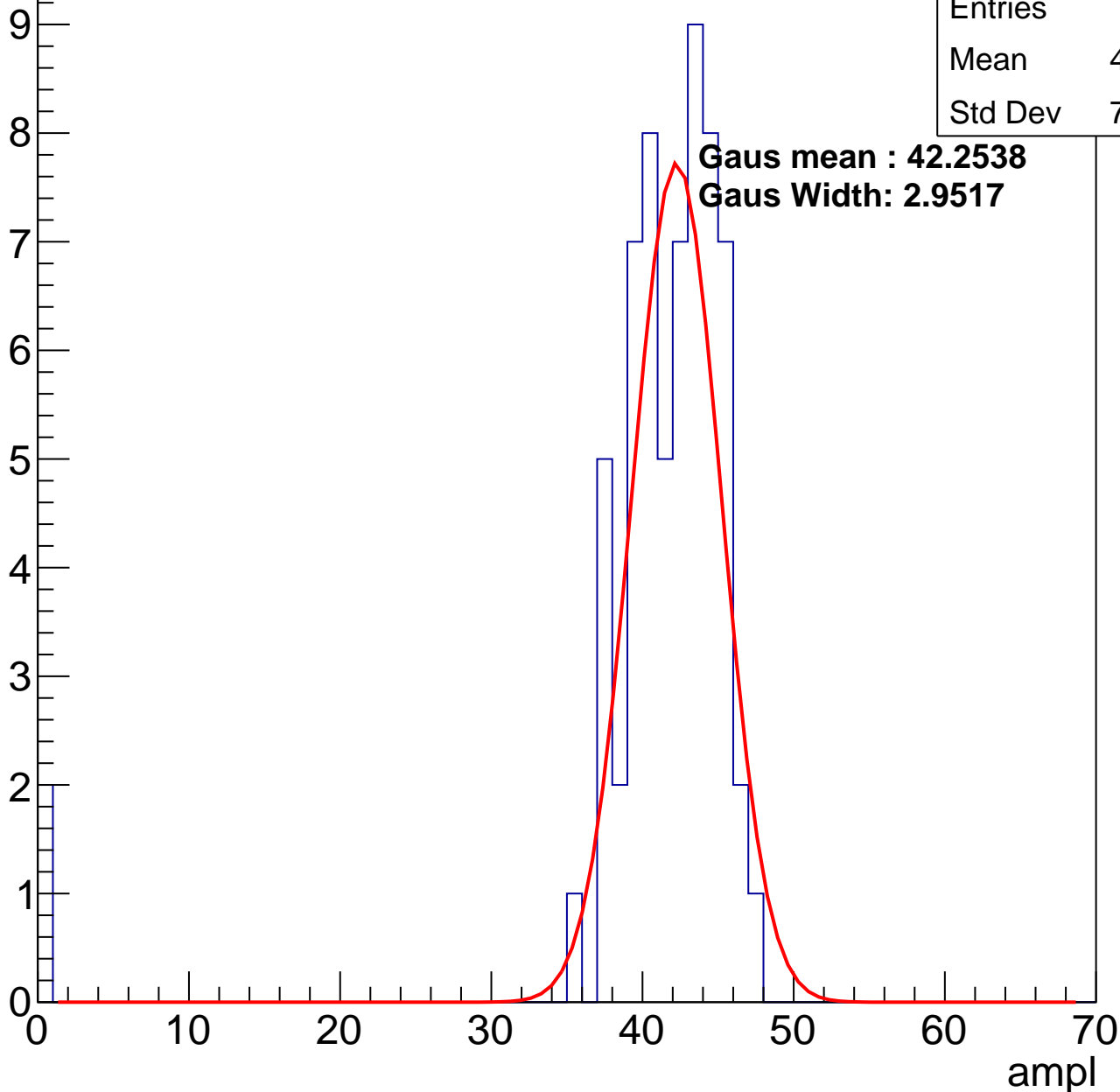
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	40.33
Std Dev	7.722

Gaus mean : 42.2538

Gaus Width: 2.9517

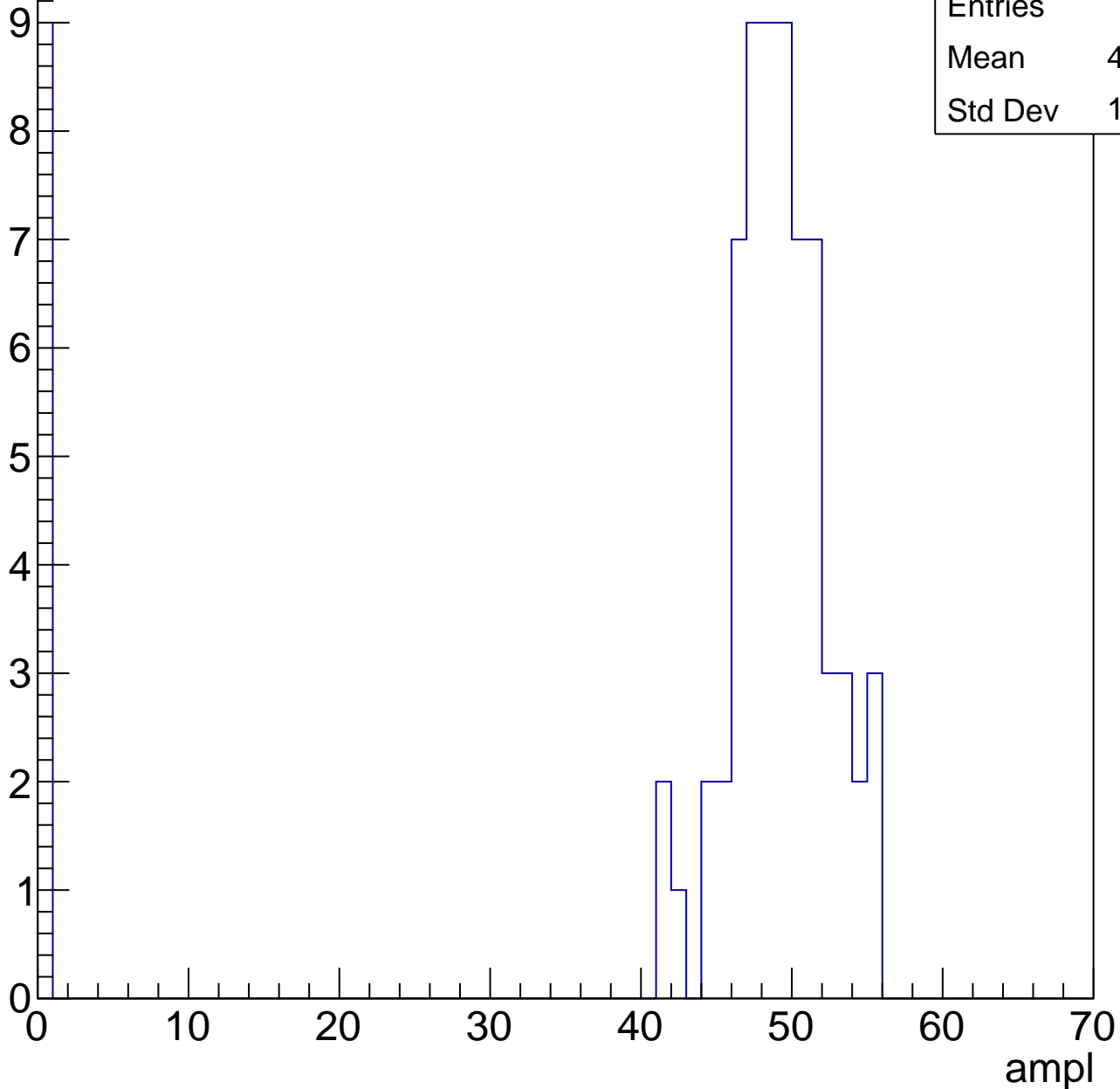


B1L103S, U24-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	42.87
Std Dev	16.09

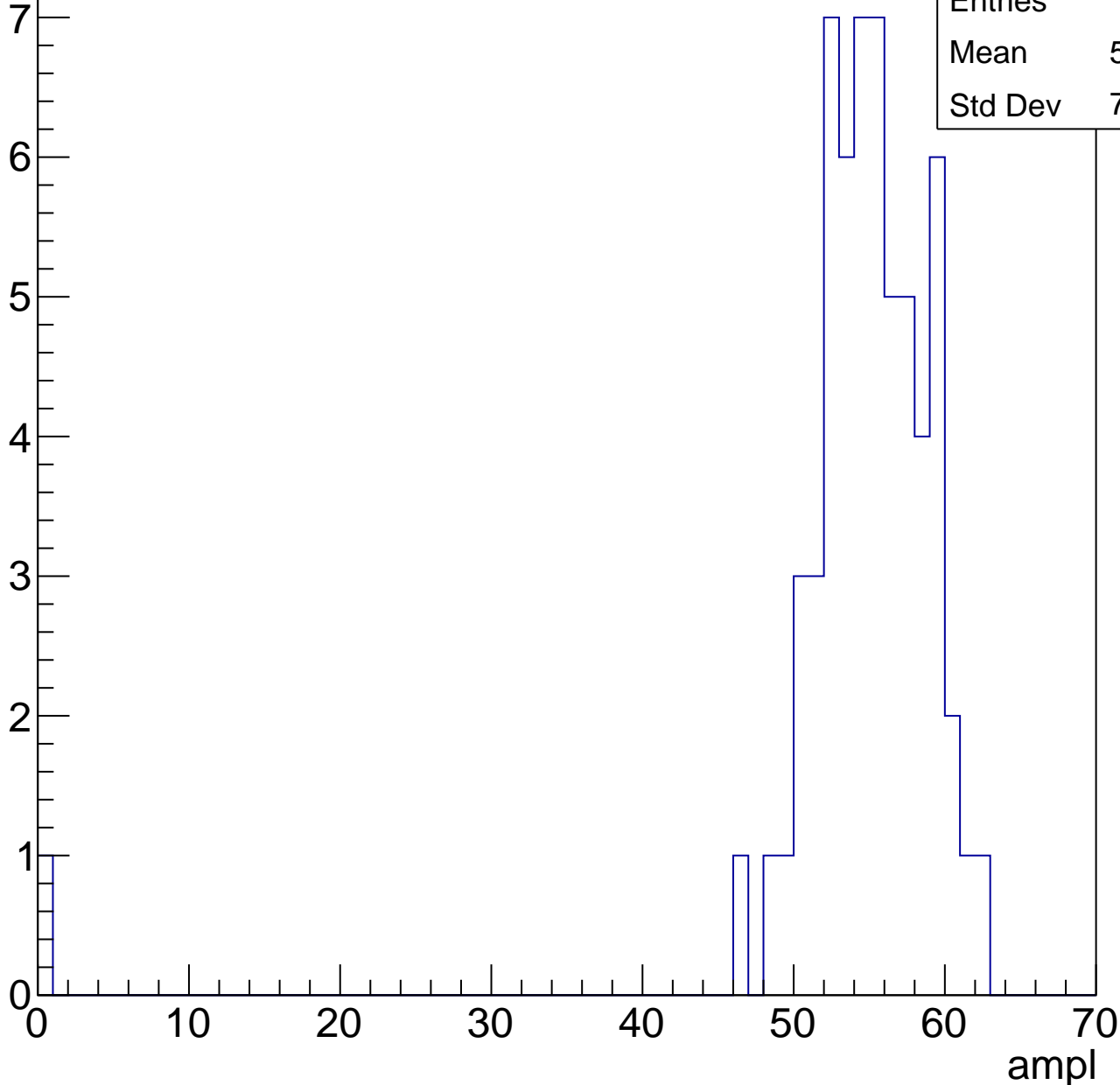


B1L103S, U24-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	53.85
Std Dev	7.703

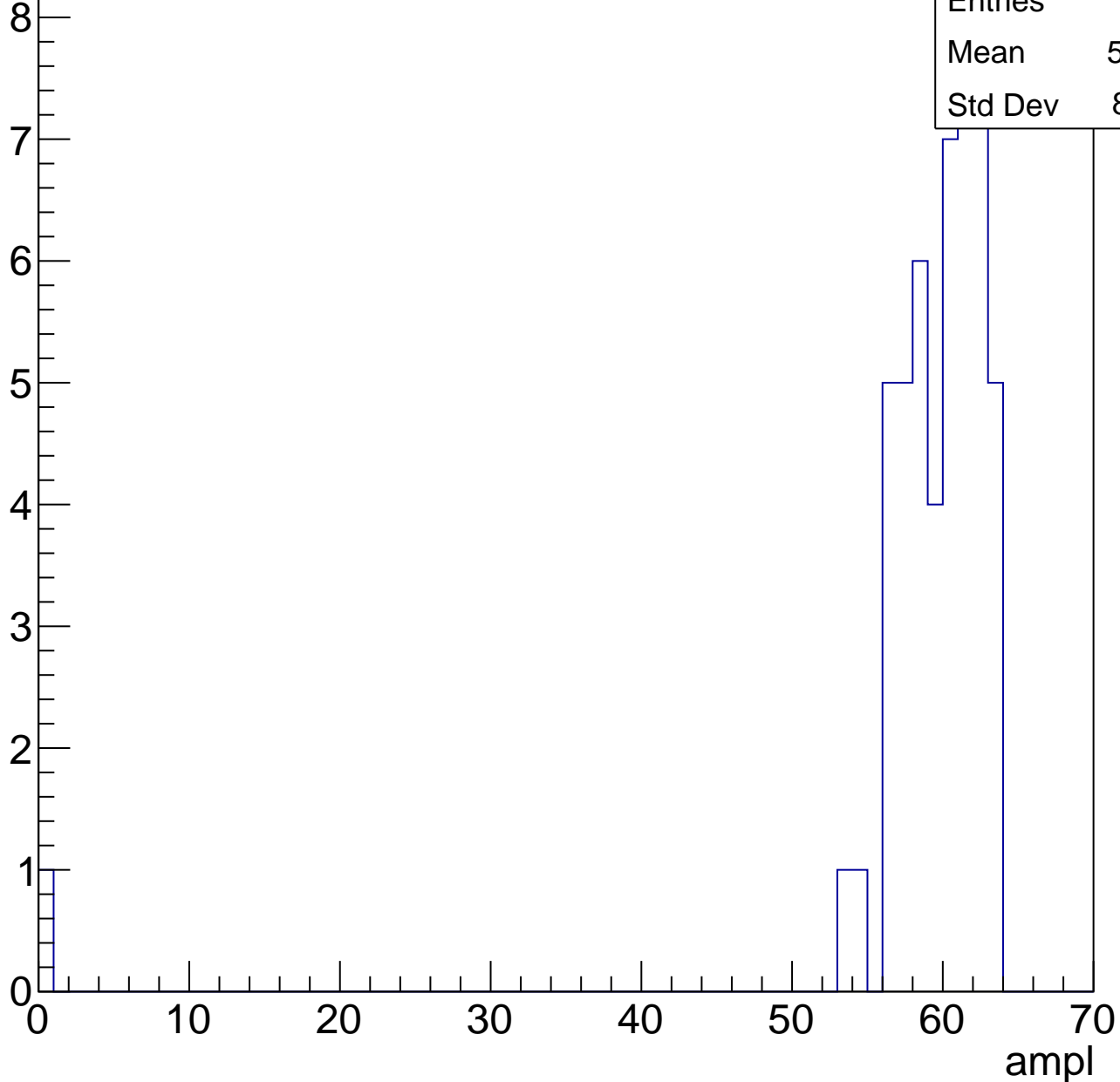


B1L103S, U24-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

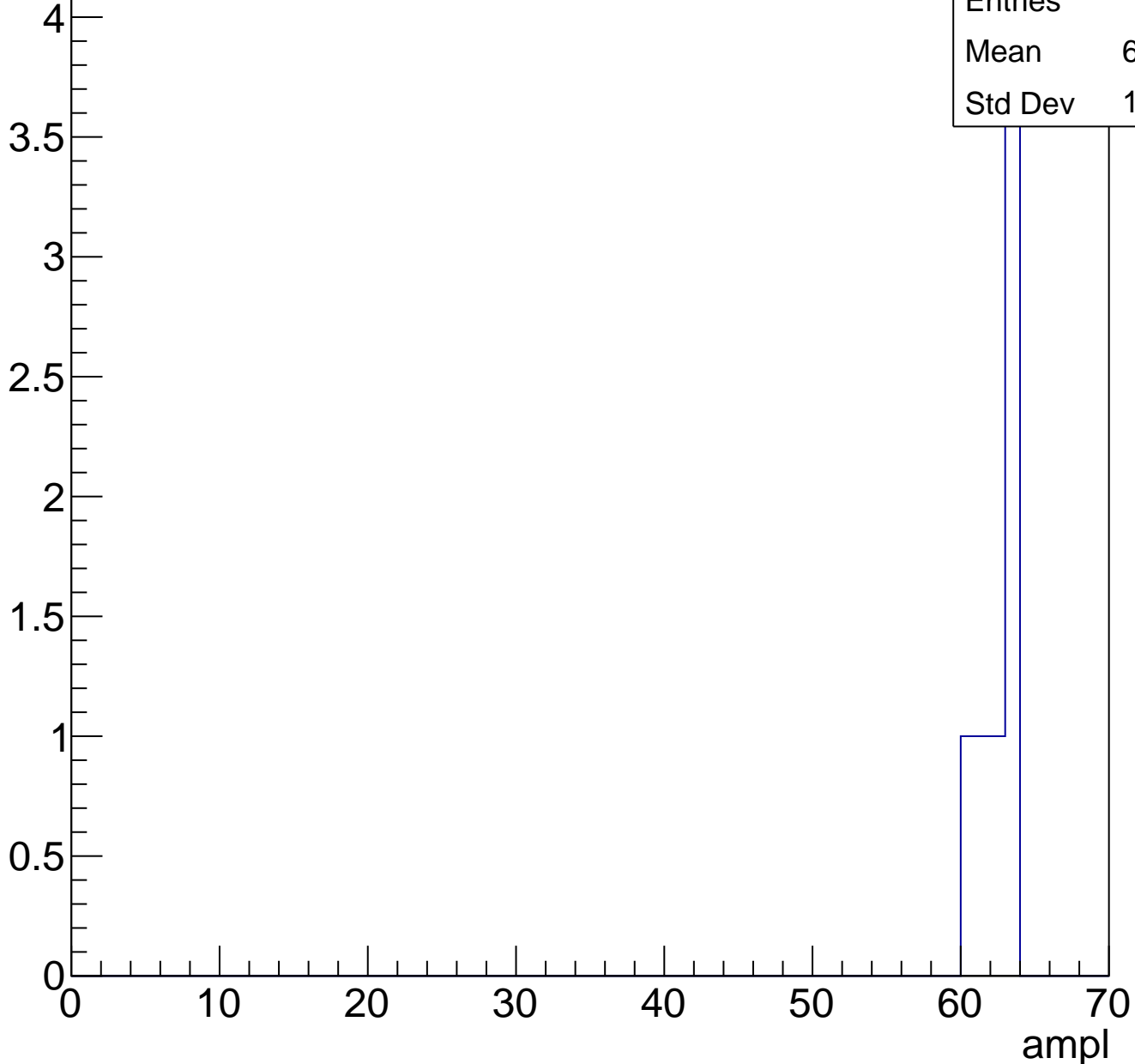
Entries	51
Mean	58.33
Std Dev	8.611



B1L103S, U24-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	7
Mean	62.14
Std Dev	1.125

B1L103S, U24-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.316
Std Dev	14.47

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

B1L103S, U24-ch94, adc0

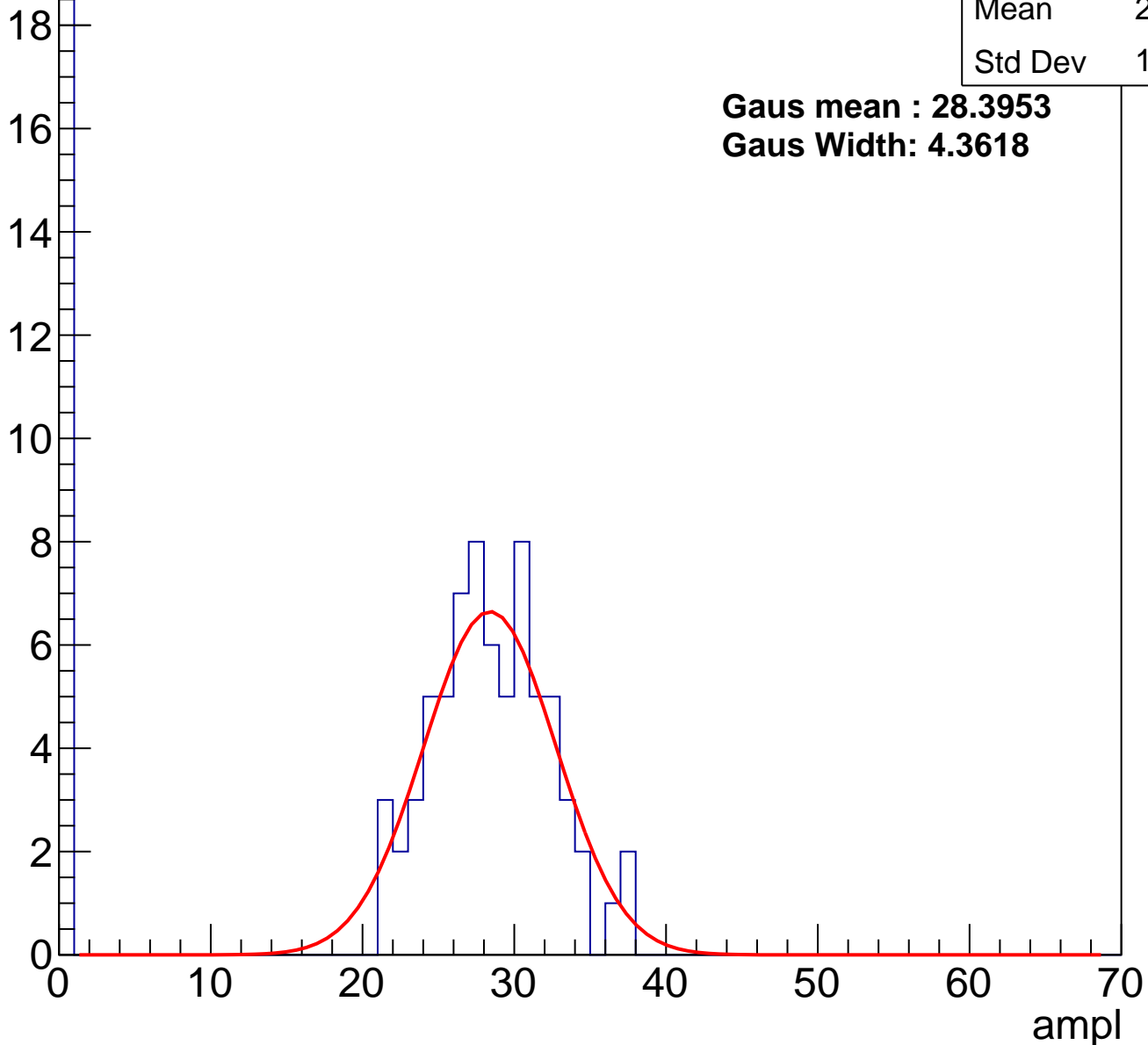
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	22.07
Std Dev	11.97

Gaus mean : 28.3953

Gaus Width: 4.3618

Entry



B1L103S, U24-ch94, adc1

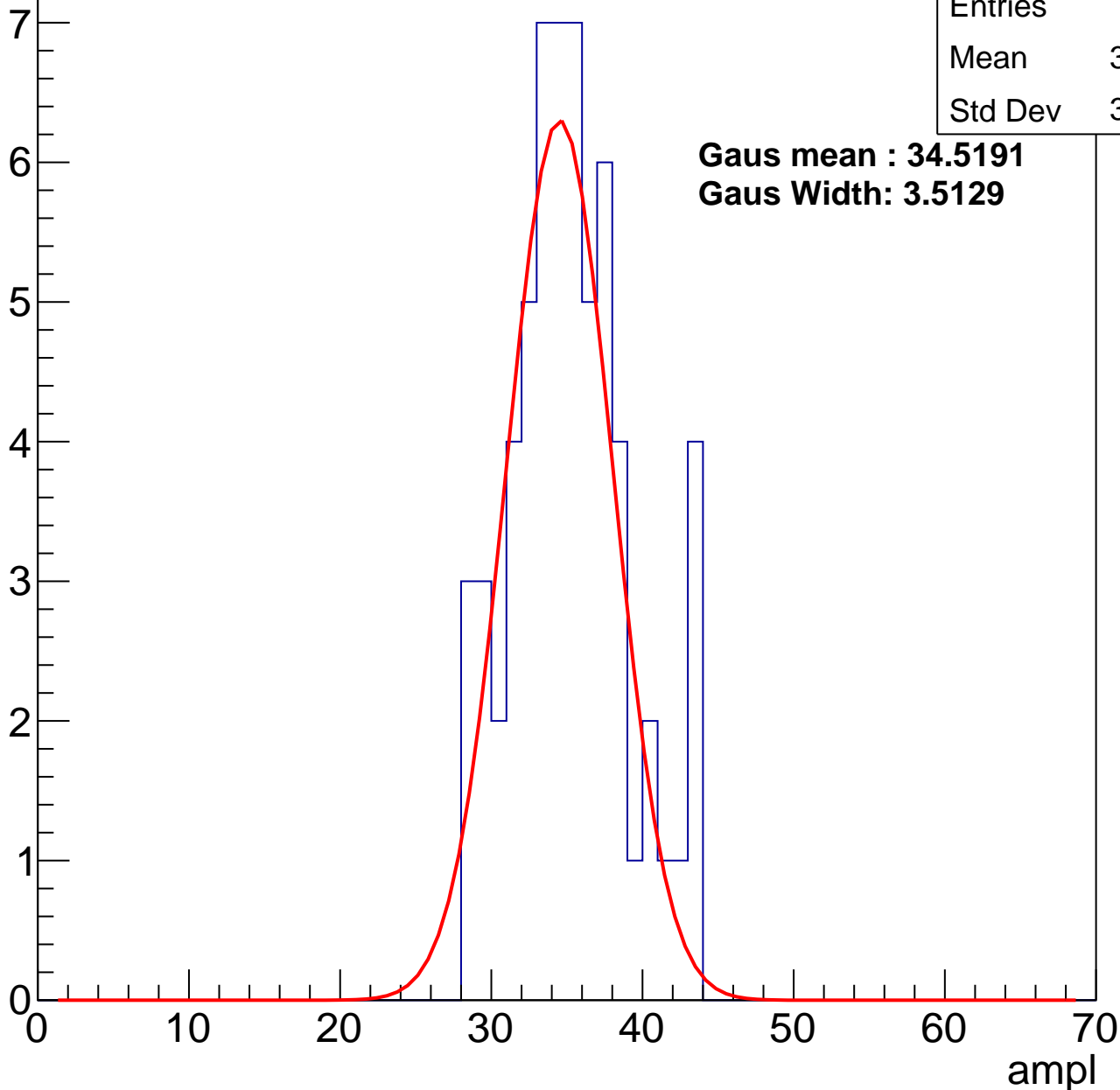
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	34.79
Std Dev	3.848

Gaus mean : 34.5191

Gaus Width: 3.5129



B1L103S, U24-ch94, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	35.69
Std Dev	14.58

Gaus mean : 41.6041

Gaus Width: 4.5571

Entry

10

8

6

4

2

0

0

10

20

30

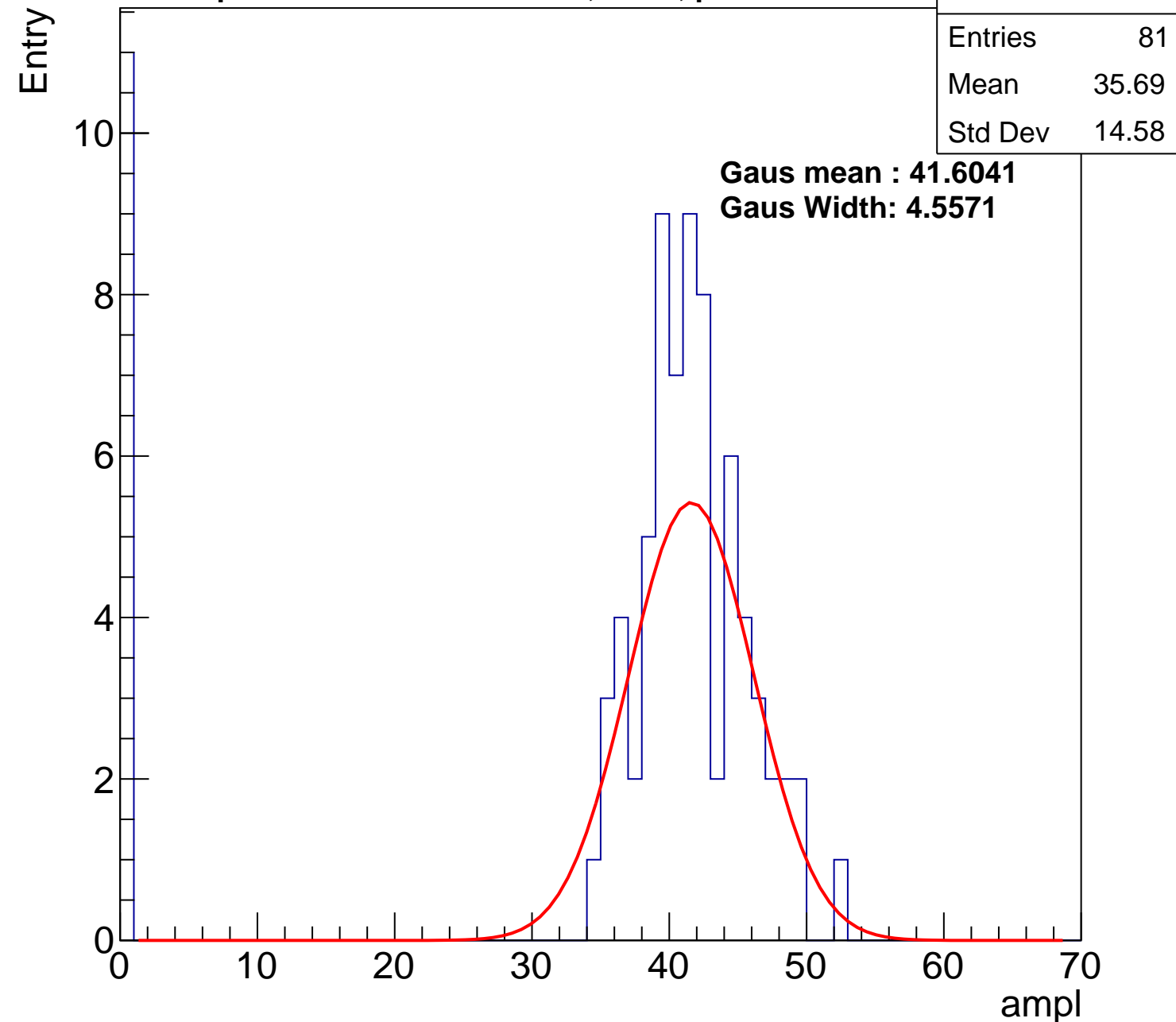
40

50

60

70

ampl

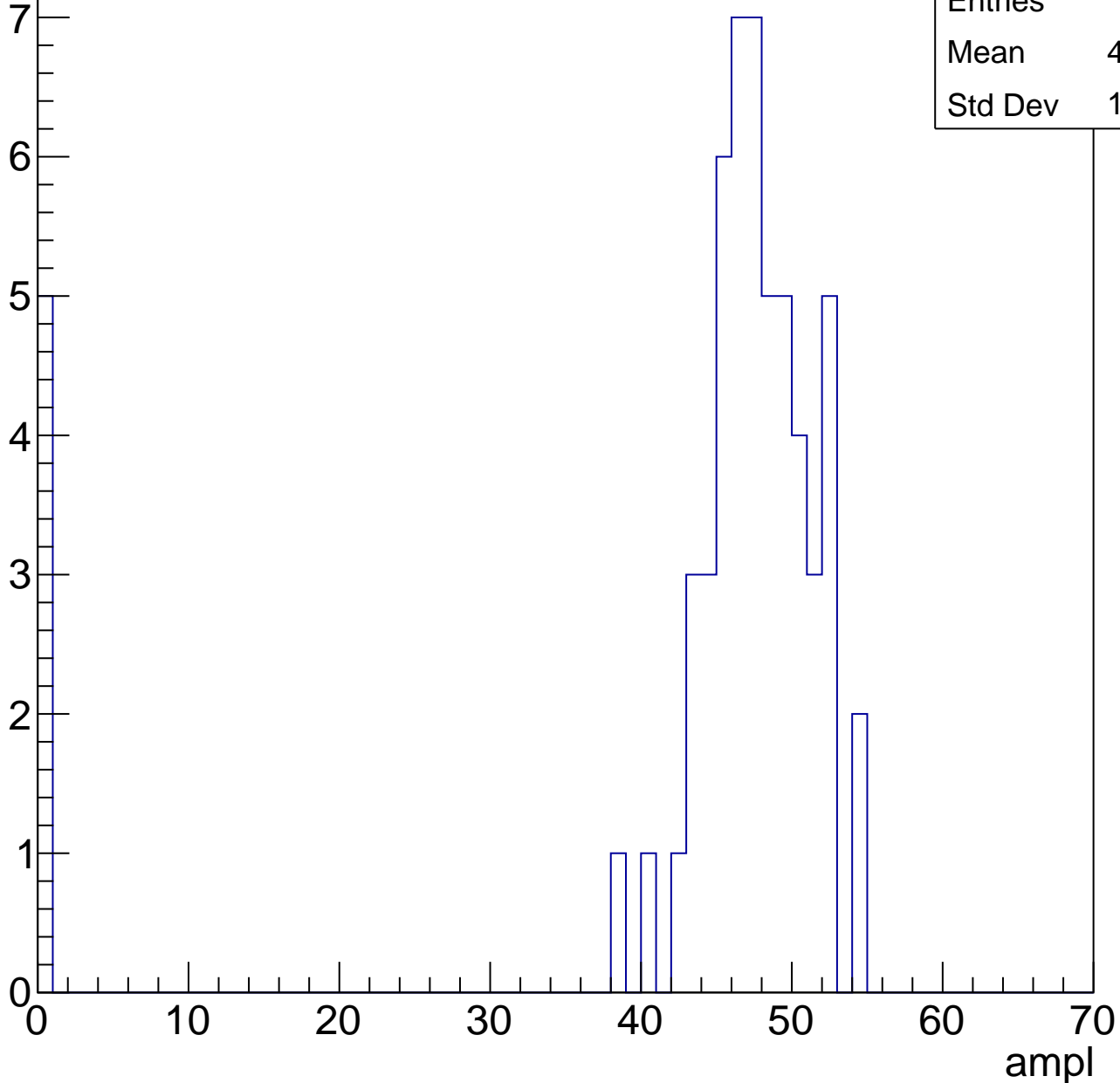


B1L103S, U24-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	43.24
Std Dev	13.66

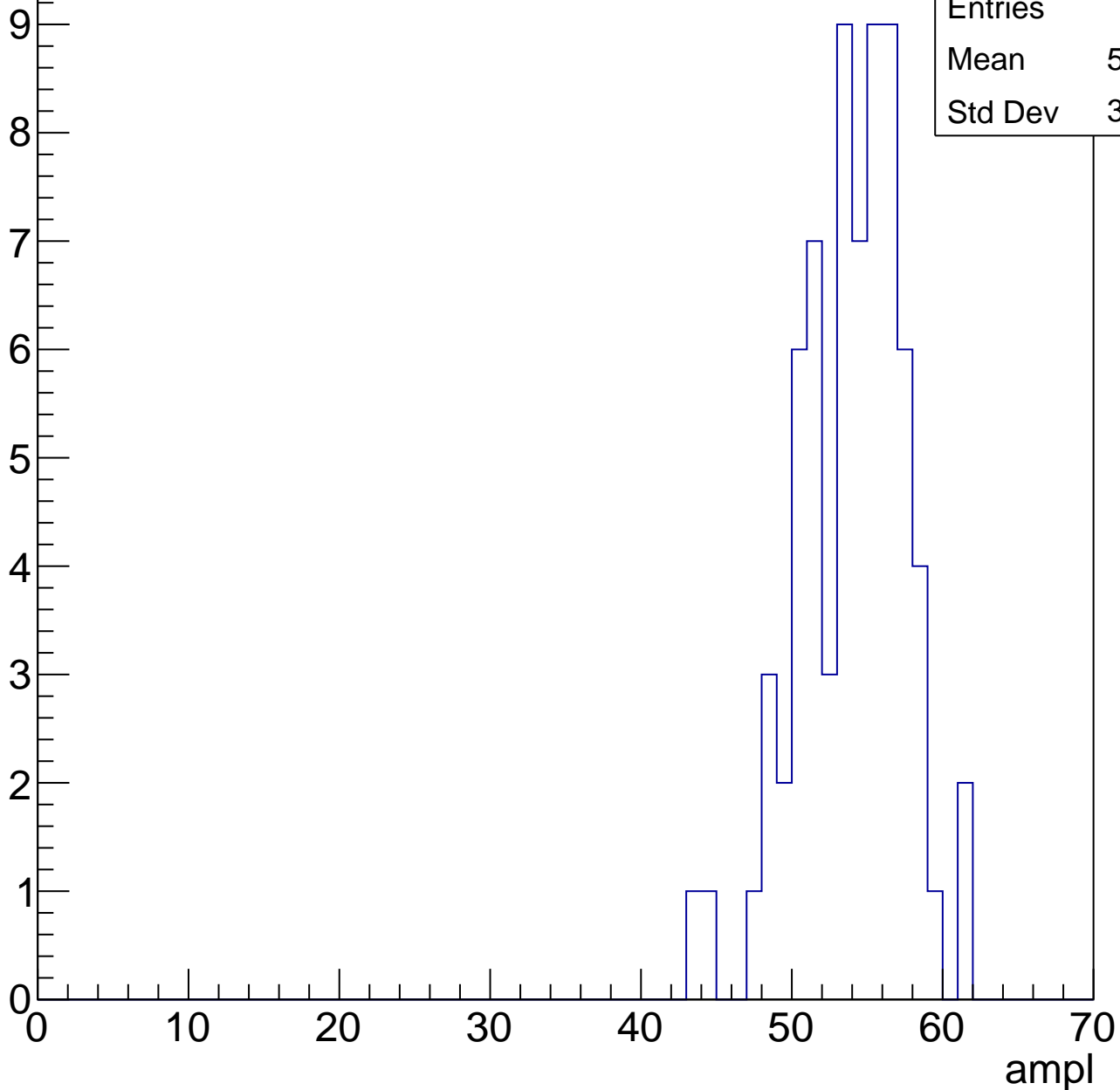


B1L103S, U24-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	53.49
Std Dev	3.512



B1L103S, U24-ch94, adc5

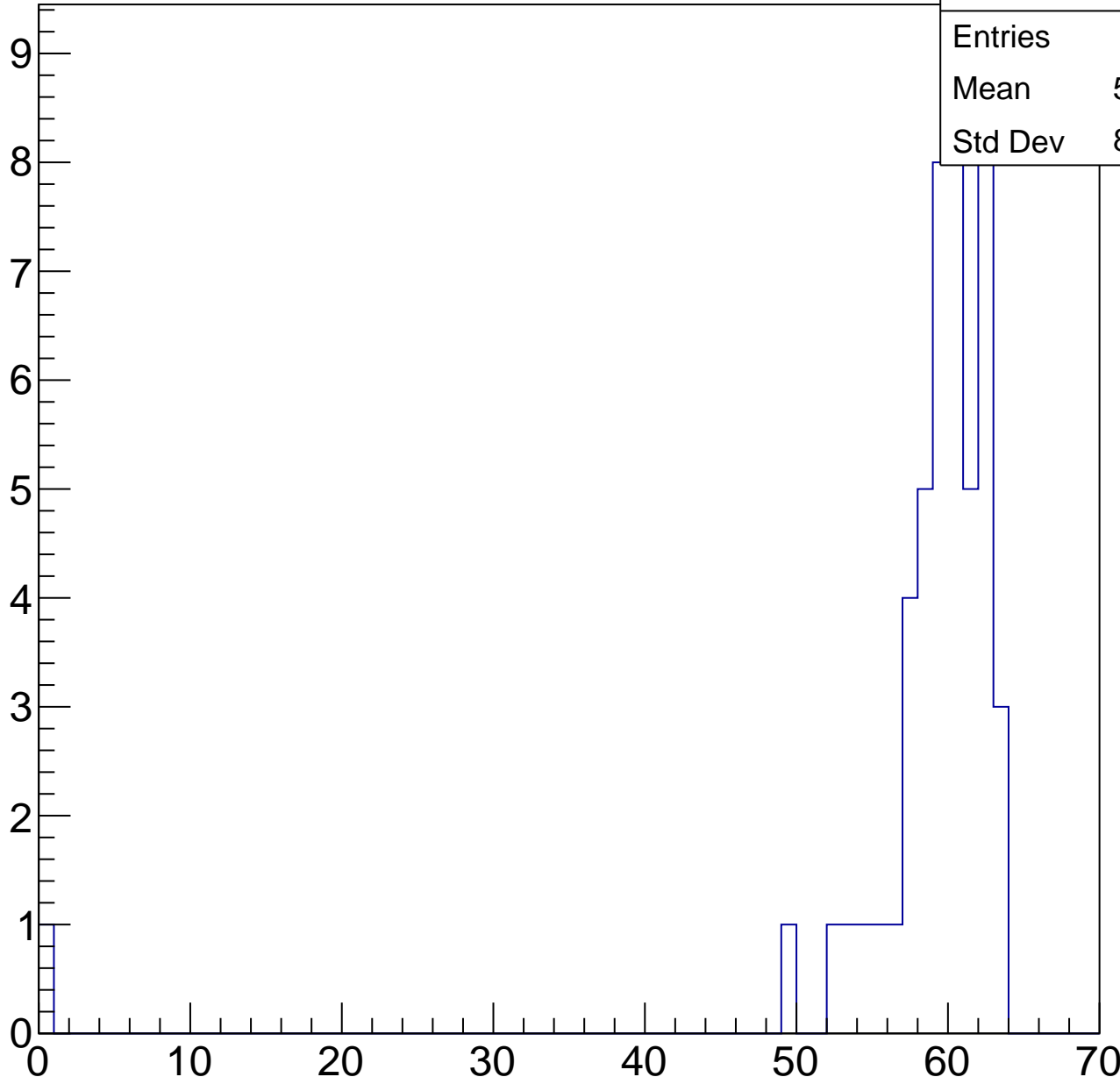
calib_packv5_041523_1651.root, FC#0, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	57.98
Std Dev	8.856

ampl

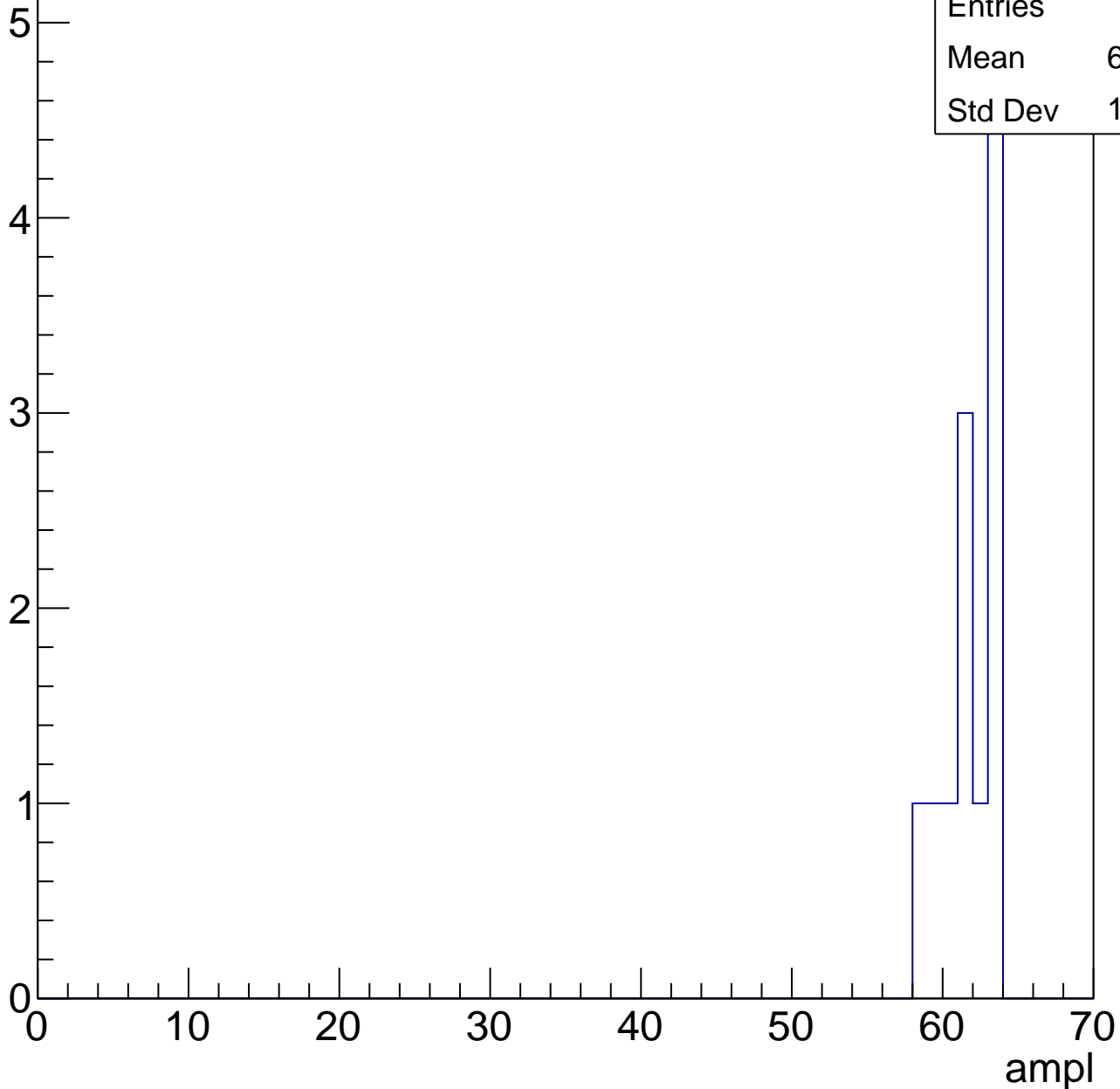


B1L103S, U24-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.42
Std Dev	1.656

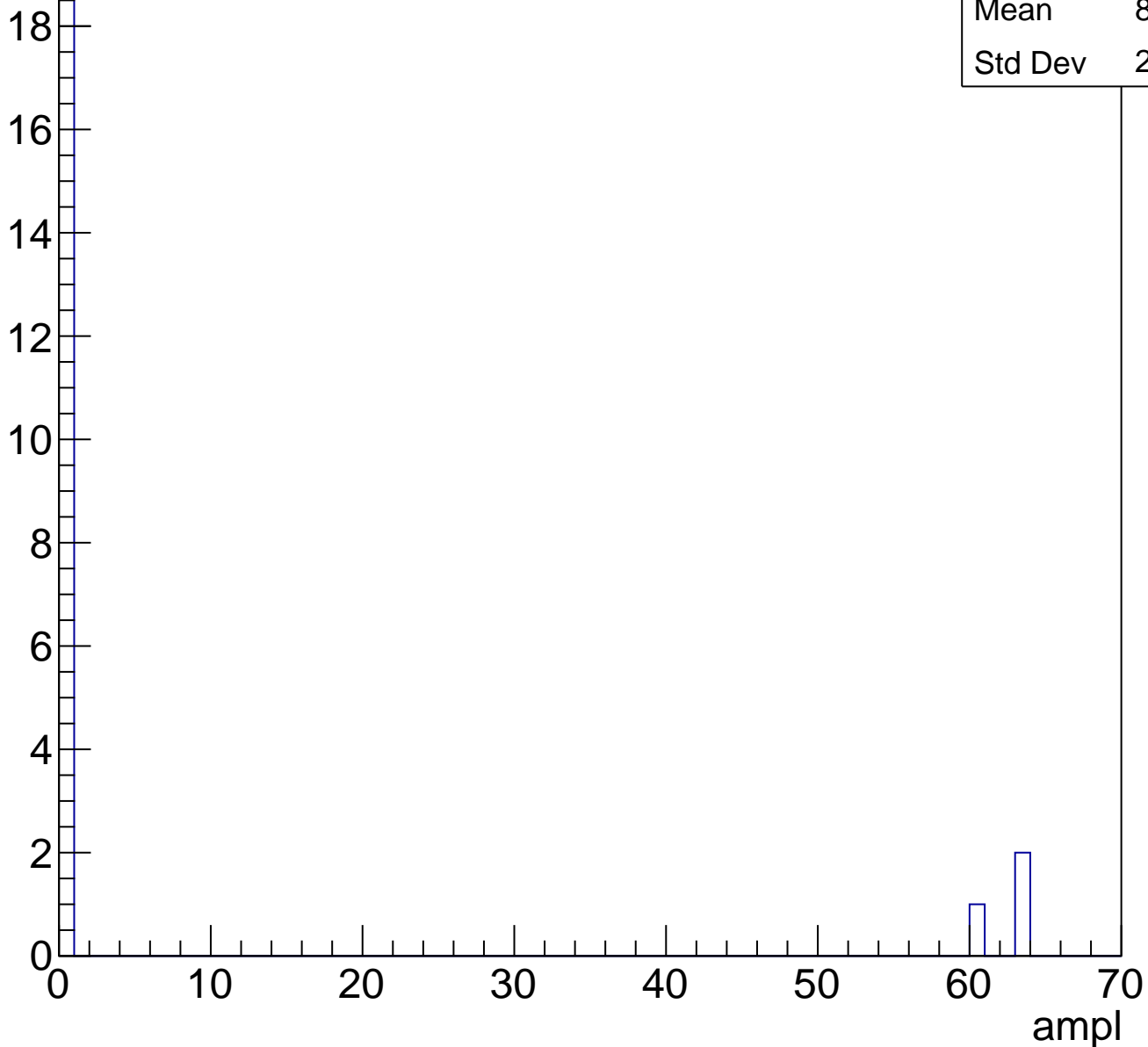


B1L103S, U24-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



B1L103S, U24-ch95, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	23.75
Std Dev	10.35

Gaus mean : 27.6254

Gaus Width: 4.1453

Entry

10

8

6

4

2

0

0

10

20

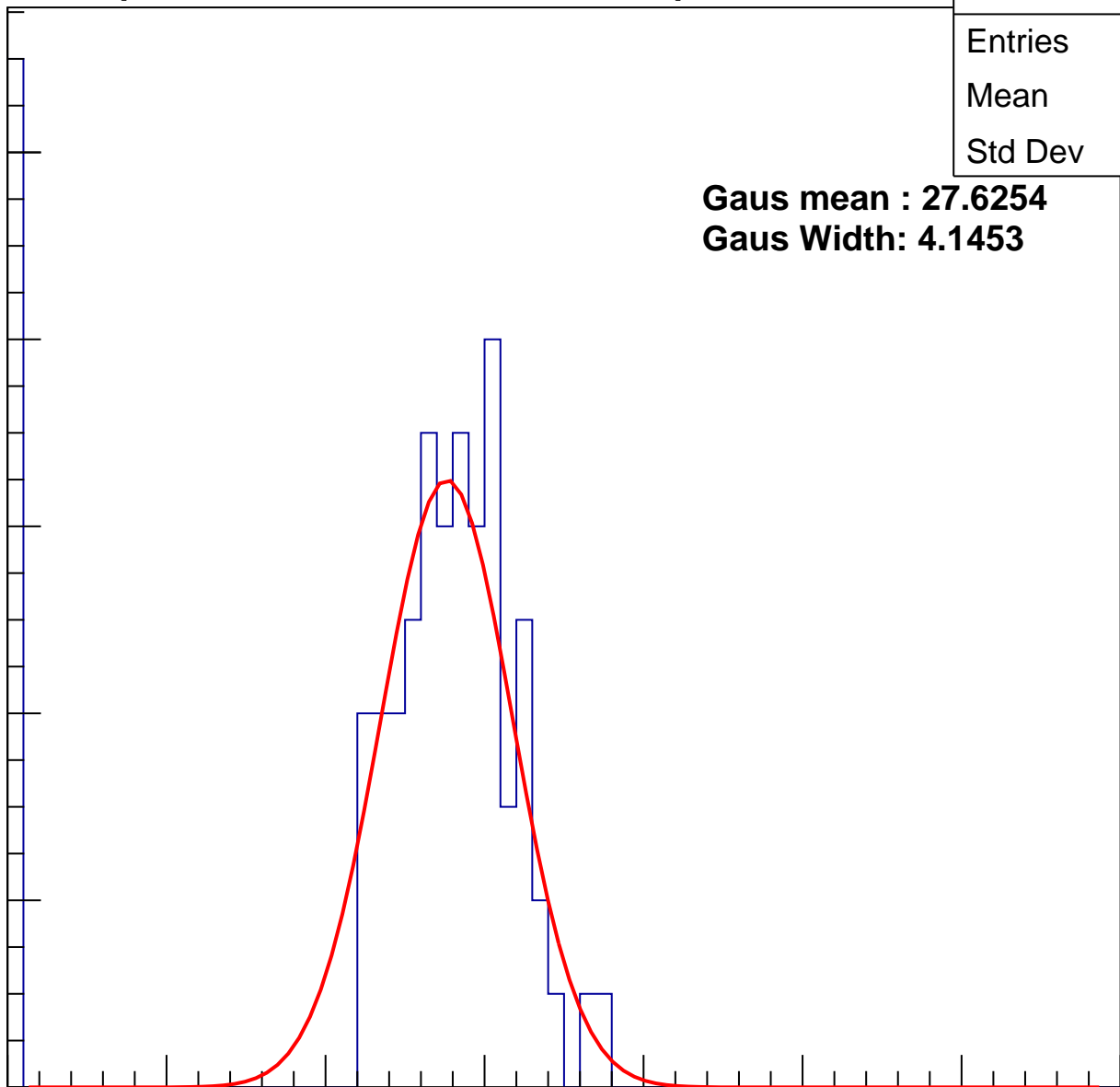
30

40

50

60

ampl



B1L103S, U24-ch95, adc1

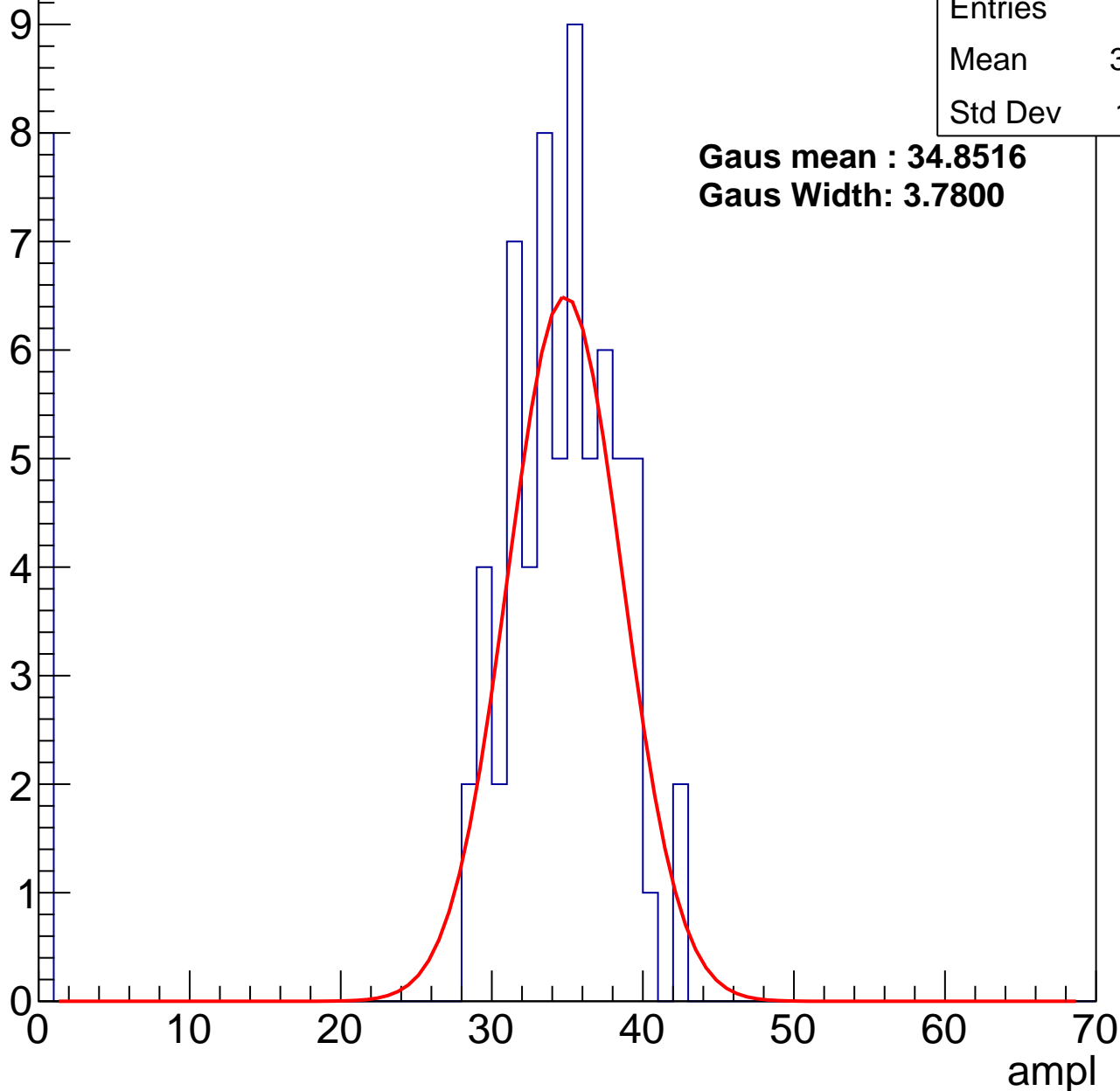
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	30.64
Std Dev	11.21

Gaus mean : 34.8516

Gaus Width: 3.7800



B1L103S, U24-ch95, adc2

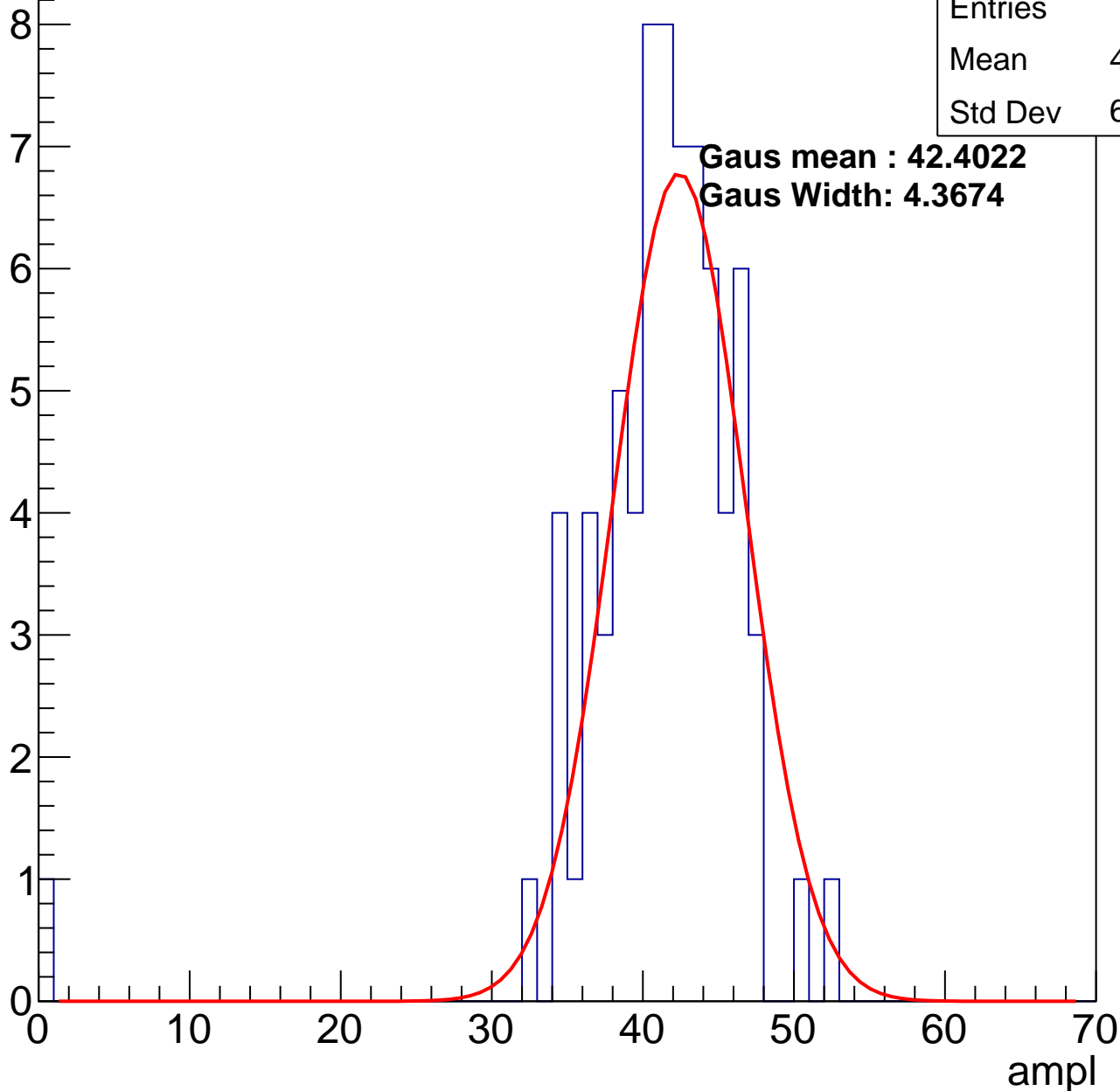
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	40.68
Std Dev	6.169

Gaus mean : 42.4022

Gaus Width: 4.3674

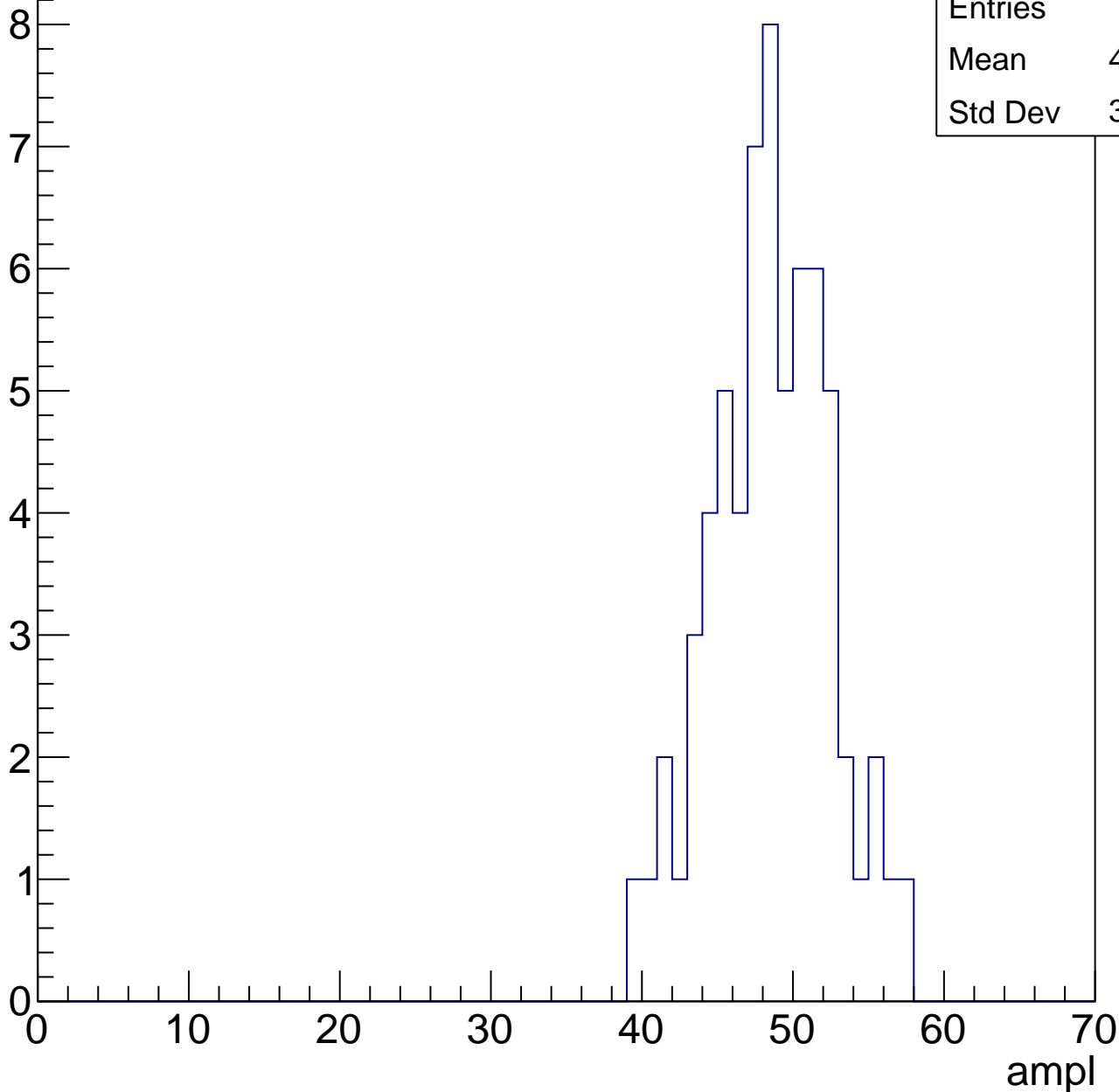


B1L103S, U24-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	48.06
Std Dev	3.878

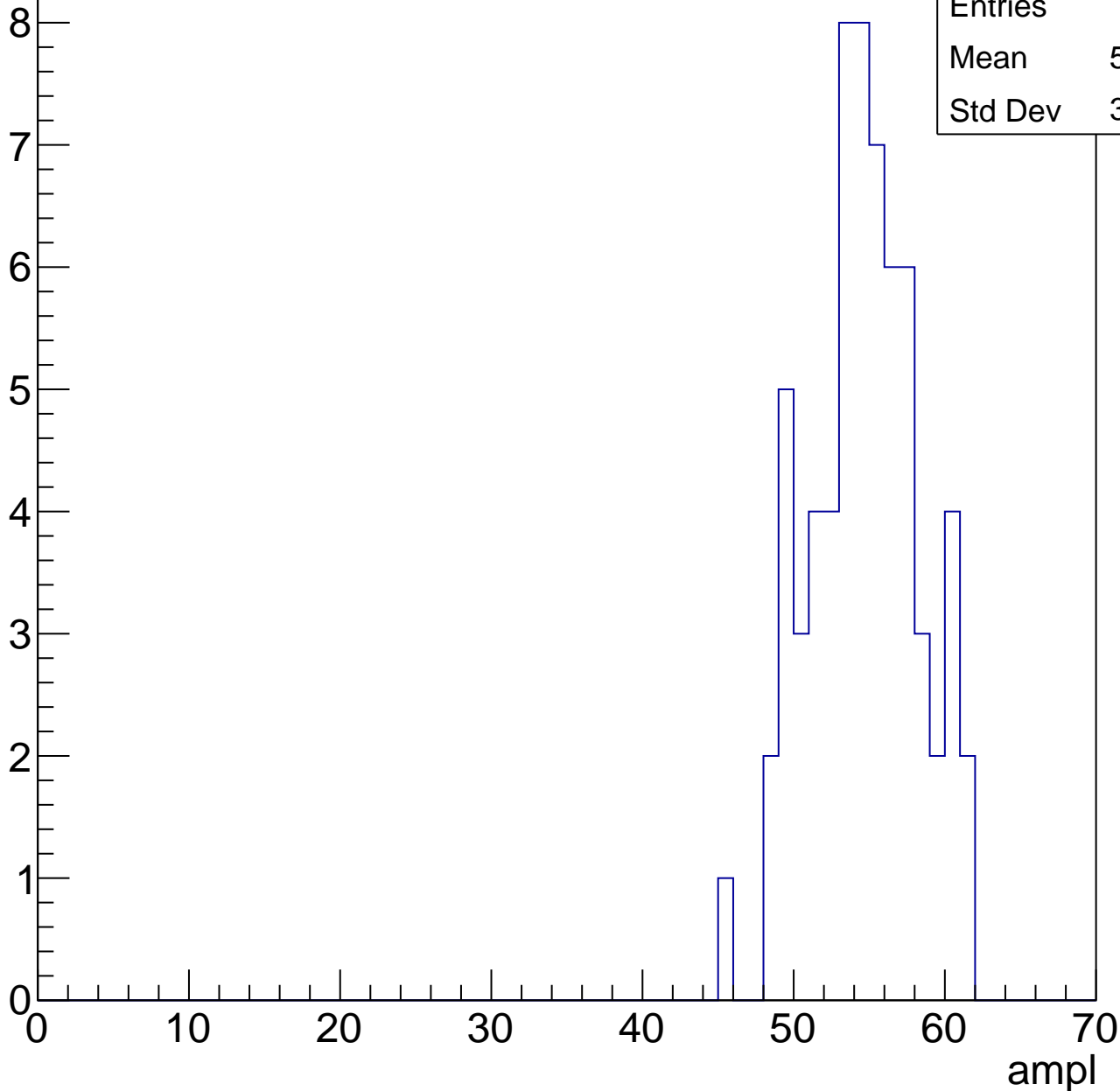


B1L103S, U24-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	54.17
Std Dev	3.528

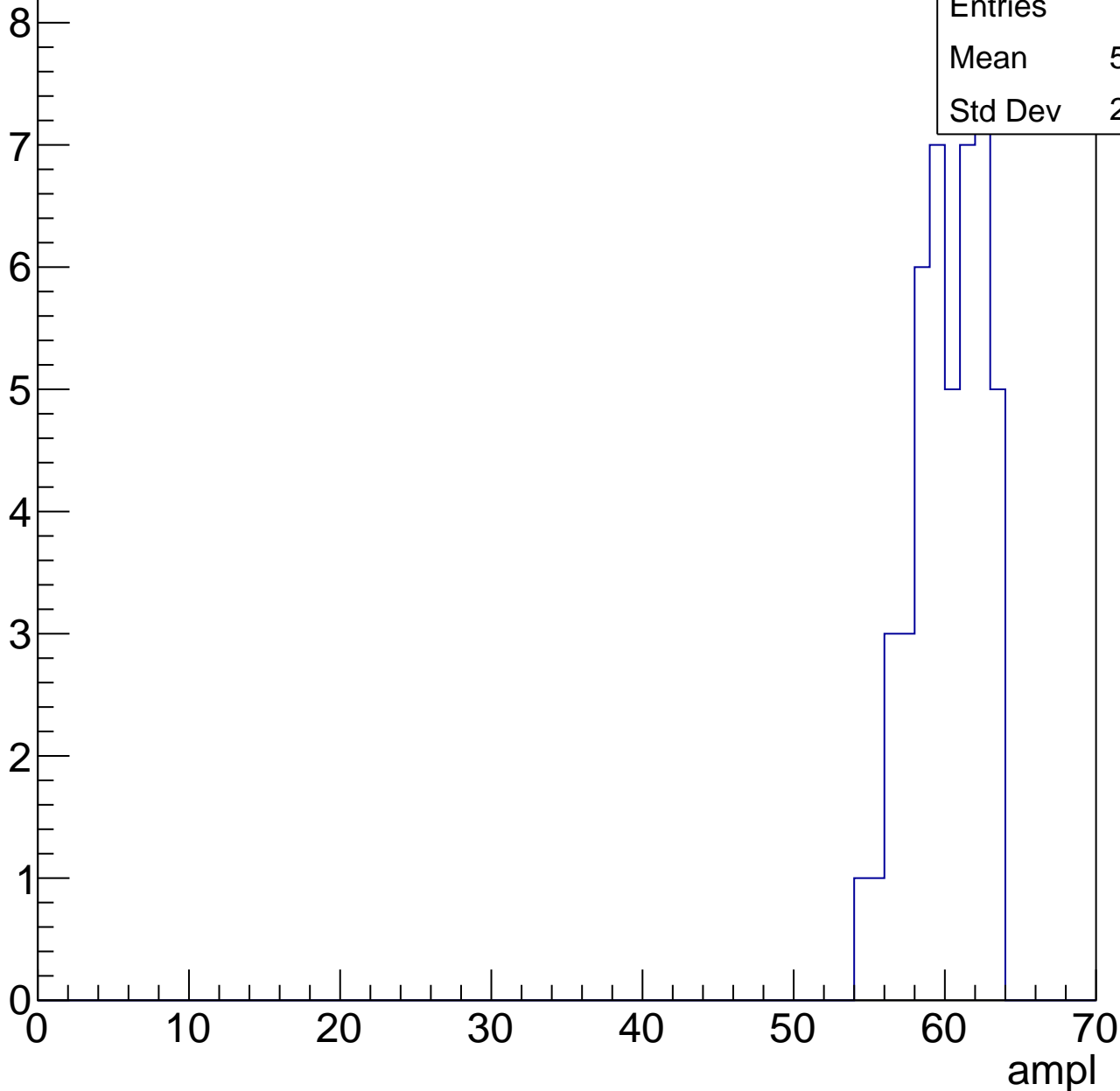


B1L103S, U24-ch95, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

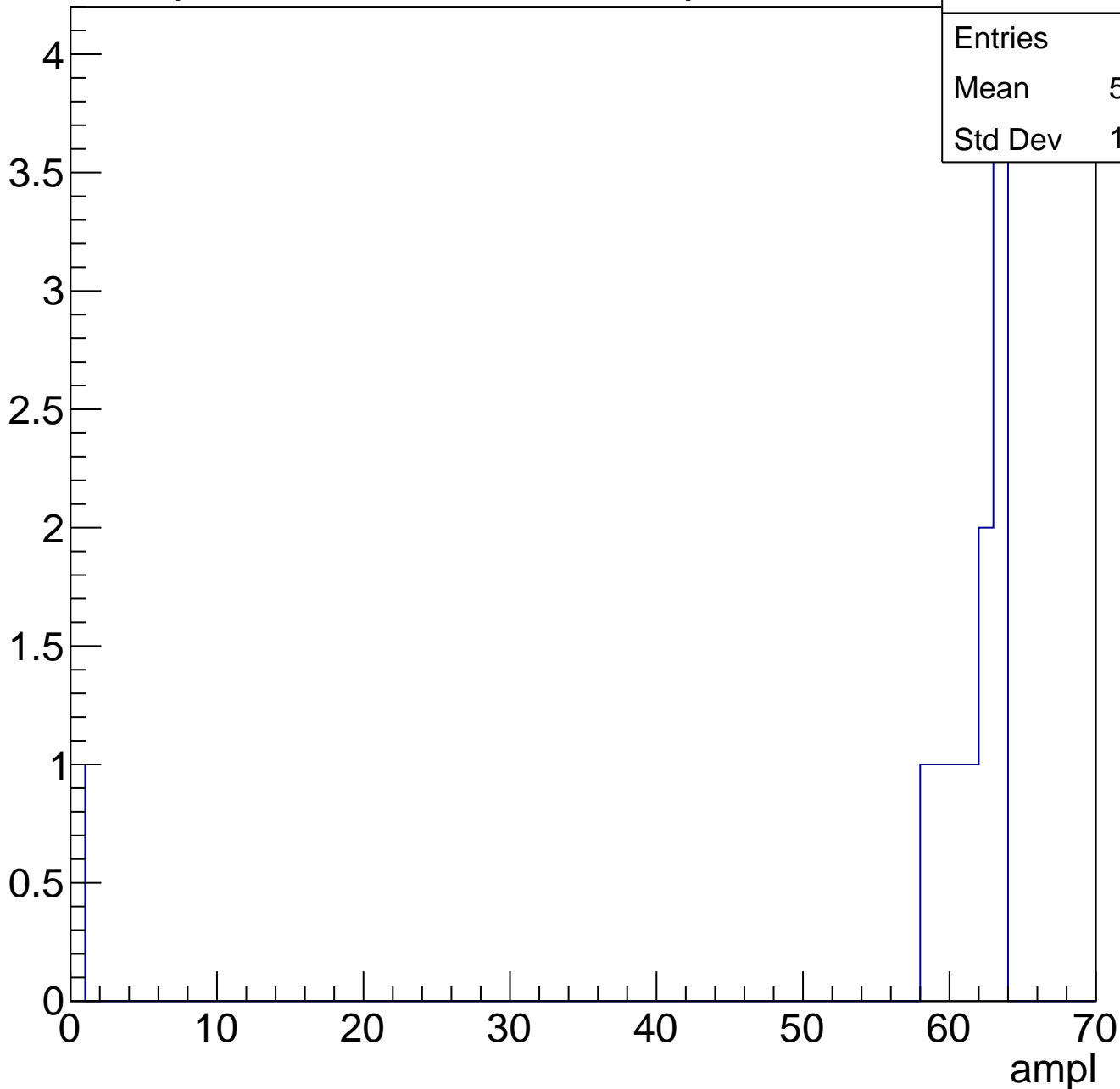
Entries	46
Mean	59.72
Std Dev	2.319



B1L103S, U24-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

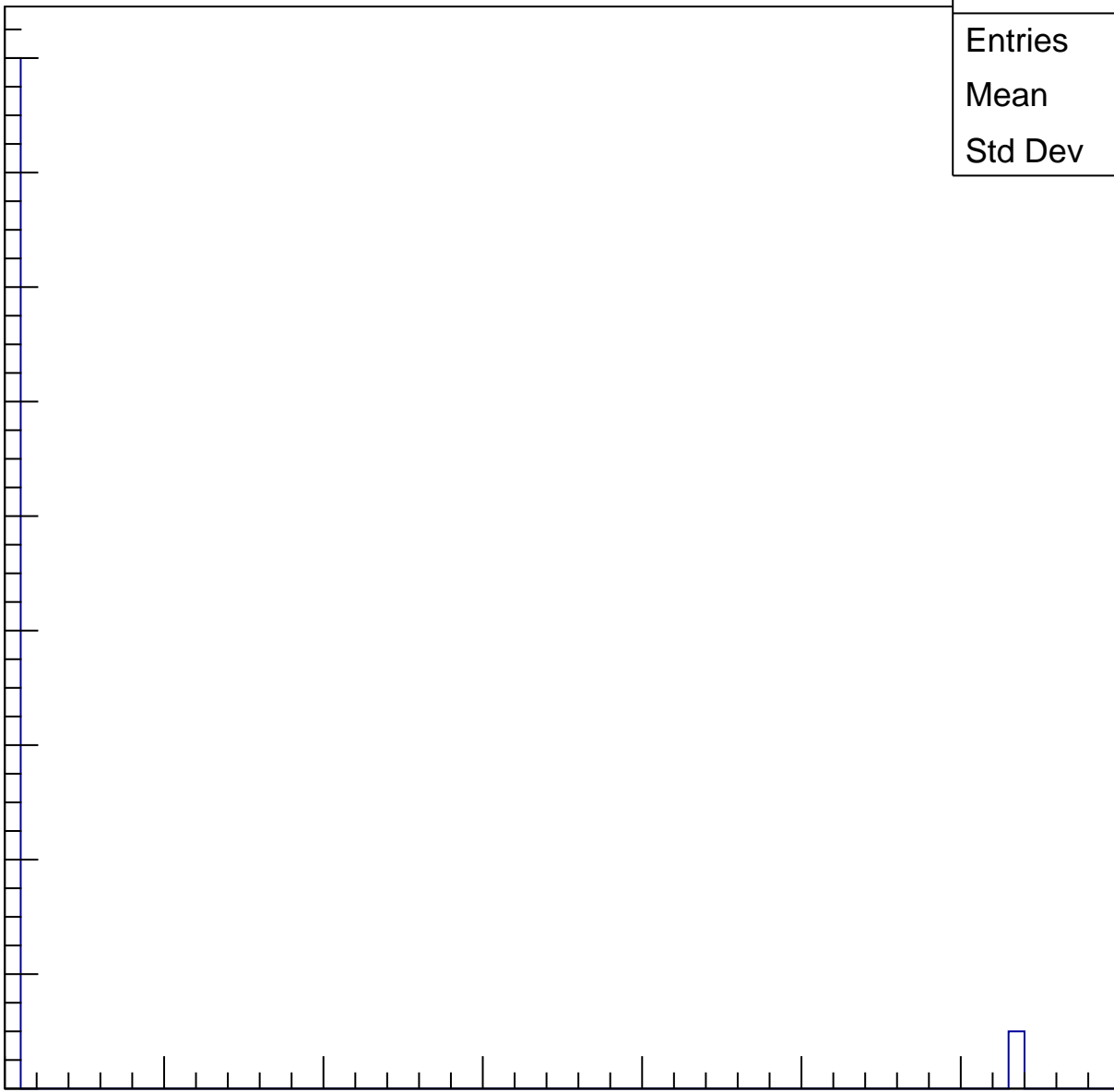
Entries	19
Mean	3.316
Std Dev	14.07

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch96, adc0

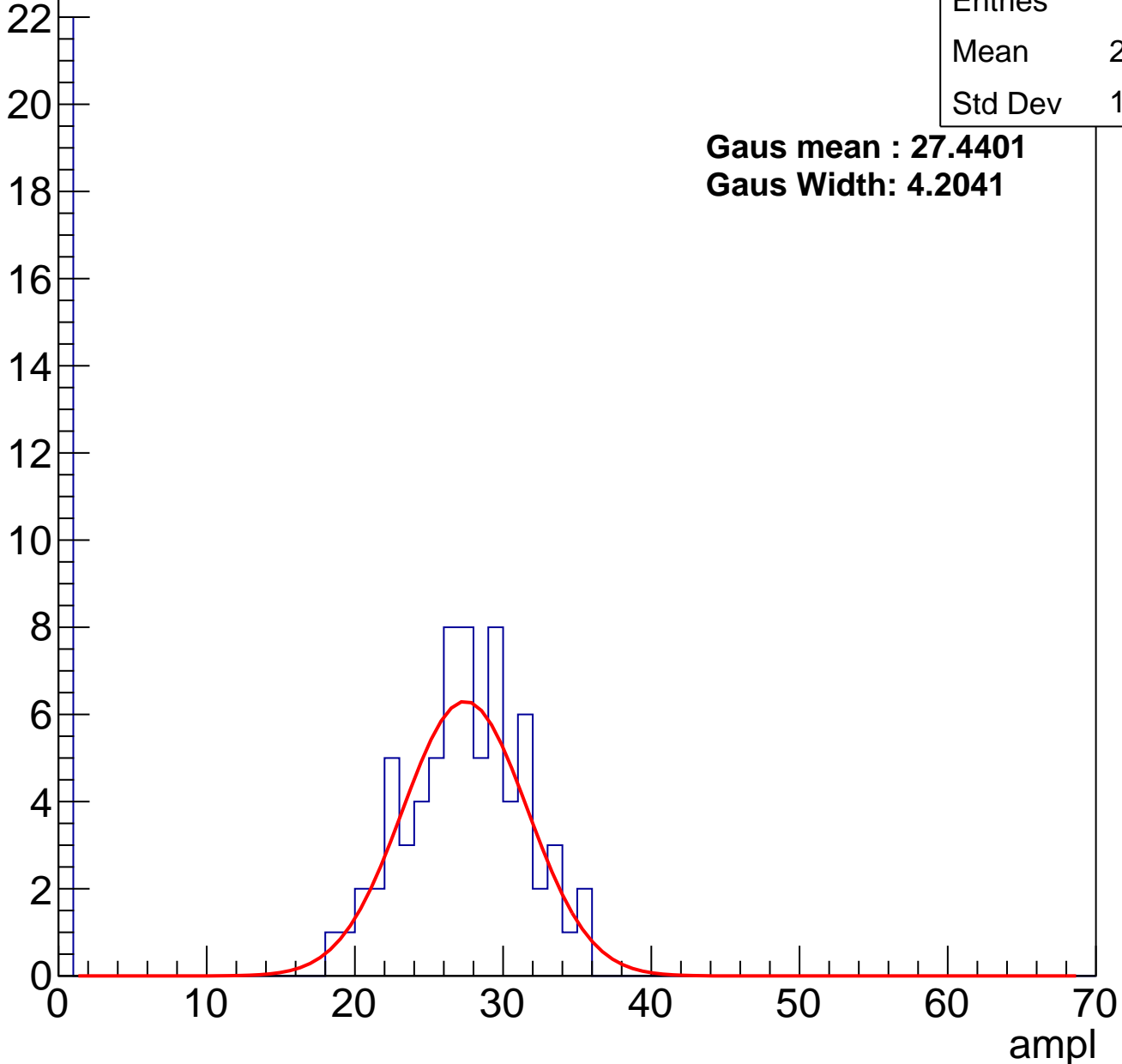
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	20.52
Std Dev	11.99

Gaus mean : 27.4401

Gaus Width: 4.2041

Entry



B1L103S, U24-ch96, adc1

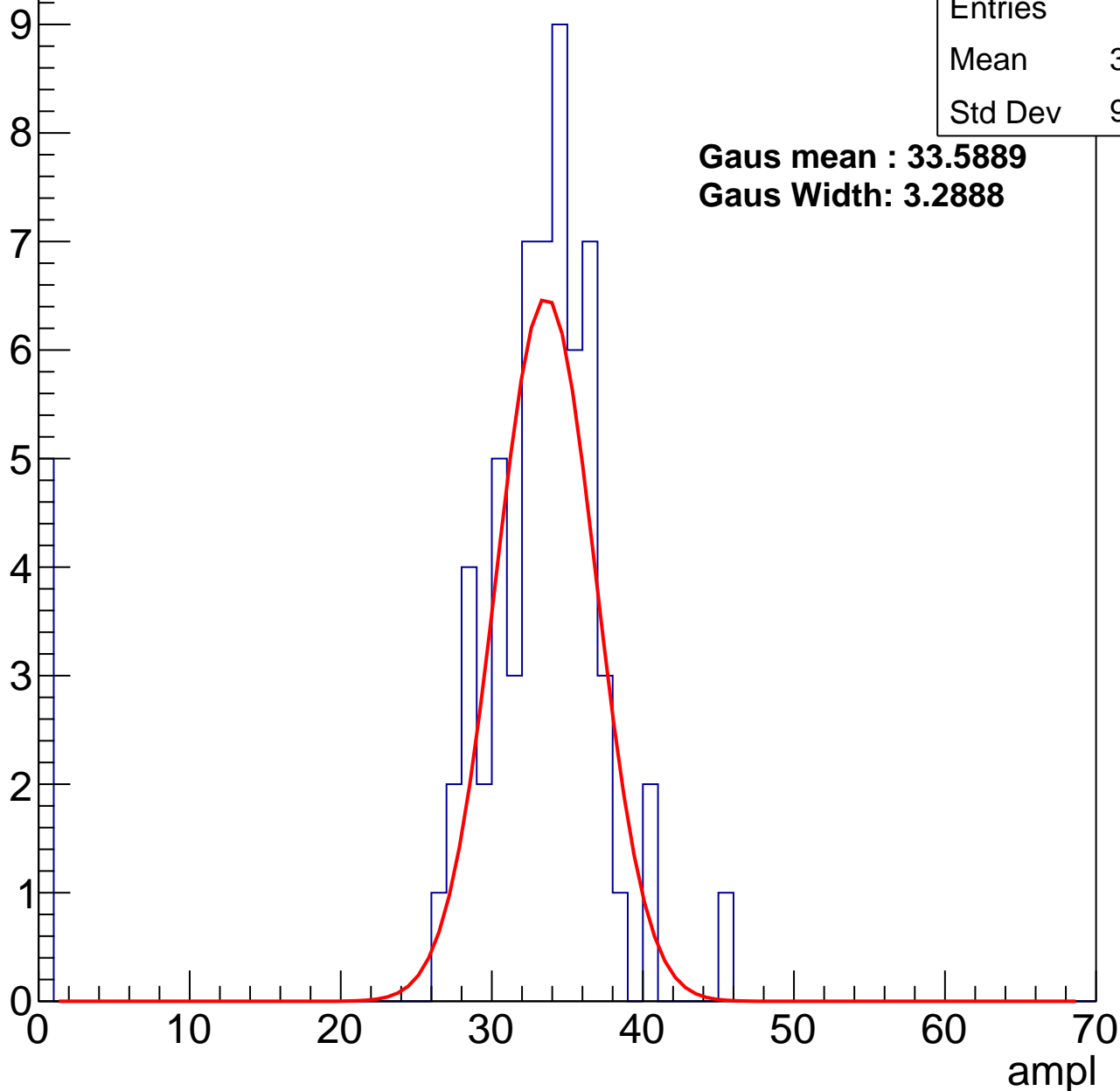
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	30.62
Std Dev	9.449

Gaus mean : 33.5889

Gaus Width: 3.2888



B1L103S, U24-ch96, adc2

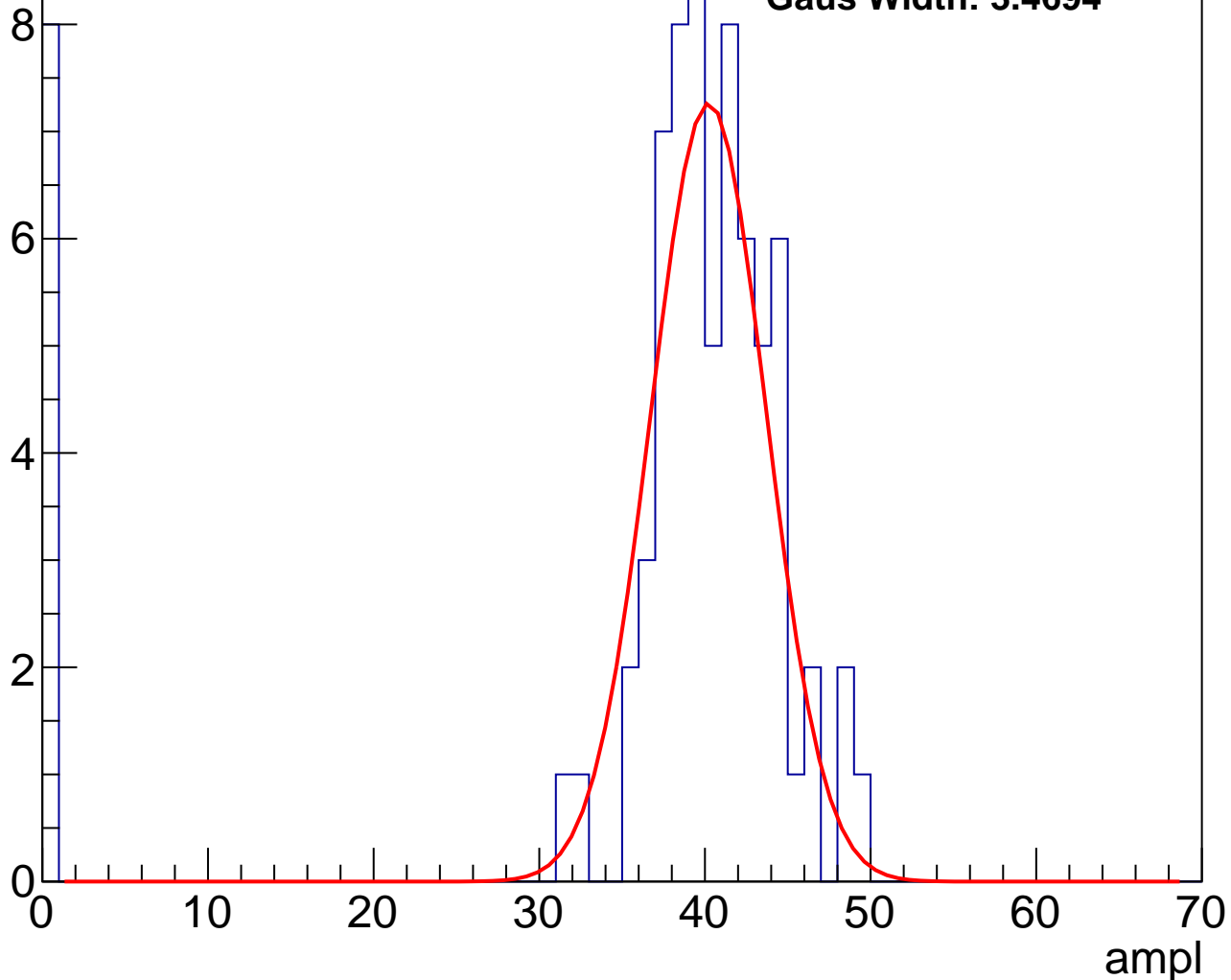
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	35.99
Std Dev	12.77

Gaus mean : 40.2250

Gaus Width: 3.4694

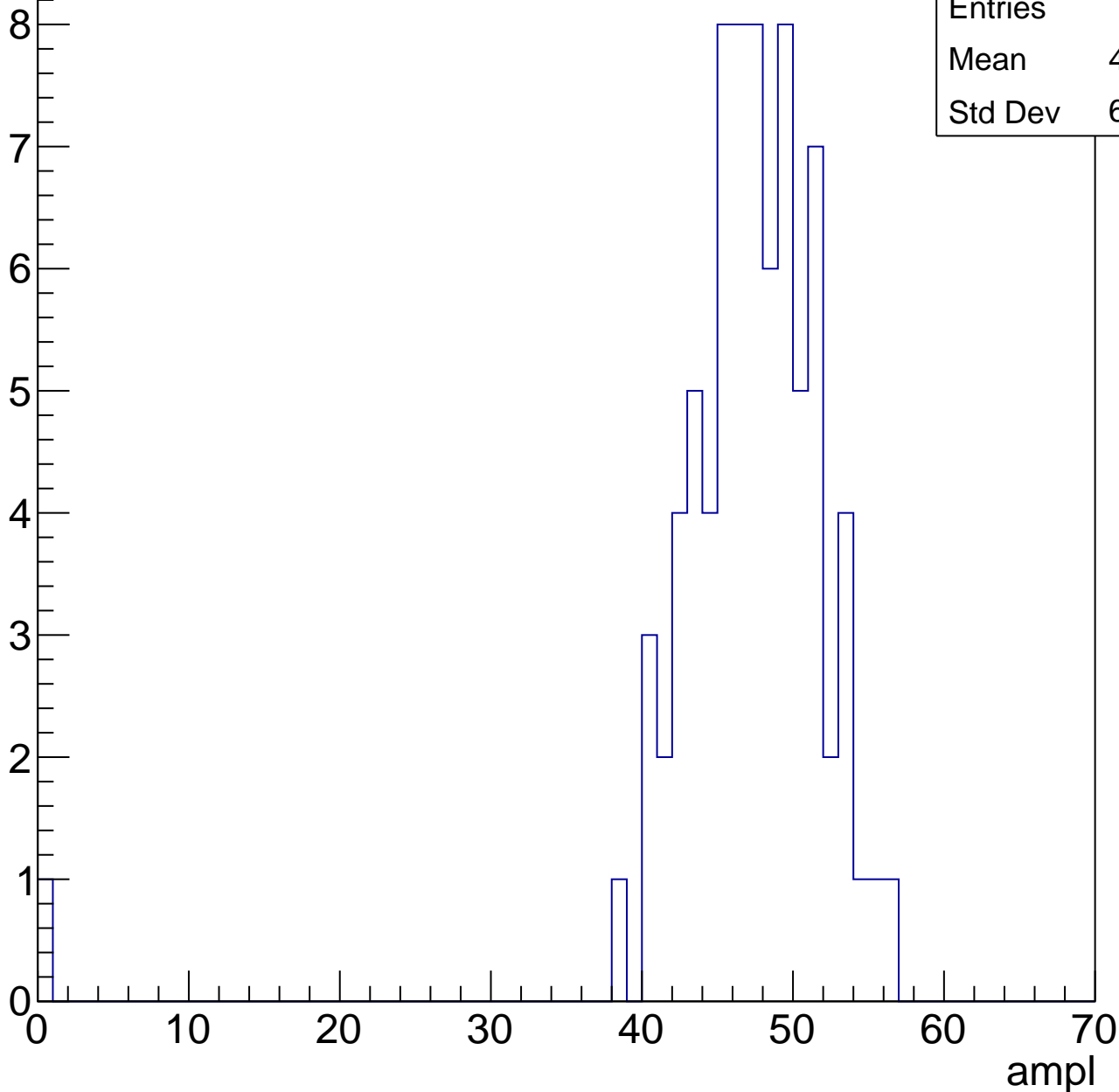


B1L103S, U24-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	46.47
Std Dev	6.488

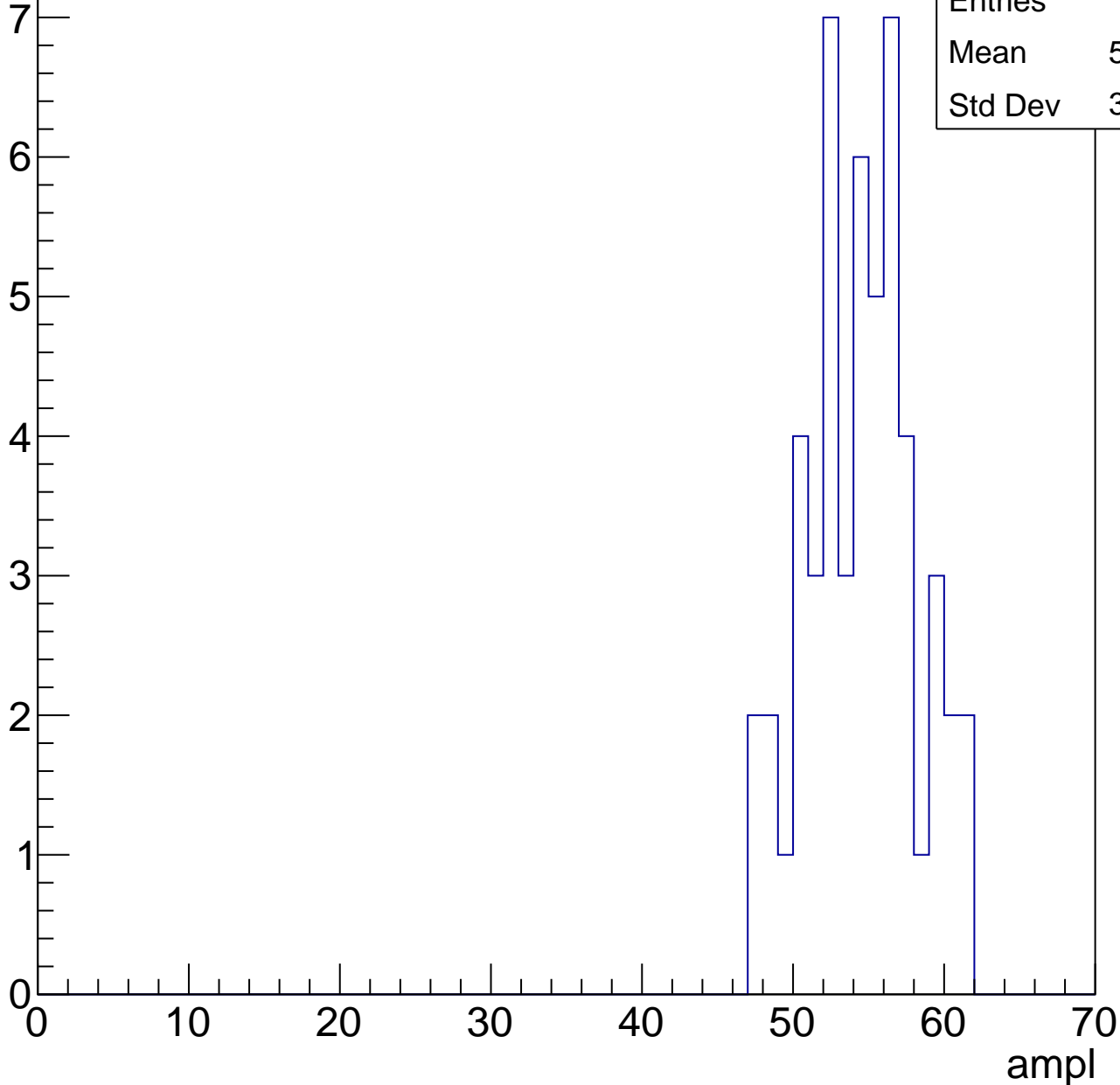


B1L103S, U24-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	54.06
Std Dev	3.527

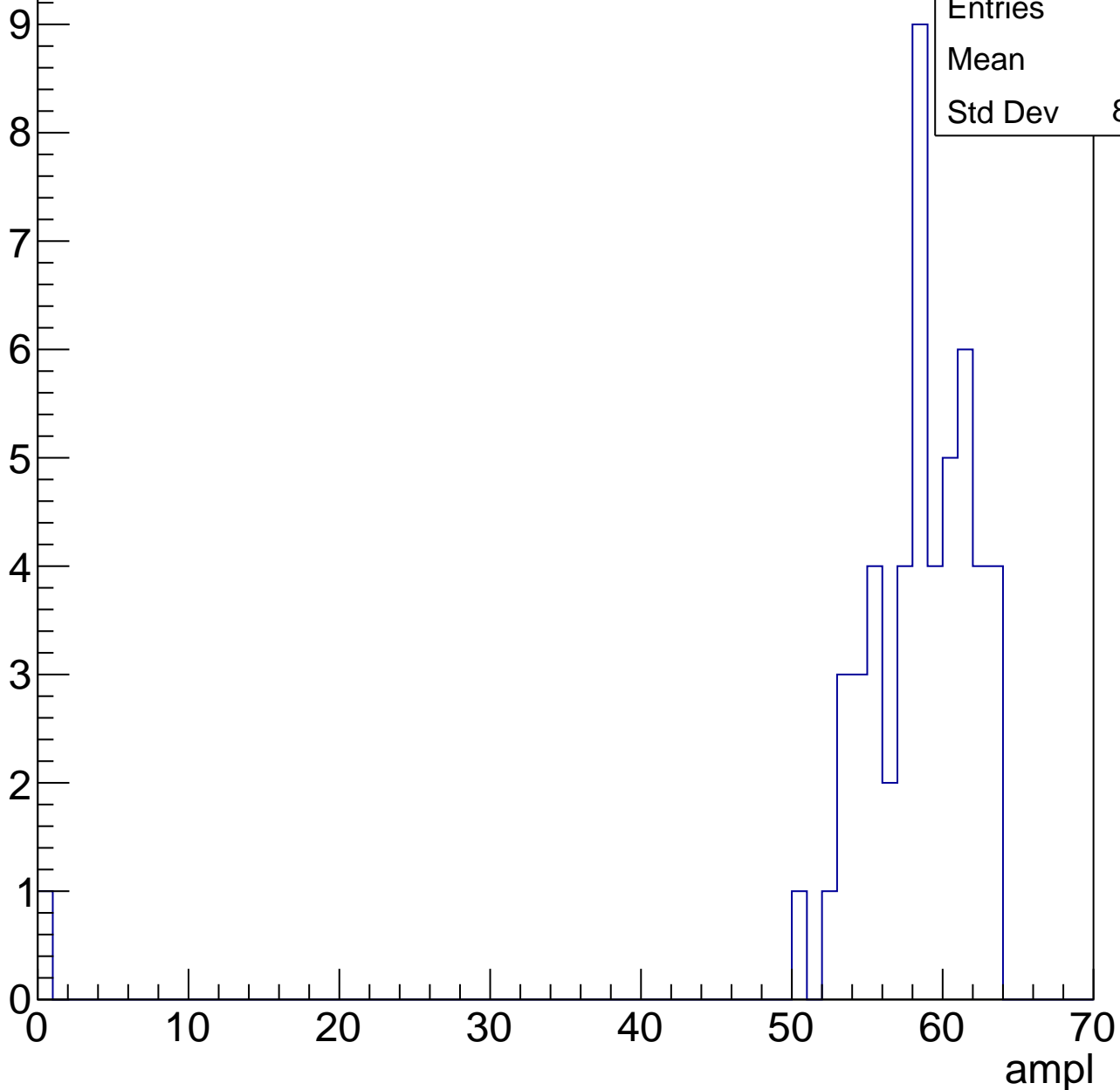


B1L103S, U24-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57
Std Dev	8.661

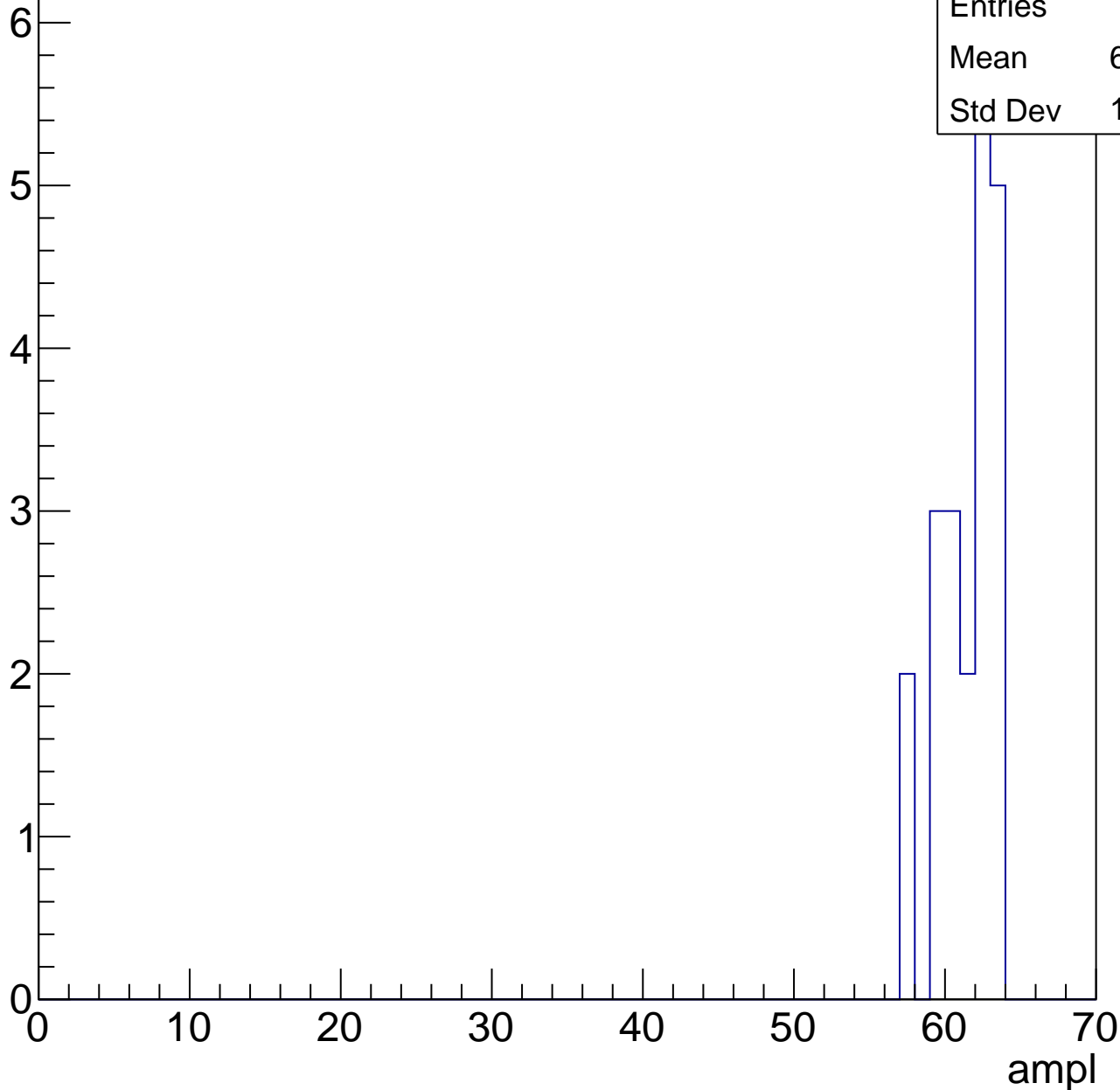


B1L103S, U24-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	60.95
Std Dev	1.864



B1L103S, U24-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



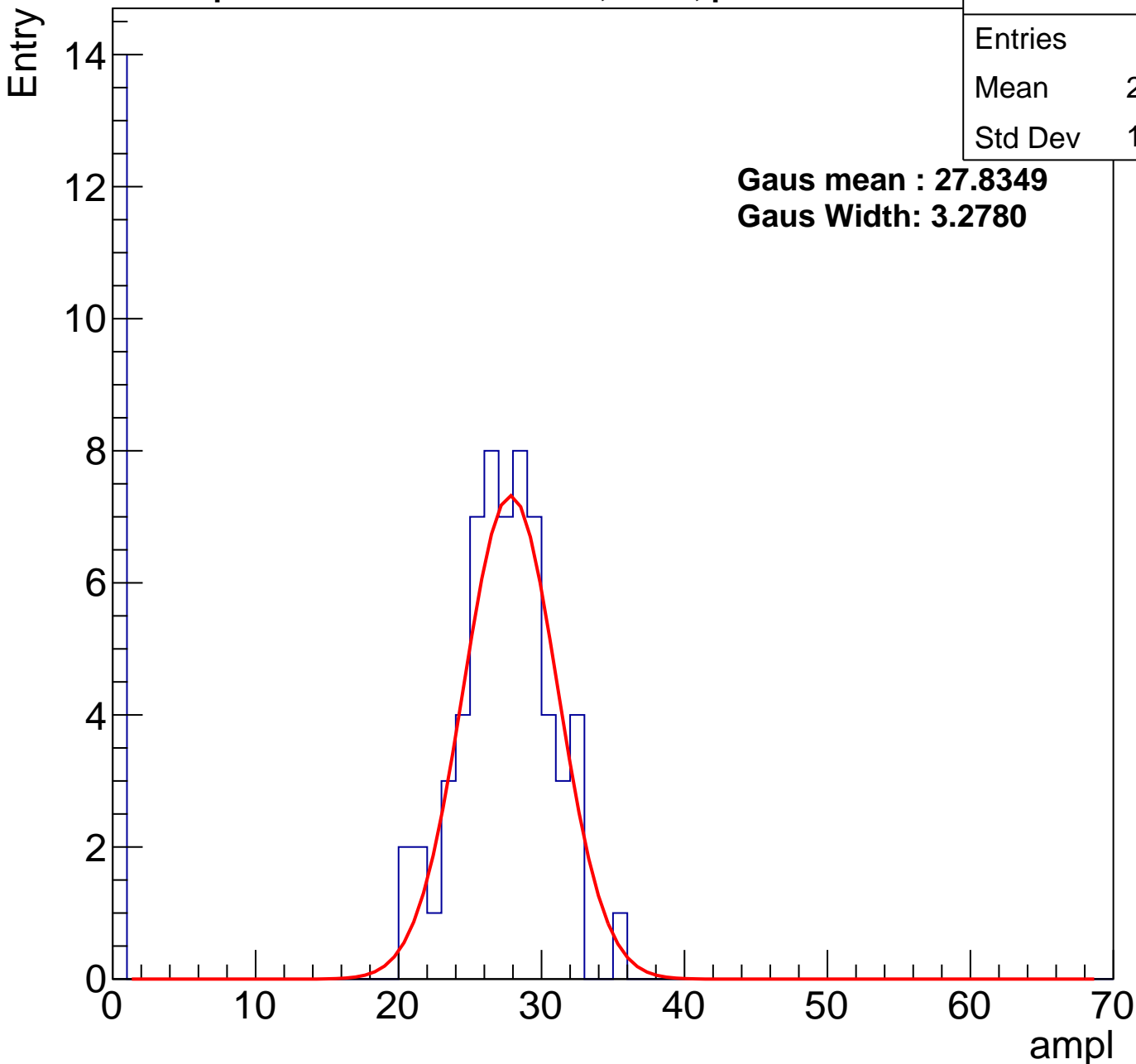
B1L103S, U24-ch97, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	21.92
Std Dev	10.87

Gaus mean : 27.8349

Gaus Width: 3.2780



B1L103S, U24-ch97, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	34.85
Std Dev	3.571

Gaus mean : 35.7275

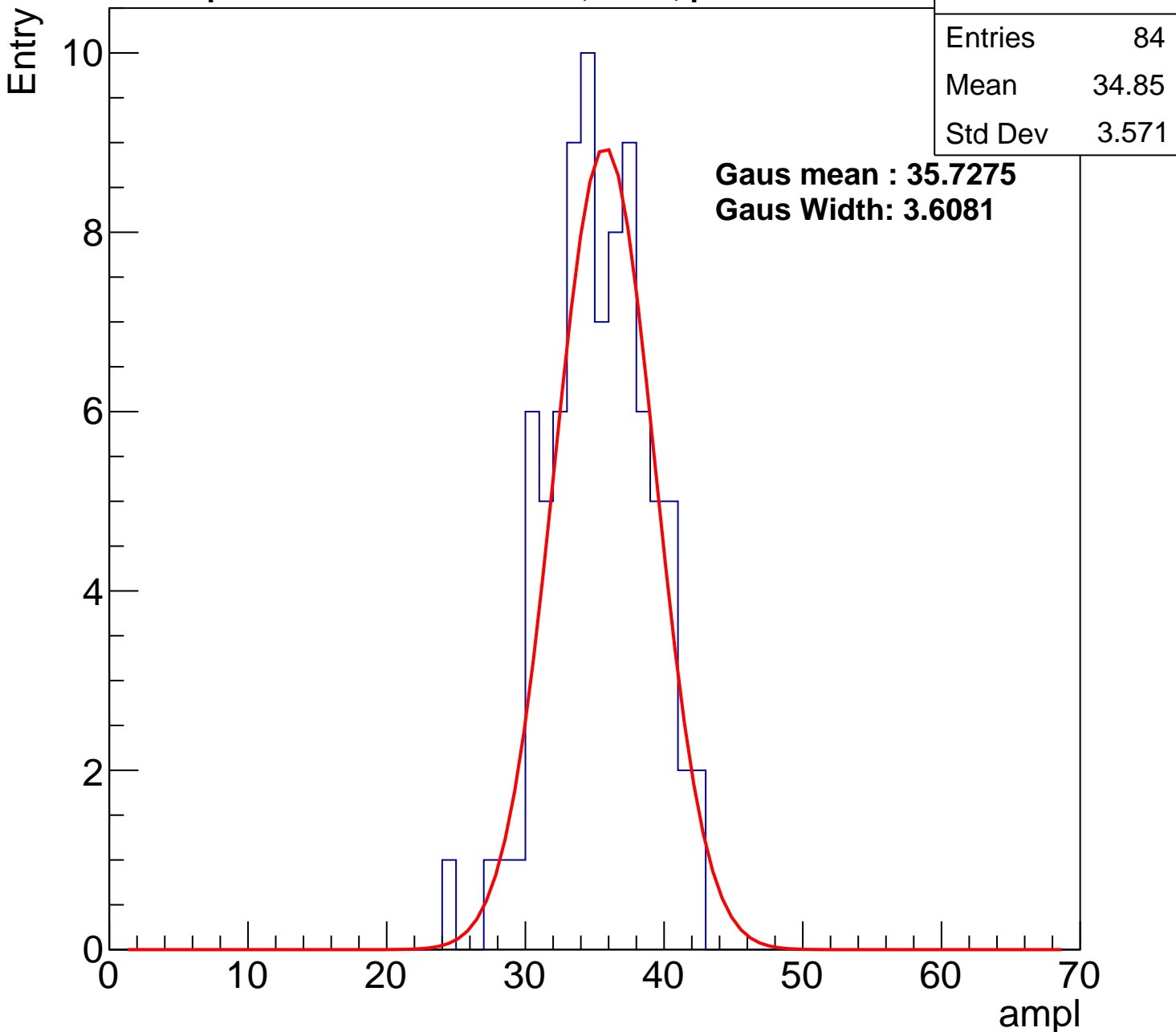
Gaus Width: 3.6081

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch97, adc2

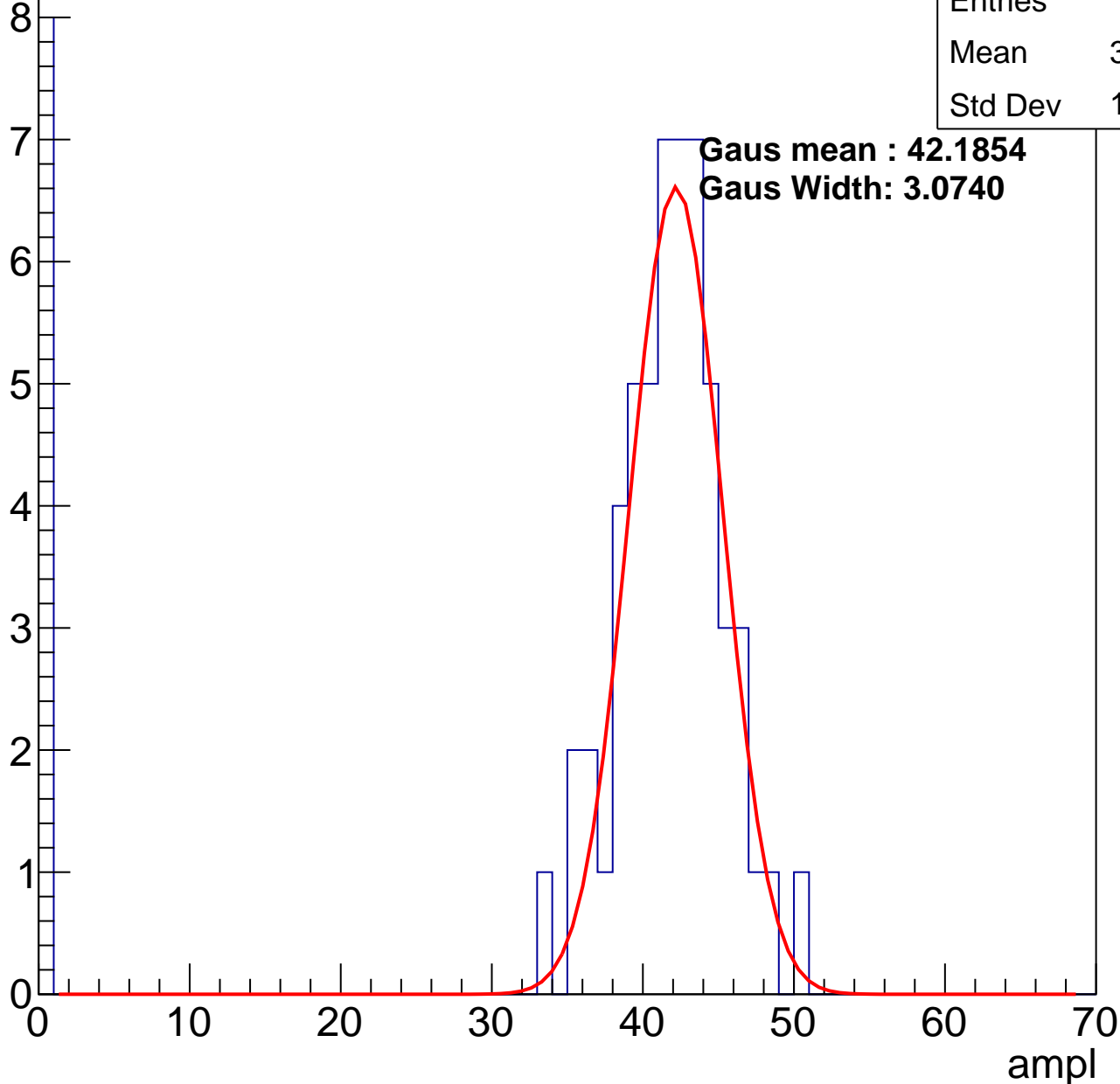
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.17
Std Dev	14.15

Gaus mean : 42.1854

Gaus Width: 3.0740

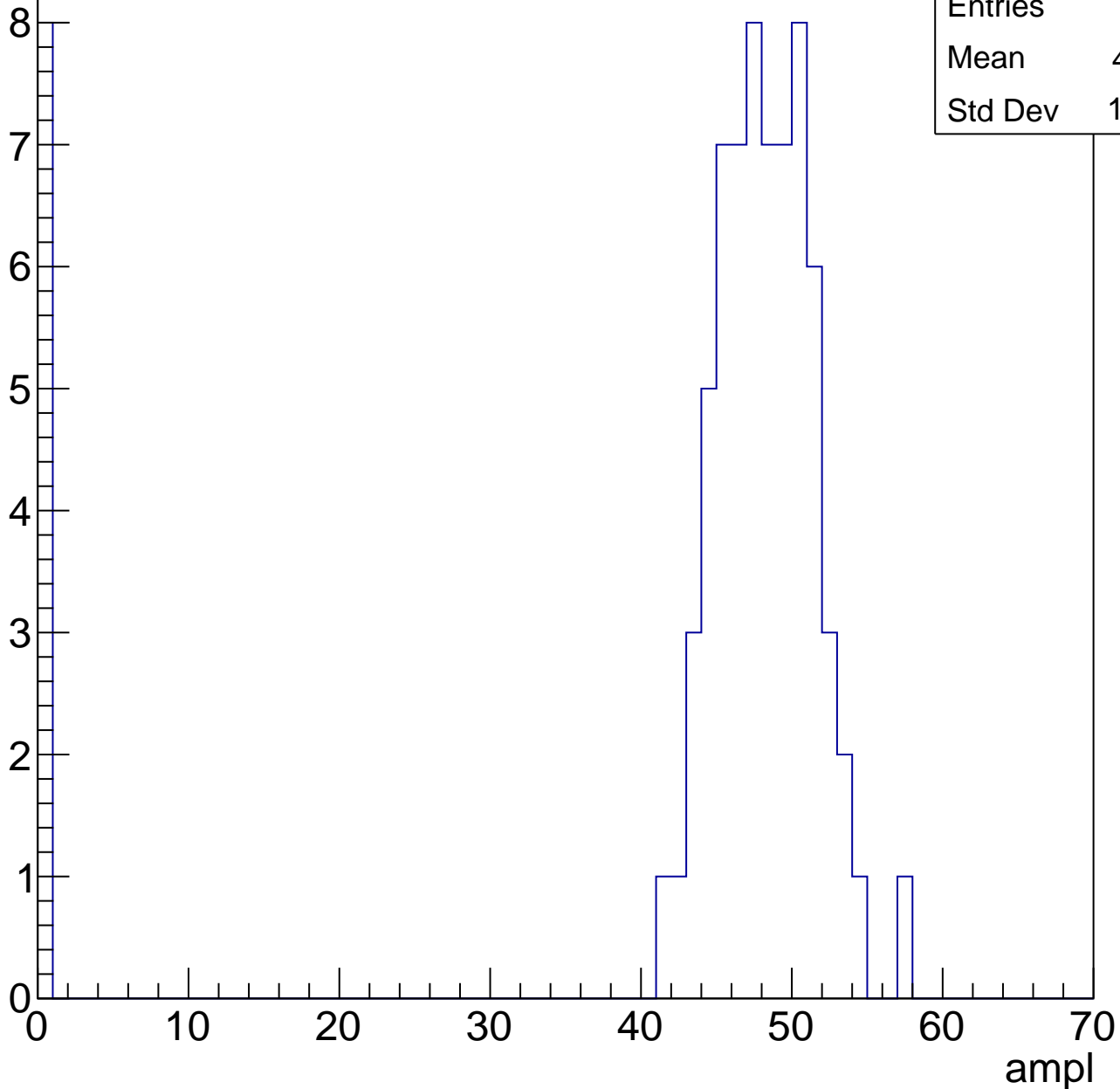


B1L103S, U24-ch97, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	42.71
Std Dev	15.05

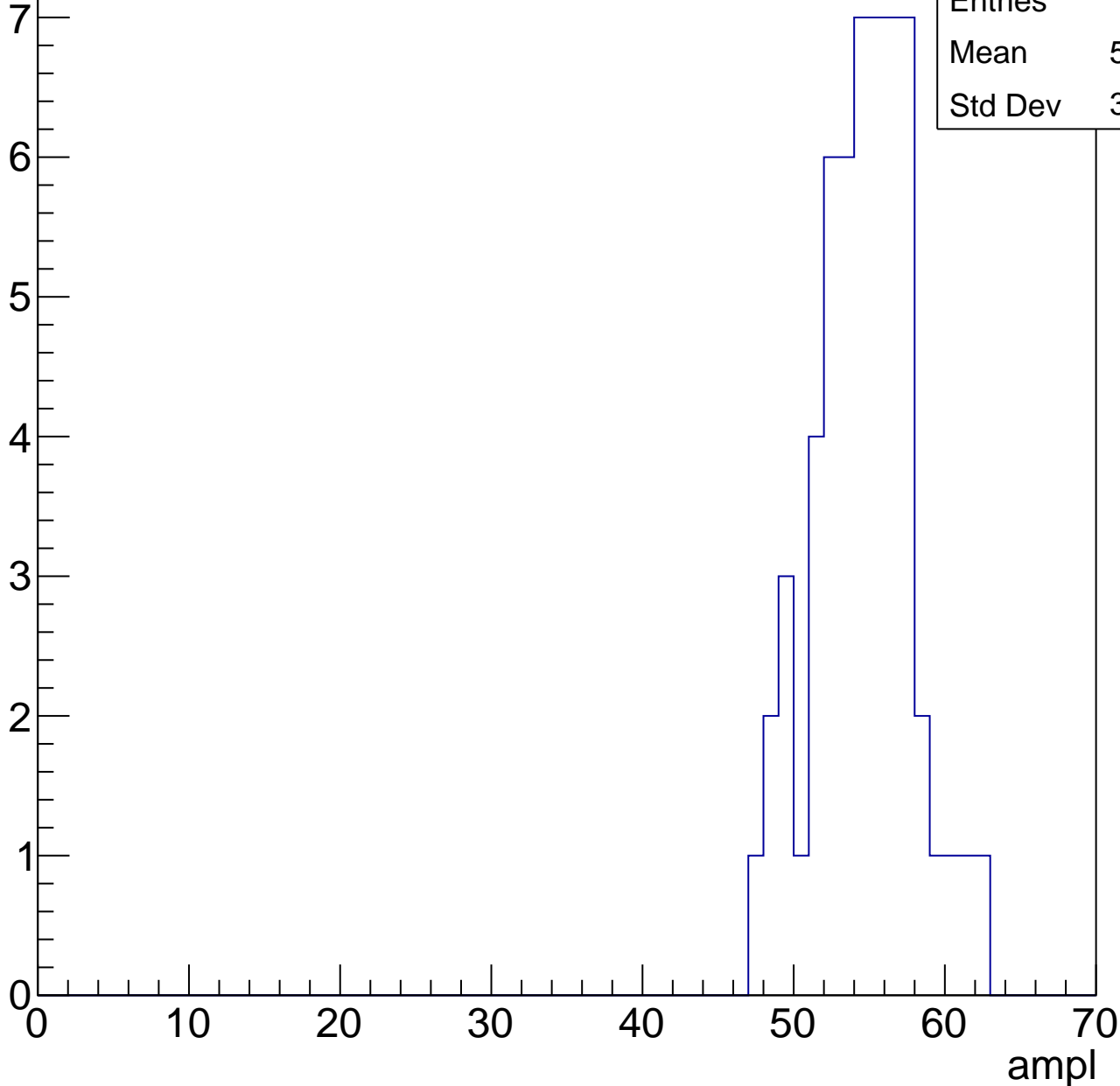


B1L103S, U24-ch97, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

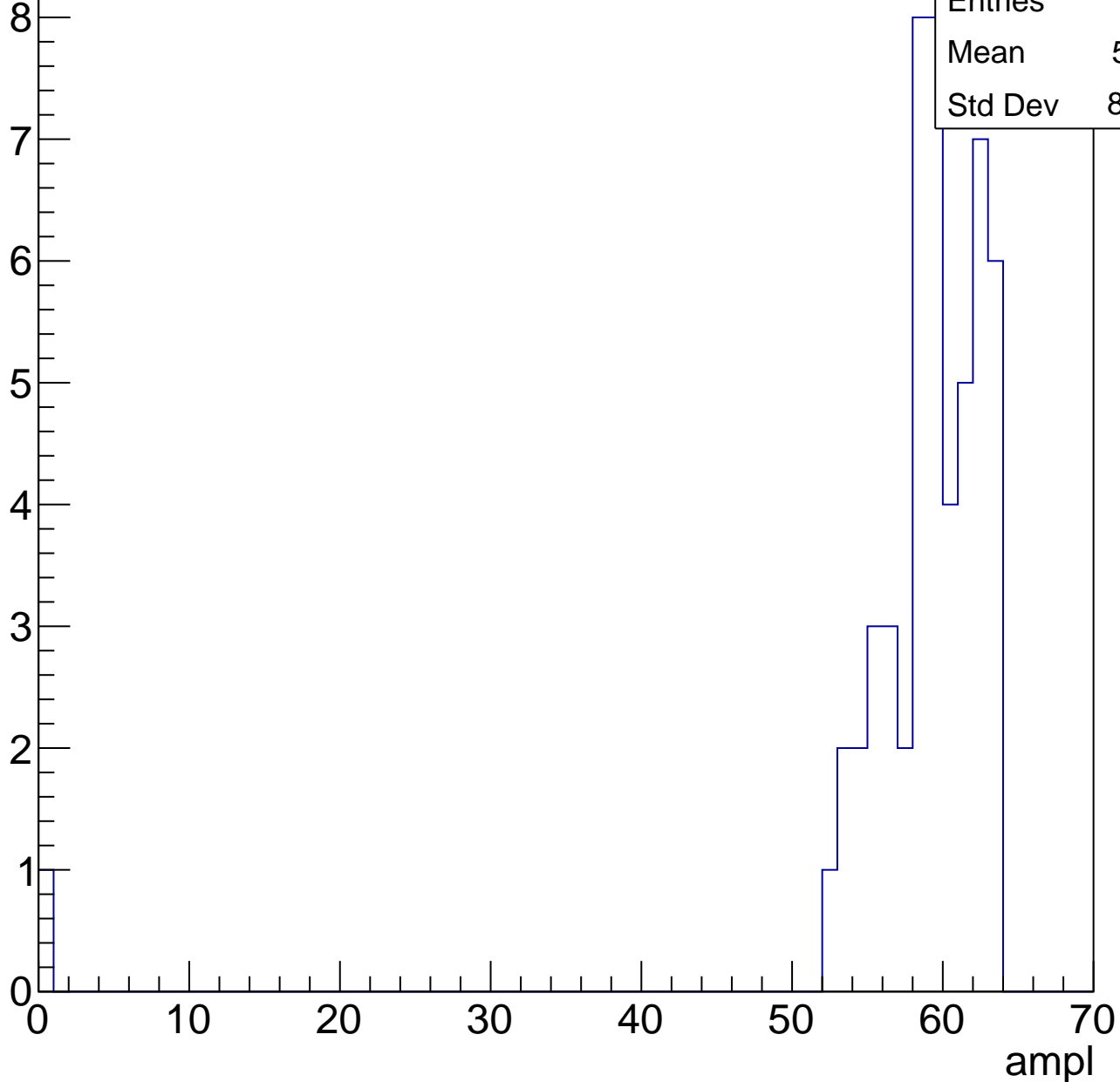
Entries	57
Mean	54.14
Std Dev	3.192



B1L103S, U24-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

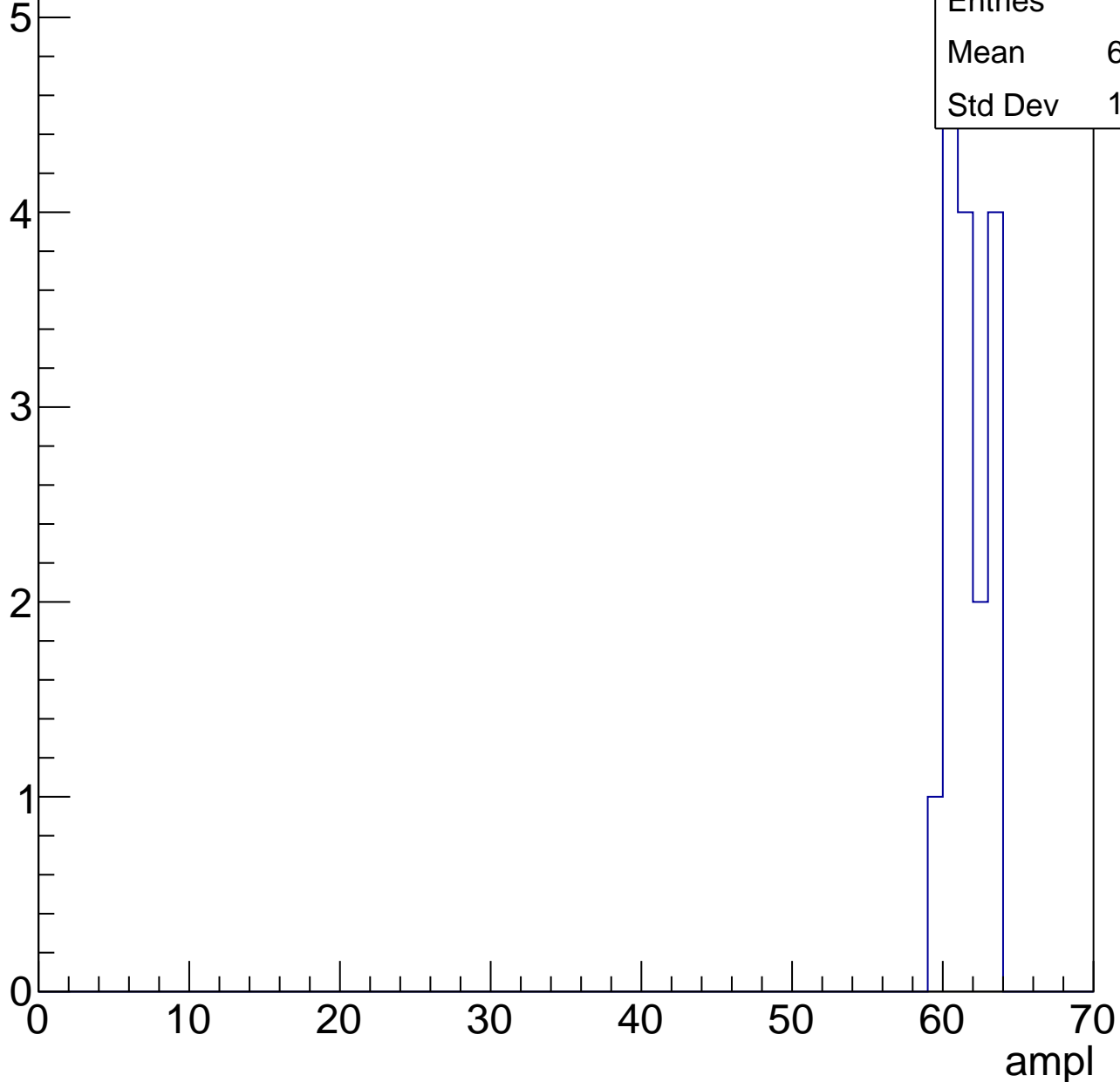


Entries	52
Mean	57.81
Std Dev	8.607

B1L103S, U24-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch98, adc0

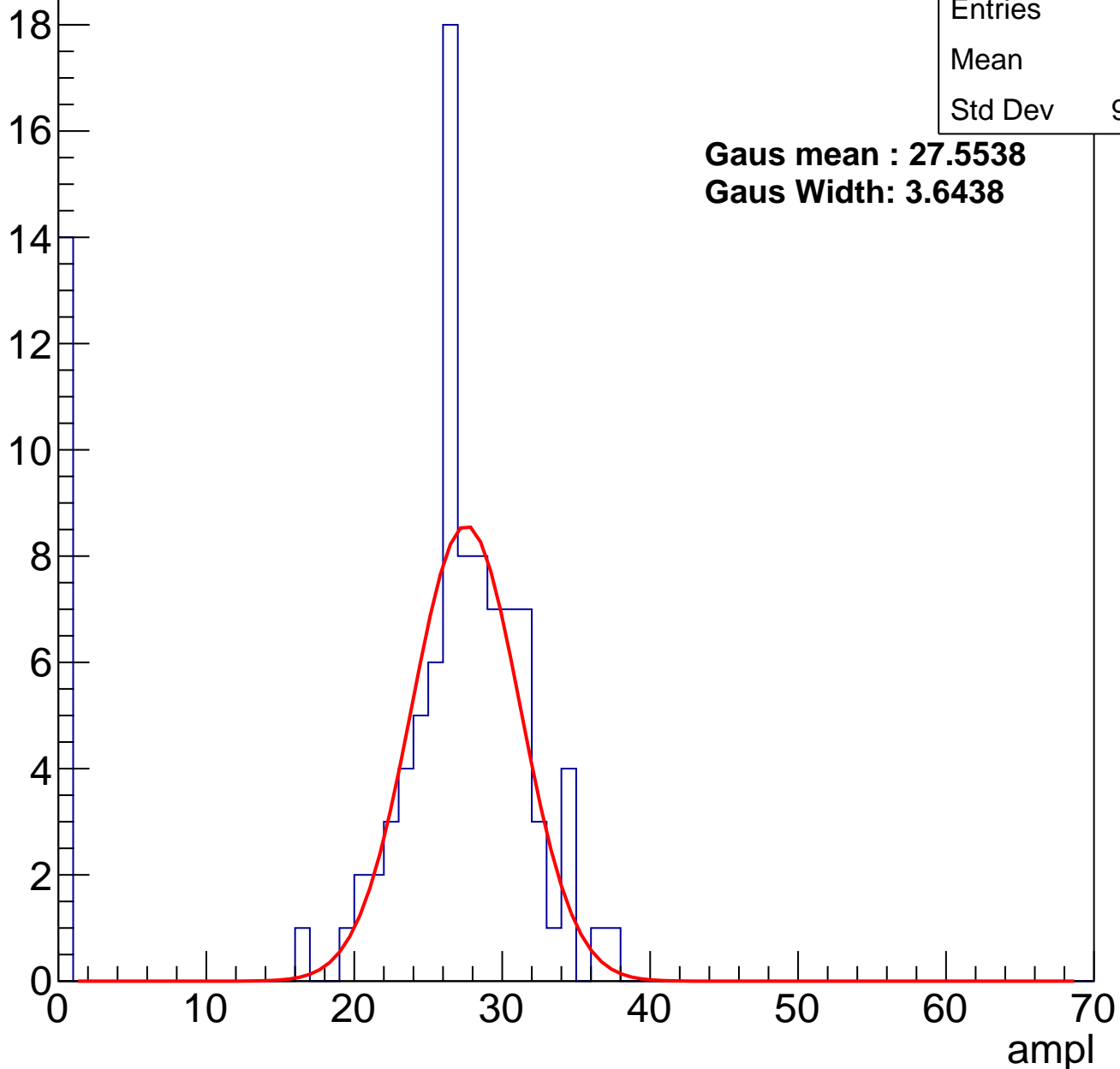
calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	23.5
Std Dev	9.965

Gaus mean : 27.5538

Gaus Width: 3.6438

Entry



B1L103S, U24-ch98, adc1

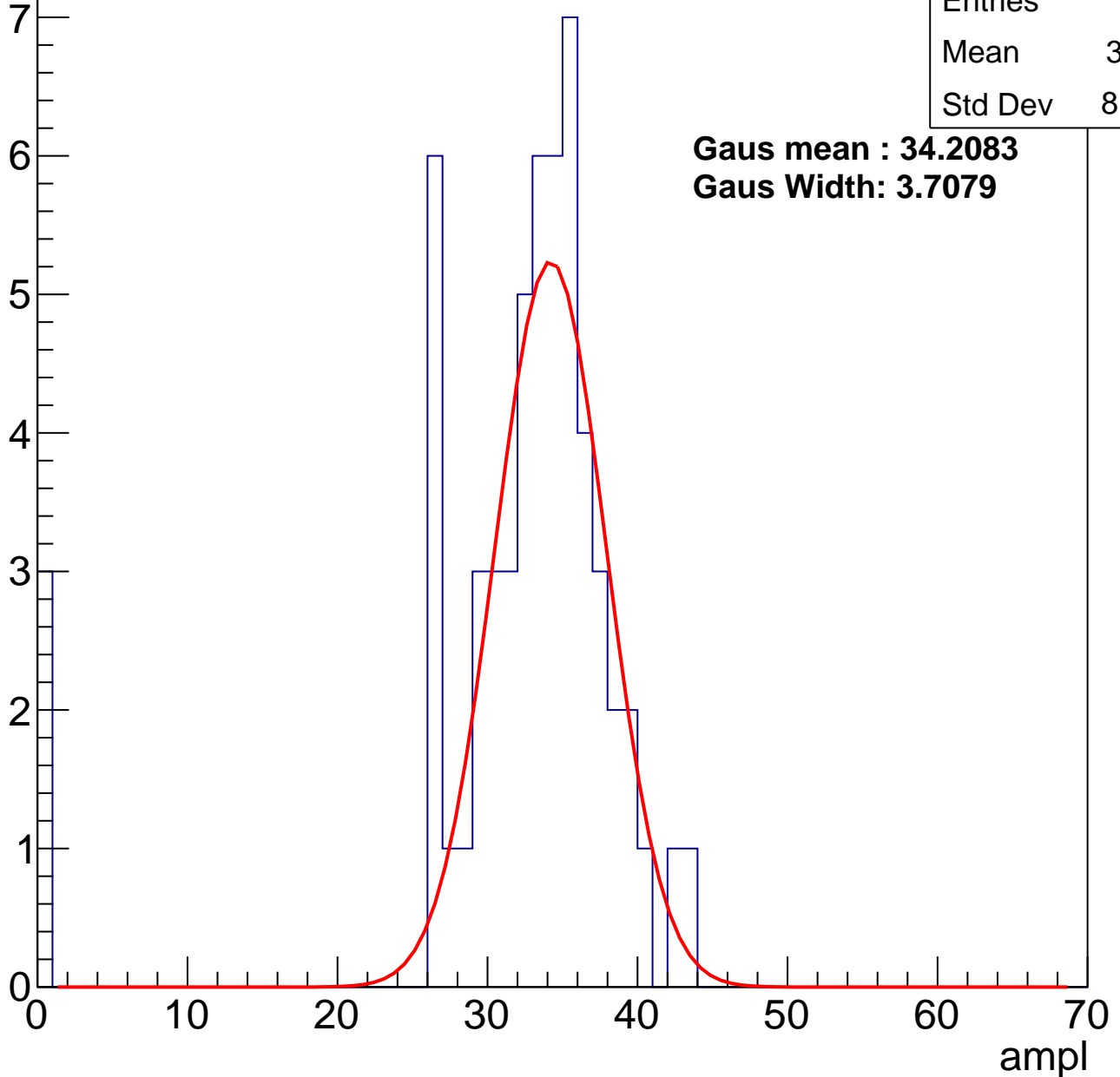
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	31.41
Std Dev	8.344

Gaus mean : 34.2083

Gaus Width: 3.7079



B1L103S, U24-ch98, adc2

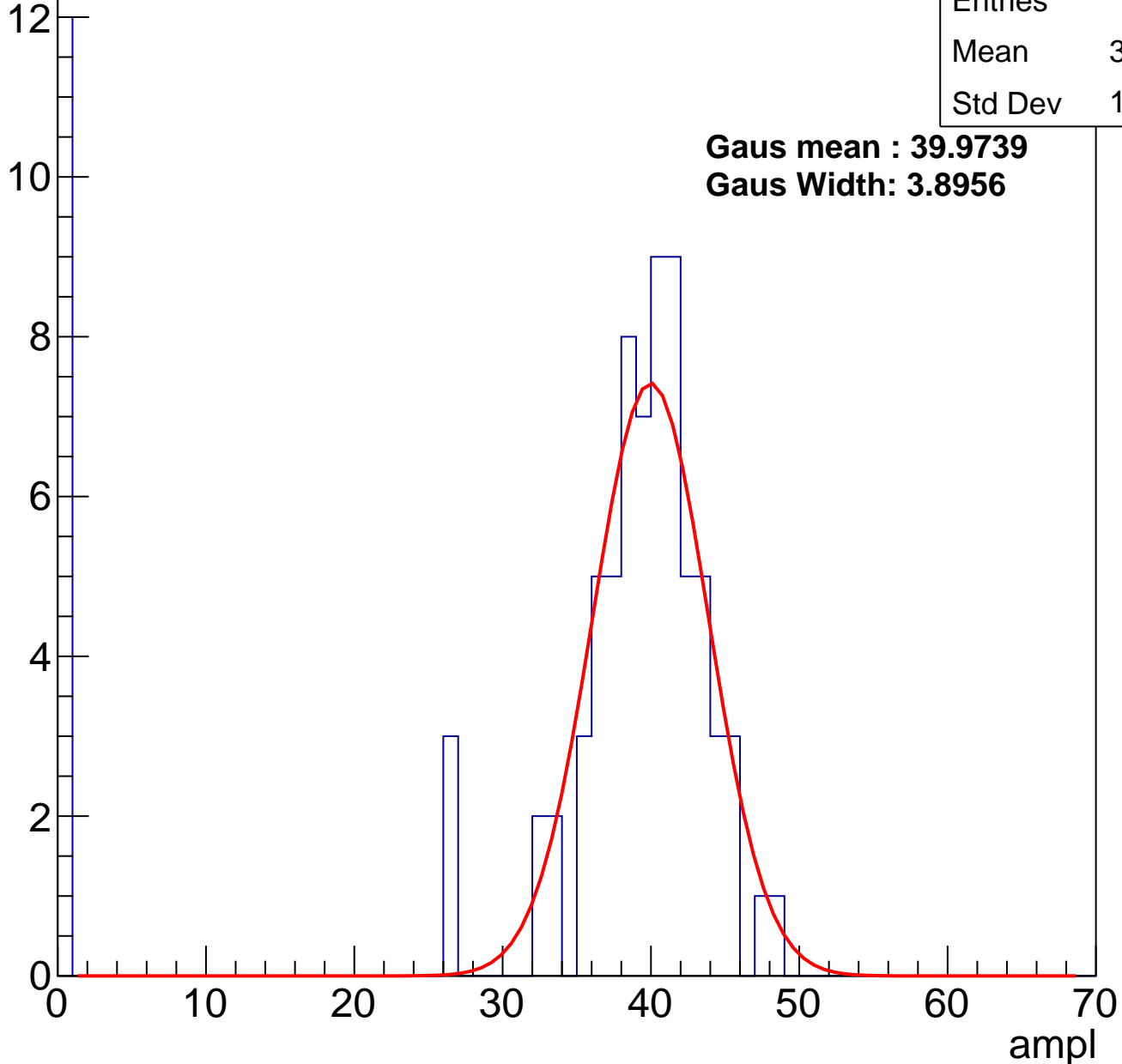
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	33.39
Std Dev	14.28

Gaus mean : 39.9739

Gaus Width: 3.8956

Entry



B1L103S, U24-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	47.03
Std Dev	3.636

Entry

10

8

6

4

2

0

0

10

20

30

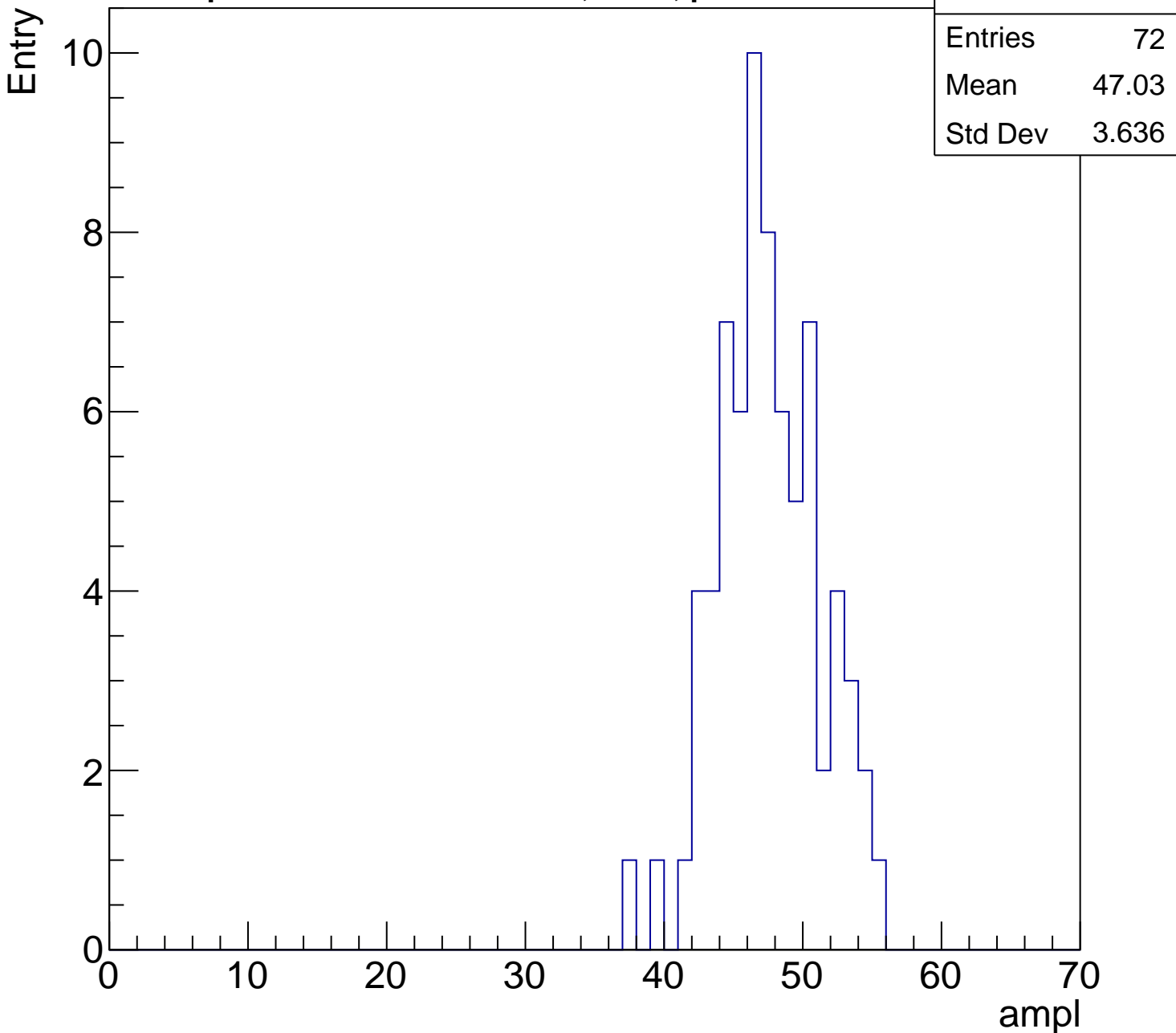
40

50

60

70

ampl

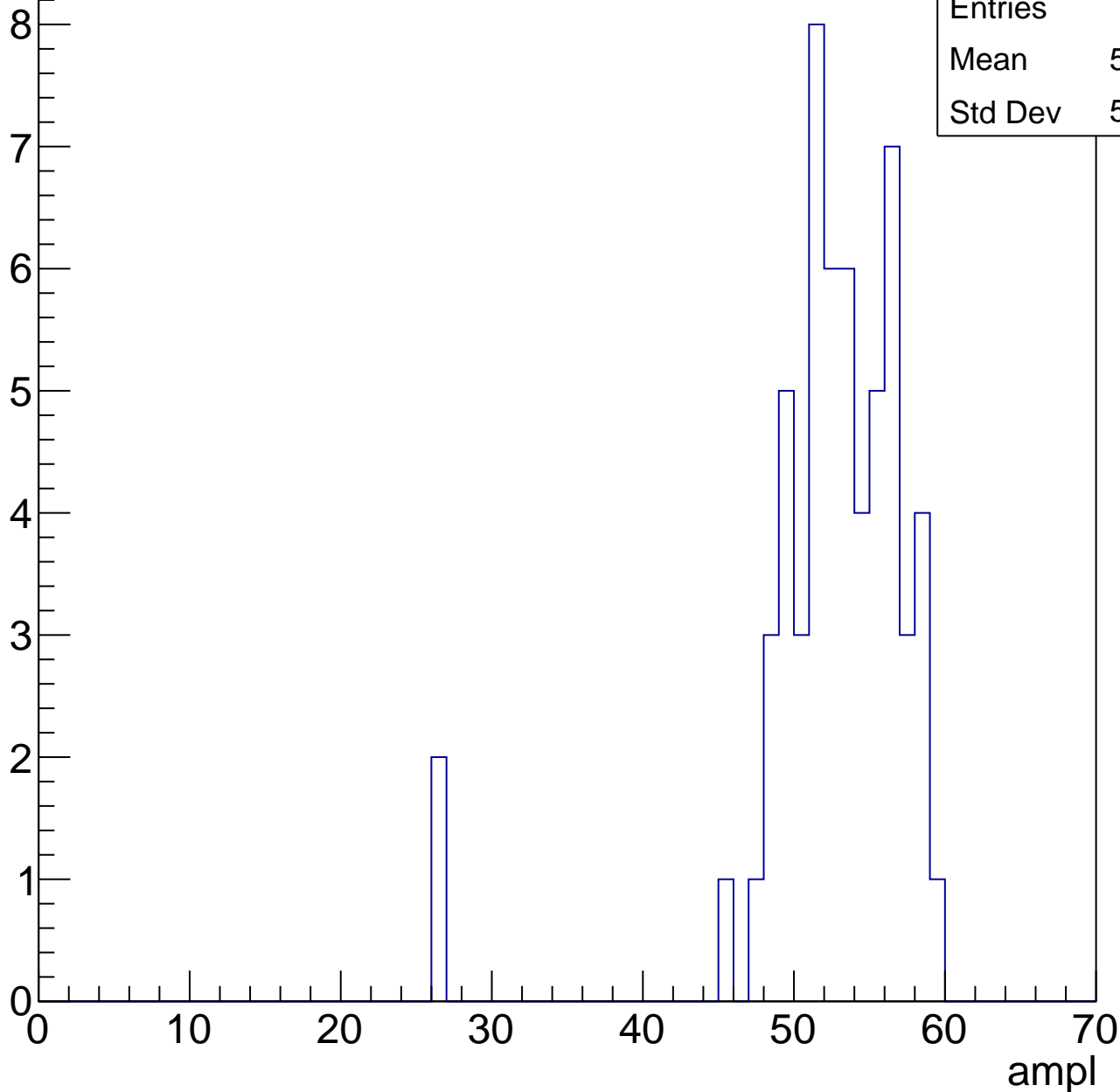


B1L103S, U24-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	51.97
Std Dev	5.802

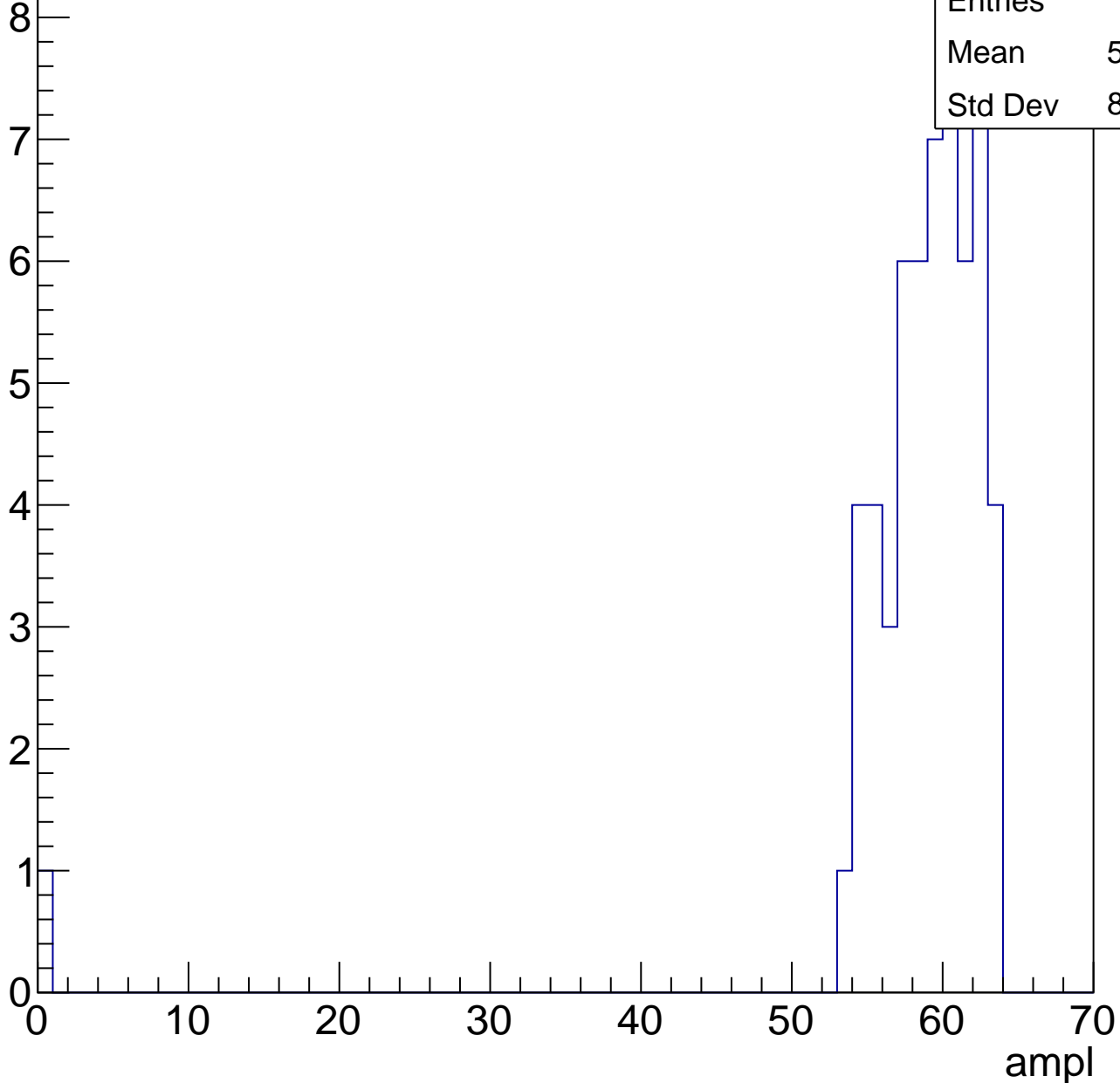


B1L103S, U24-ch98, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.83
Std Dev	8.122

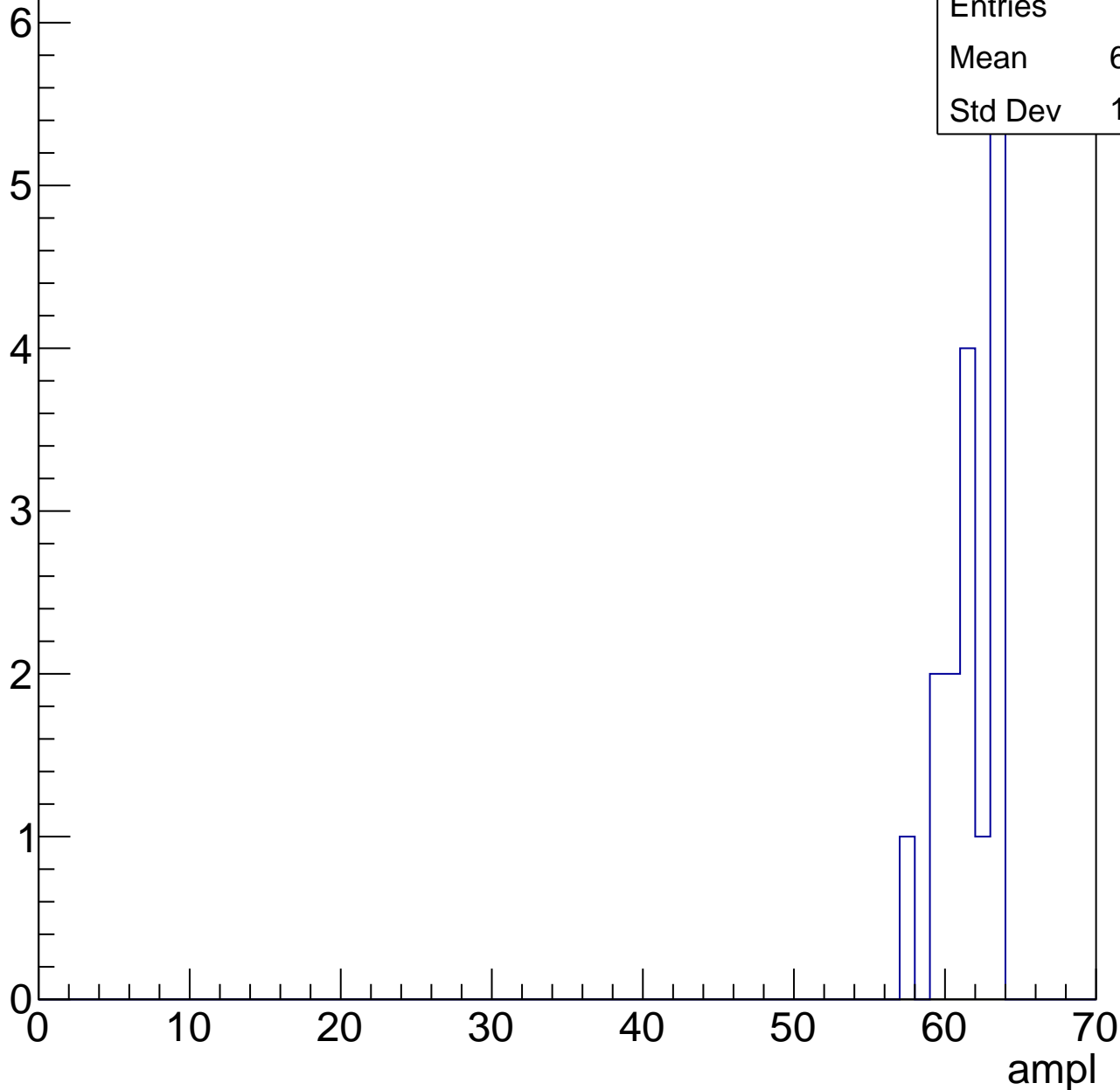


B1L103S, U24-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.19
Std Dev	1.775

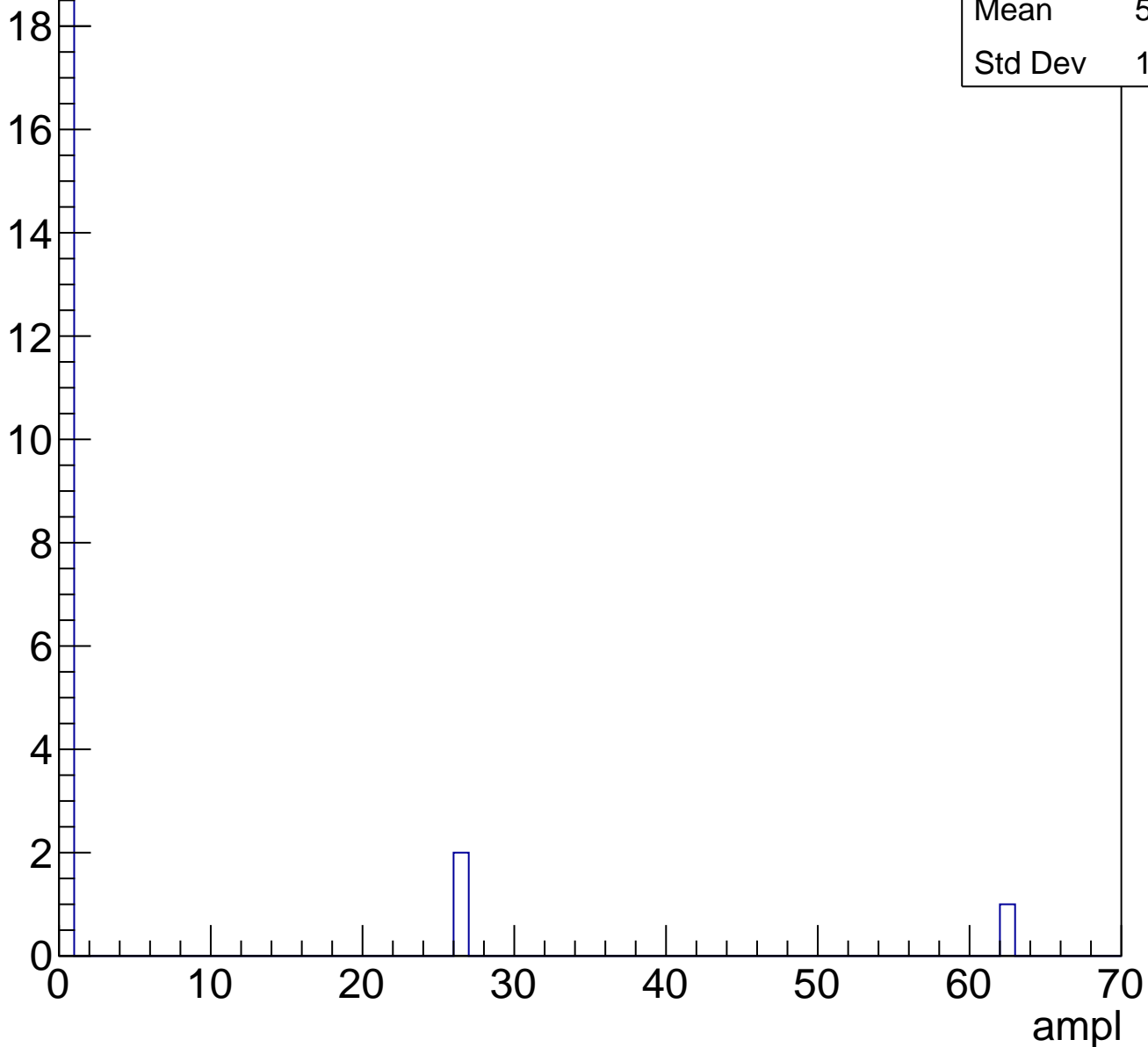


B1L103S, U24-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	5.182
Std Dev	14.47

Entry



B1L103S, U24-ch99, adc0

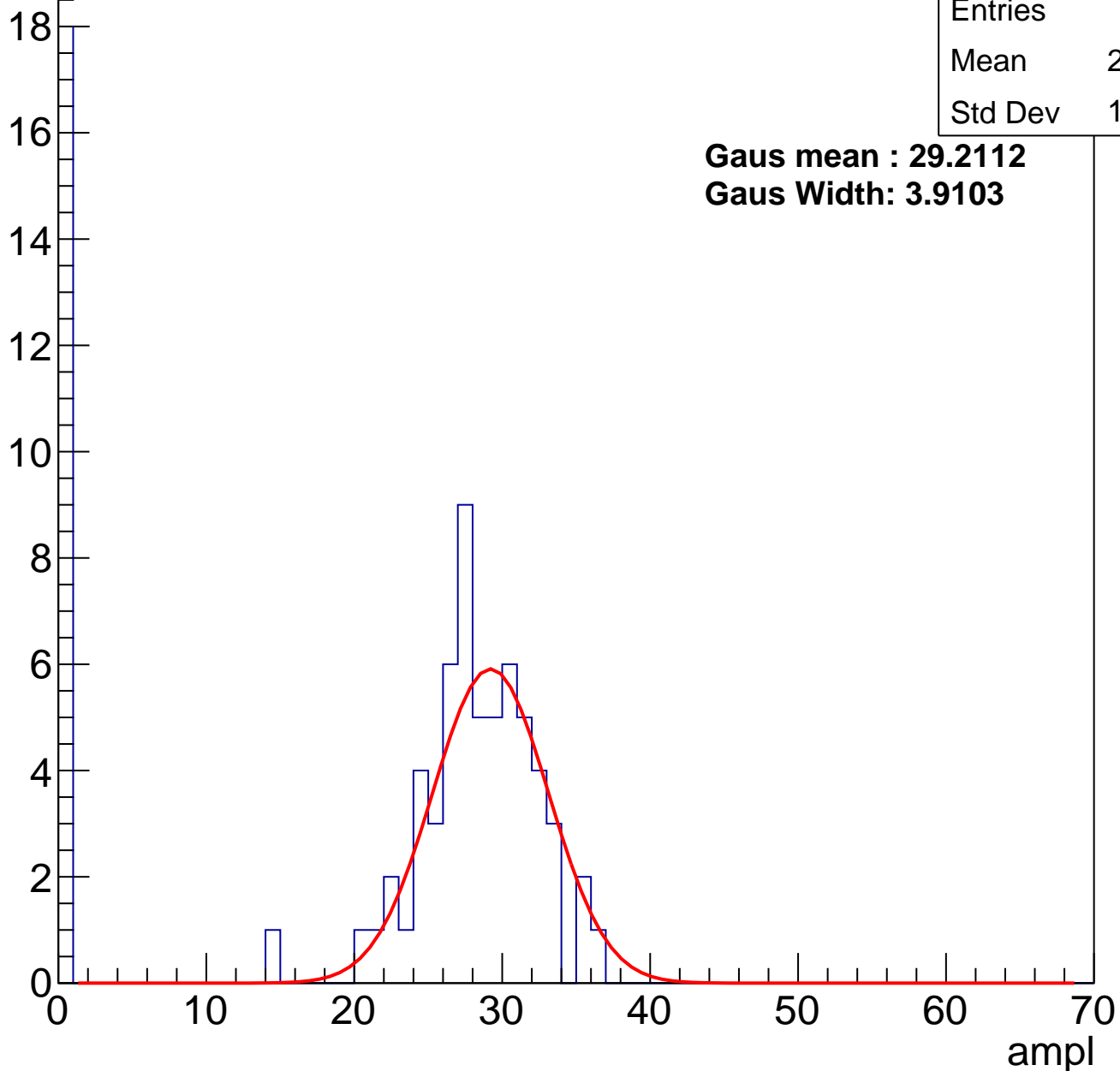
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	21.36
Std Dev	12.29

Gaus mean : 29.2112

Gaus Width: 3.9103

Entry



B1L103S, U24-ch99, adc1

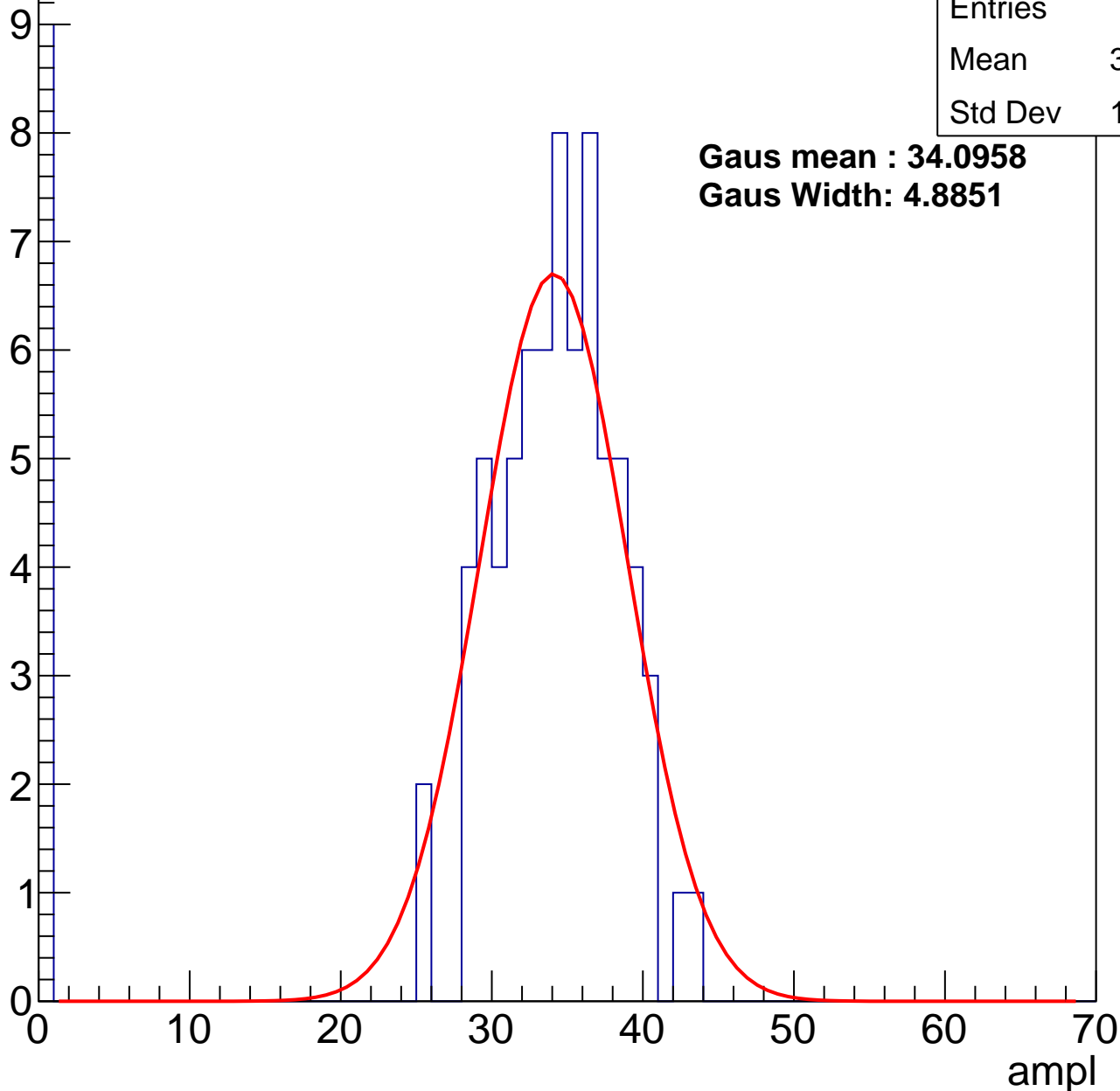
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	30.22
Std Dev	11.22

Gaus mean : 34.0958

Gaus Width: 4.8851



B1L103S, U24-ch99, adc2

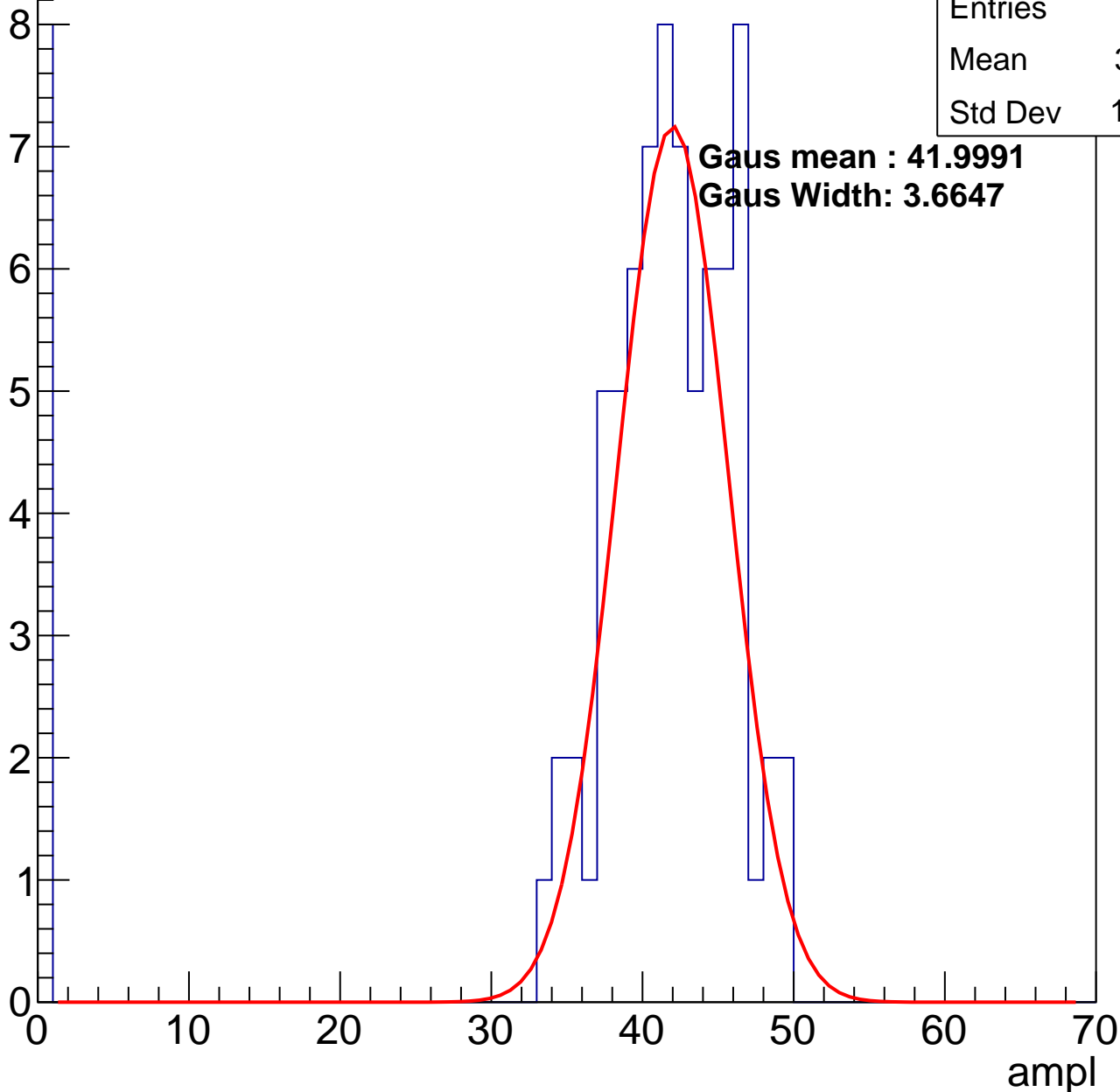
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	37.51
Std Dev	12.83

Gaus mean : 41.9991

Gaus Width: 3.6647

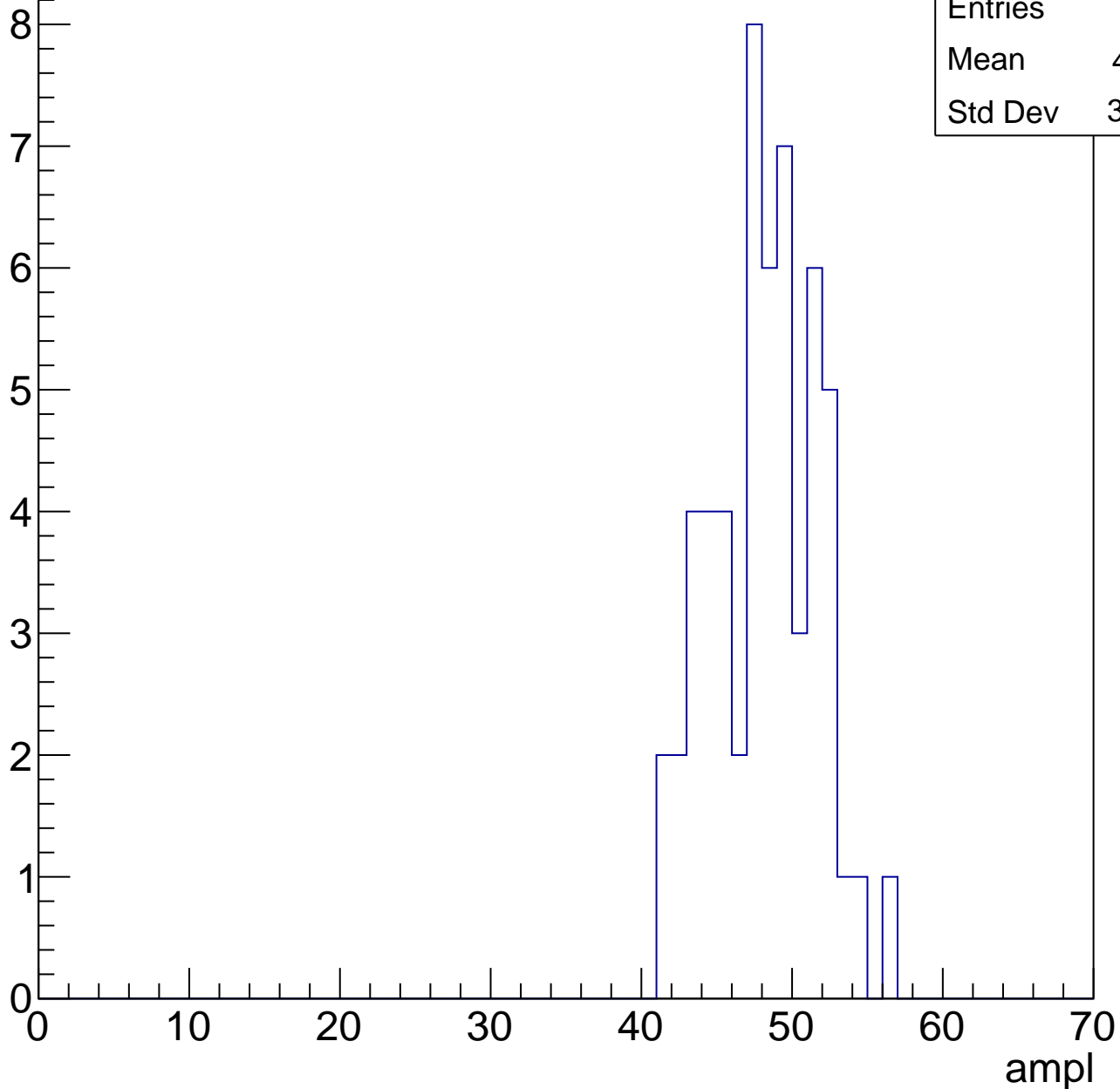


B1L103S, U24-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	47.71
Std Dev	3.432

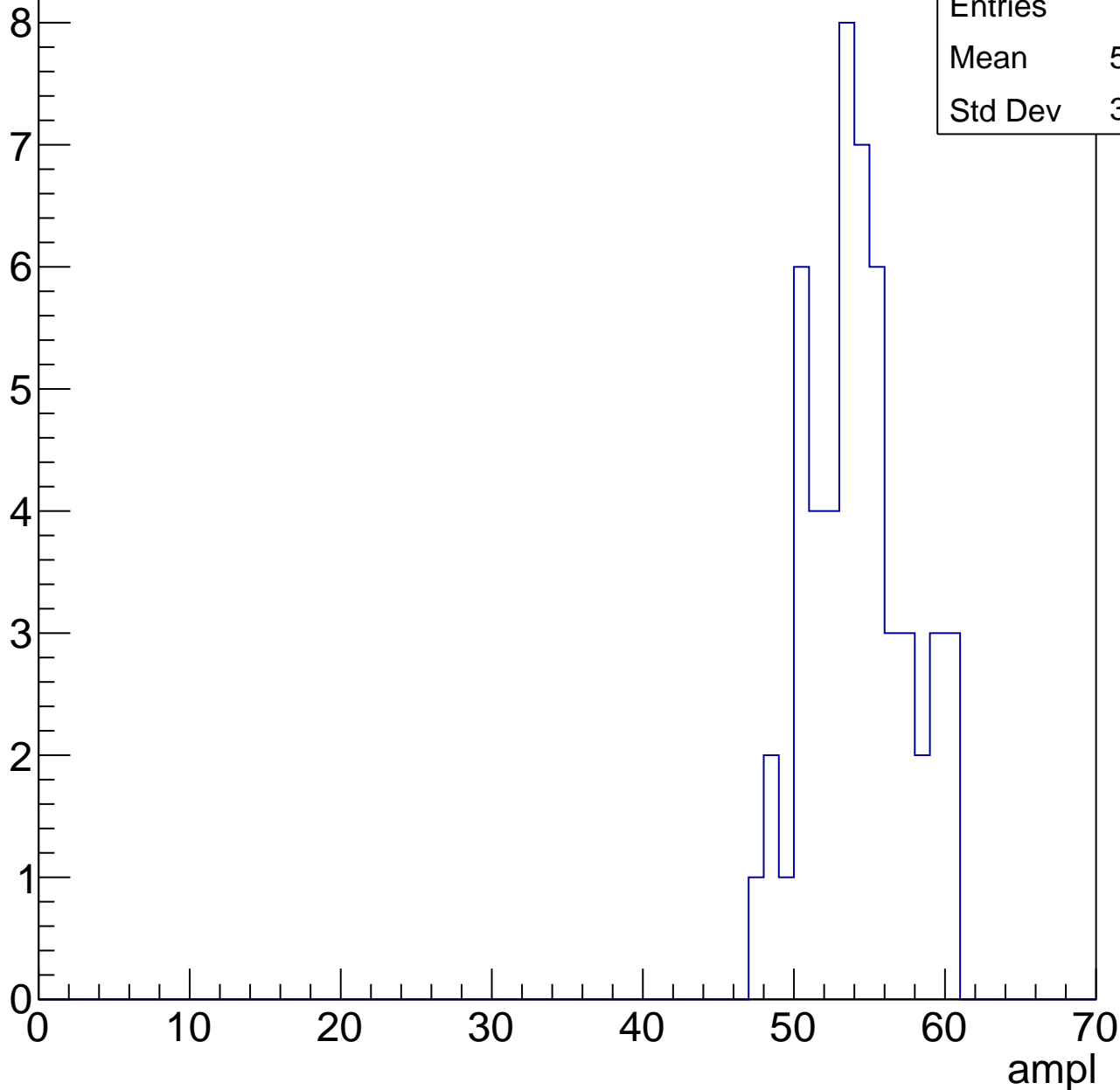


B1L103S, U24-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	53.74
Std Dev	3.246

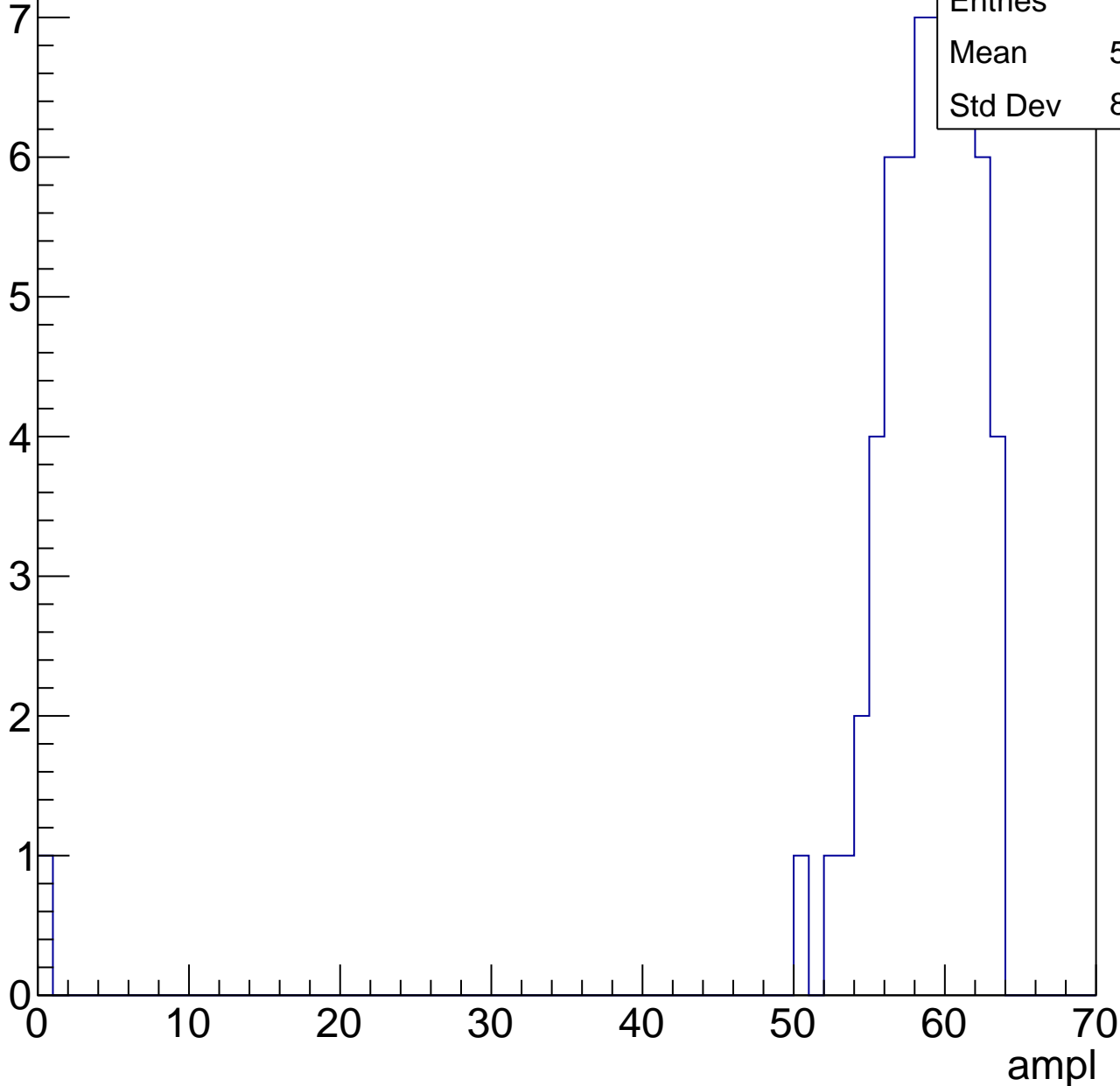


B1L103S, U24-ch99, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.52
Std Dev	8.028

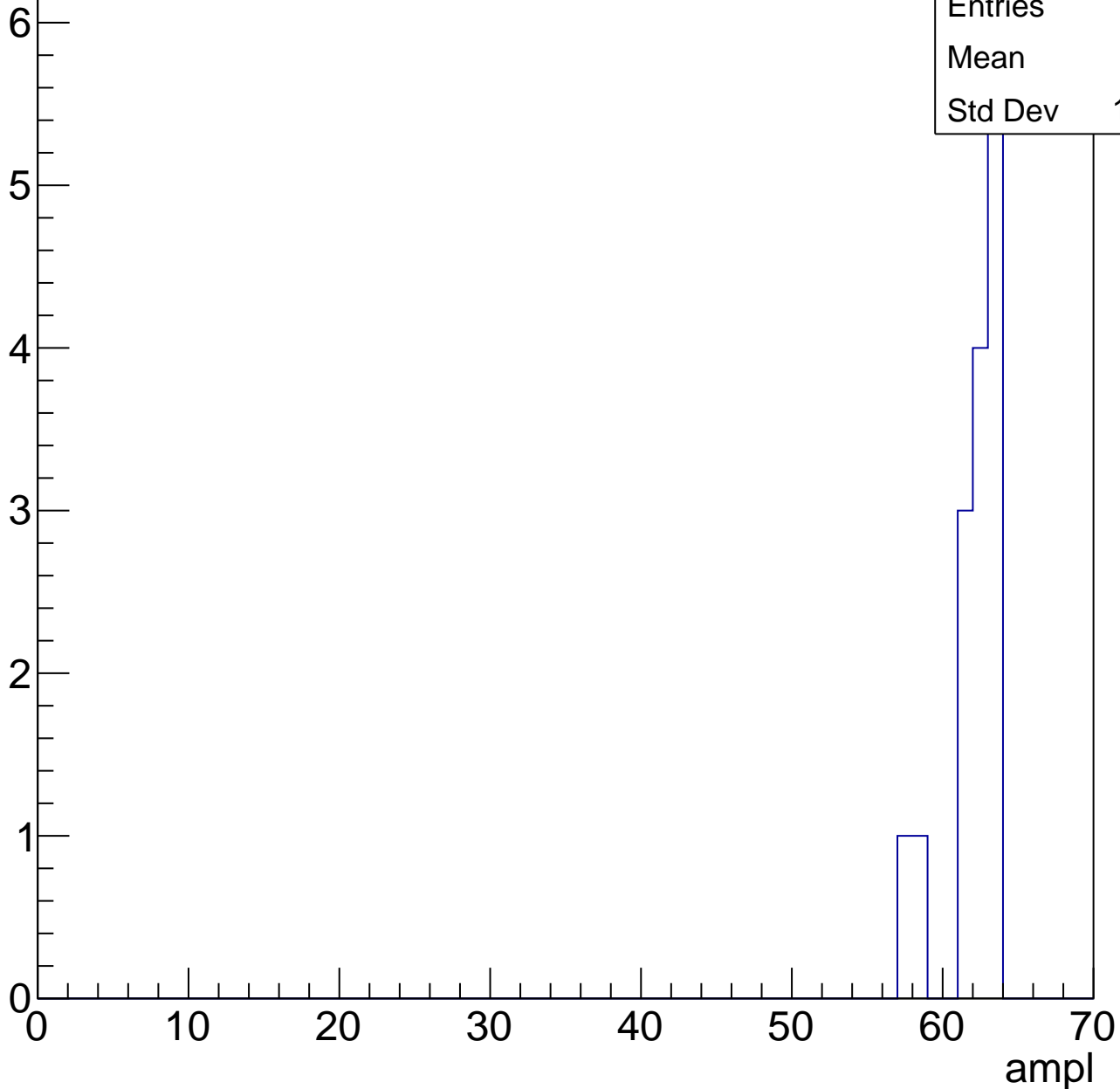


B1L103S, U24-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.6
Std Dev	1.781



B1L103S, U24-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch100, adc0

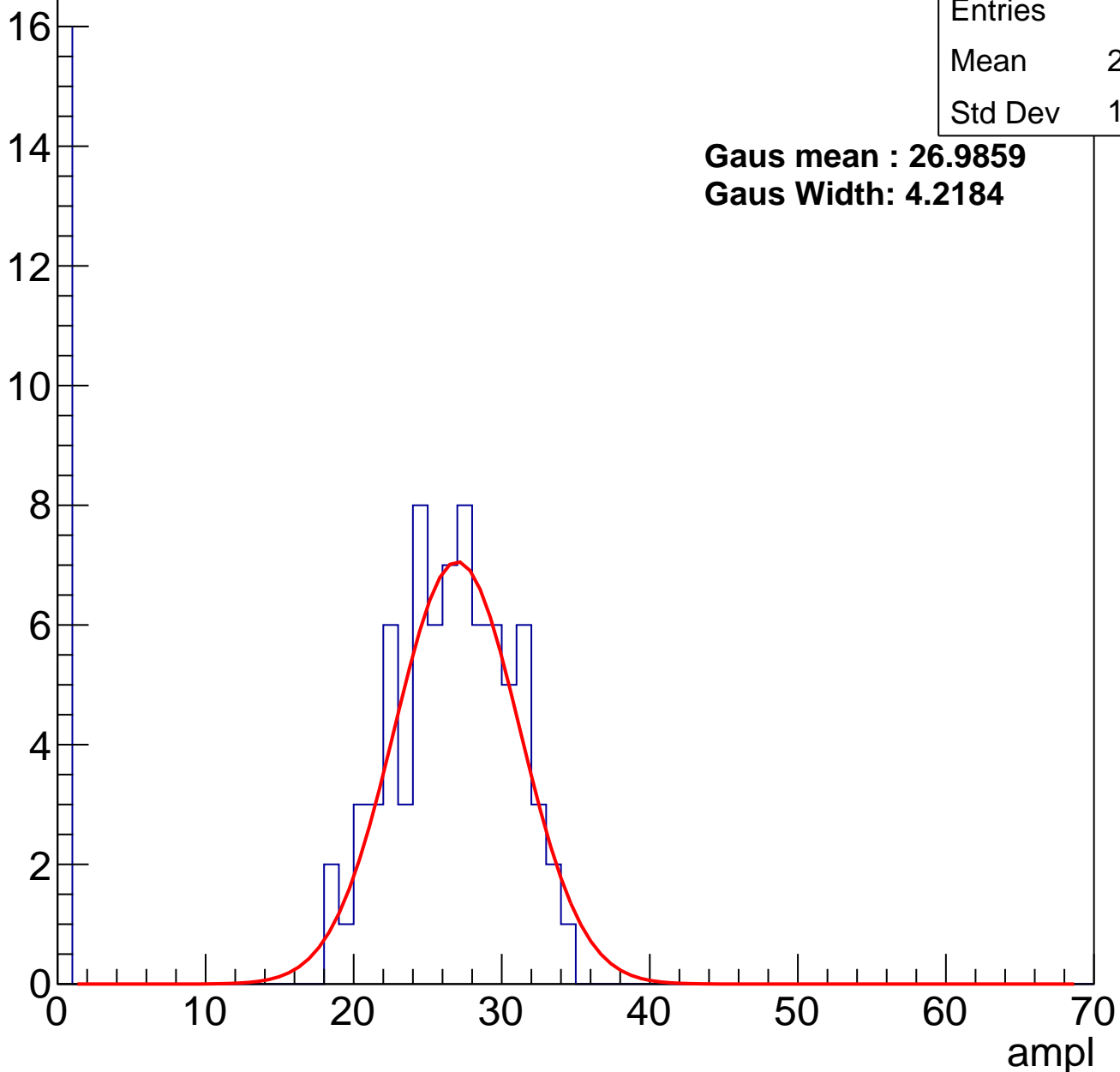
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	21.66
Std Dev	10.53

Gaus mean : 26.9859

Gaus Width: 4.2184

Entry



B1L103S, U24-ch100, adc1

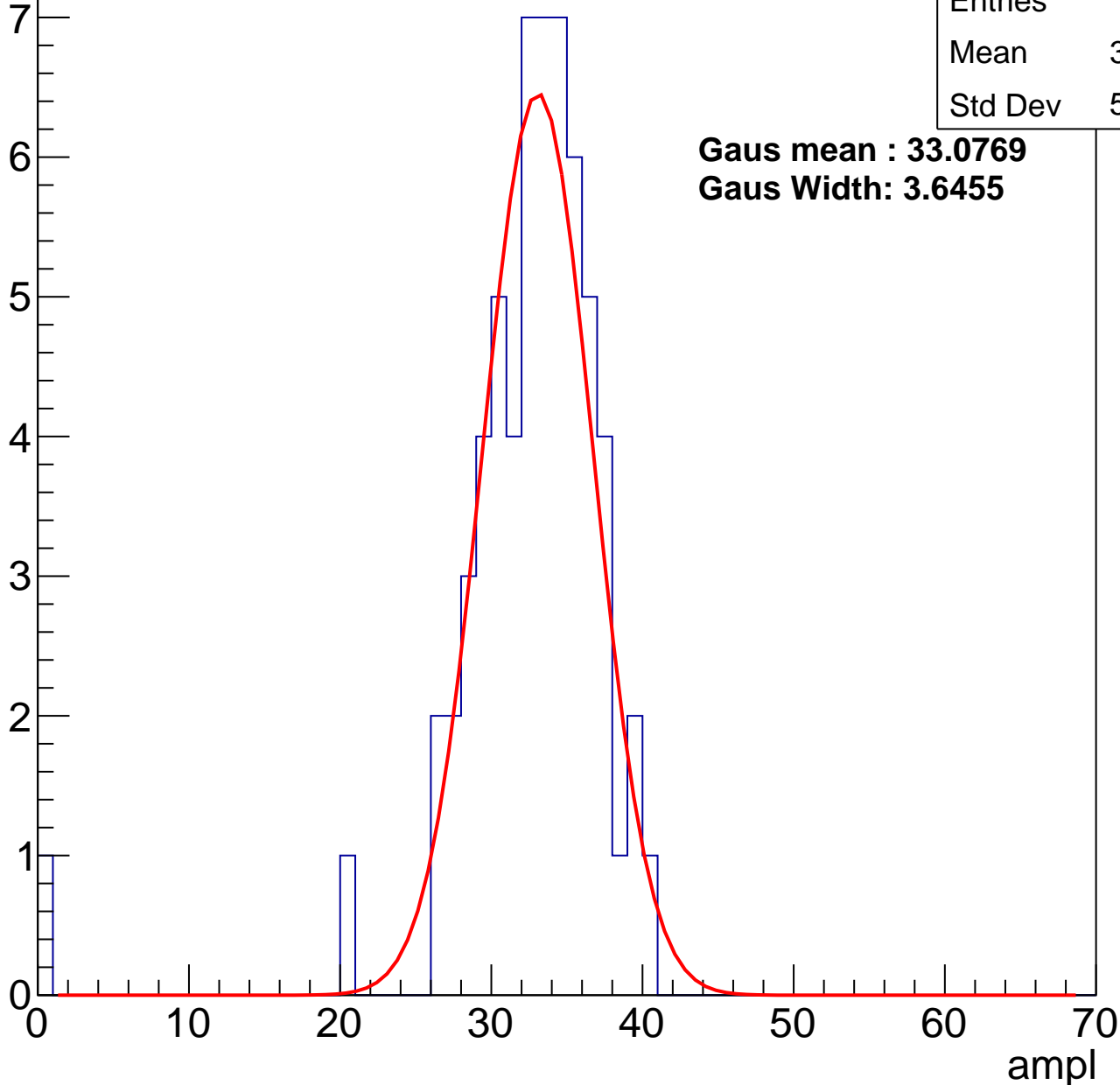
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.05
Std Dev	5.484

Gaus mean : 33.0769

Gaus Width: 3.6455



B1L103S, U24-ch100, adc2

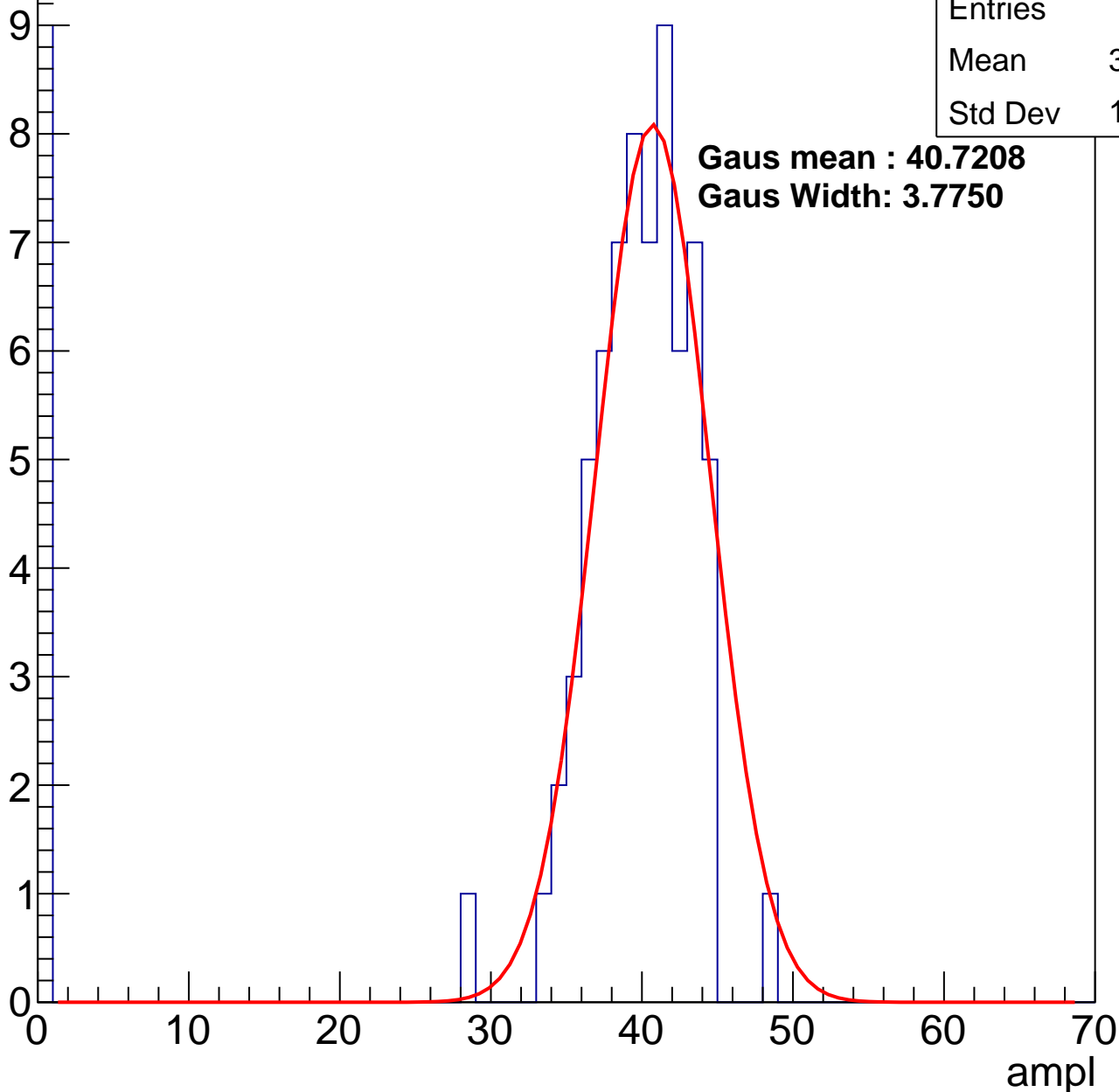
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	34.86
Std Dev	13.05

Gaus mean : 40.7208

Gaus Width: 3.7750

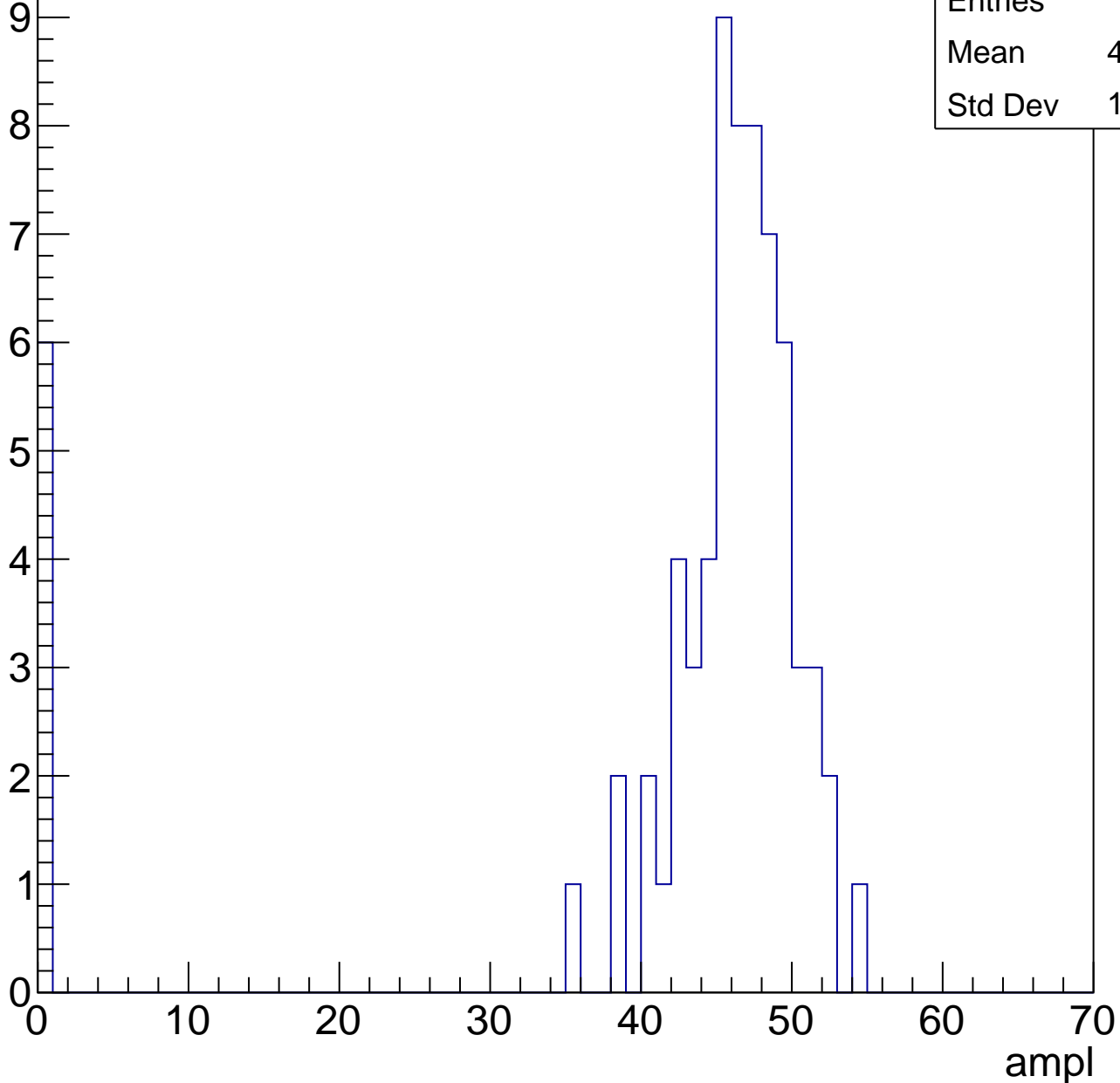


B1L103S, U24-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	42.07
Std Dev	13.32

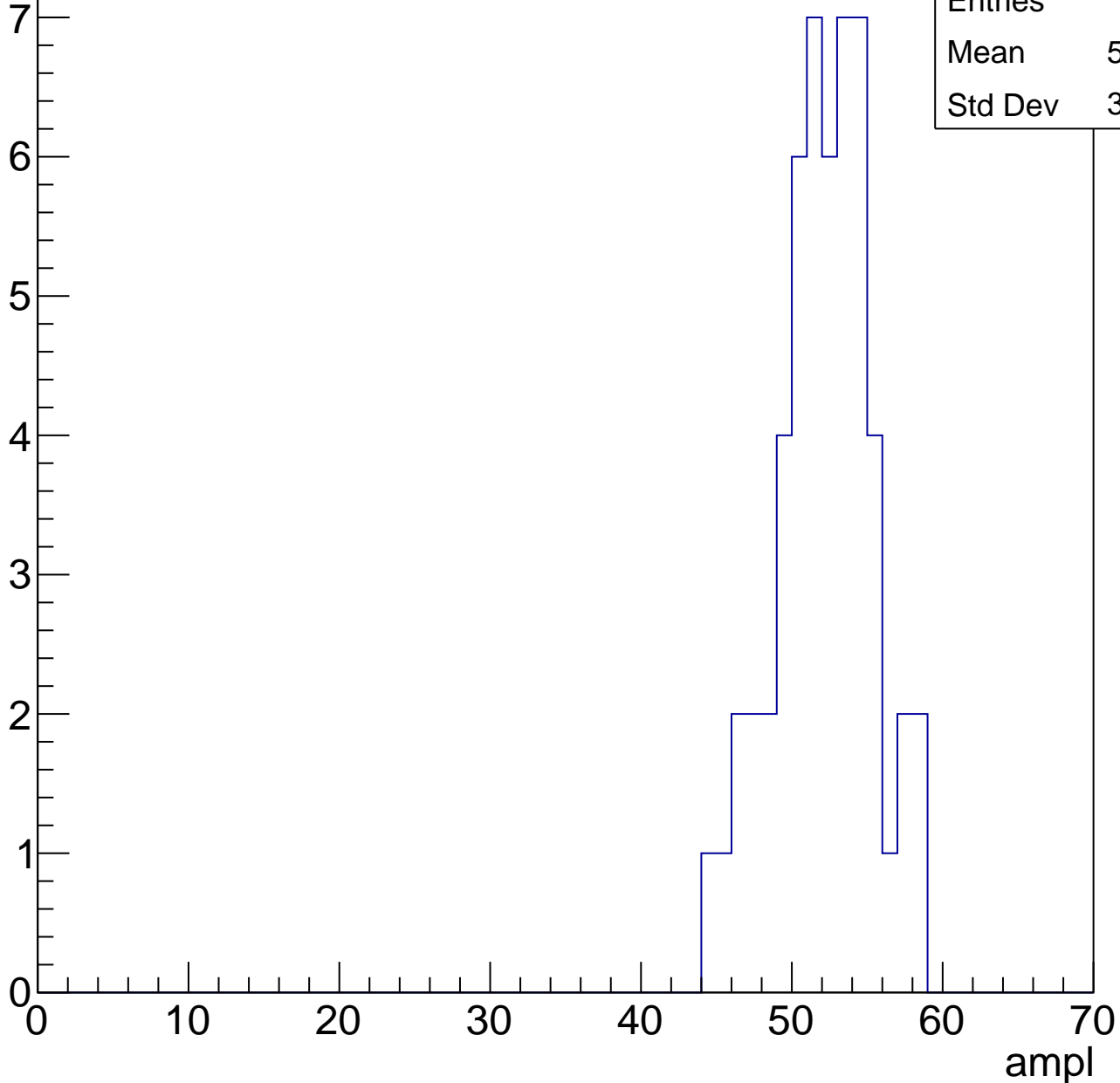


B1L103S, U24-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	51.69
Std Dev	3.155

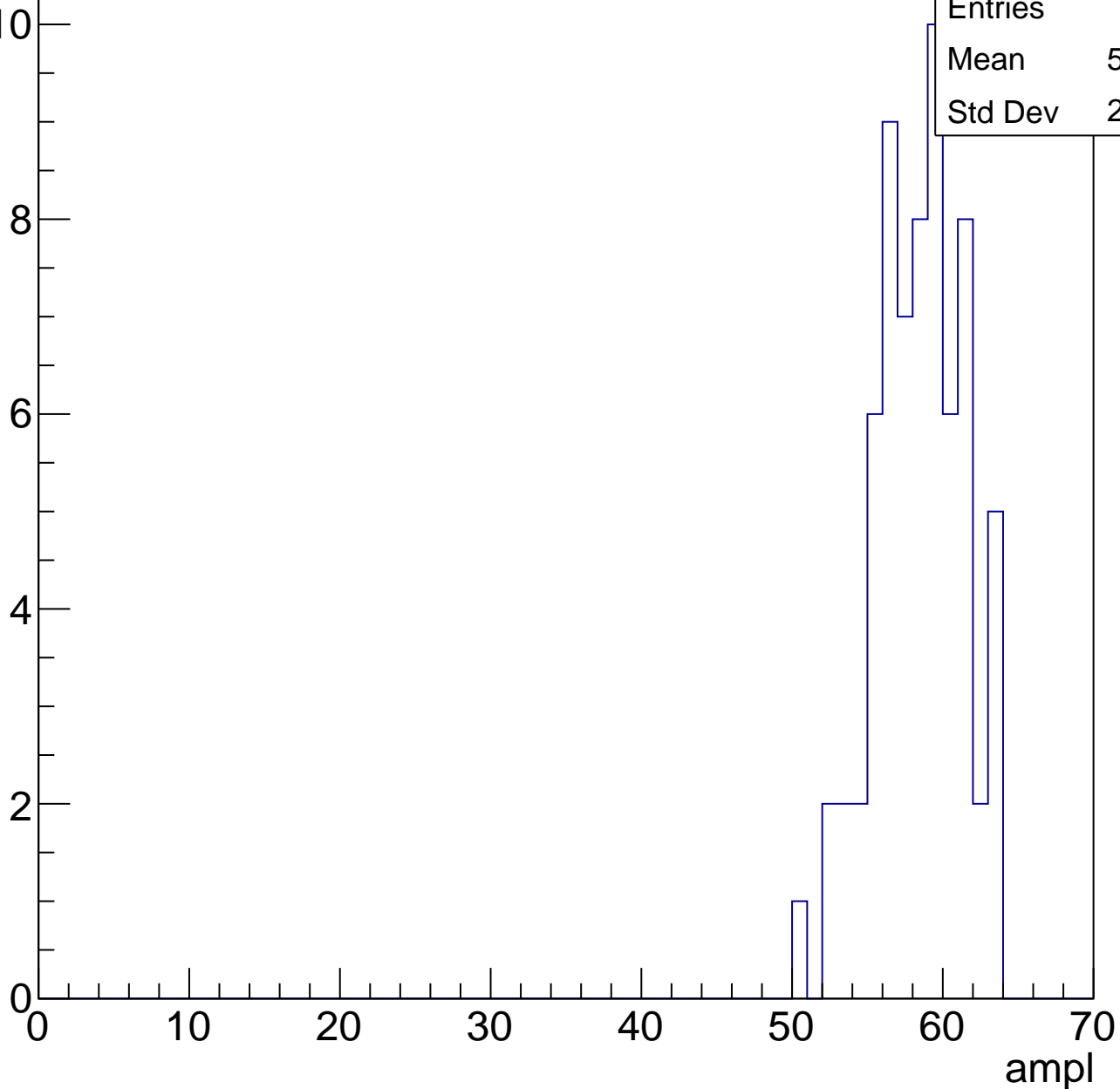


B1L103S, U24-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	57.97
Std Dev	2.915

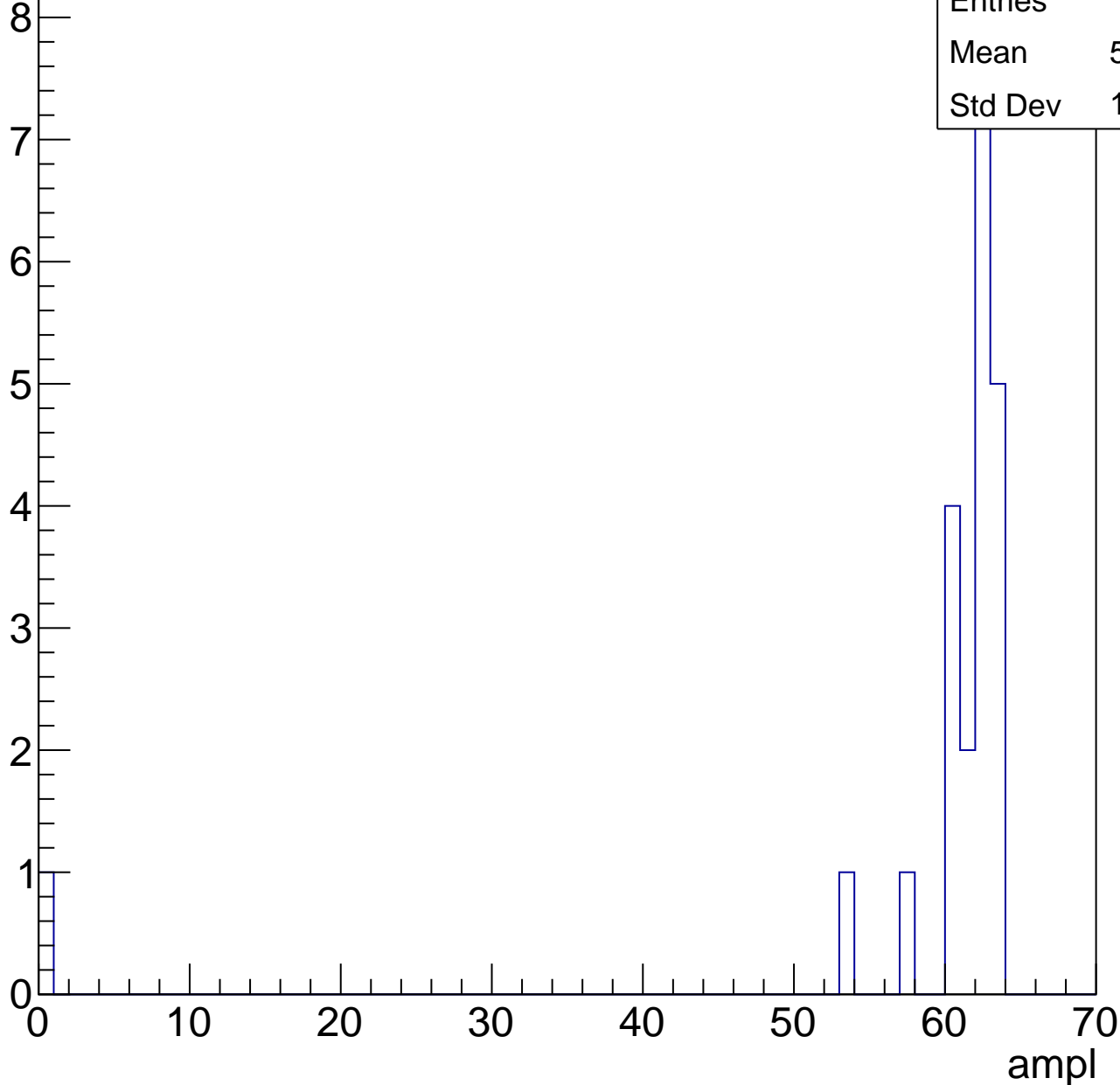


B1L103S, U24-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.32
Std Dev	12.92



B1L103S, U24-ch100, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch101, adc0

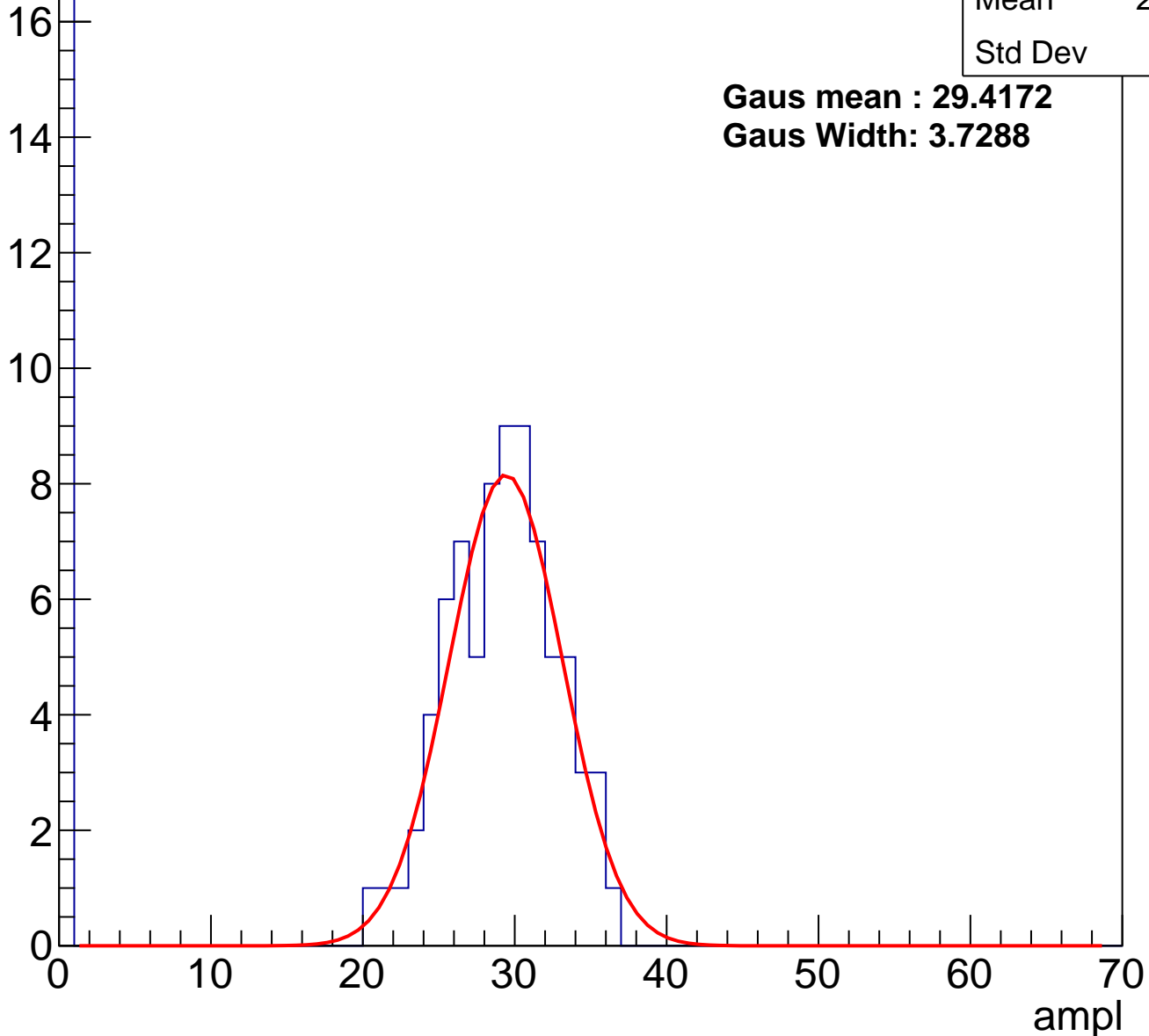
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	23.53
Std Dev	11.5

Gaus mean : 29.4172

Gaus Width: 3.7288

Entry



B1L103S, U24-ch101, adc1

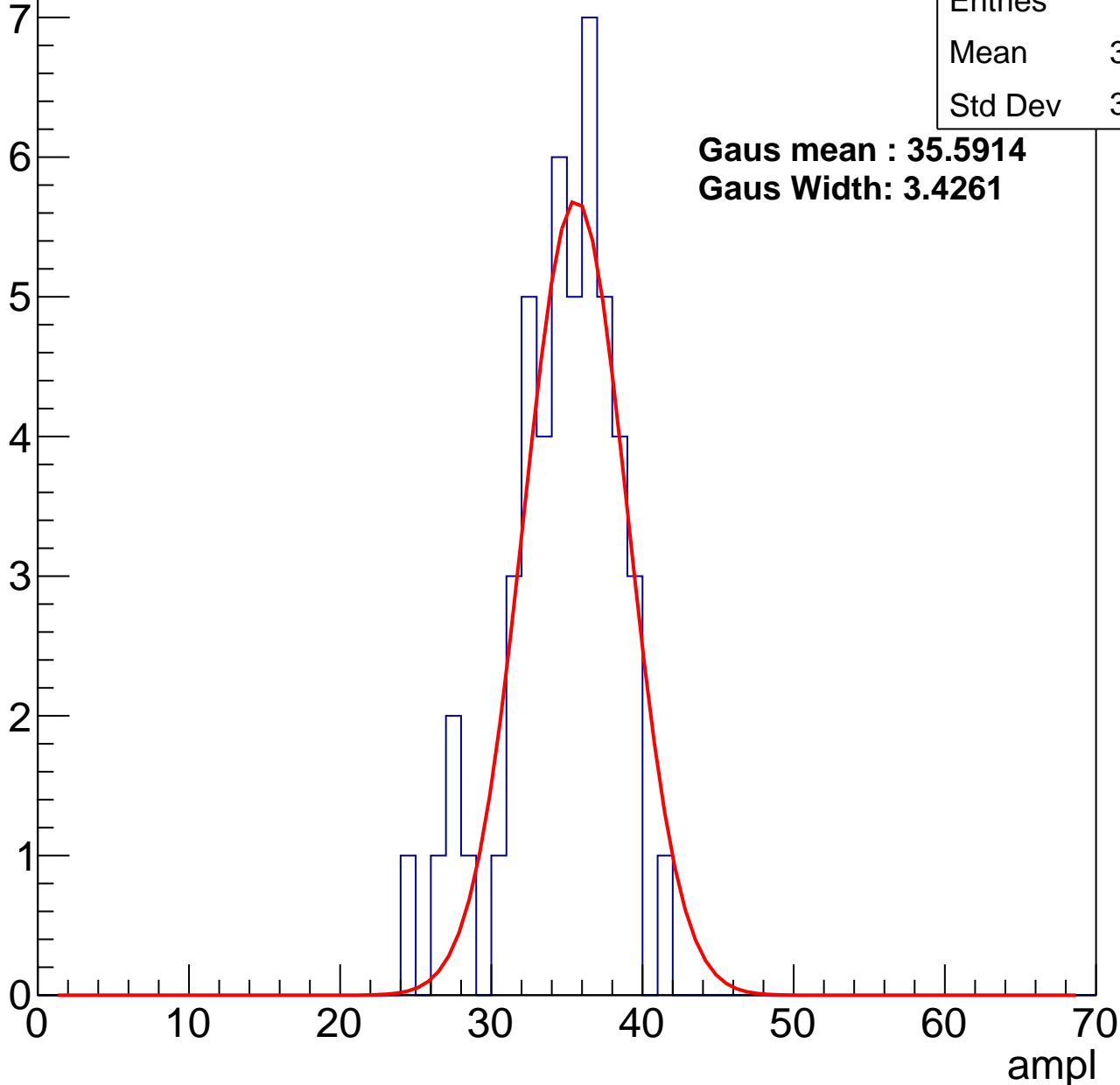
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	34.14
Std Dev	3.586

Gaus mean : 35.5914

Gaus Width: 3.4261



B1L103S, U24-ch101, adc2

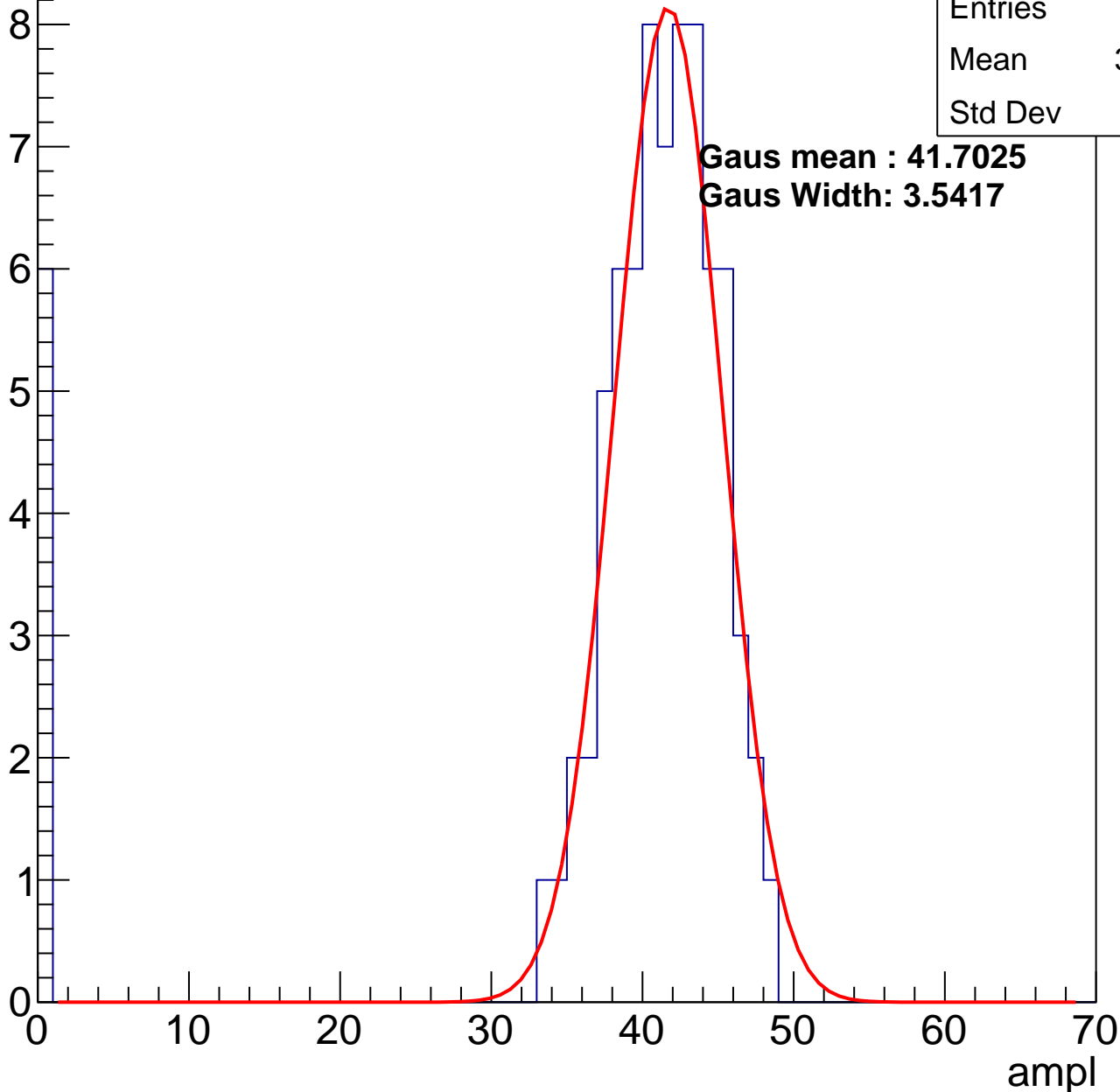
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	37.91
Std Dev	11.4

Gaus mean : 41.7025

Gaus Width: 3.5417

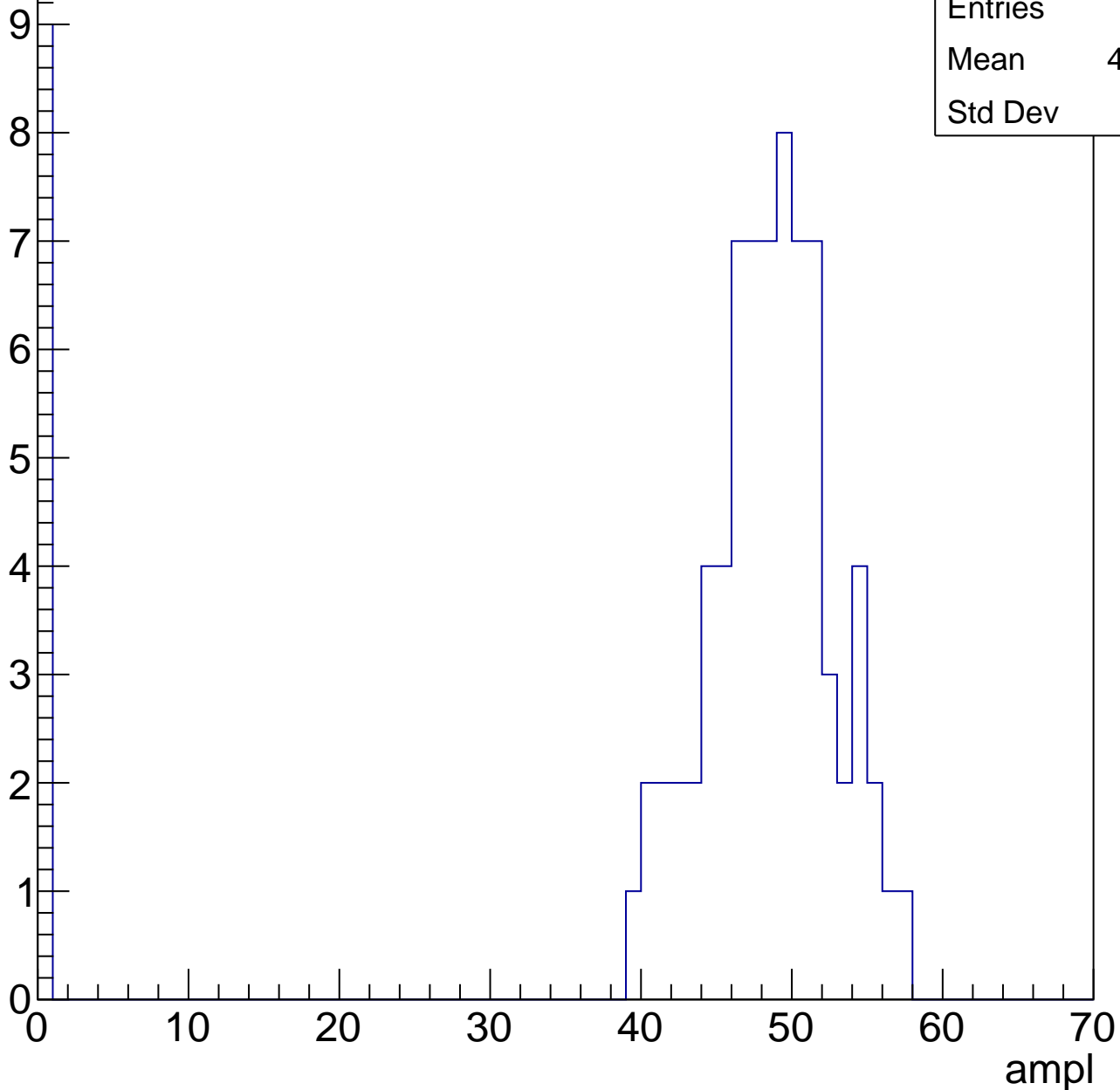


B1L103S, U24-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	42.85
Std Dev	15.5

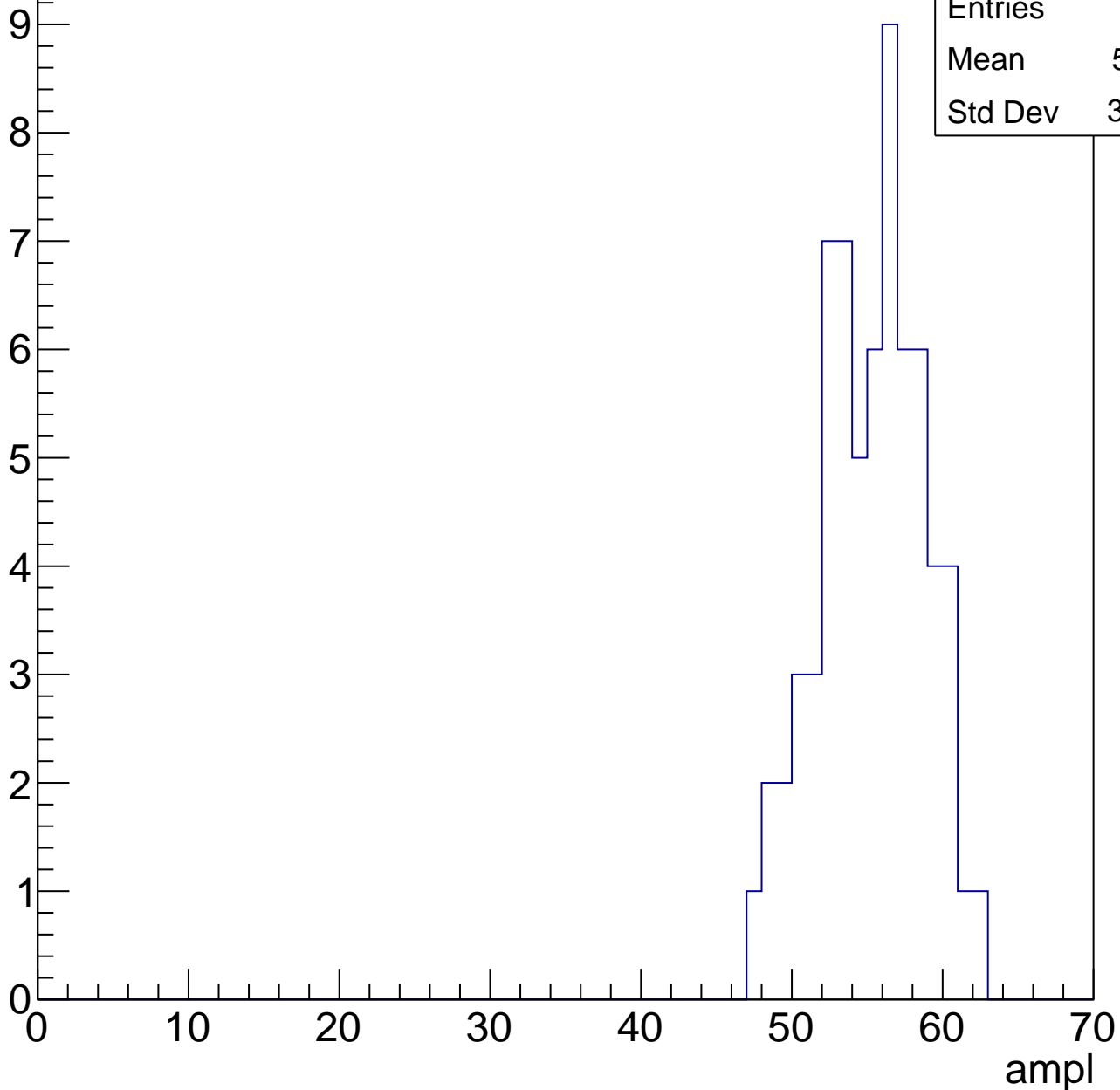


B1L103S, U24-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	54.81
Std Dev	3.426

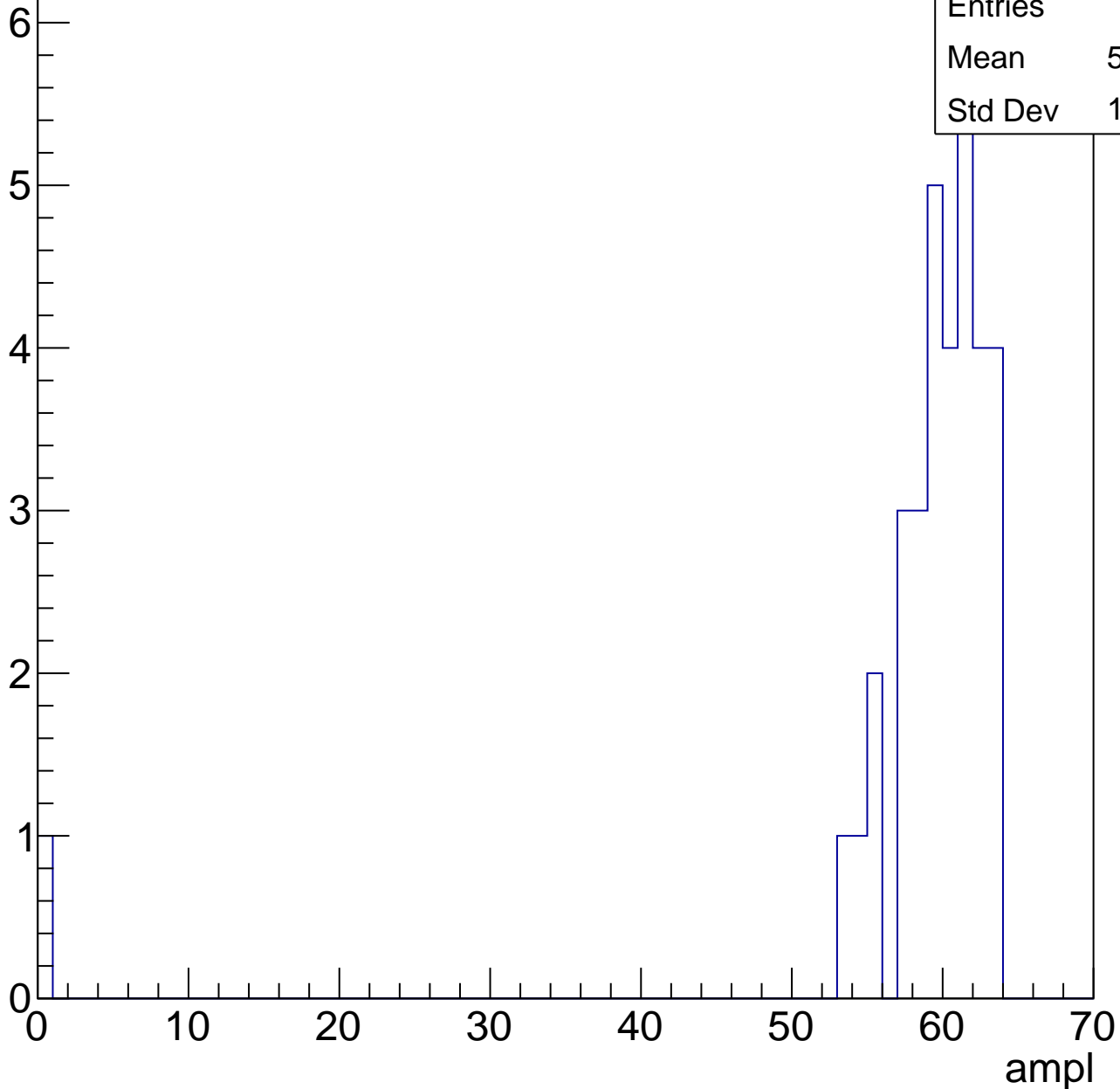


B1L103S, U24-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	34
Mean	57.74
Std Dev	10.38

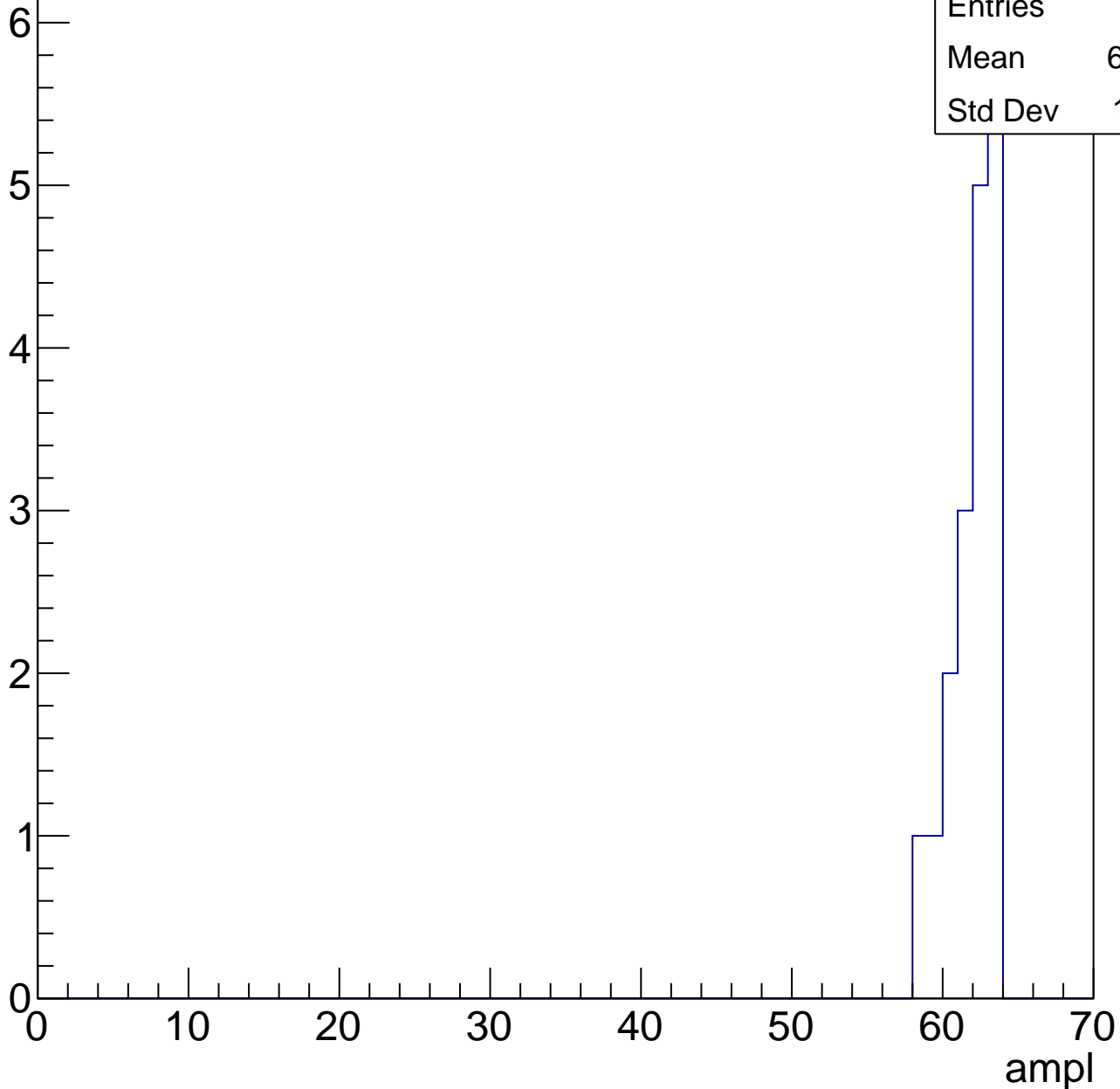


B1L103S, U24-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.56
Std Dev	1.461

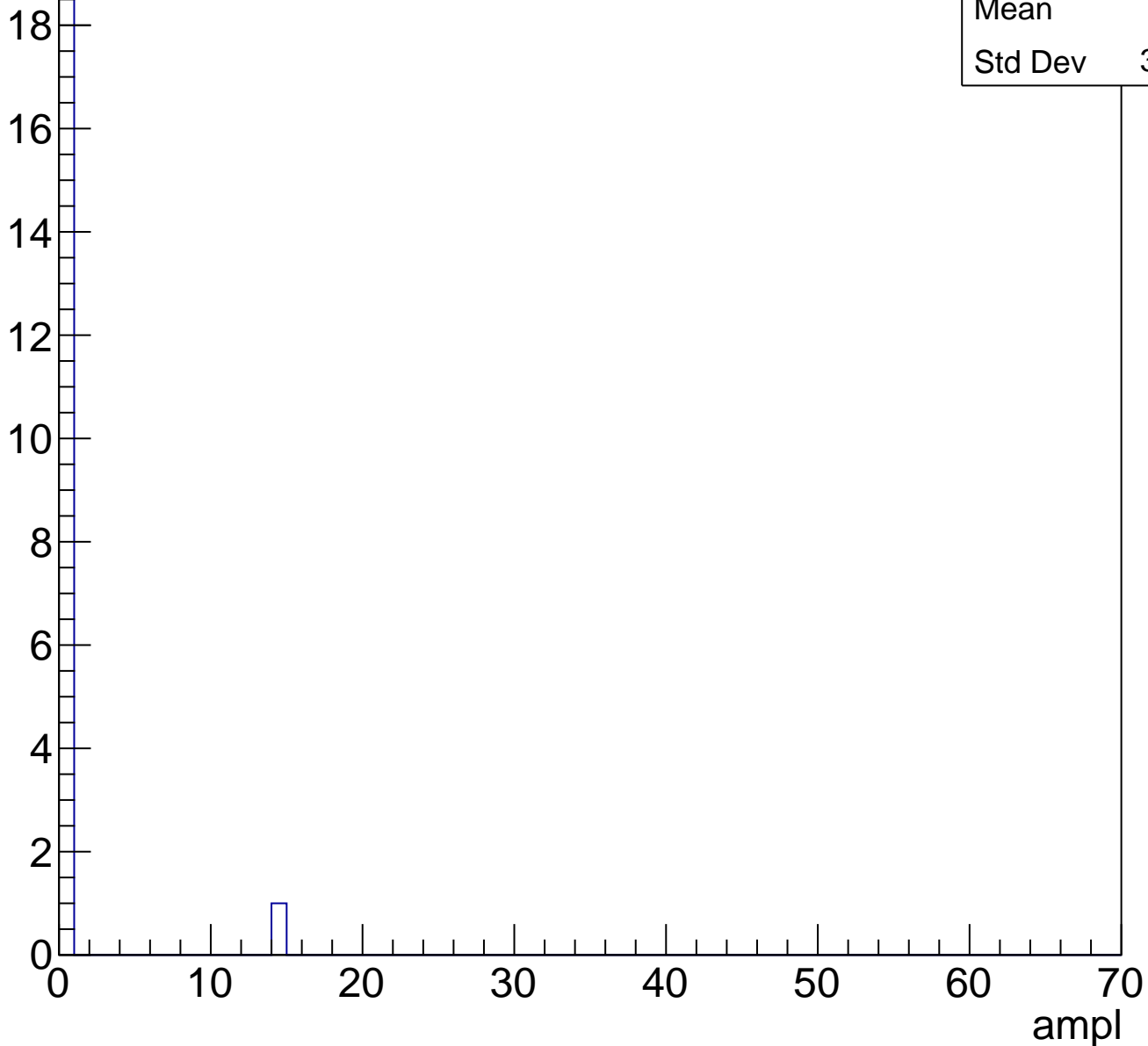


B1L103S, U24-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	0.7
Std Dev	3.051

Entry



B1L103S, U24-ch102, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	21.63
Std Dev	11.07

Gaus mean : 27.1398

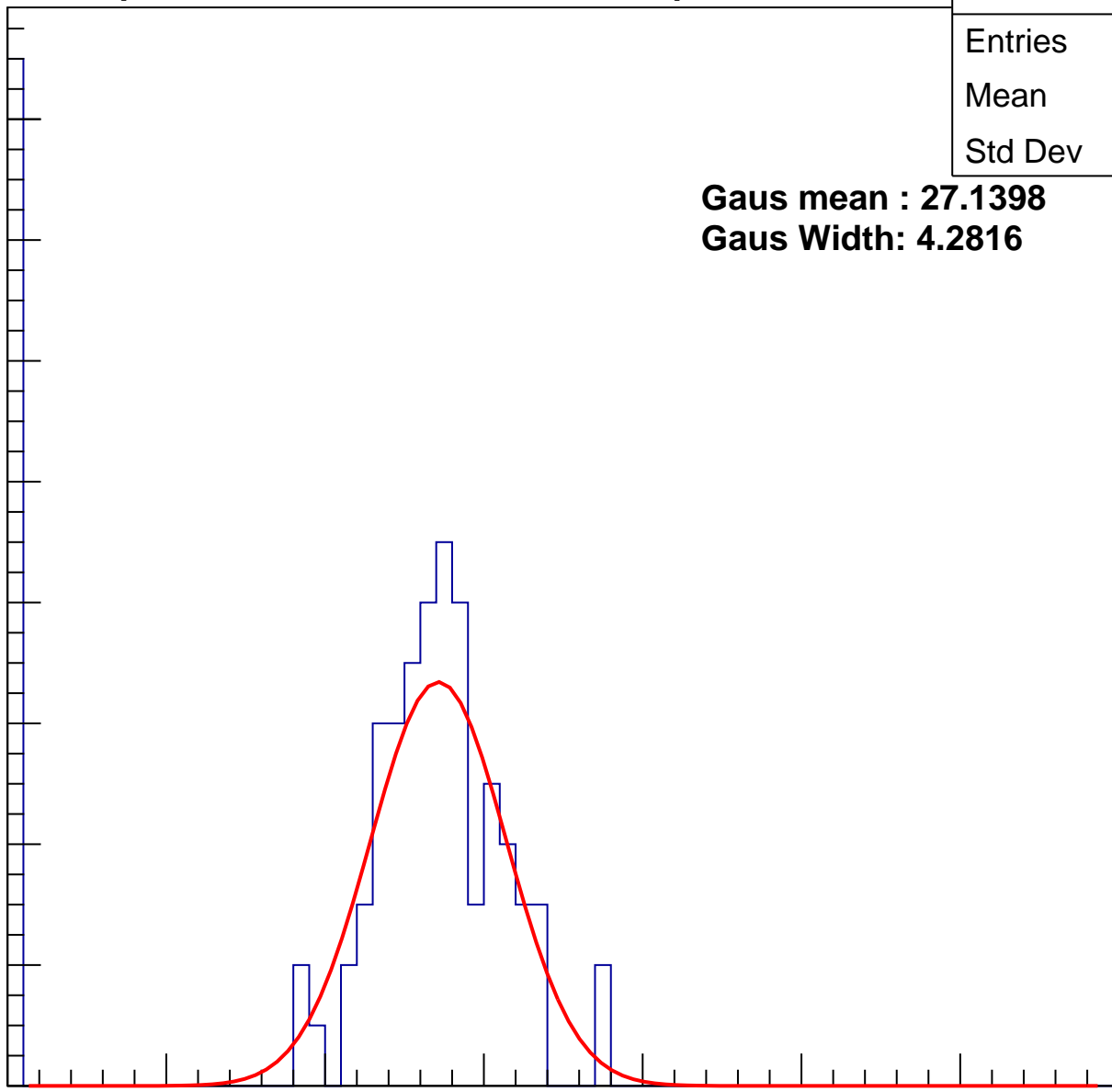
Gaus Width: 4.2816

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch102, adc1

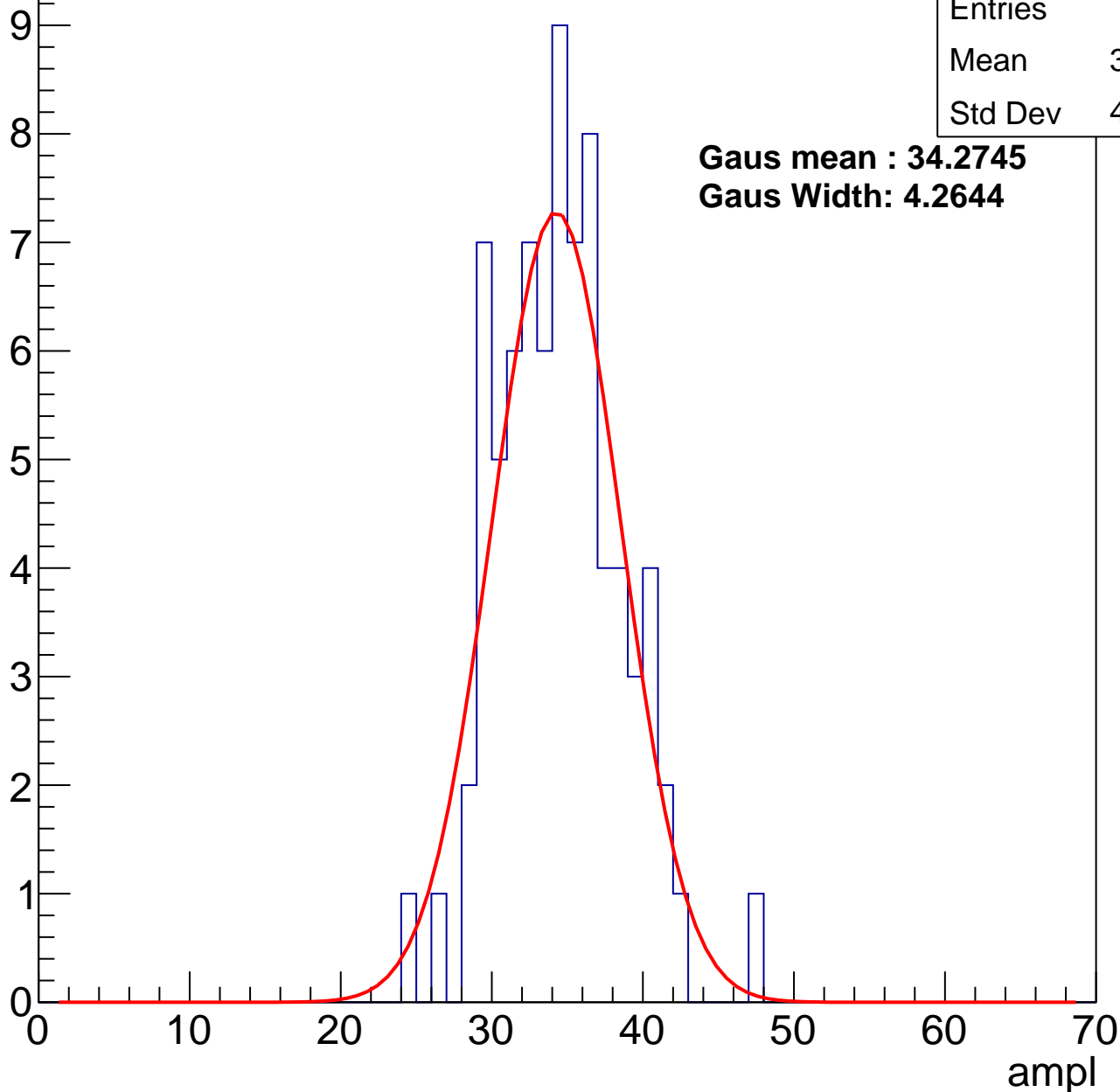
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	34.03
Std Dev	4.032

Gaus mean : 34.2745

Gaus Width: 4.2644



B1L103S, U24-ch102, adc2

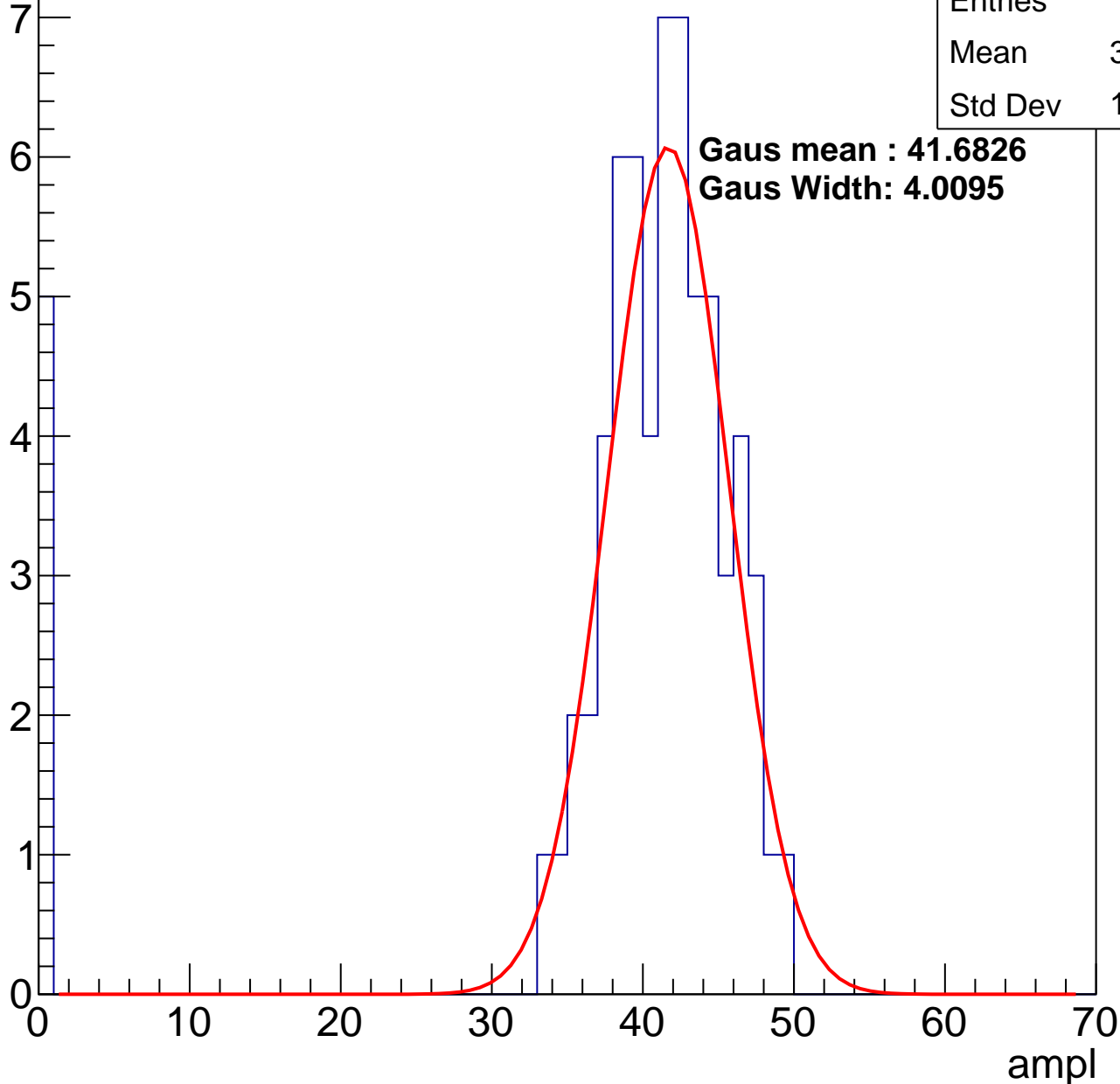
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	38.09
Std Dev	11.37

Gaus mean : 41.6826

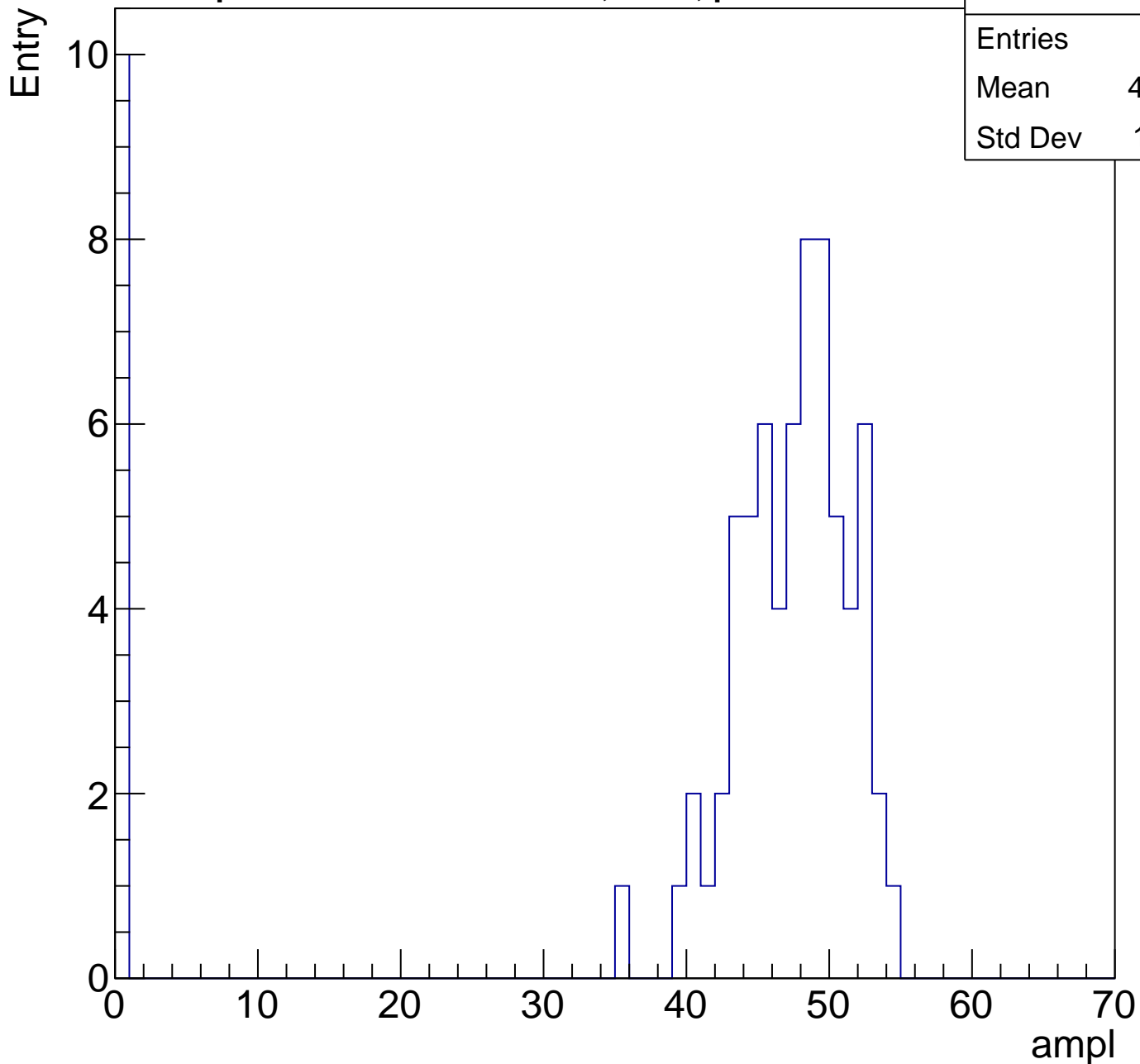
Gaus Width: 4.0095



B1L103S, U24-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	40.94
Std Dev	16.21

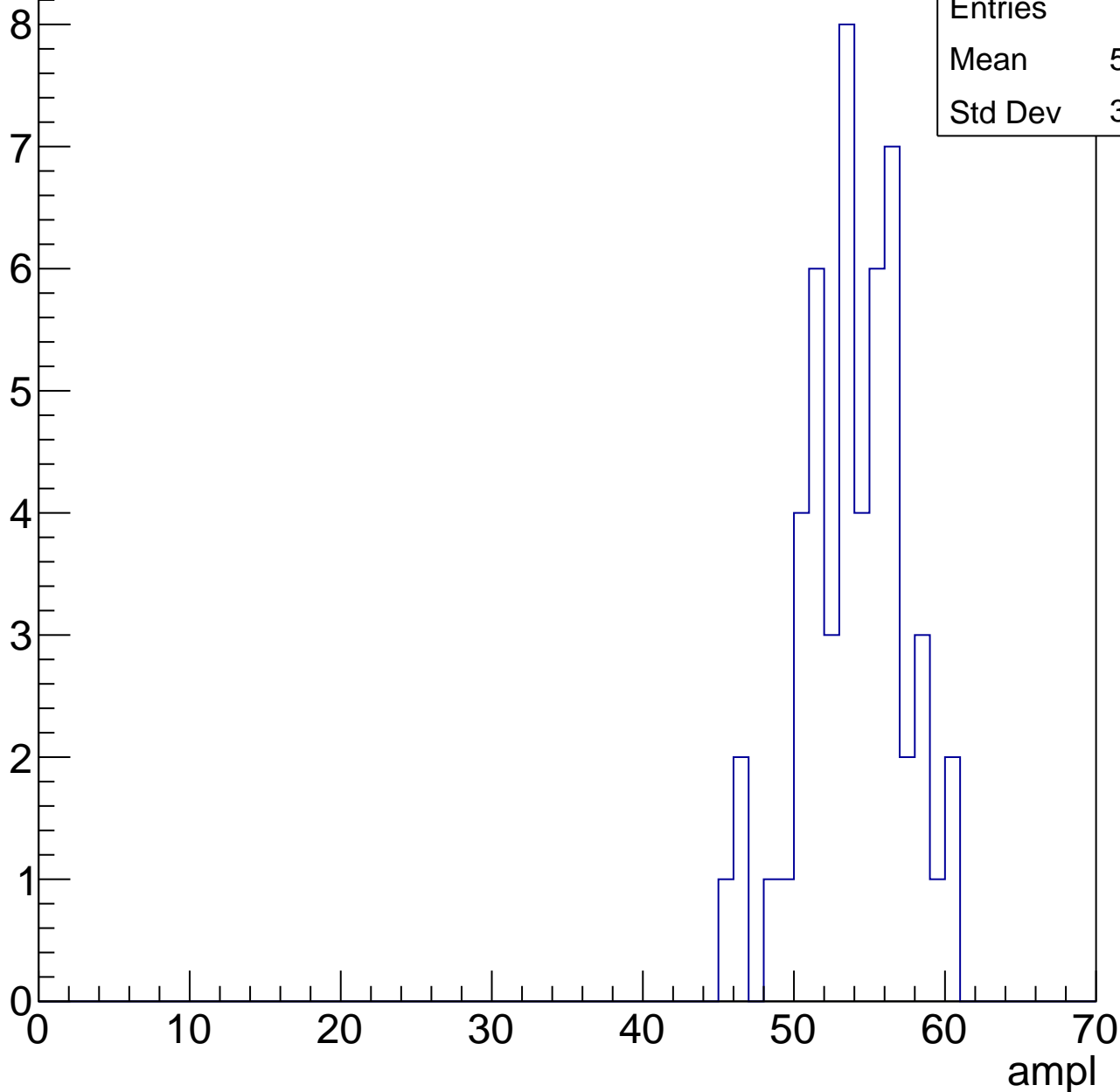


B1L103S, U24-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	53.43
Std Dev	3.397

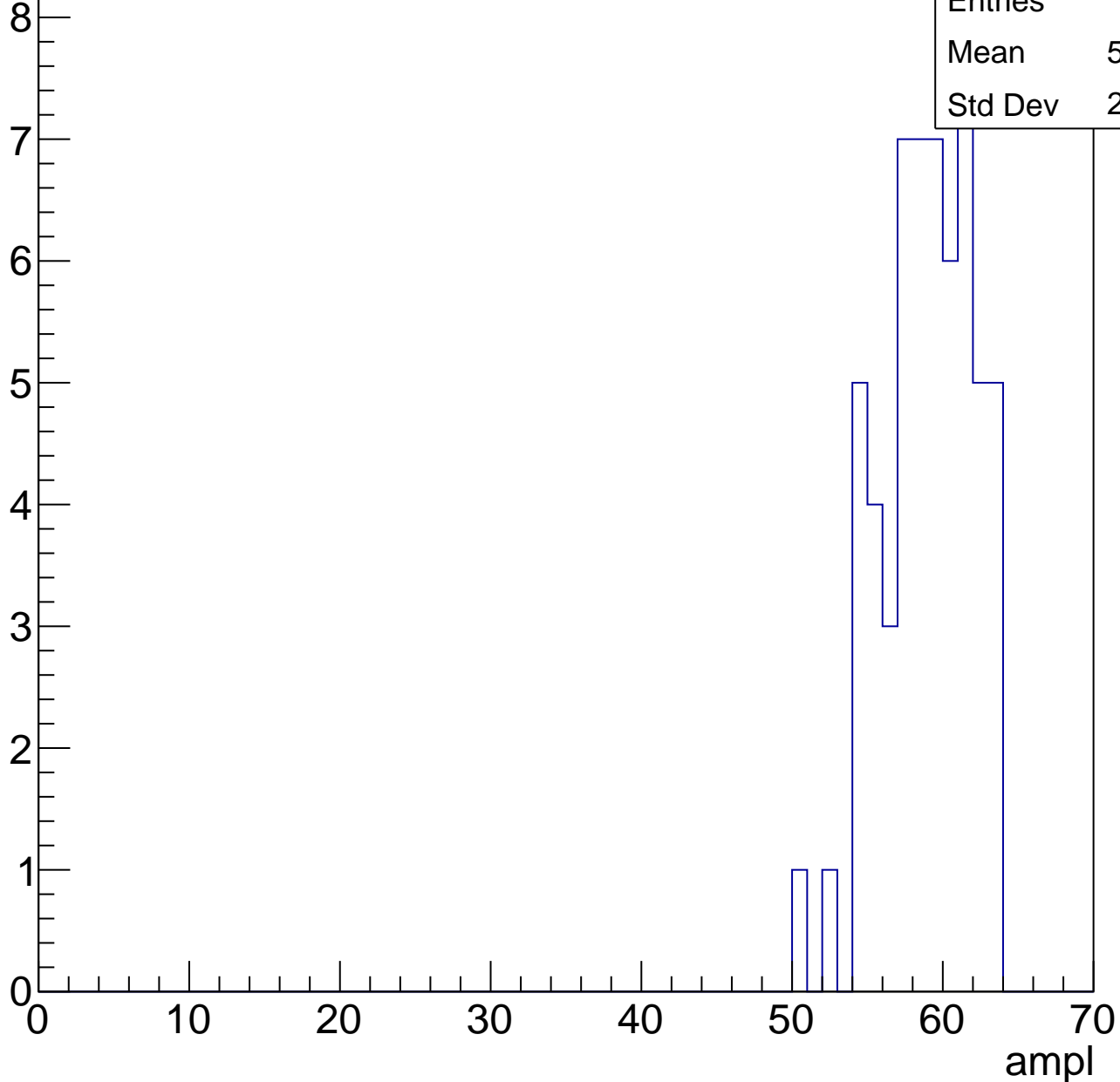


B1L103S, U24-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	58.49
Std Dev	2.994

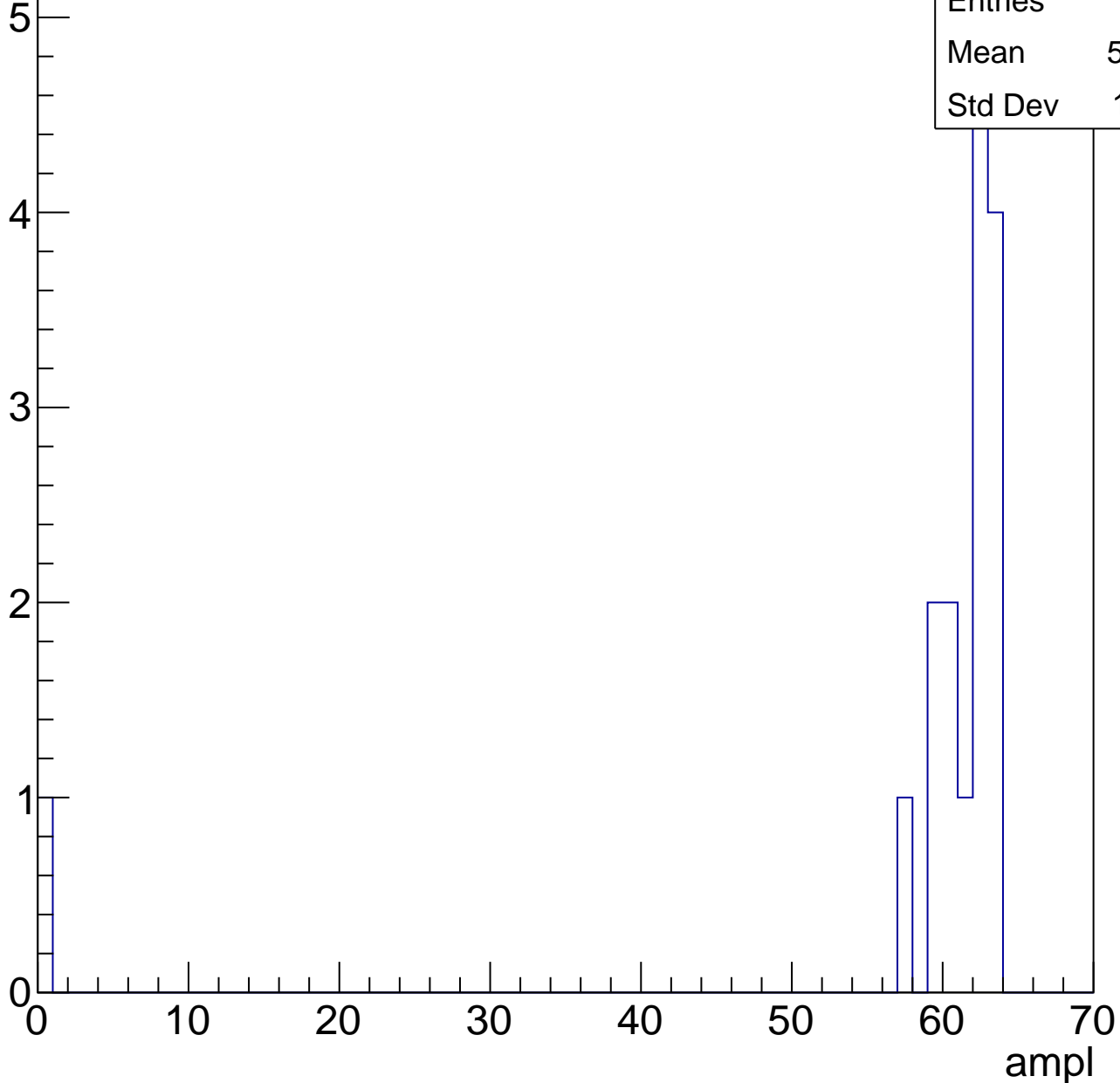


B1L103S, U24-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.38
Std Dev	14.91

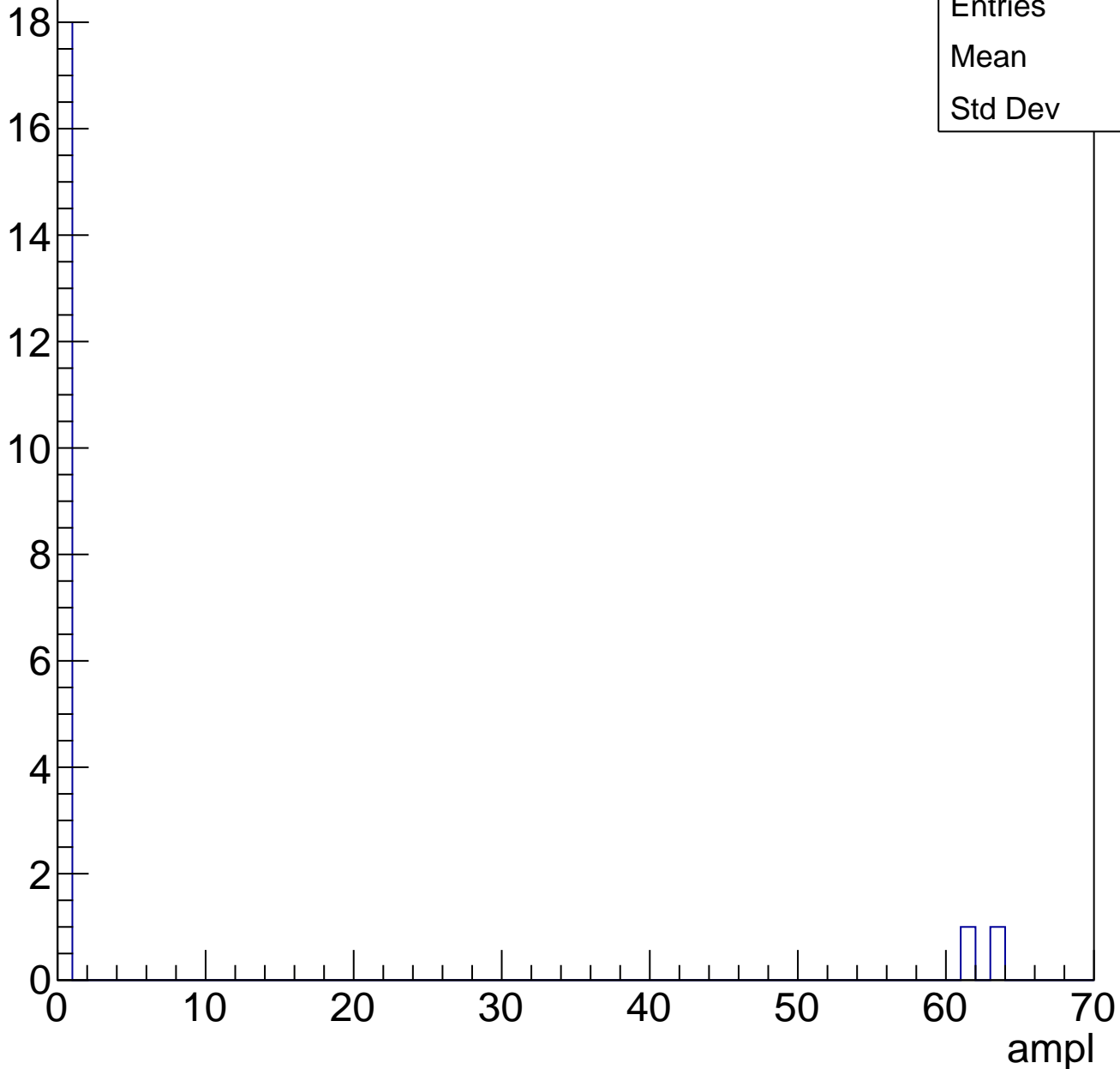


B1L103S, U24-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.2
Std Dev	18.6

Entry



B1L103S, U24-ch103, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	19.9
Std Dev	12.74

Gaus mean : 28.3413

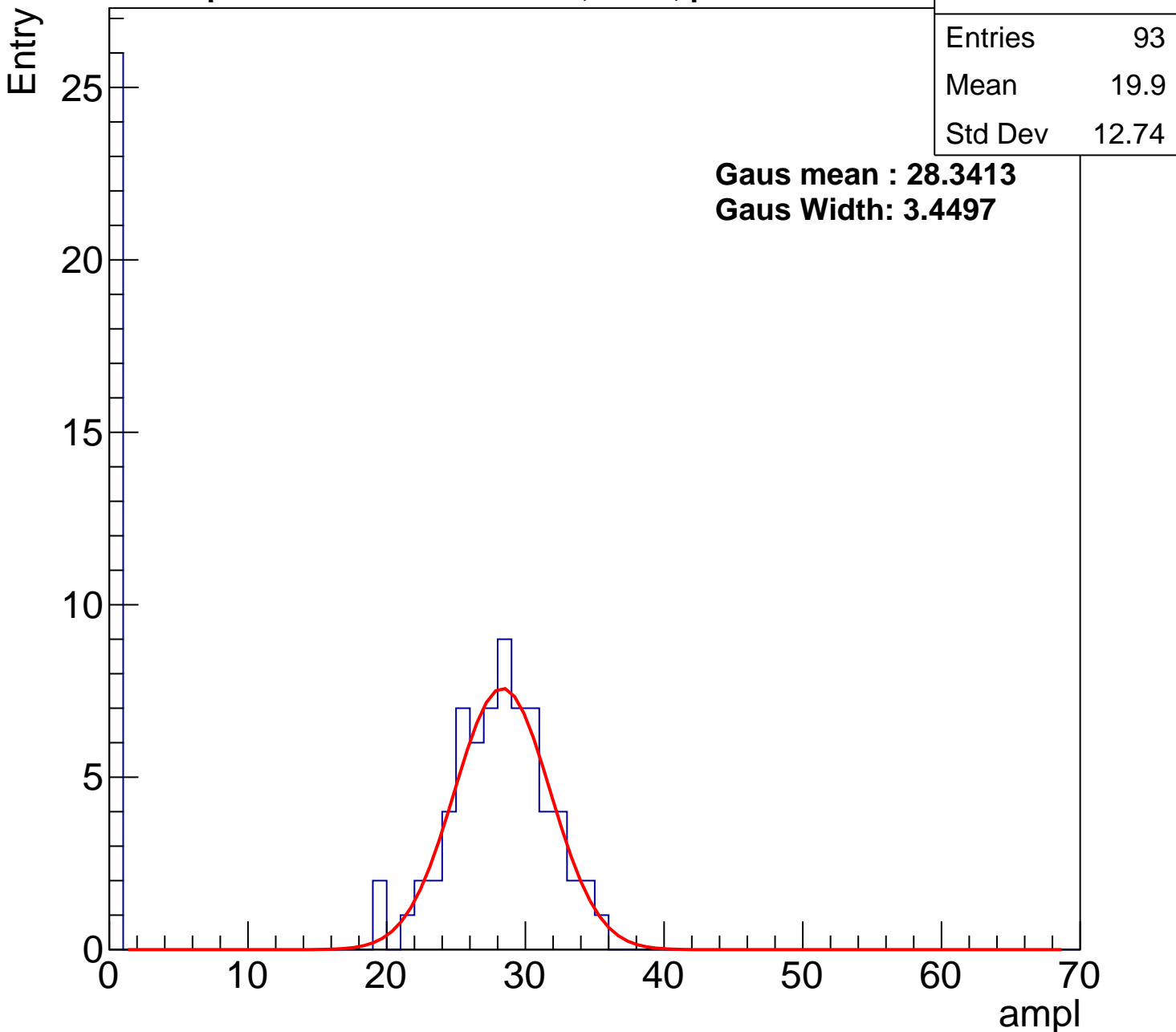
Gaus Width: 3.4497

Entry

25
20
15
10
5
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch103, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	31.2
Std Dev	12.01

Gaus mean : 35.4185

Gaus Width: 3.8986

Entry

10

8

6

4

2

0

0

10

20

30

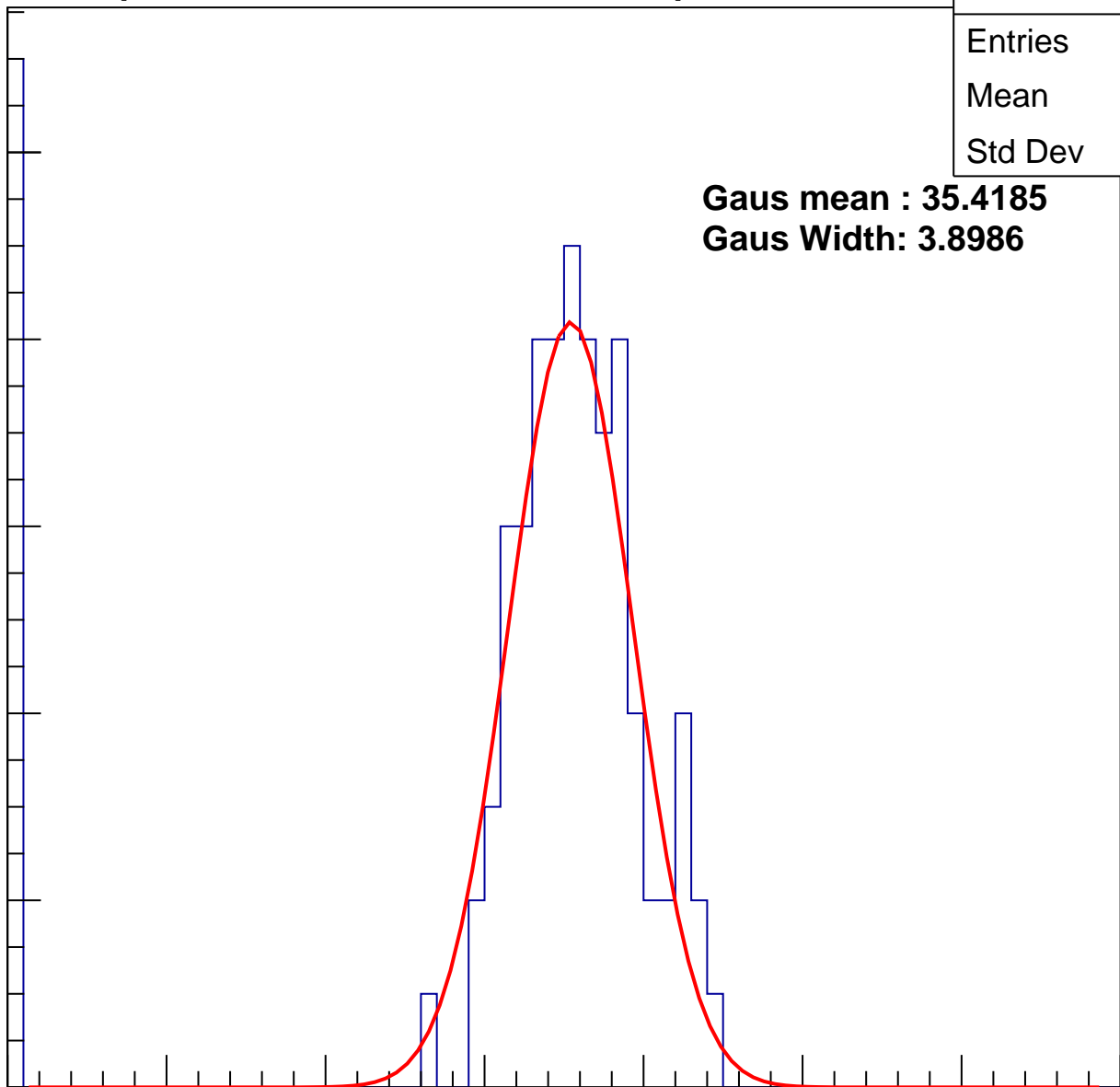
40

50

60

70

ampl



B1L103S, U24-ch103, adc2

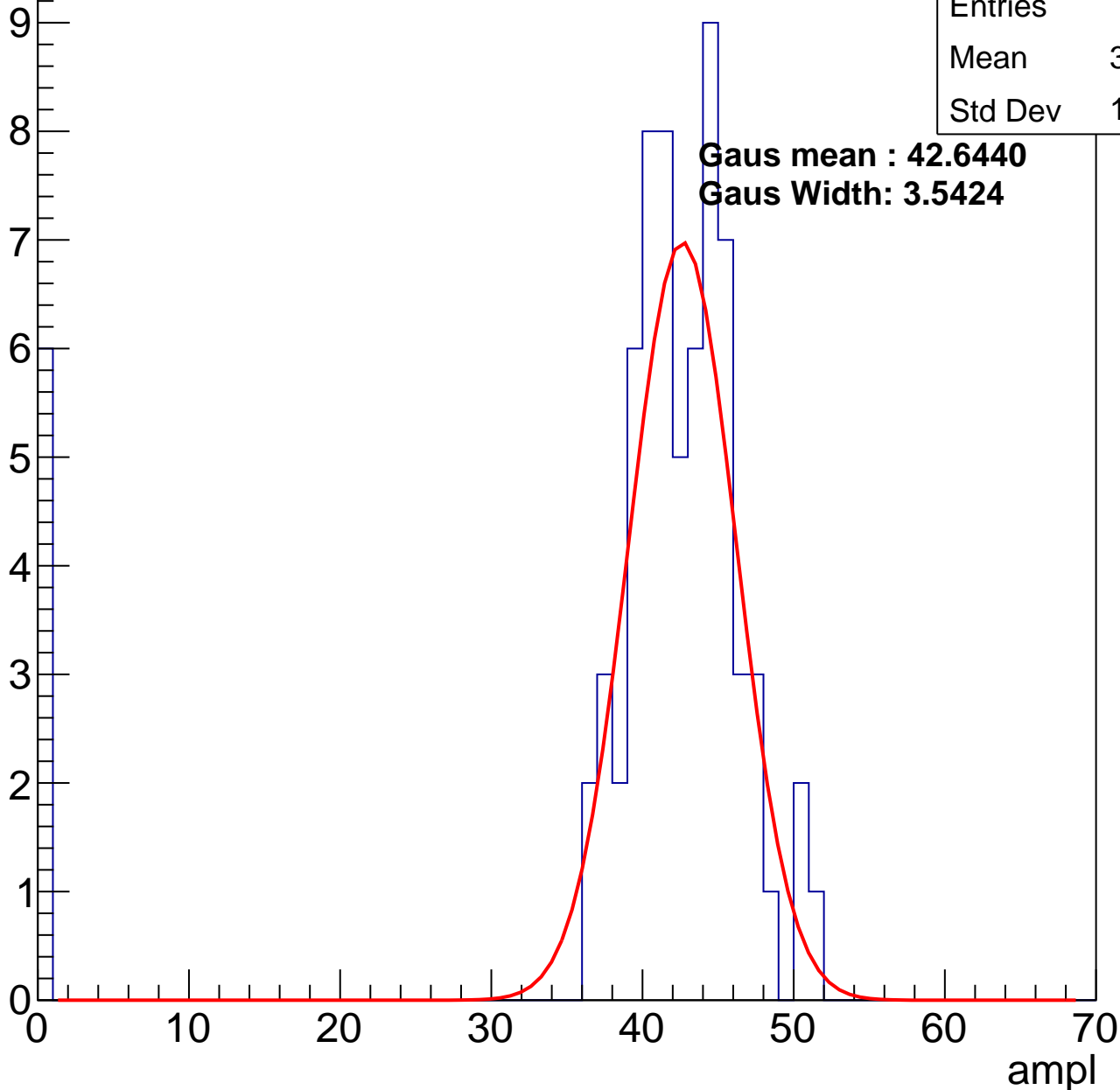
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	38.86
Std Dev	12.15

Gaus mean : 42.6440

Gaus Width: 3.5424

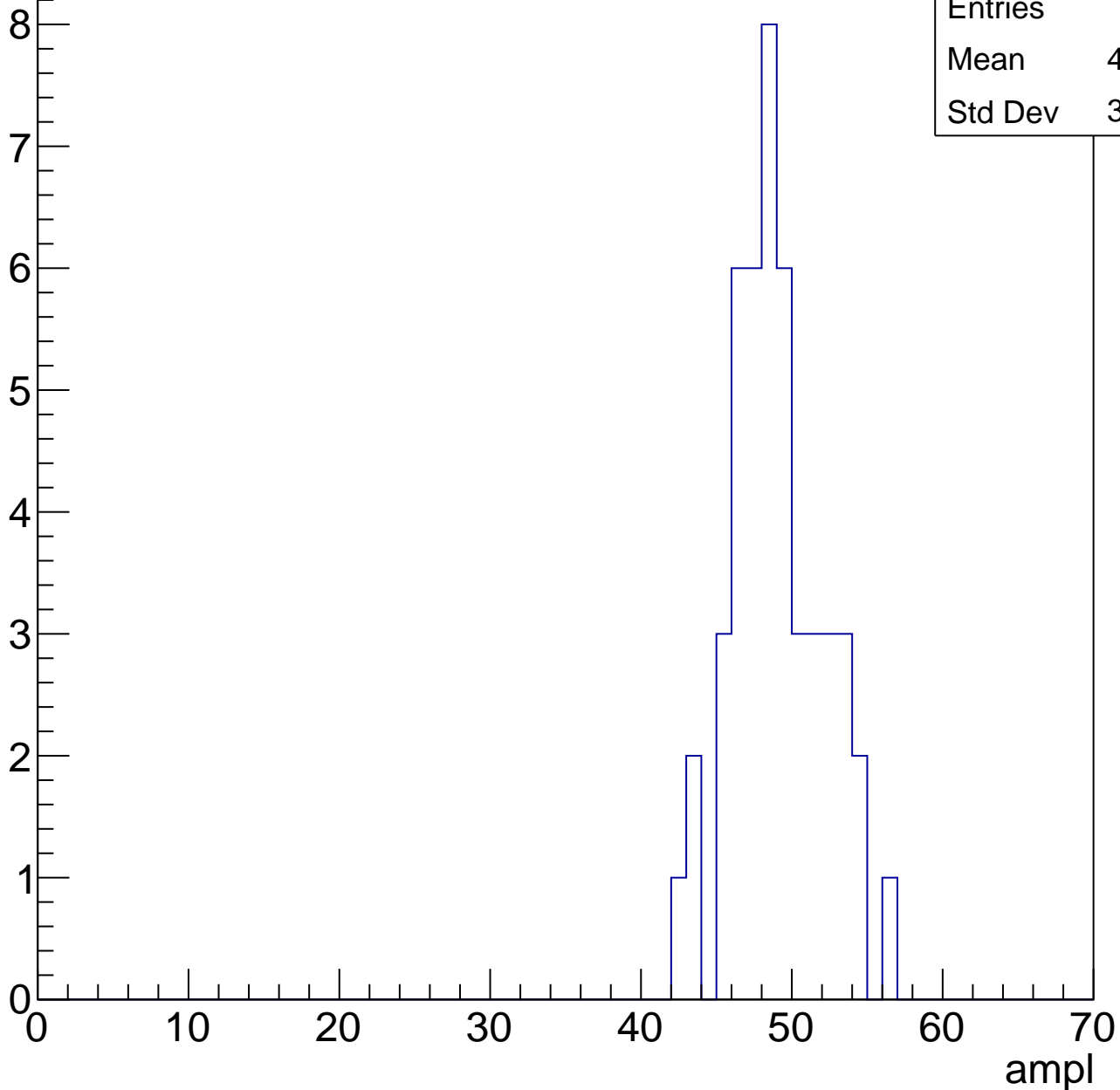


B1L103S, U24-ch103, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	48.53
Std Dev	3.038

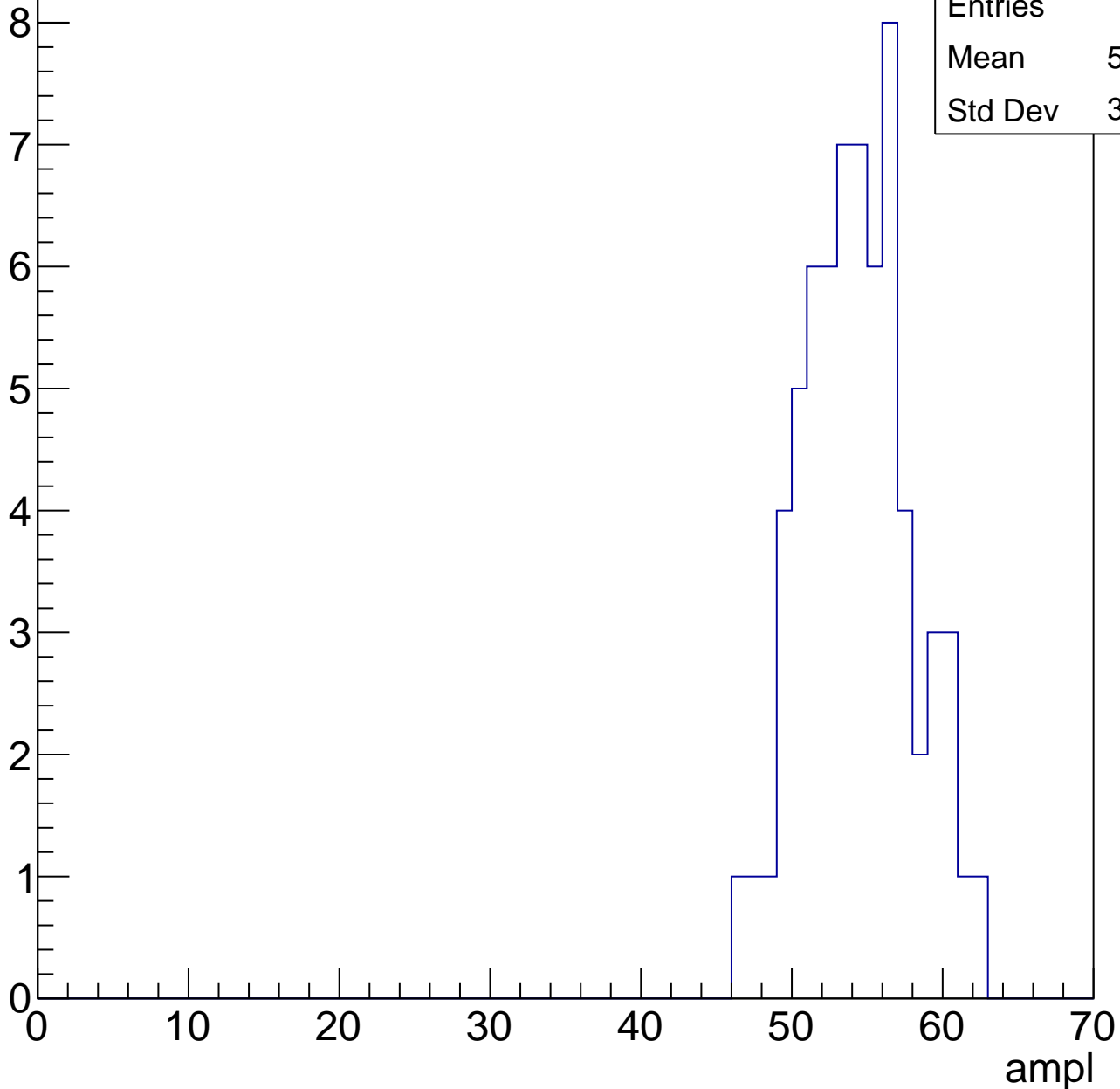


B1L103S, U24-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	53.88
Std Dev	3.514

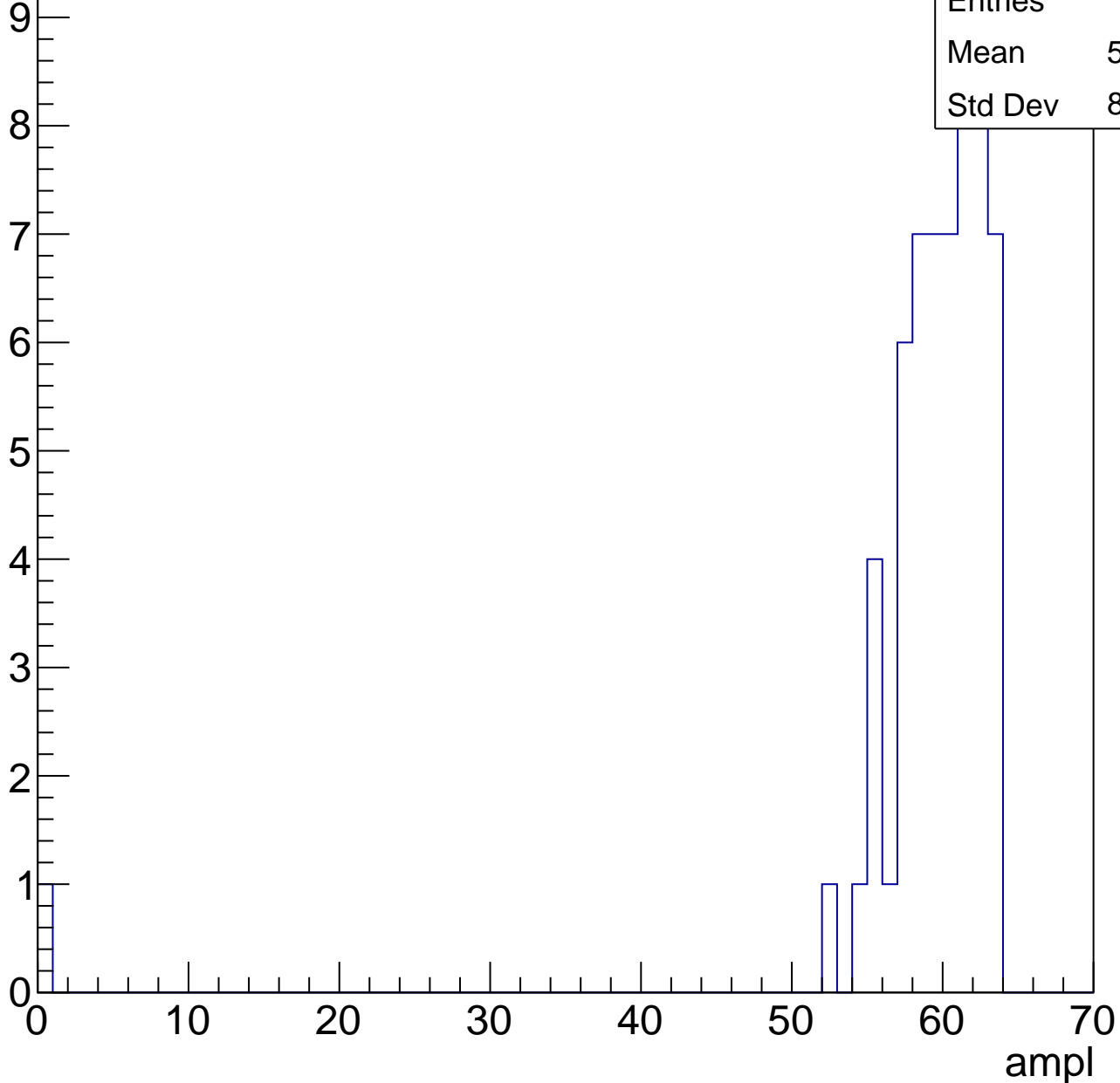


B1L103S, U24-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

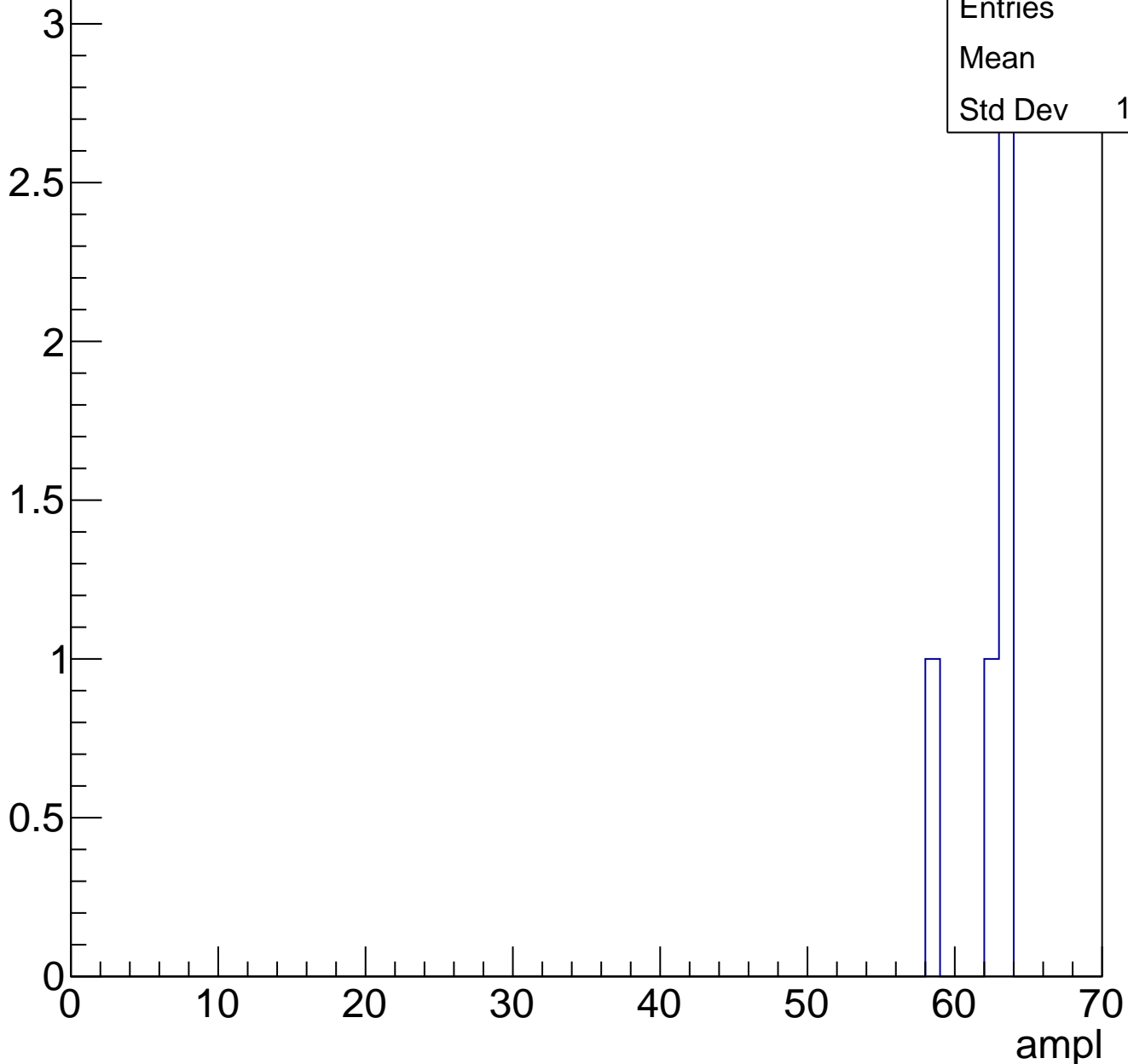
Entries	59
Mean	58.46
Std Dev	8.098



B1L103S, U24-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	5
Mean	61.8
Std Dev	1.939

B1L103S, U24-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch104, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	19.53
Std Dev	12.46

Gaus mean : 27.5445

Gaus Width: 4.6342

Entry

25

20

15

10

5

0

0

10

20

30

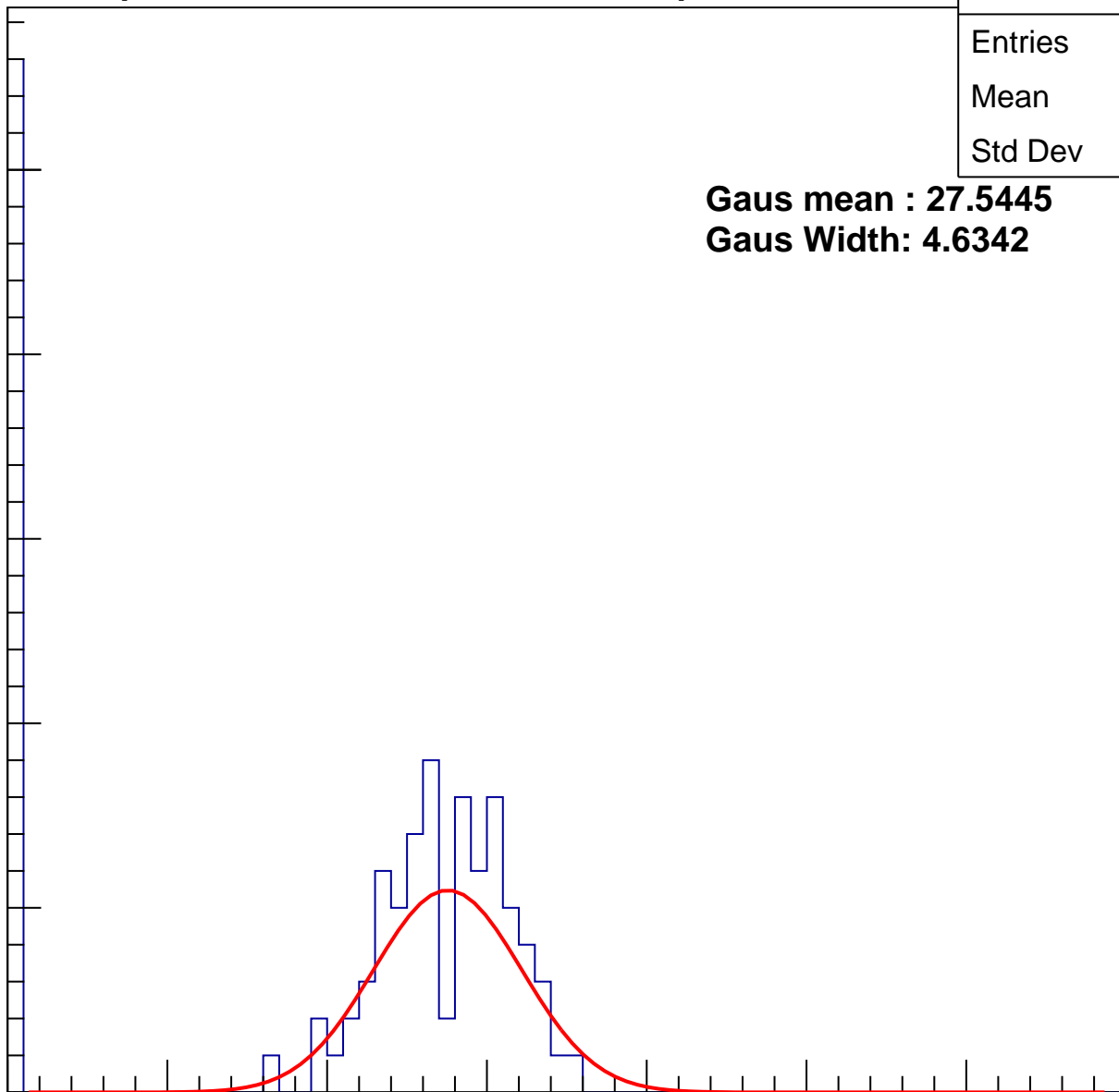
40

50

60

70

ampl



B1L103S, U24-ch104, adc1

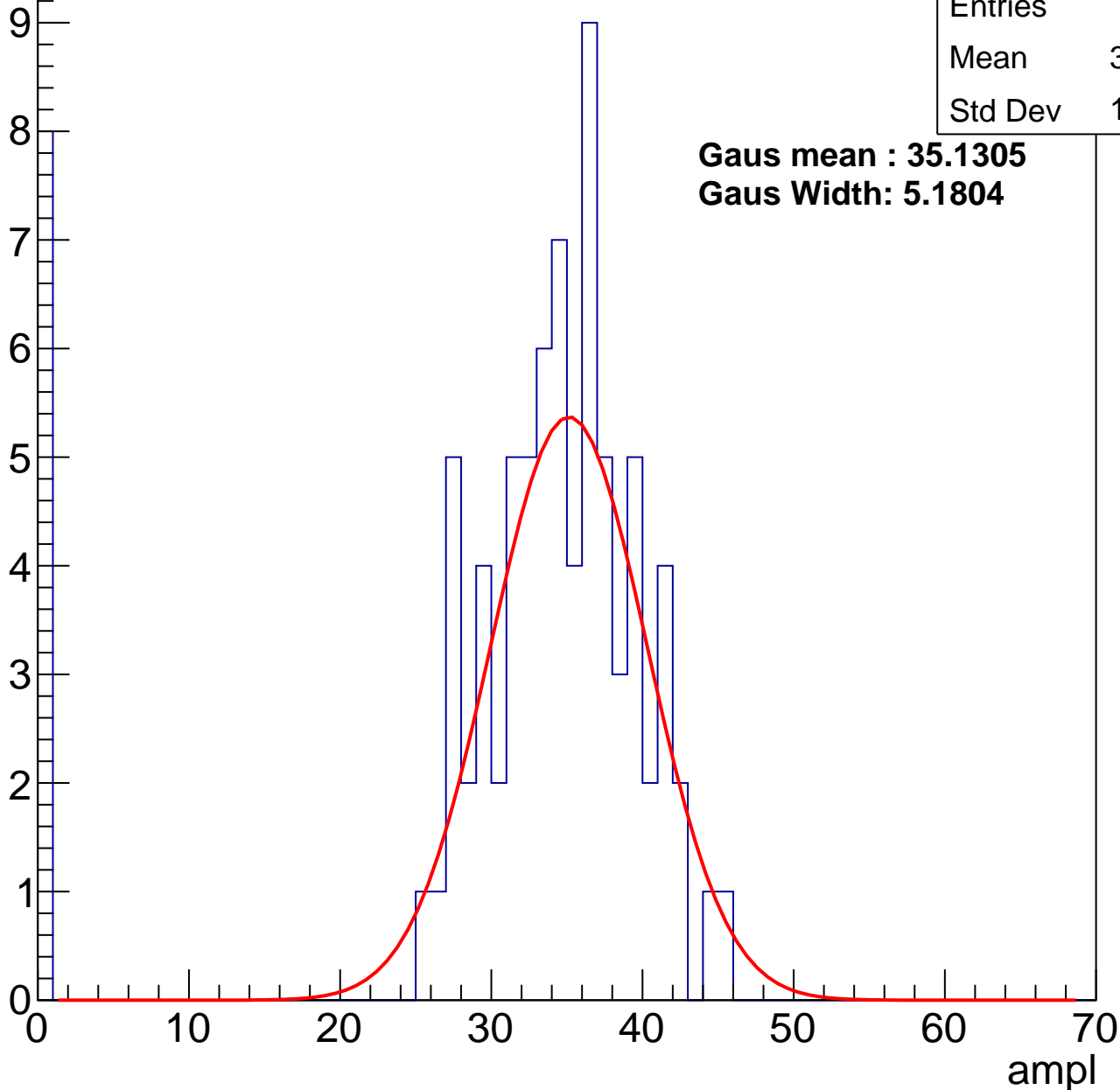
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	31.02
Std Dev	11.08

Gaus mean : 35.1305

Gaus Width: 5.1804



B1L103S, U24-ch104, adc2

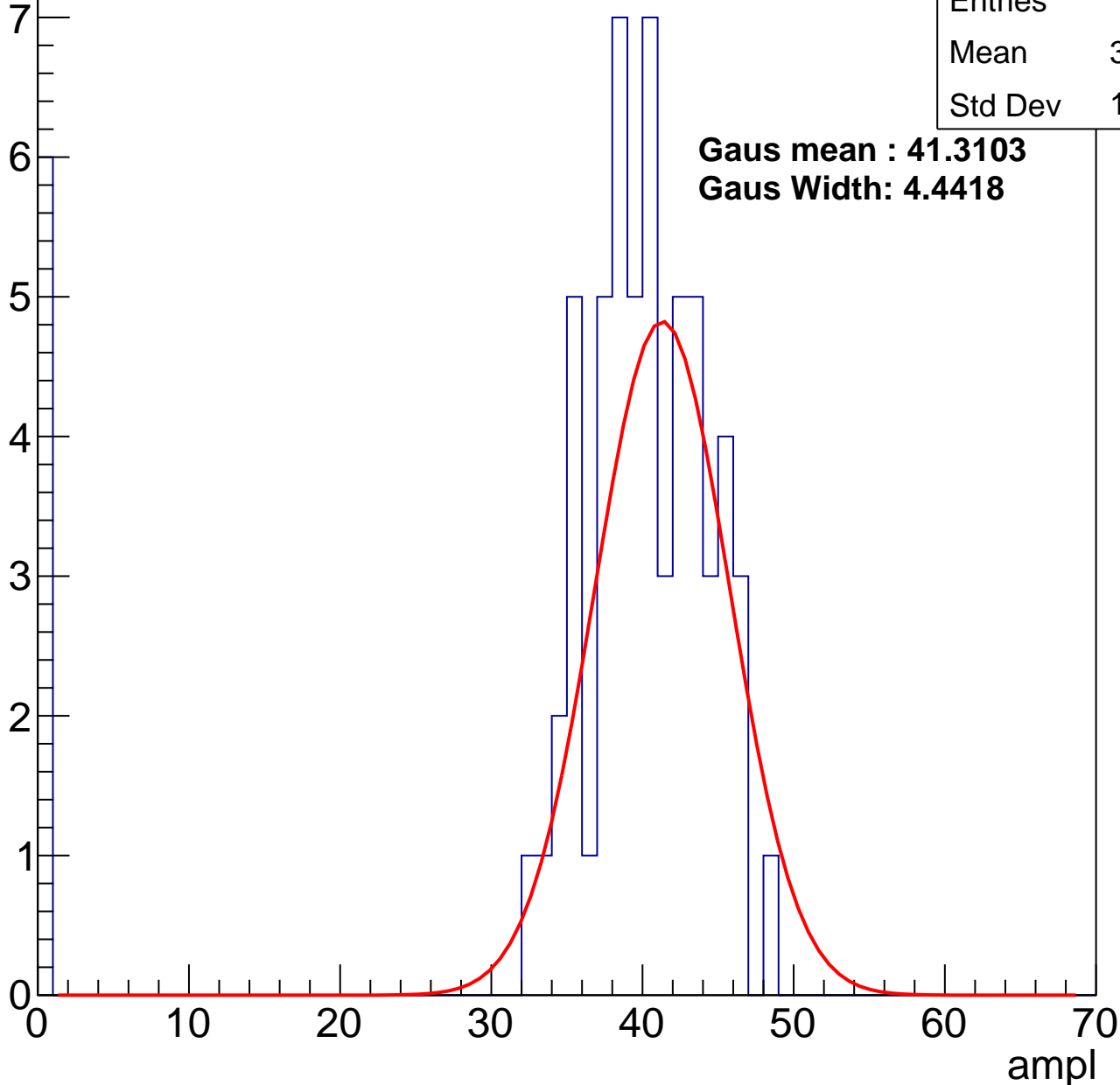
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.19
Std Dev	12.16

Gaus mean : 41.3103

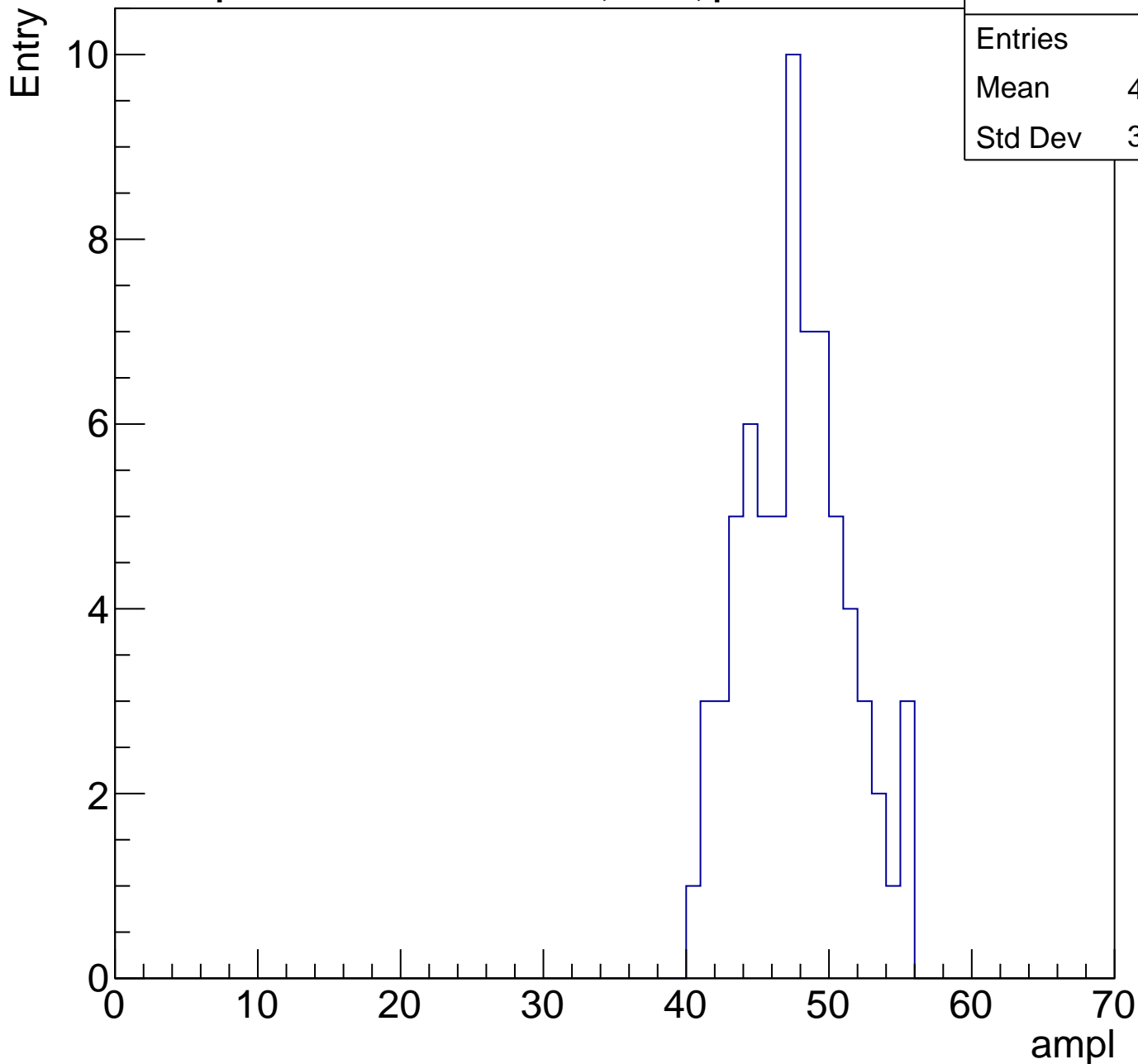
Gaus Width: 4.4418



B1L103S, U24-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	47.24
Std Dev	3.623

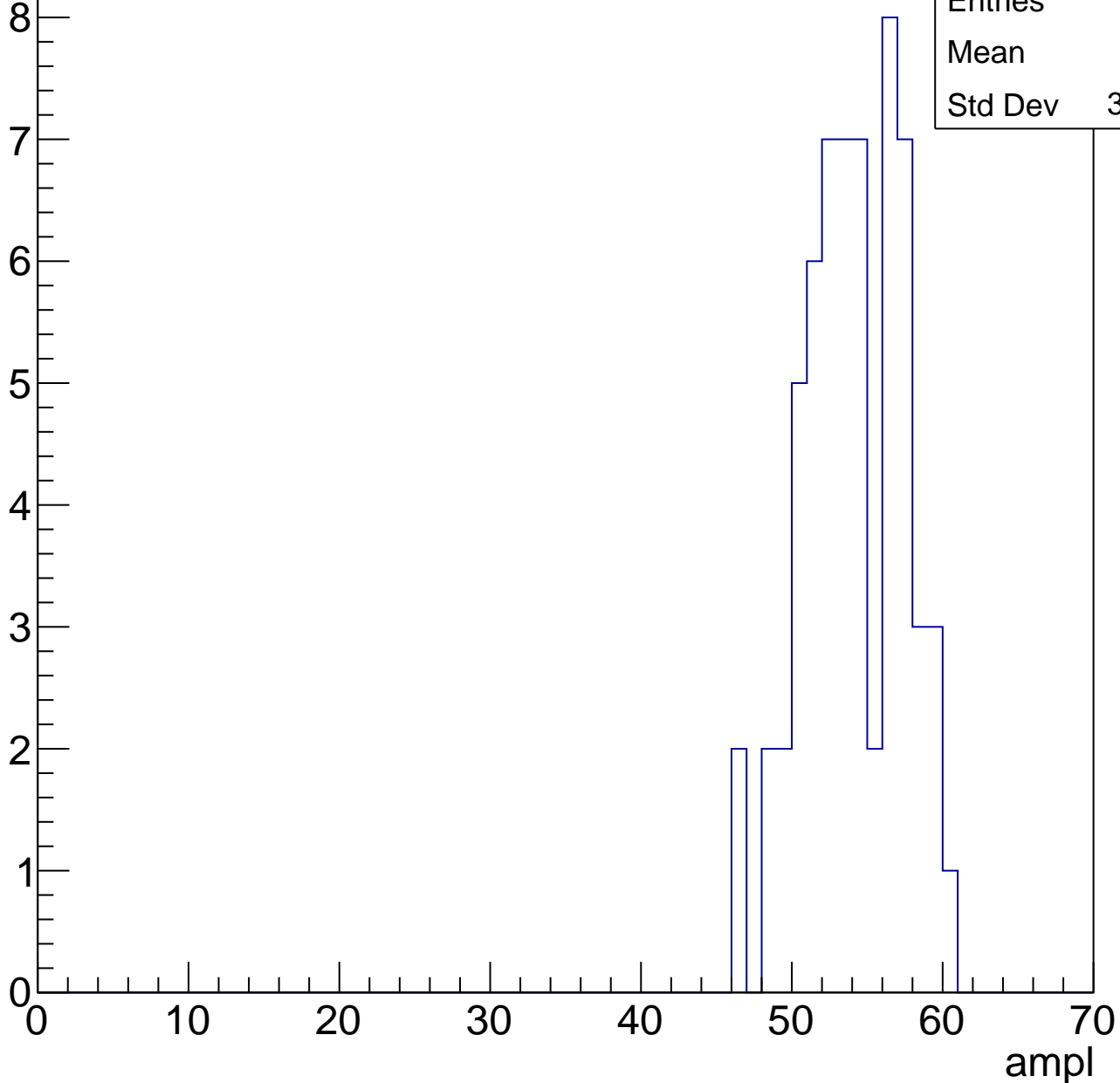


B1L103S, U24-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	53.6
Std Dev	3.275

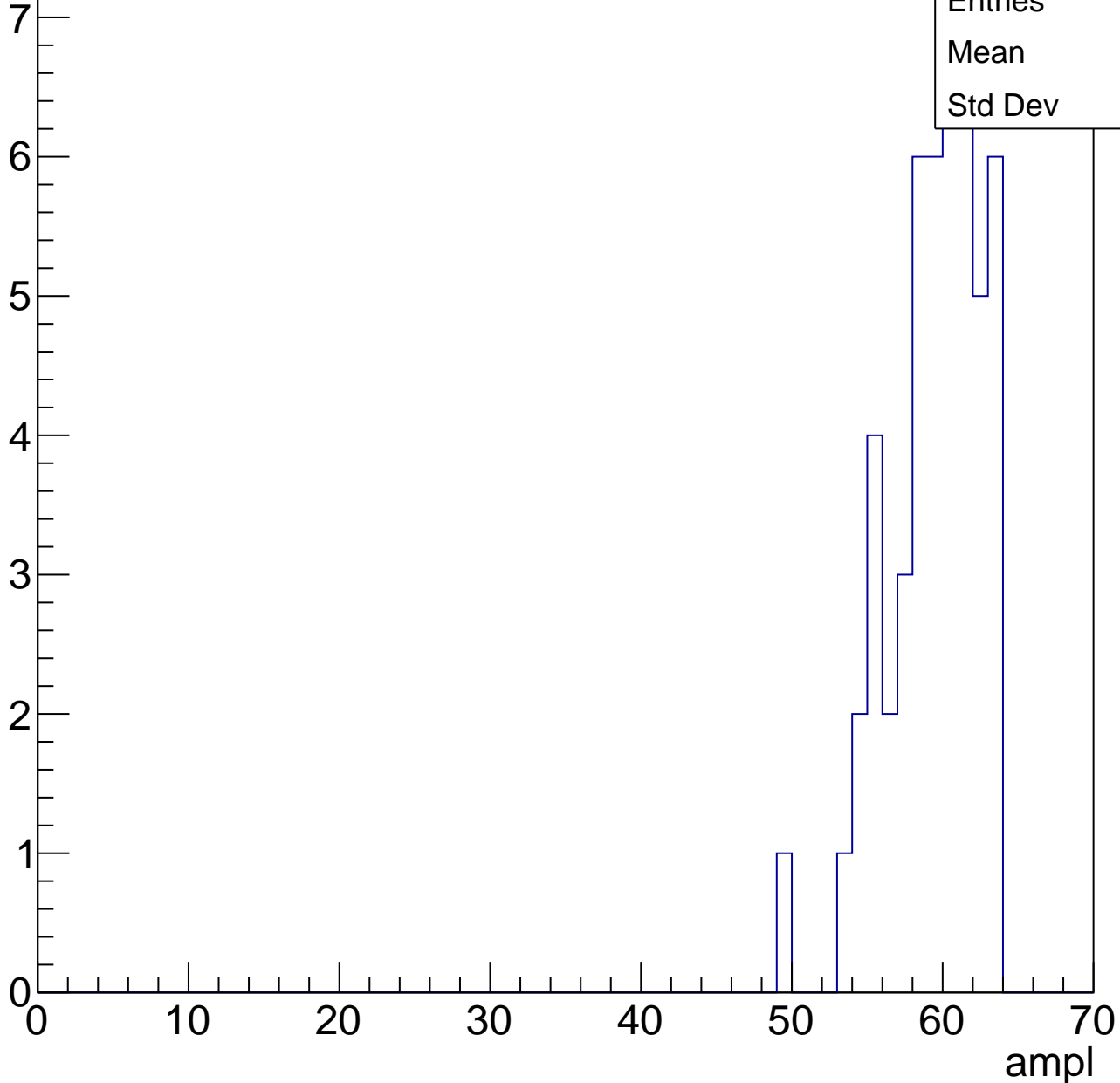


B1L103S, U24-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	59
Std Dev	3.04



B1L103S, U24-ch104, adc6

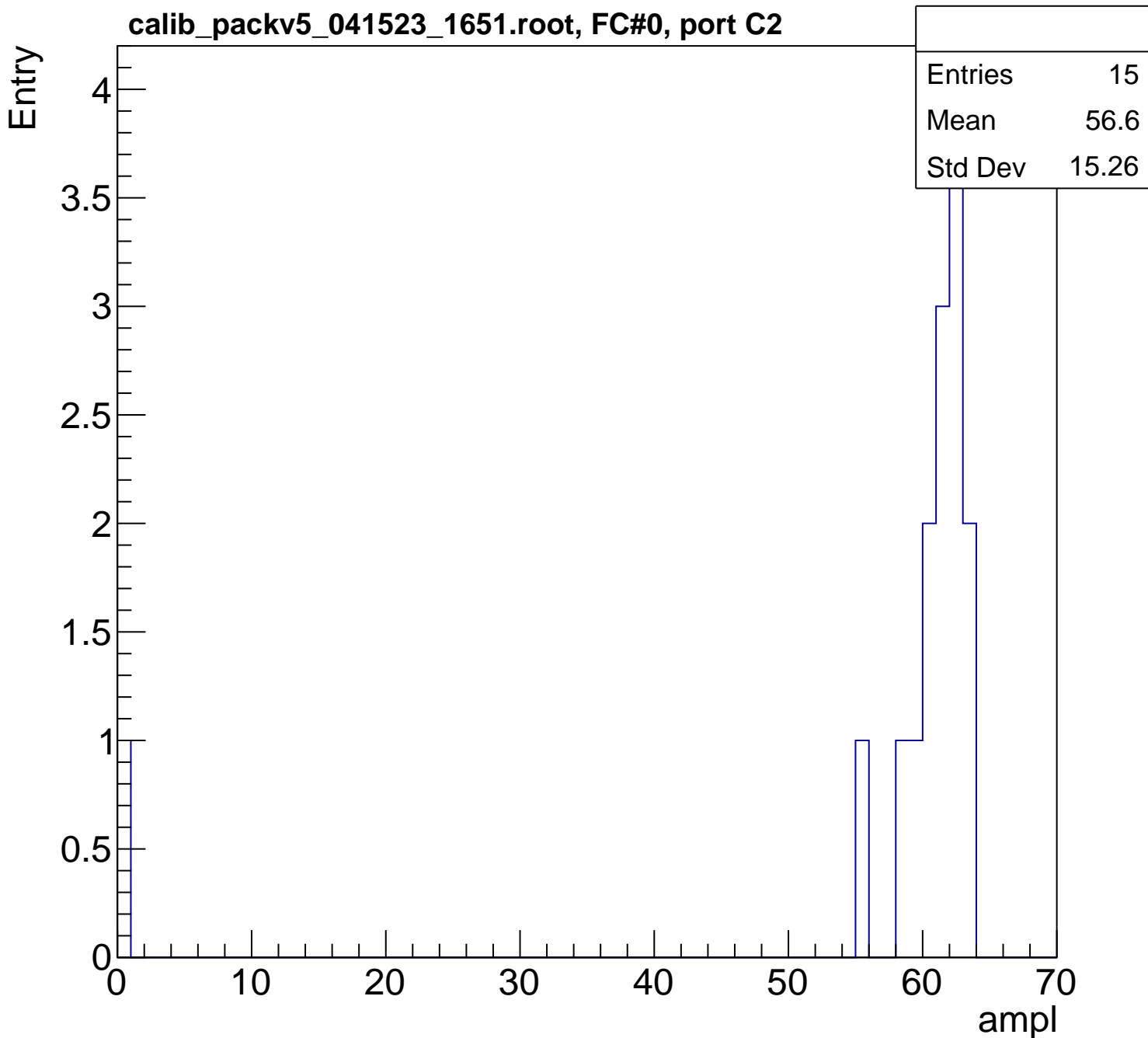
calib_packv5_041523_1651.root, FC#0, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	15
Mean	56.6
Std Dev	15.26

ampl



B1L103S, U24-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51

Entry



B1L103S, U24-ch105, adc0

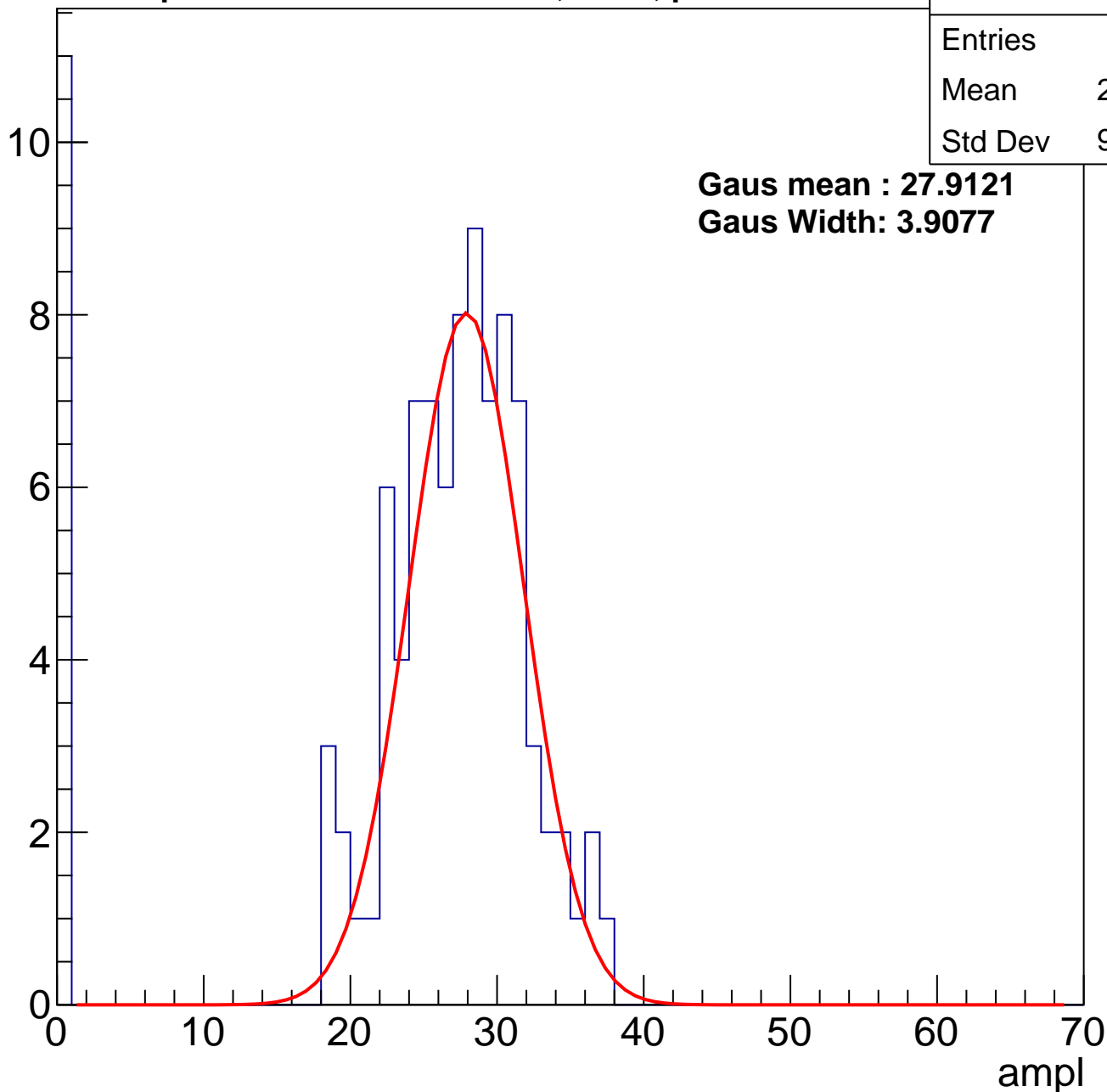
calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	24.06
Std Dev	9.439

Gaus mean : 27.9121

Gaus Width: 3.9077

Entry



B1L103S, U24-ch105, adc1

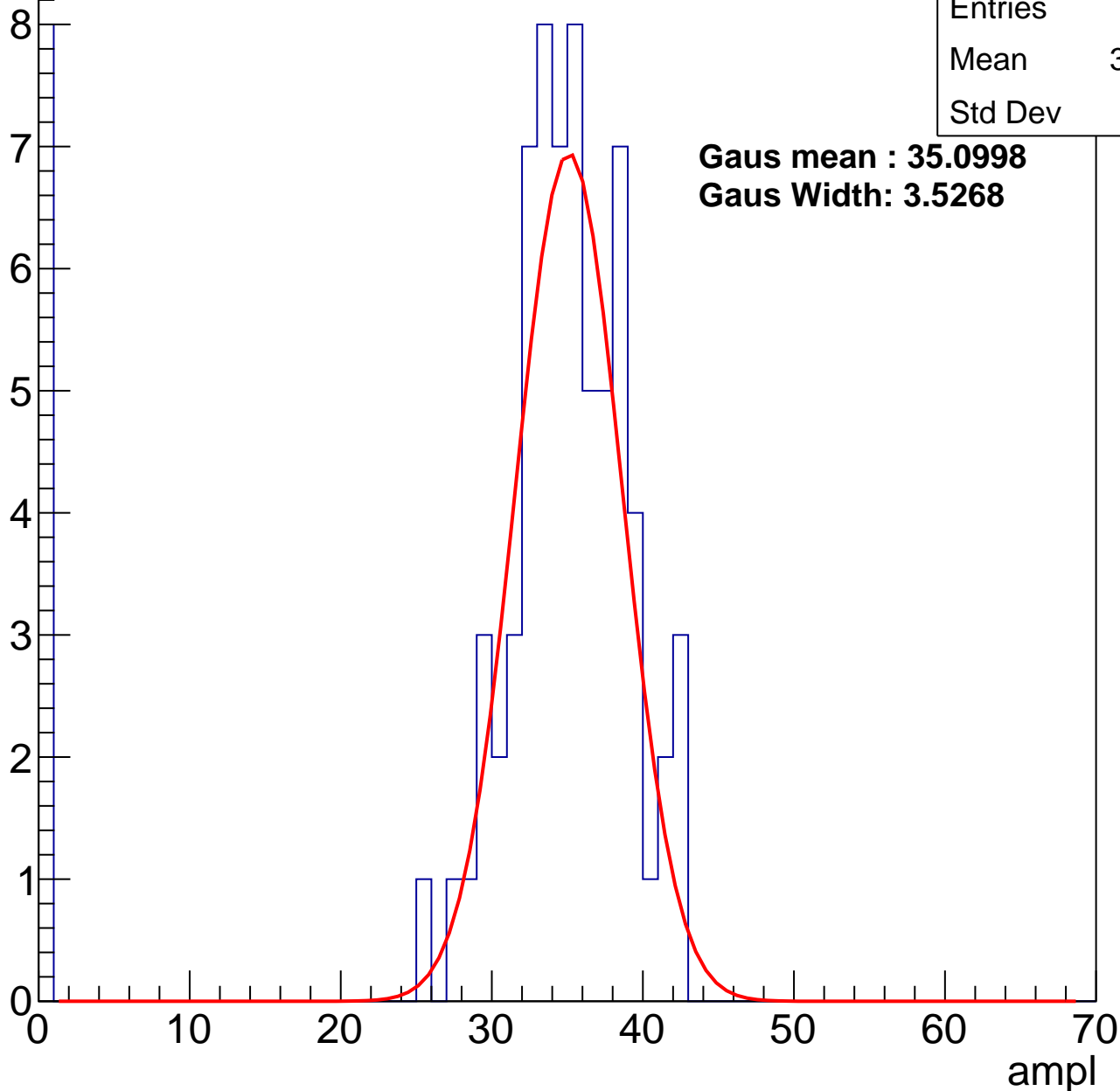
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	31.07
Std Dev	11.2

Gaus mean : 35.0998

Gaus Width: 3.5268



B1L103S, U24-ch105, adc2

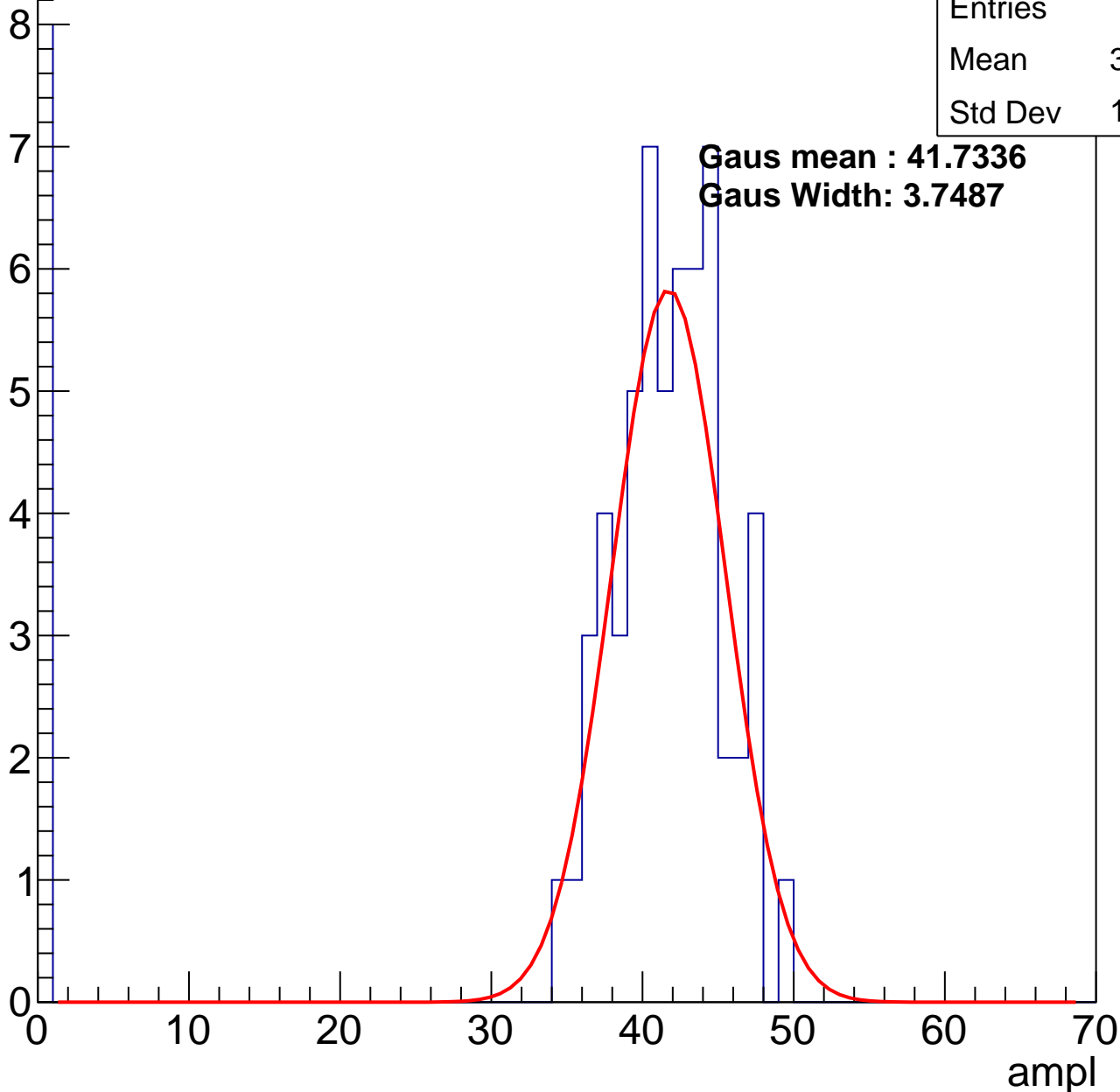
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	36.25
Std Dev	13.94

Gaus mean : 41.7336

Gaus Width: 3.7487

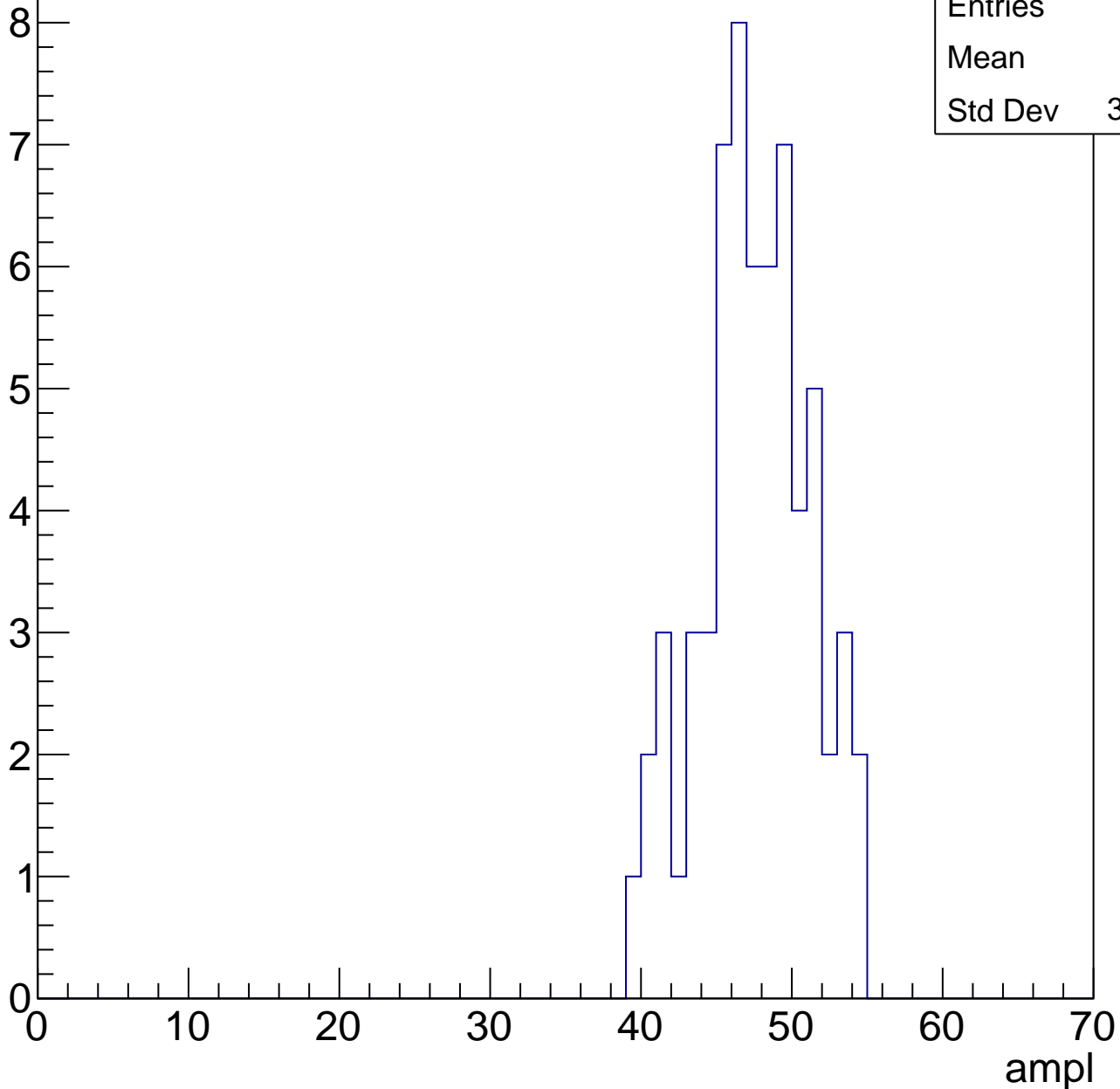


B1L103S, U24-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.1
Std Dev	3.589

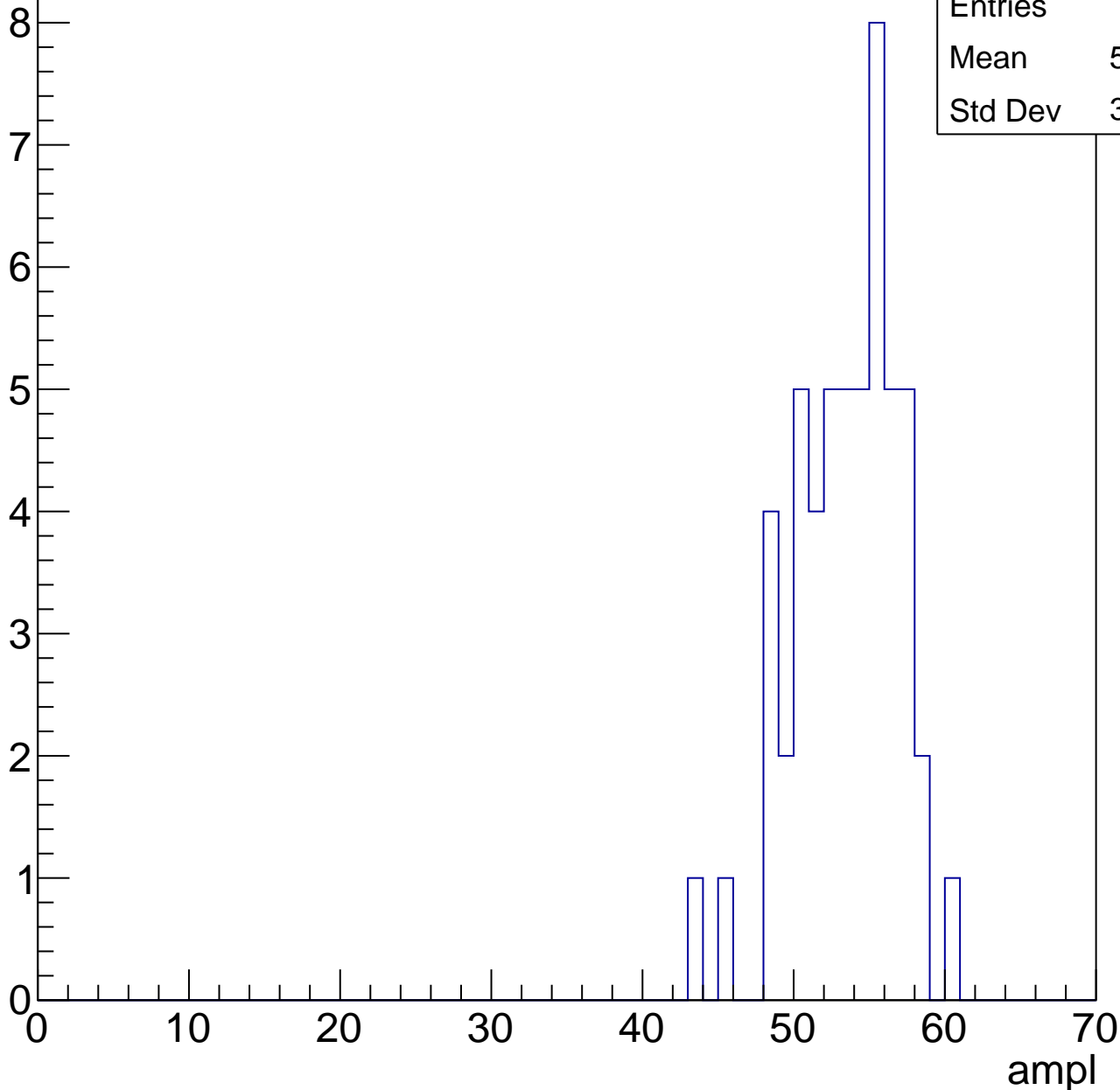


B1L103S, U24-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	52.98
Std Dev	3.428

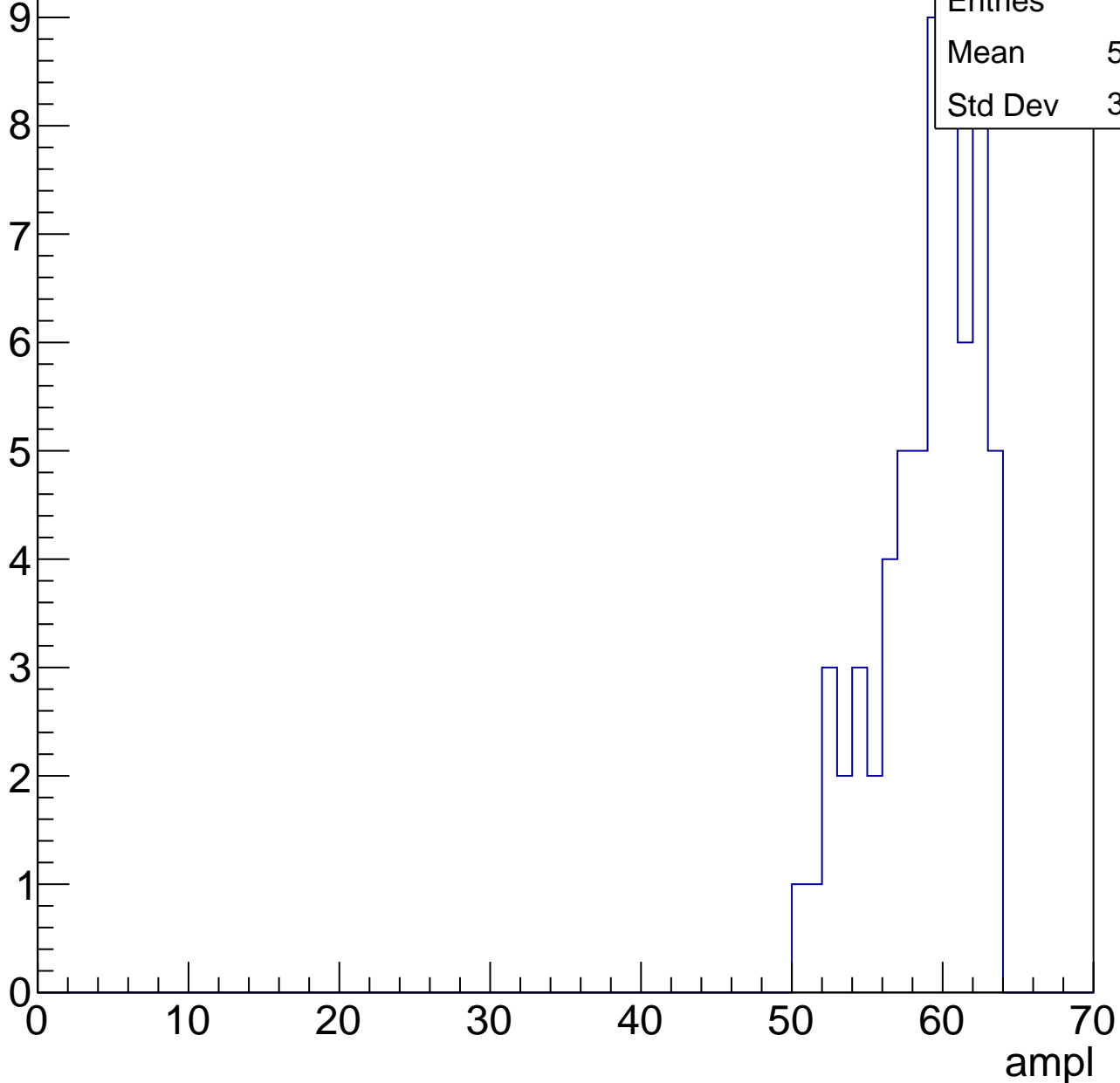


B1L103S, U24-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

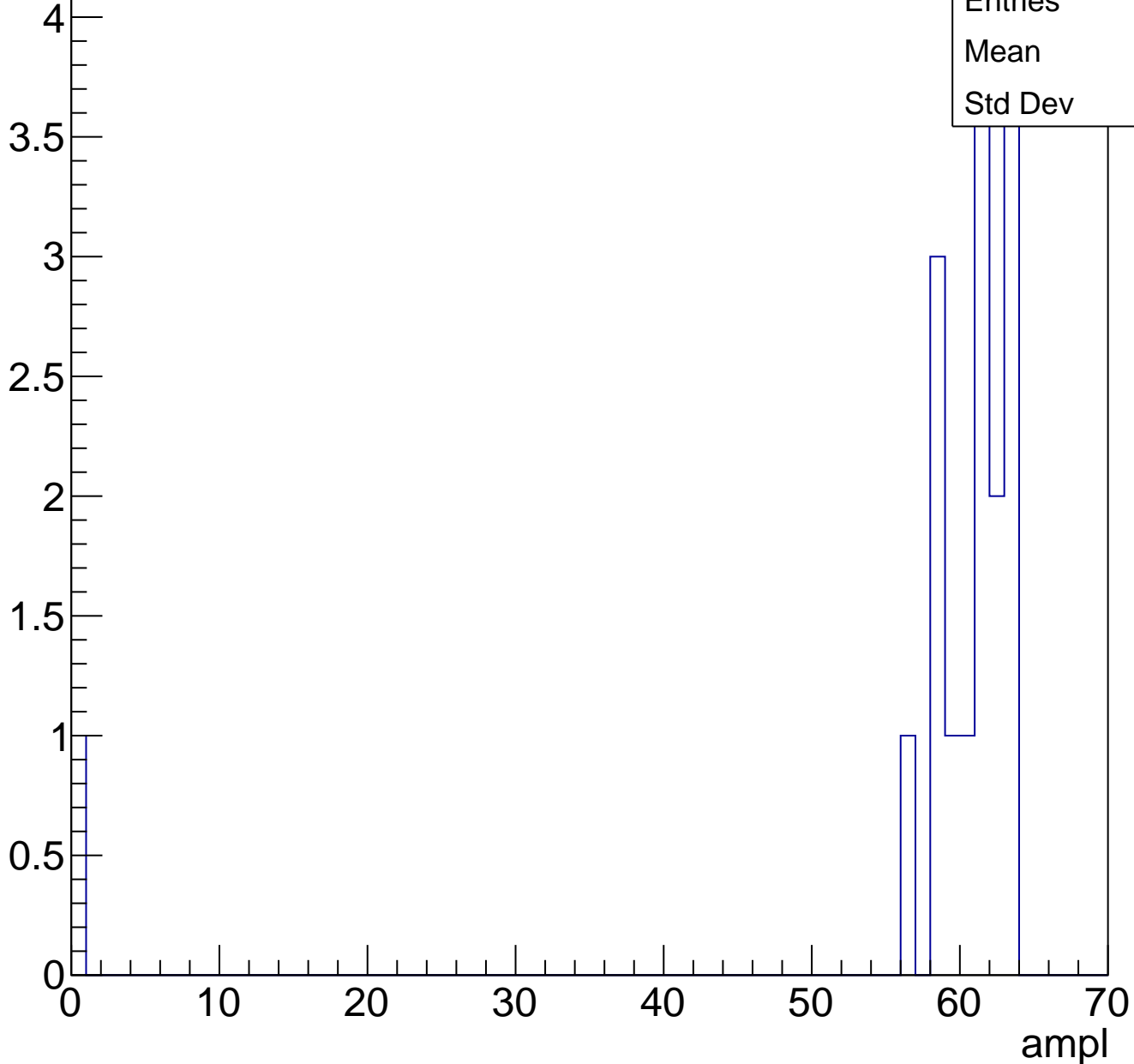
Entries	62
Mean	58.42
Std Dev	3.348



B1L103S, U24-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U24-ch106, adc0

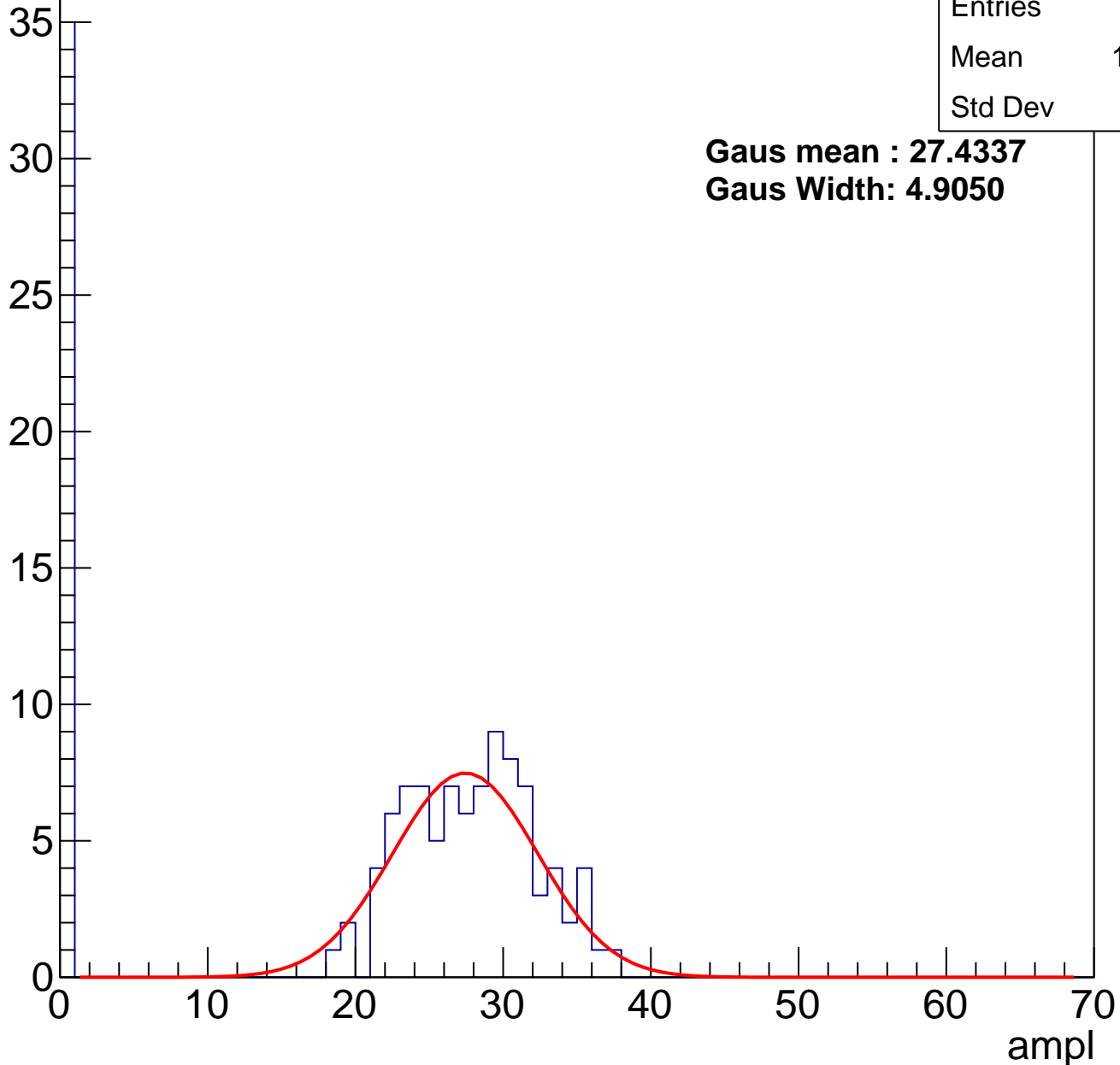
calib_packv5_041523_1651.root, FC#0, port C2

Entries	126
Mean	19.79
Std Dev	12.8

Gaus mean : 27.4337

Gaus Width: 4.9050

Entry



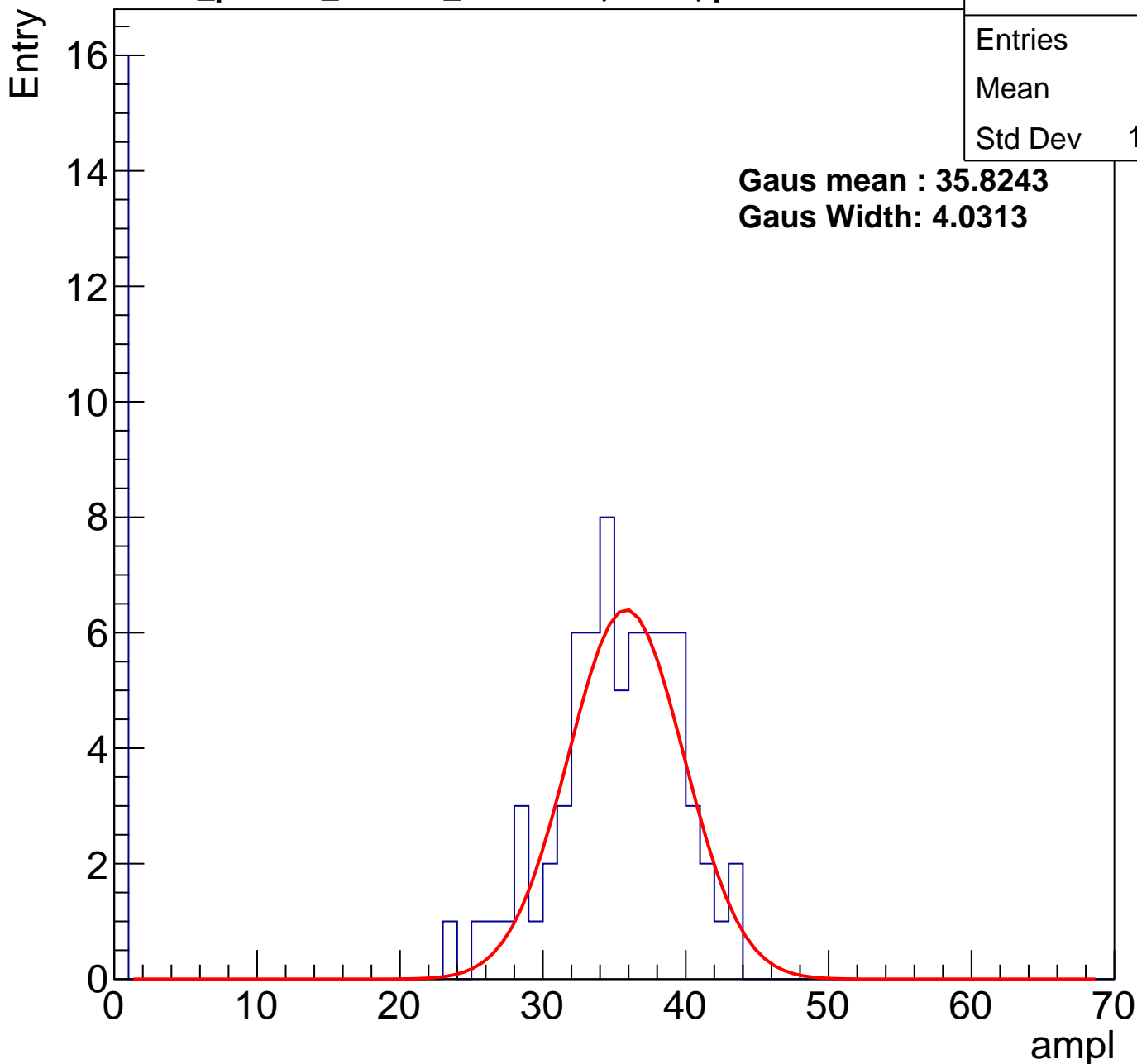
B1L103S, U24-ch106, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	28.3
Std Dev	14.06

Gaus mean : 35.8243

Gaus Width: 4.0313



B1L103S, U24-ch106, adc2

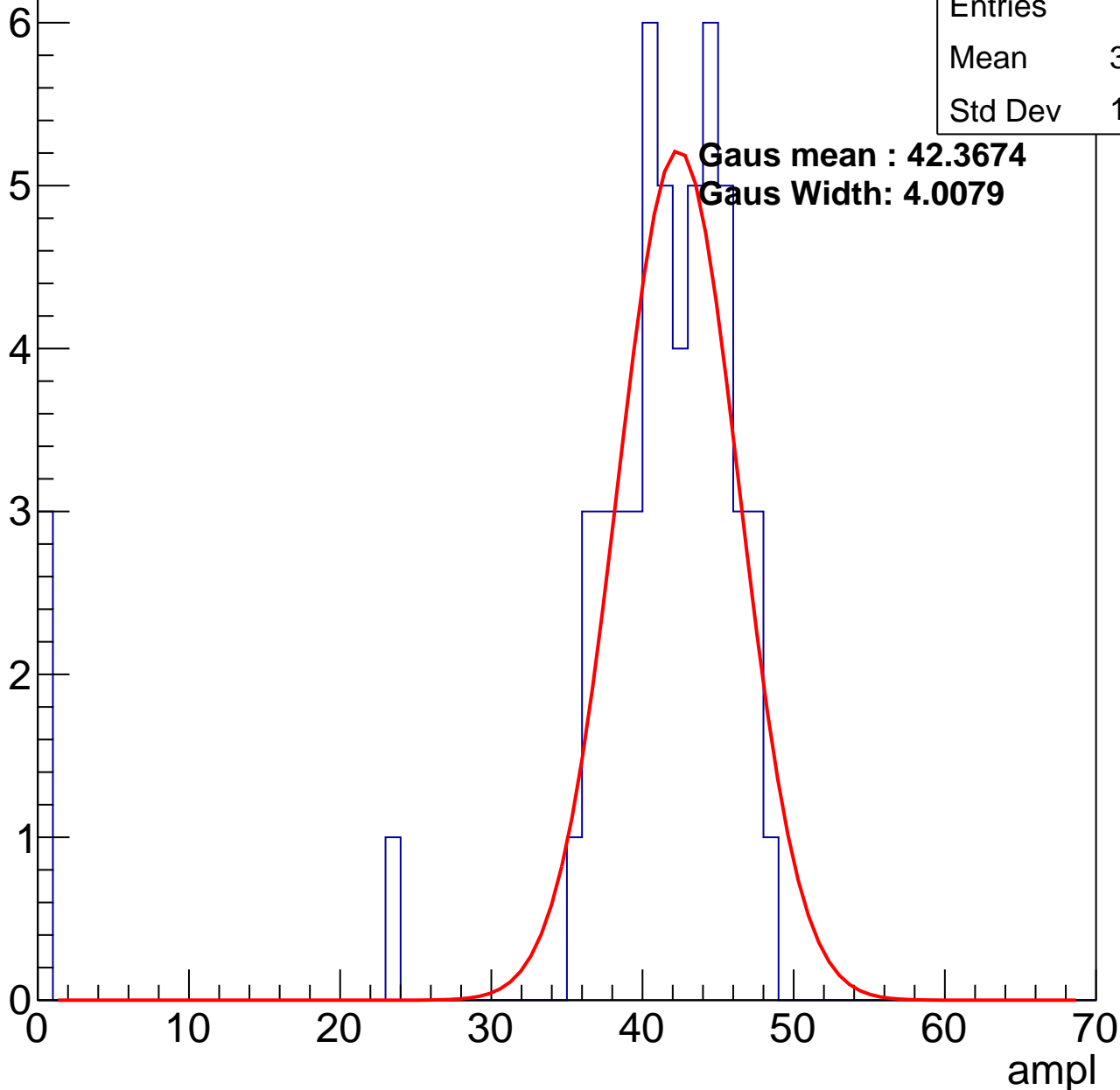
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	39.13
Std Dev	10.24

Gaus mean : 42.3674

Gaus Width: 4.0079

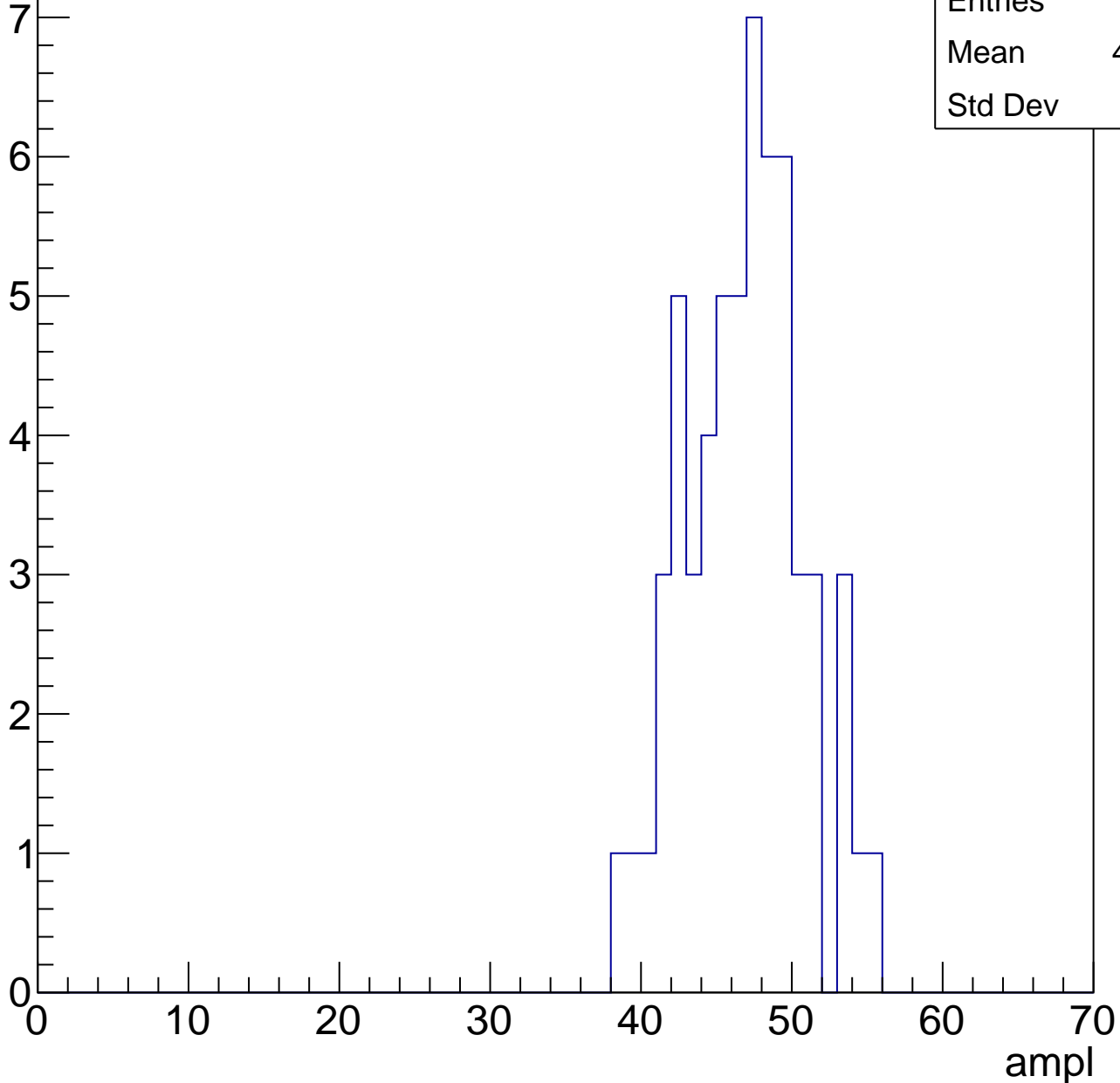


B1L103S, U24-ch106, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	46.41
Std Dev	3.81

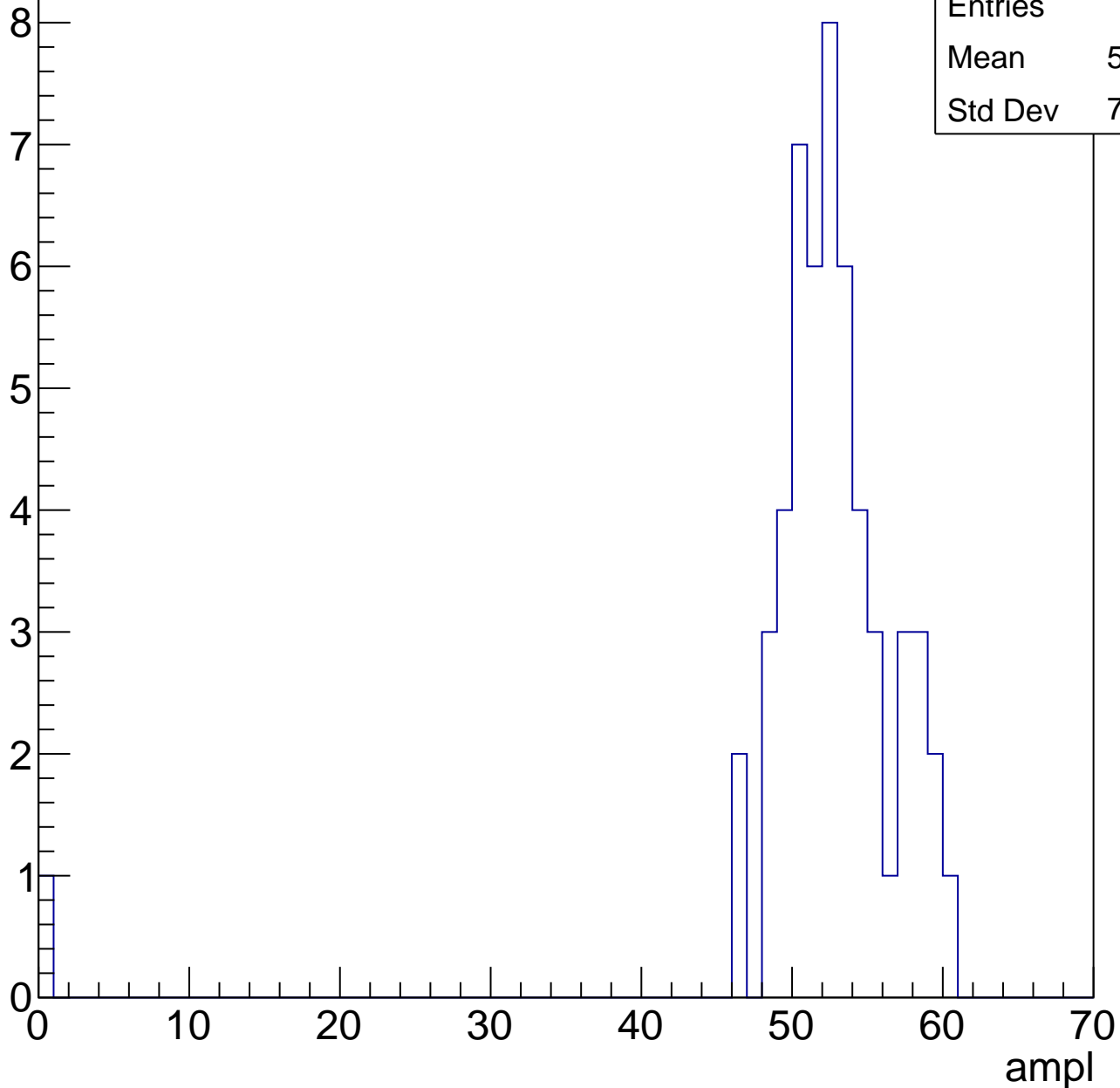


B1L103S, U24-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	51.52
Std Dev	7.807

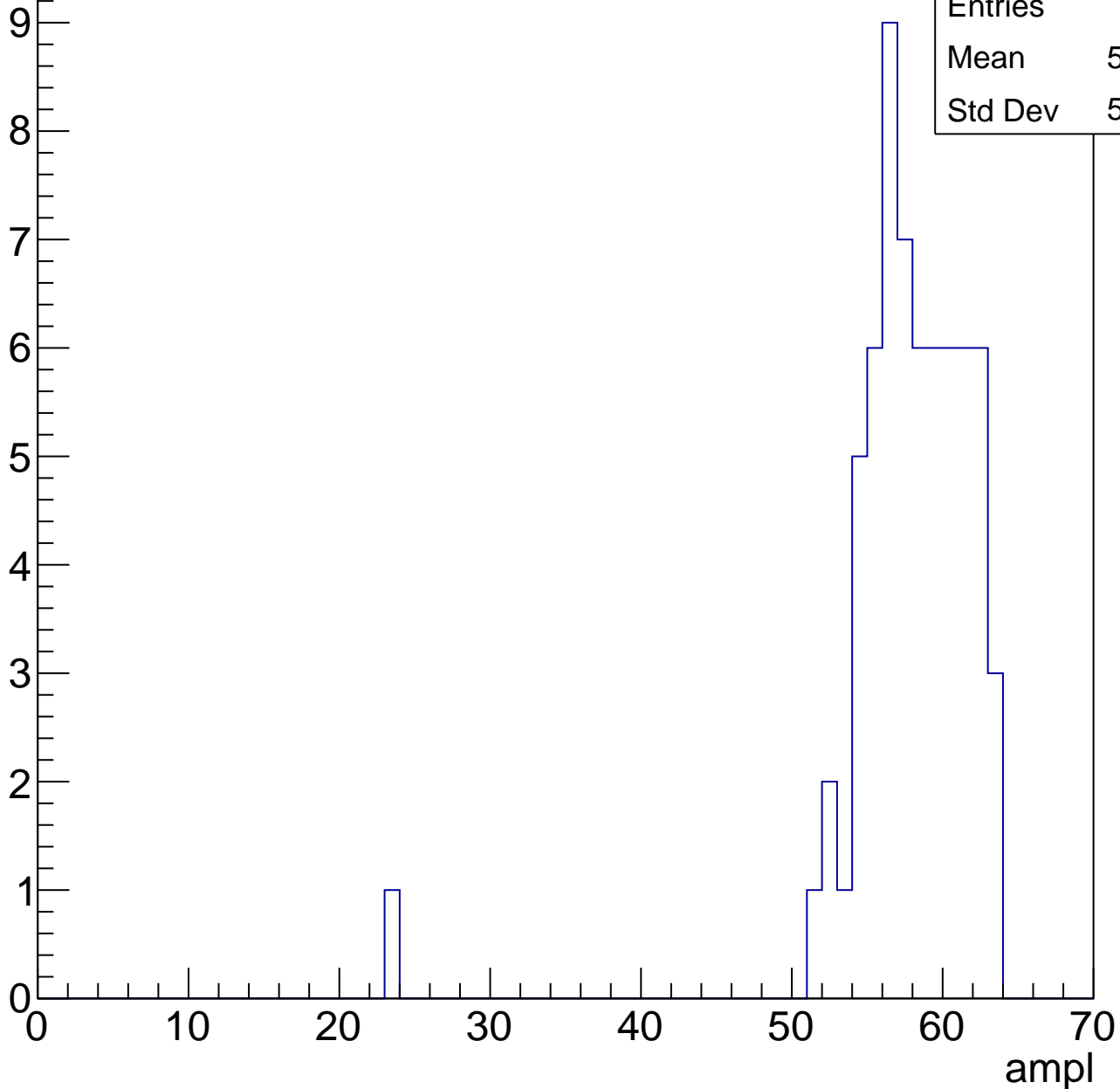


B1L103S, U24-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	57.28
Std Dev	5.217

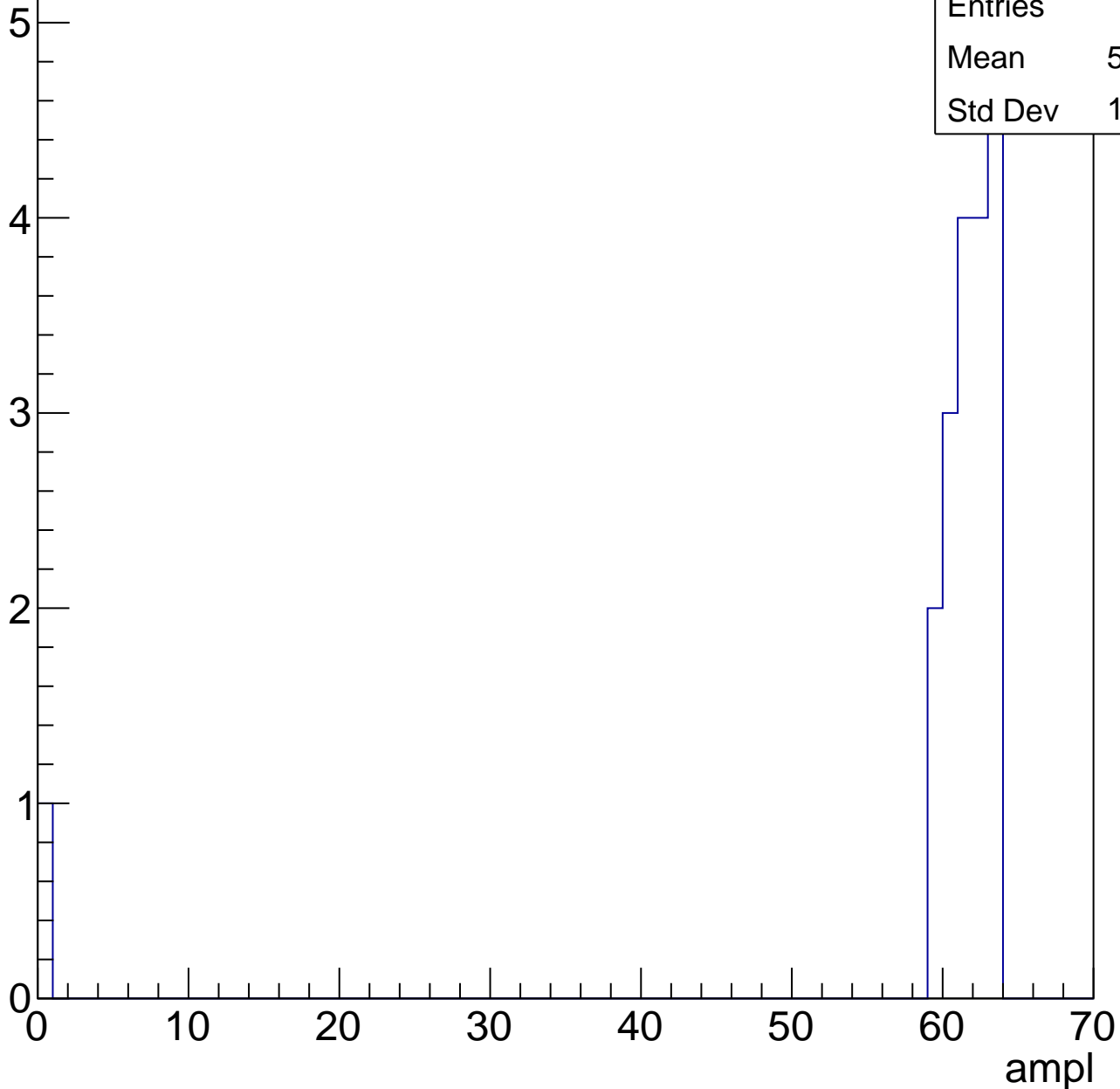


B1L103S, U24-ch106, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	58.16
Std Dev	13.77

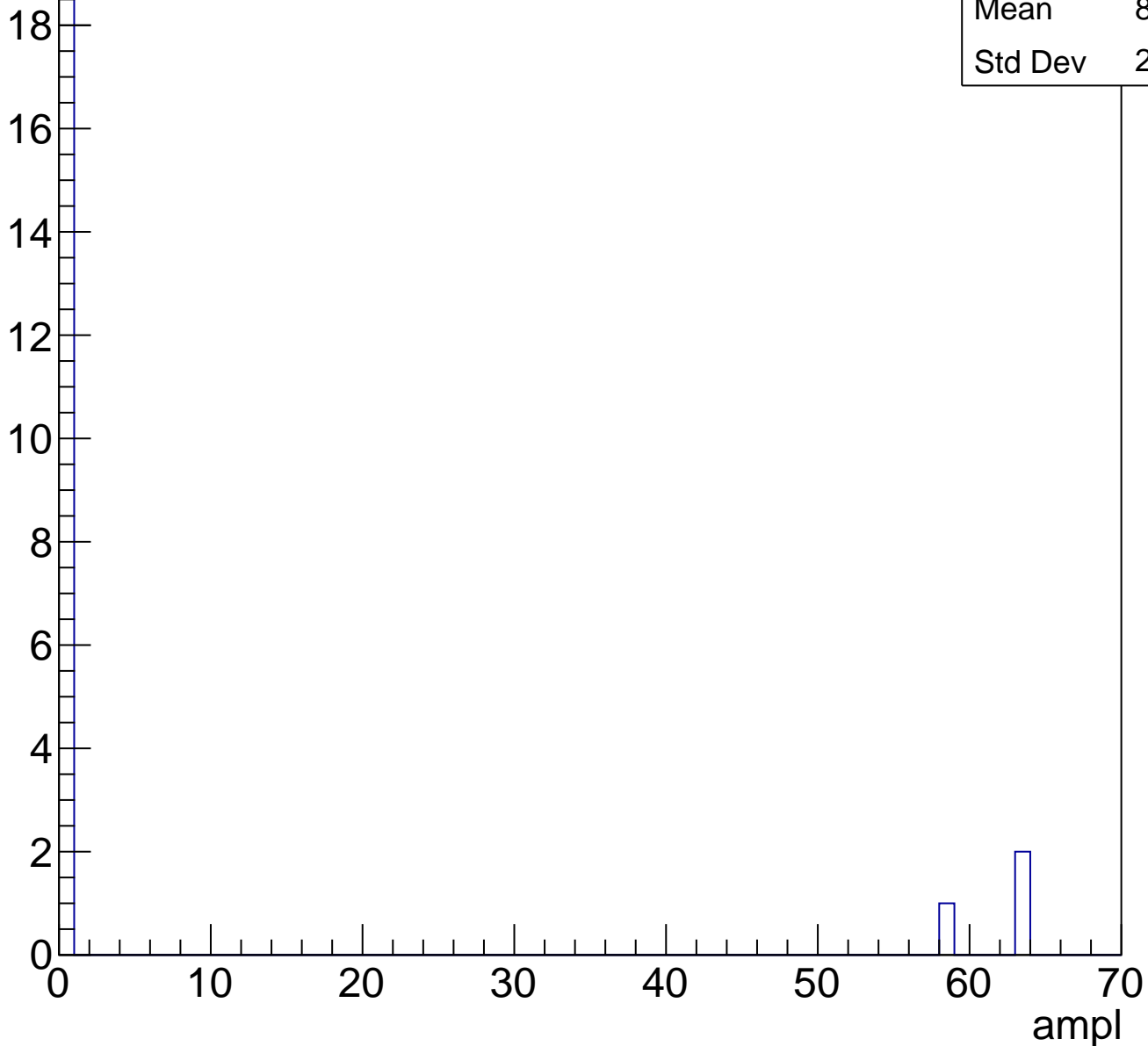


B1L103S, U24-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.364
Std Dev	21.07

Entry



B1L103S, U24-ch107, adc0

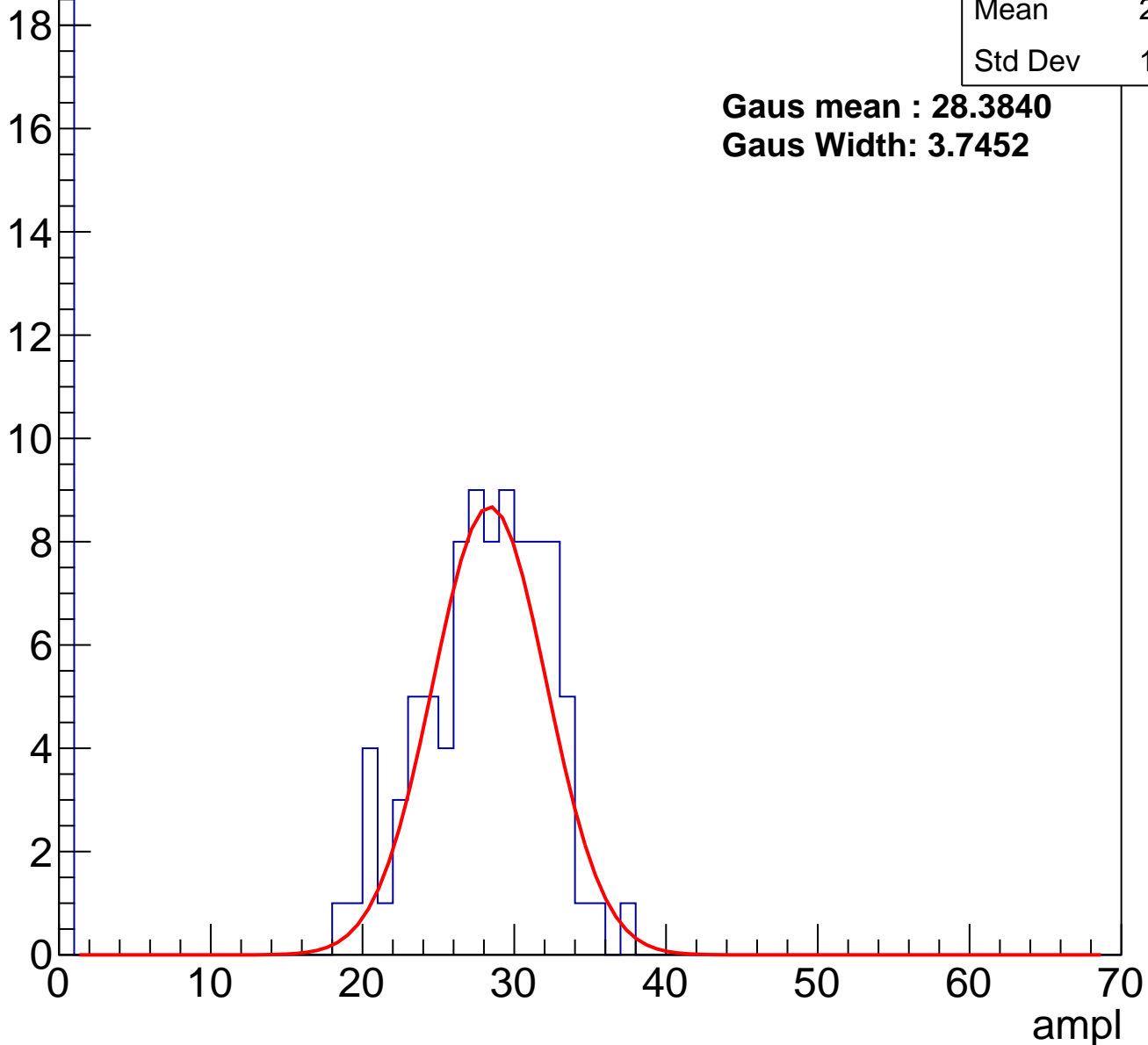
calib_packv5_041523_1651.root, FC#0, port C2

Entries	109
Mean	22.84
Std Dev	11.09

Gaus mean : 28.3840

Gaus Width: 3.7452

Entry



B1L103S, U24-ch107, adc1

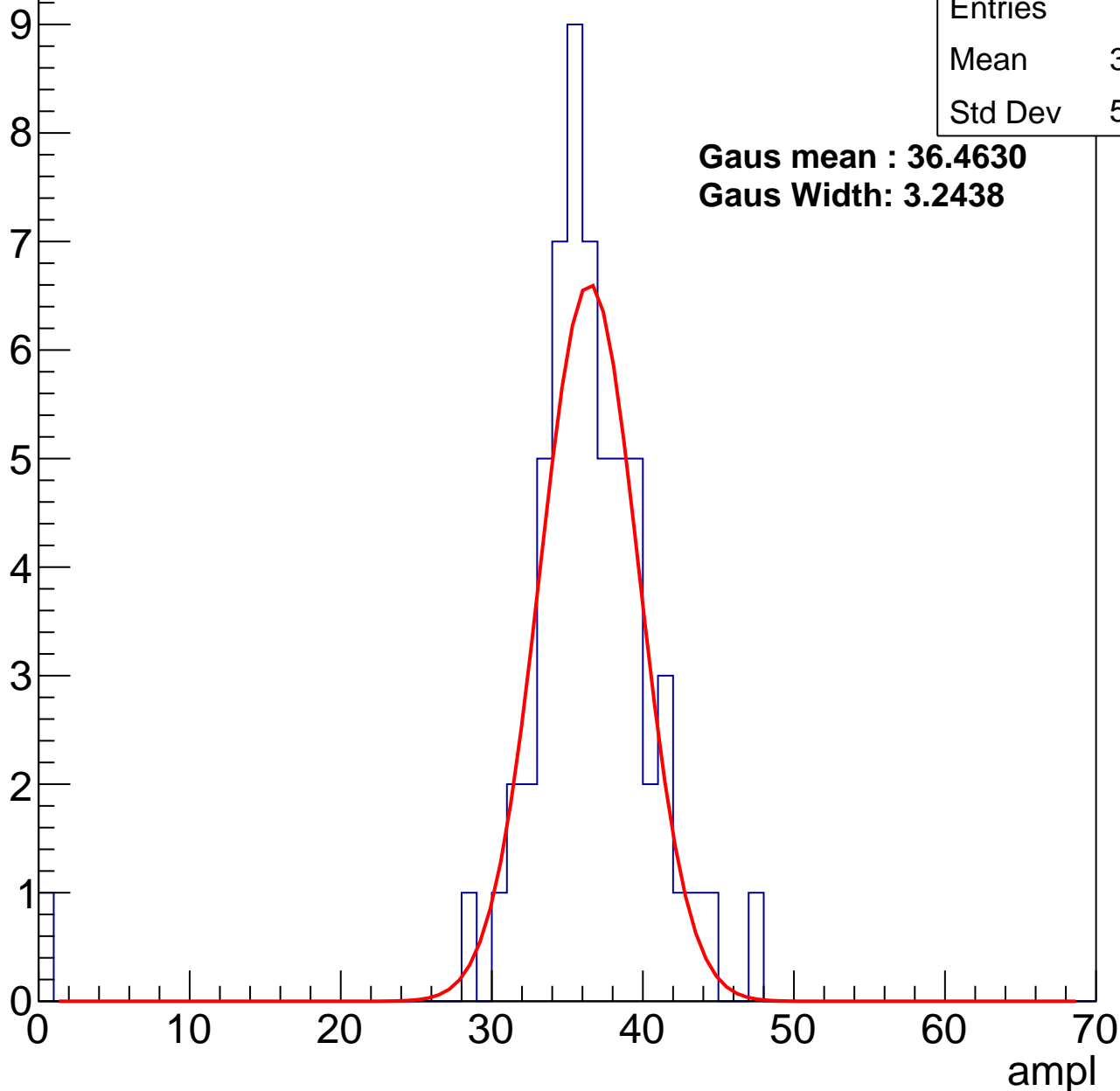
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.64
Std Dev	5.822

Gaus mean : 36.4630

Gaus Width: 3.2438



B1L103S, U24-ch107, adc2

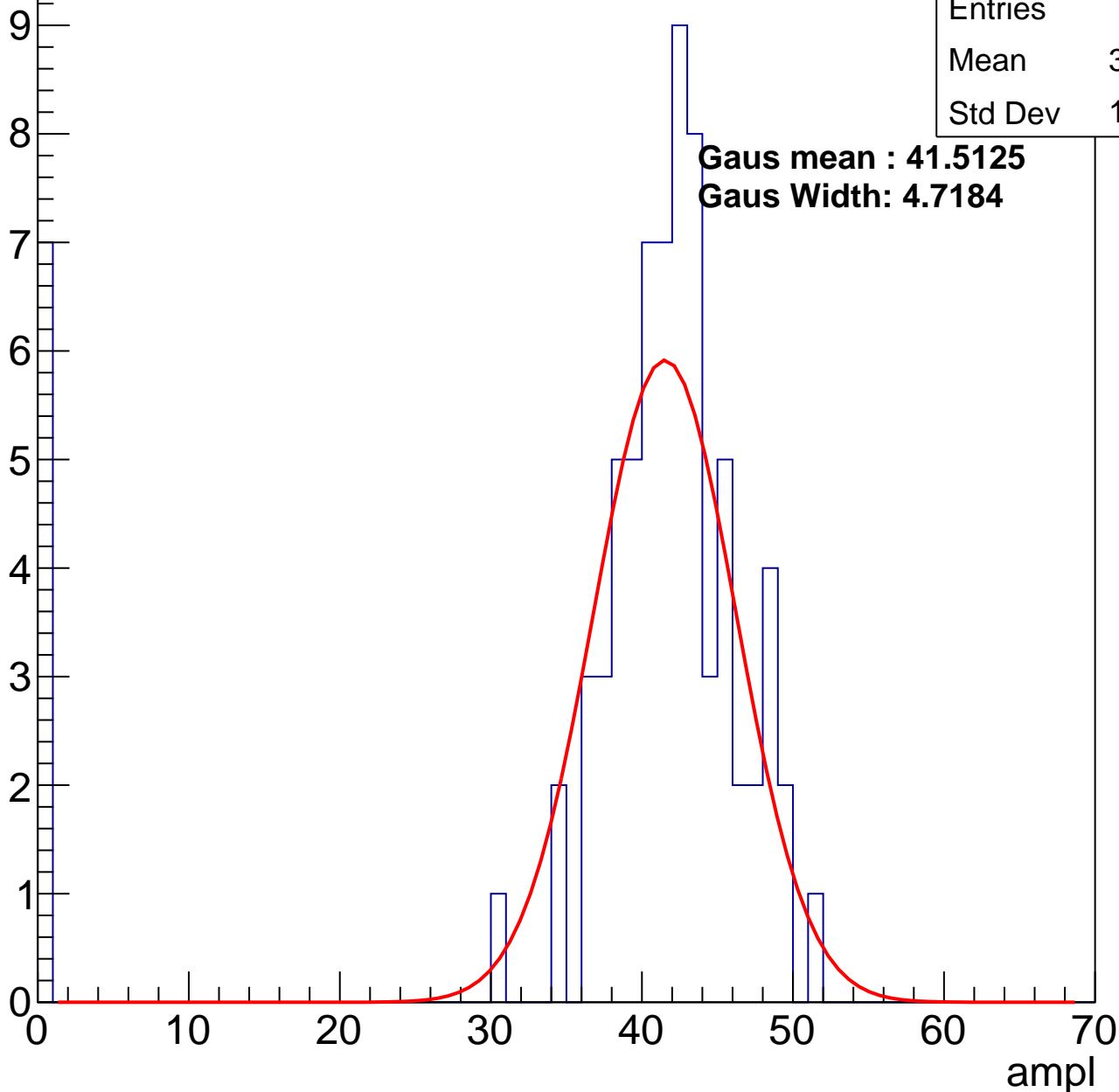
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	37.83
Std Dev	12.62

Gaus mean : 41.5125

Gaus Width: 4.7184

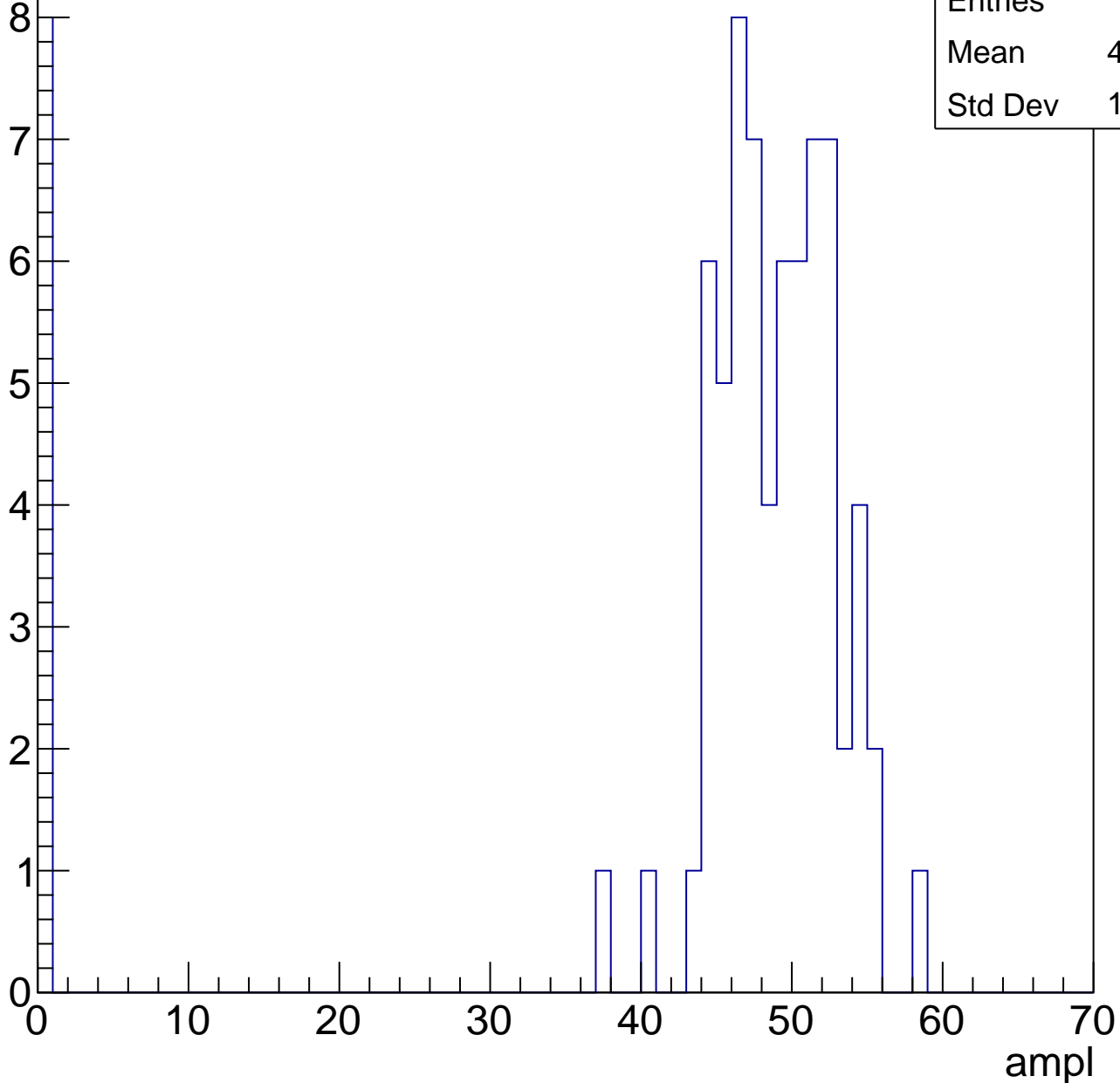


B1L103S, U24-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	43.46
Std Dev	15.33

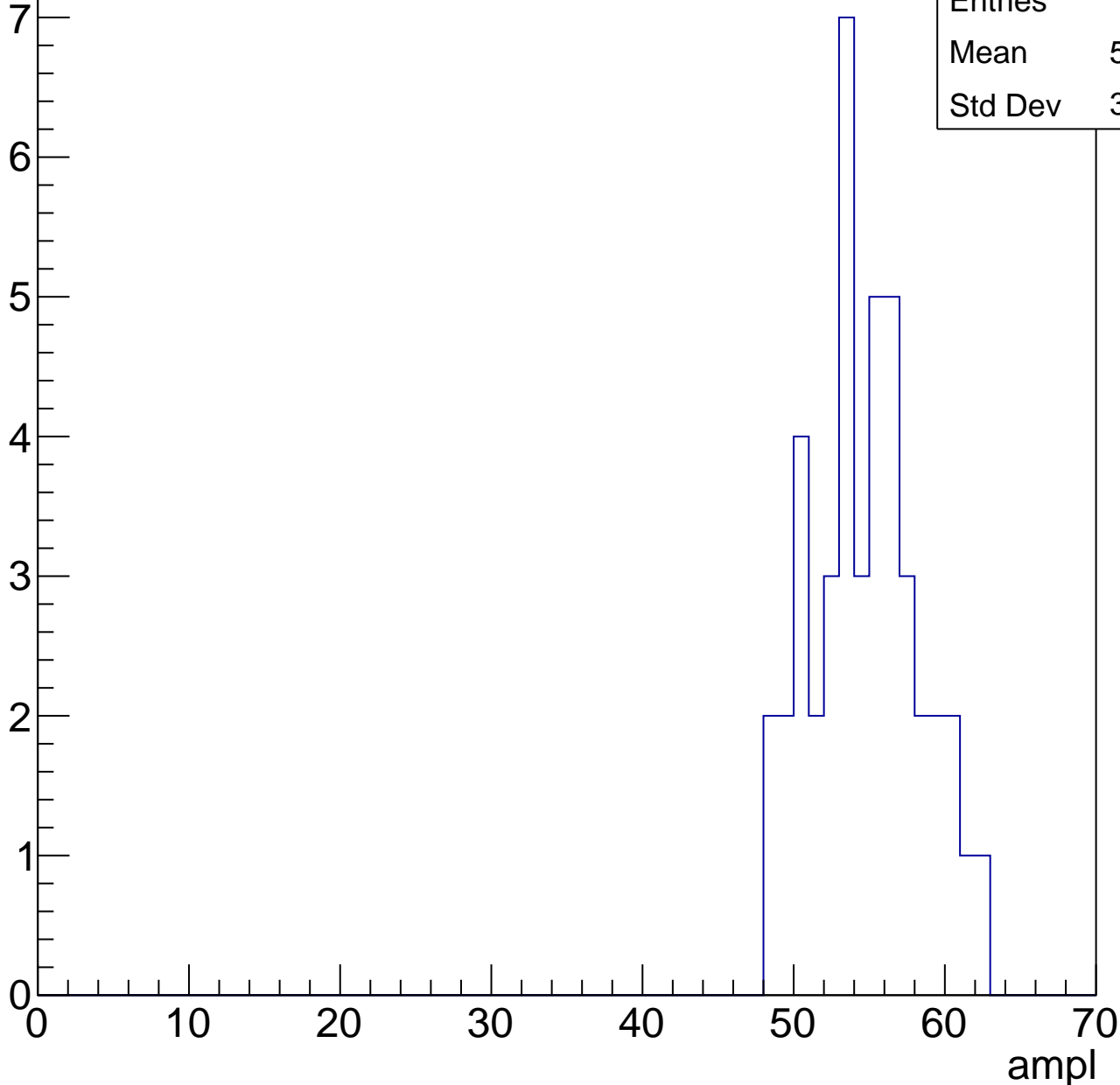


B1L103S, U24-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	54.27
Std Dev	3.499

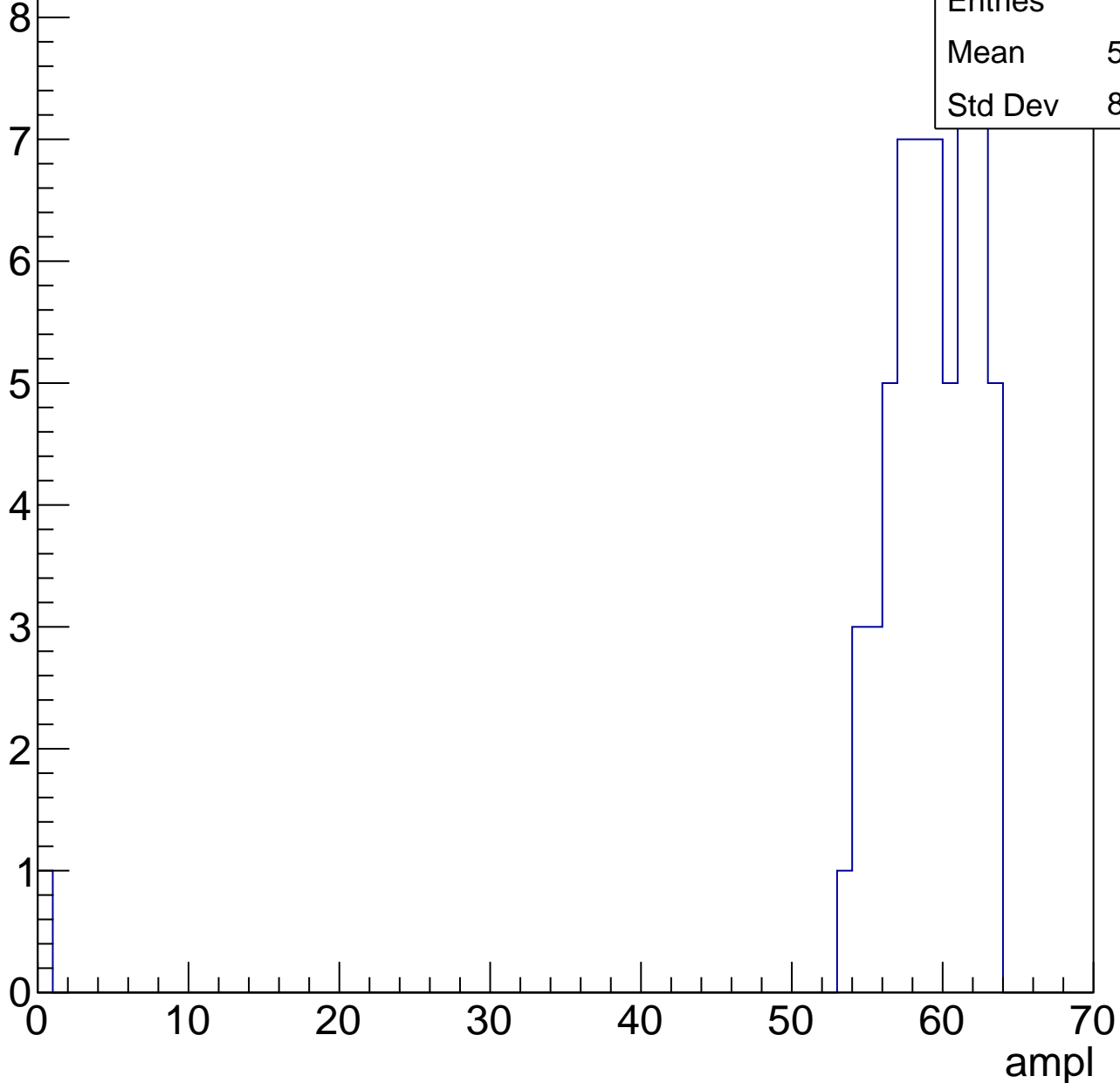


B1L103S, U24-ch107, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.95
Std Dev	8.003

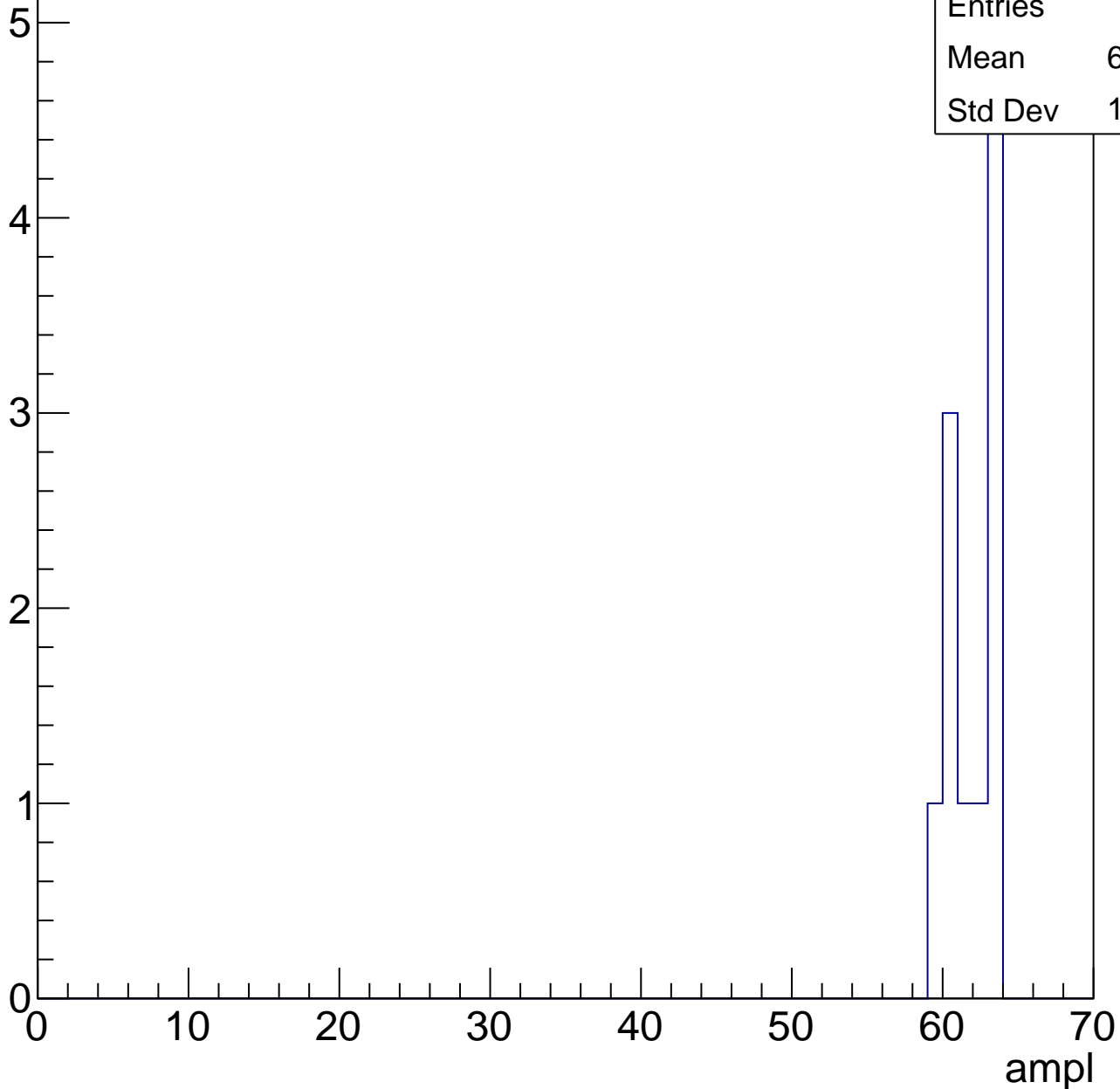


B1L103S, U24-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	61.55
Std Dev	1.499



B1L103S, U24-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch108, adc0

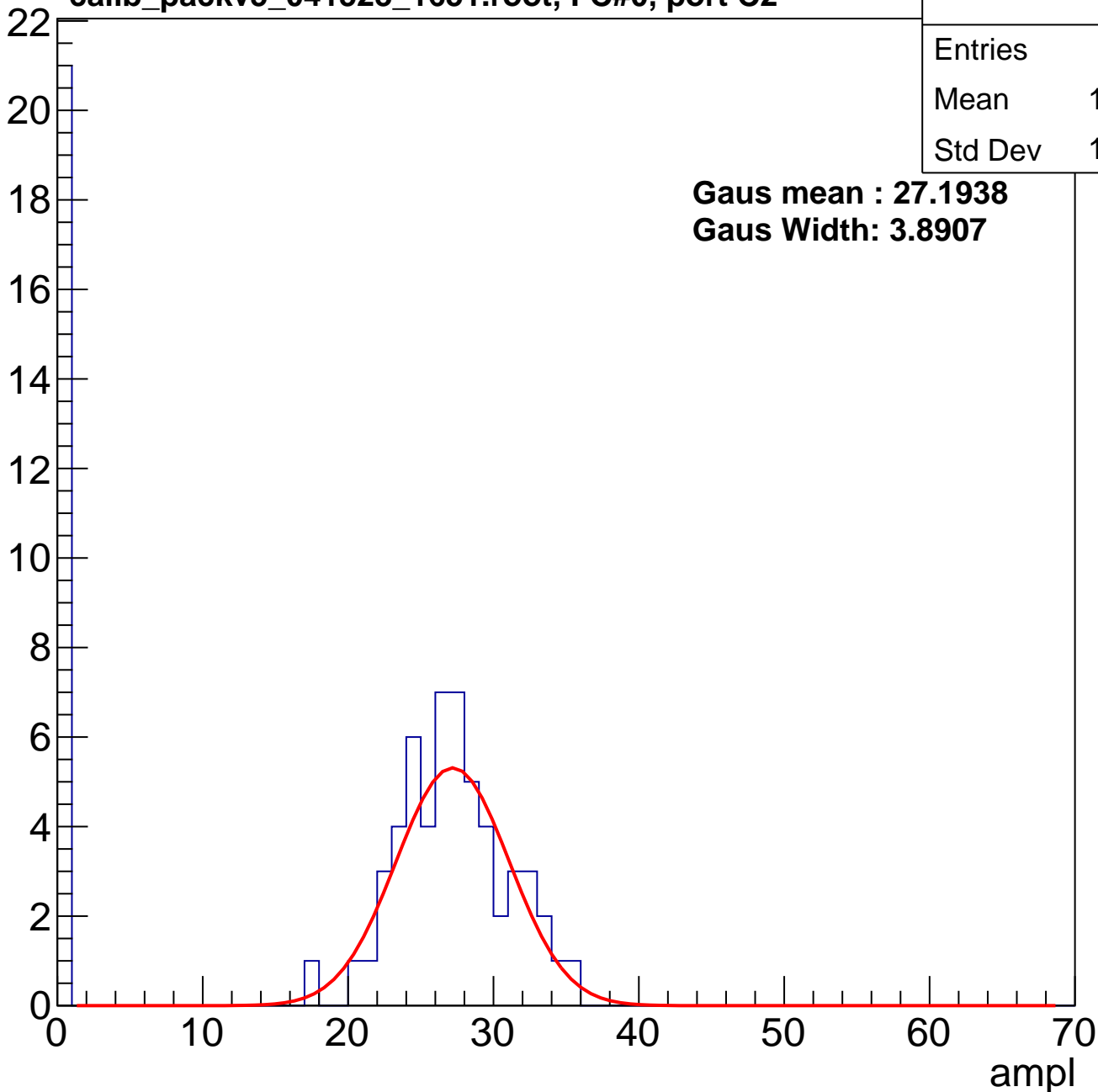
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	19.36
Std Dev	12.36

Gaus mean : 27.1938

Gaus Width: 3.8907

Entry



B1L103S, U24-ch108, adc1

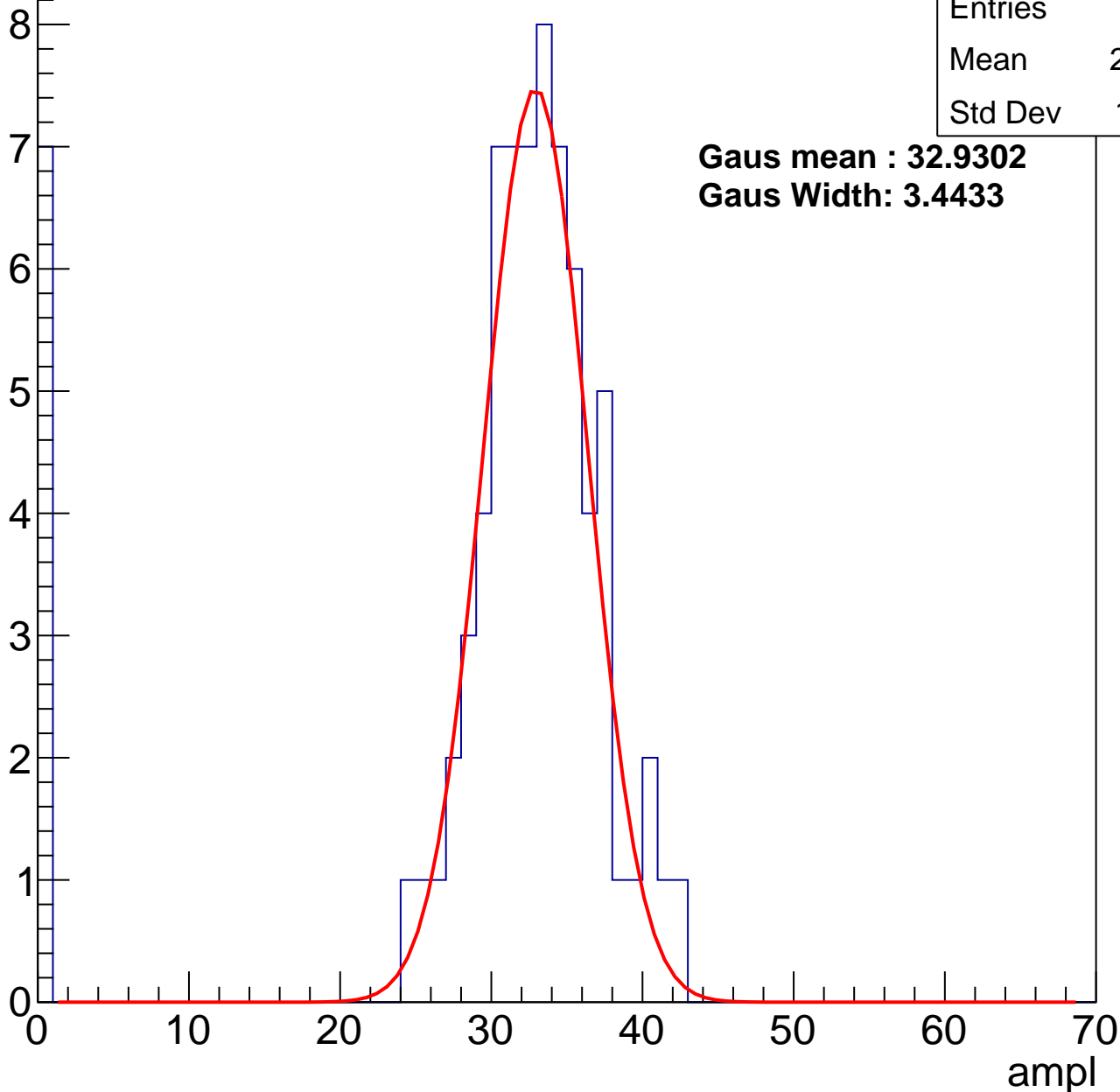
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	29.75
Std Dev	10.11

Gaus mean : 32.9302

Gaus Width: 3.4433



B1L103S, U24-ch108, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	35.2
Std Dev	13.74

Gaus mean : 40.6253

Gaus Width: 3.9446

Entry

10

8

6

4

2

0

0

10

20

30

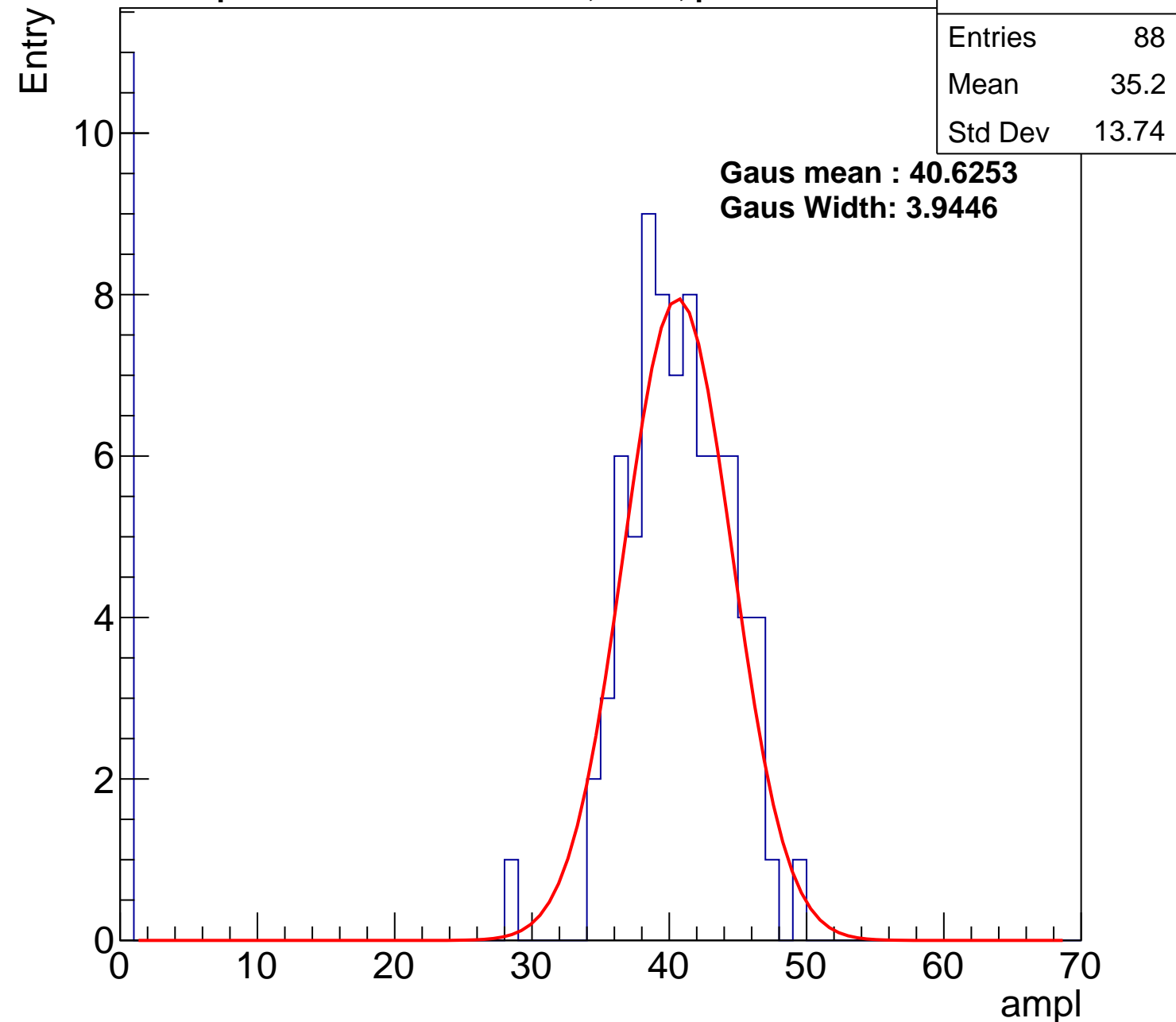
40

50

60

70

ampl

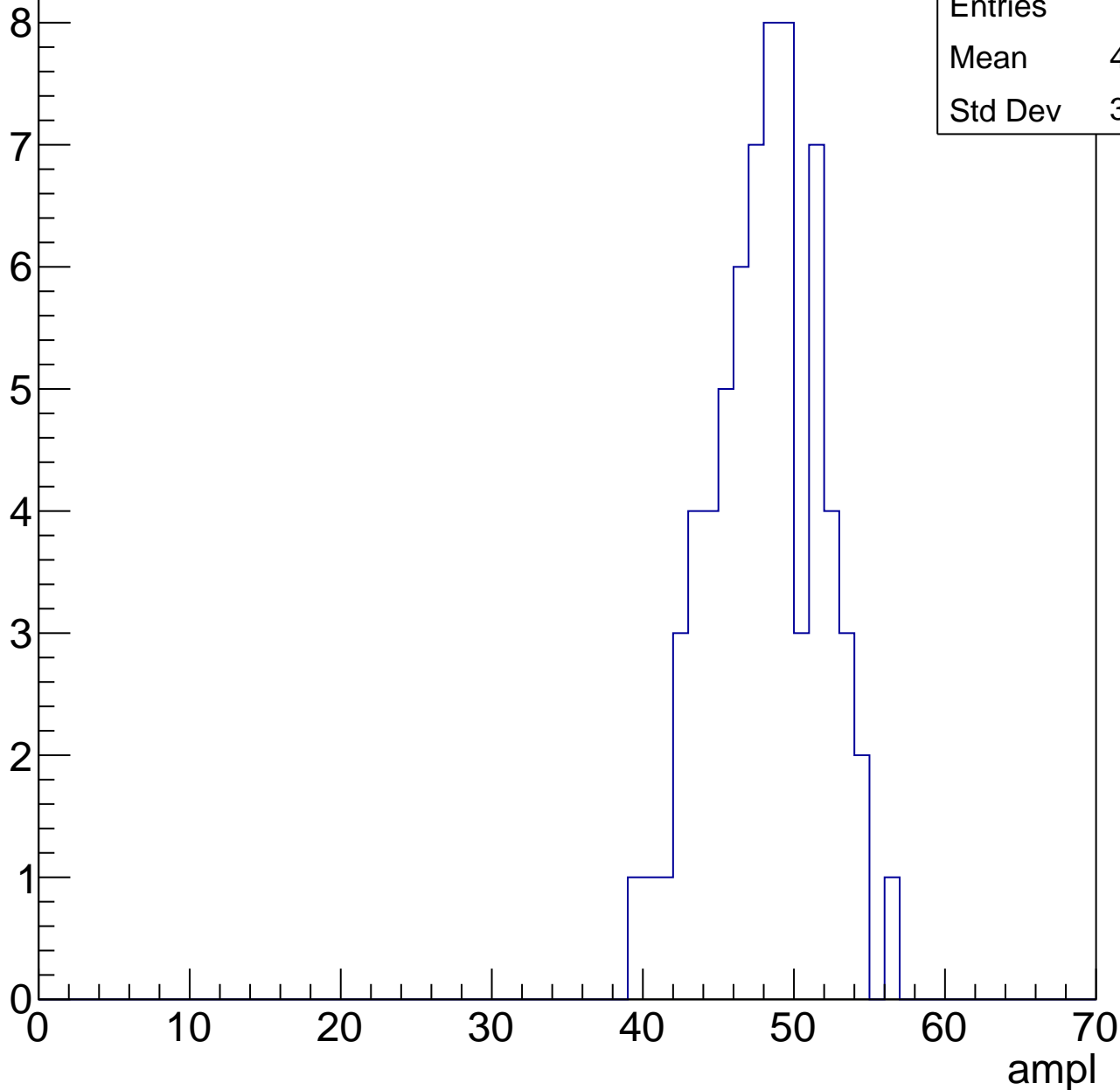


B1L103S, U24-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	47.62
Std Dev	3.618

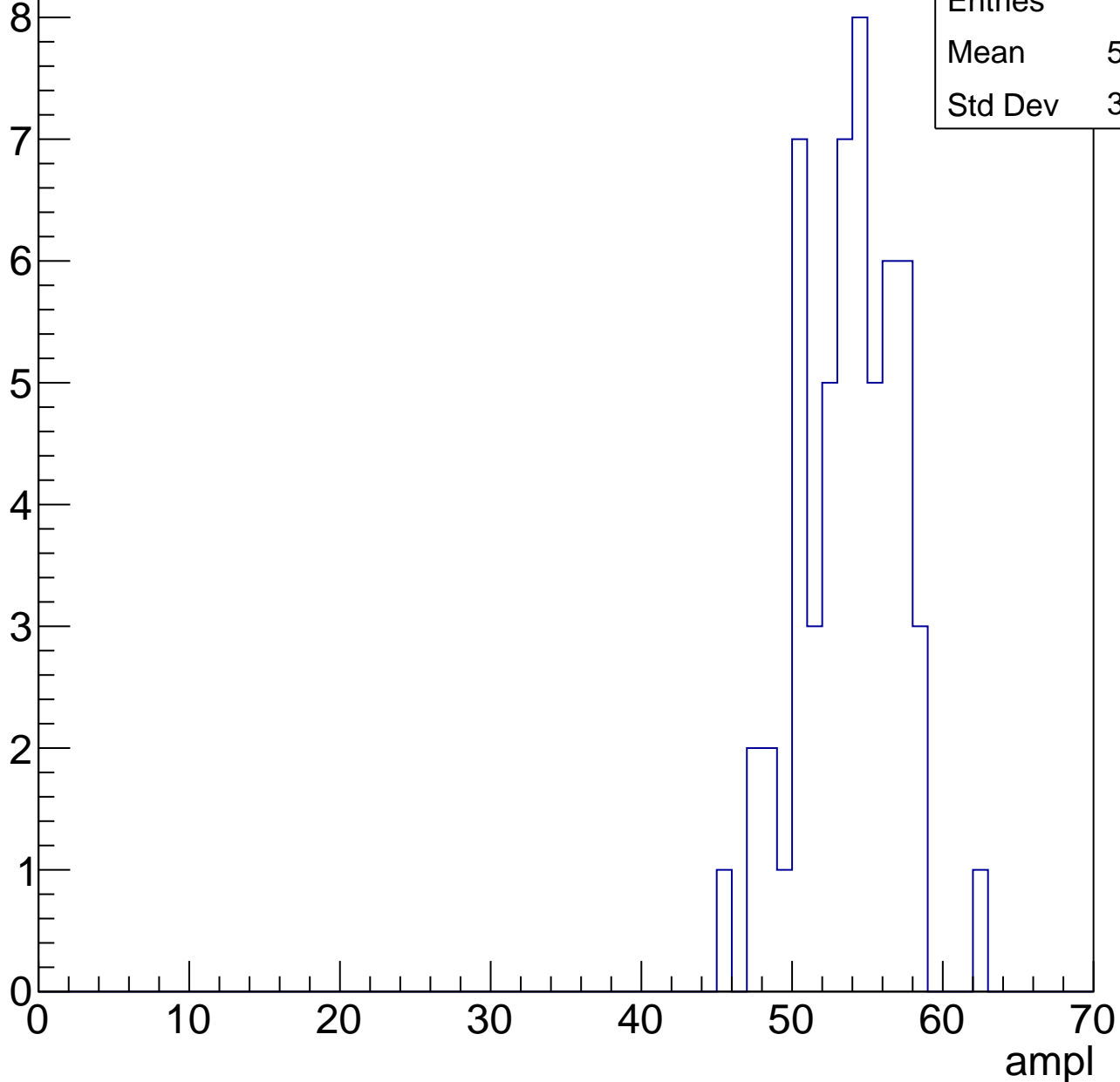


B1L103S, U24-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	53.32
Std Dev	3.272

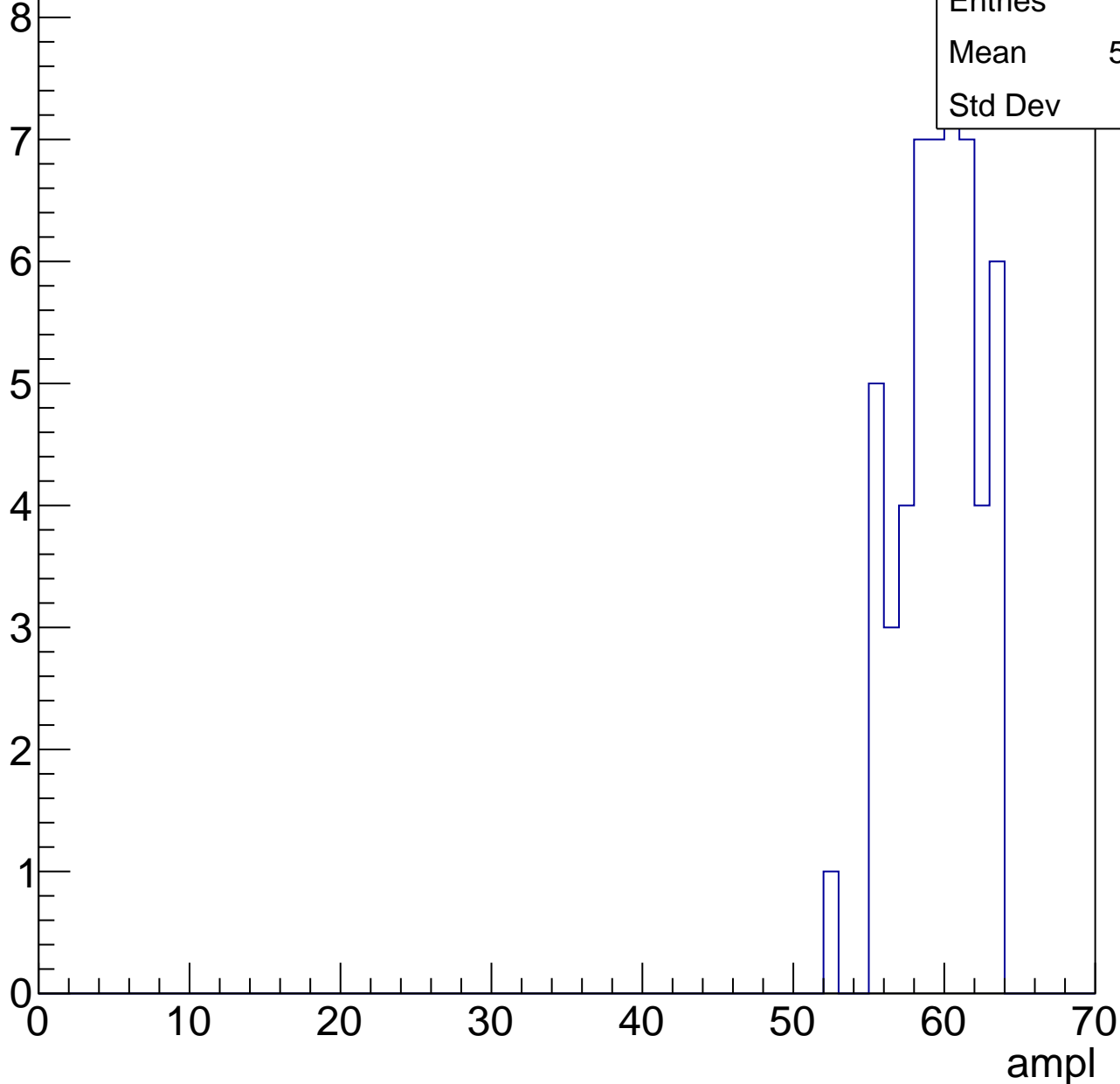


B1L103S, U24-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	59.13
Std Dev	2.58

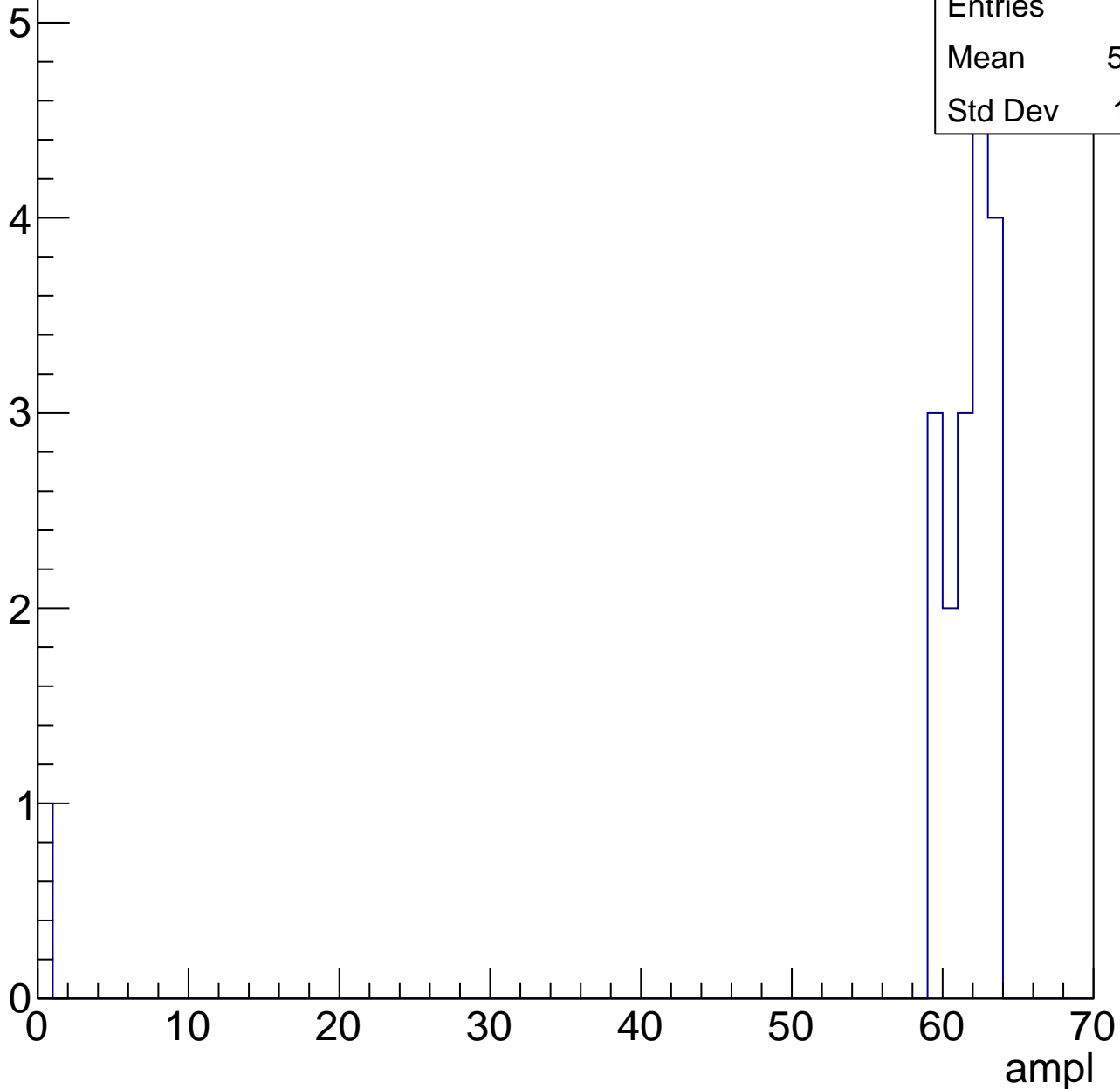


B1L103S, U24-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.89
Std Dev	14.11



B1L103S, U24-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch109, adc0

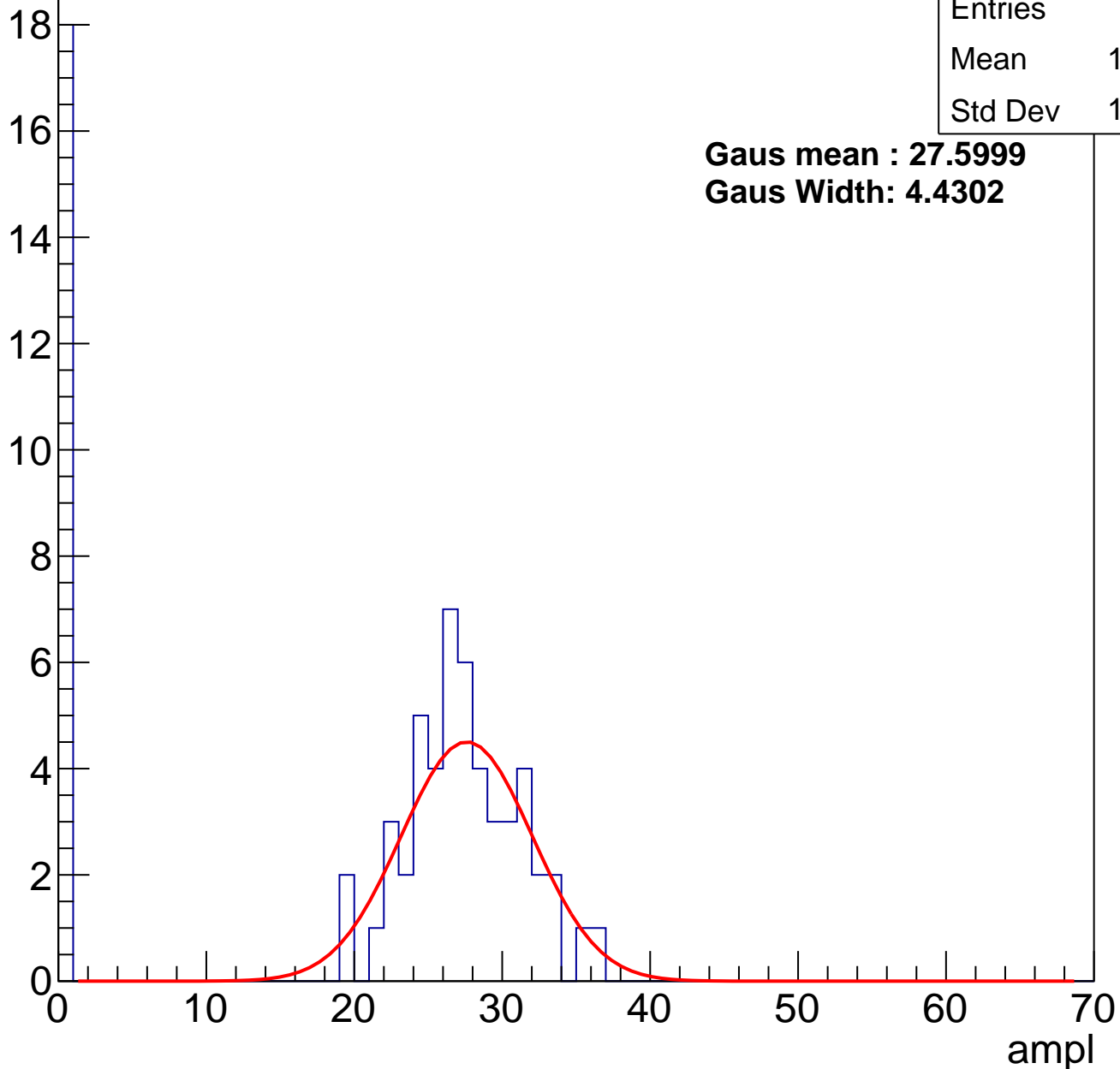
calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	19.84
Std Dev	12.34

Gaus mean : 27.5999

Gaus Width: 4.4302

Entry



B1L103S, U24-ch109, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	33.6
Std Dev	5.162

Gaus mean : 34.2874

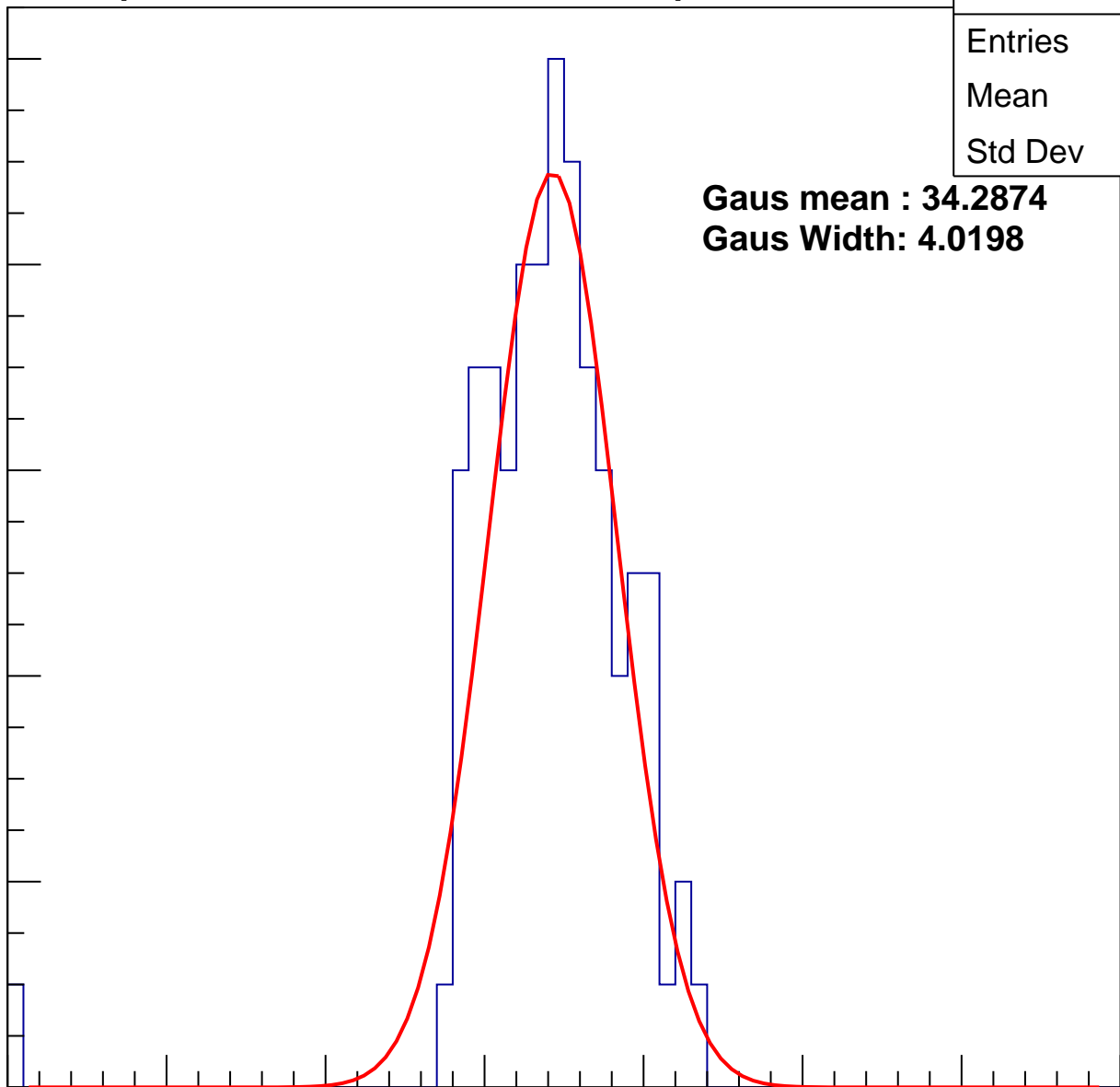
Gaus Width: 4.0198

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch109, adc2

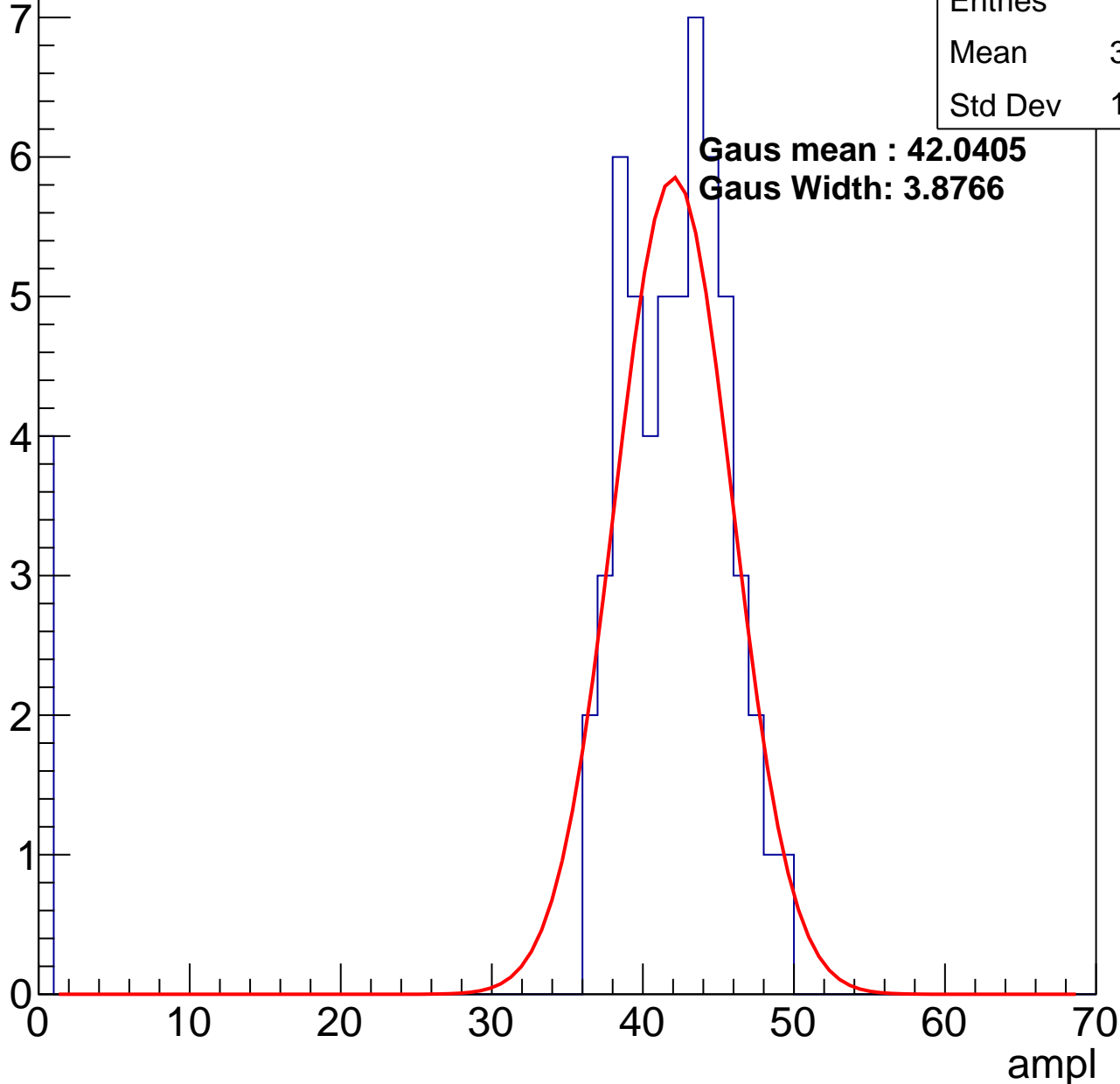
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	38.98
Std Dev	10.96

Gaus mean : 42.0405

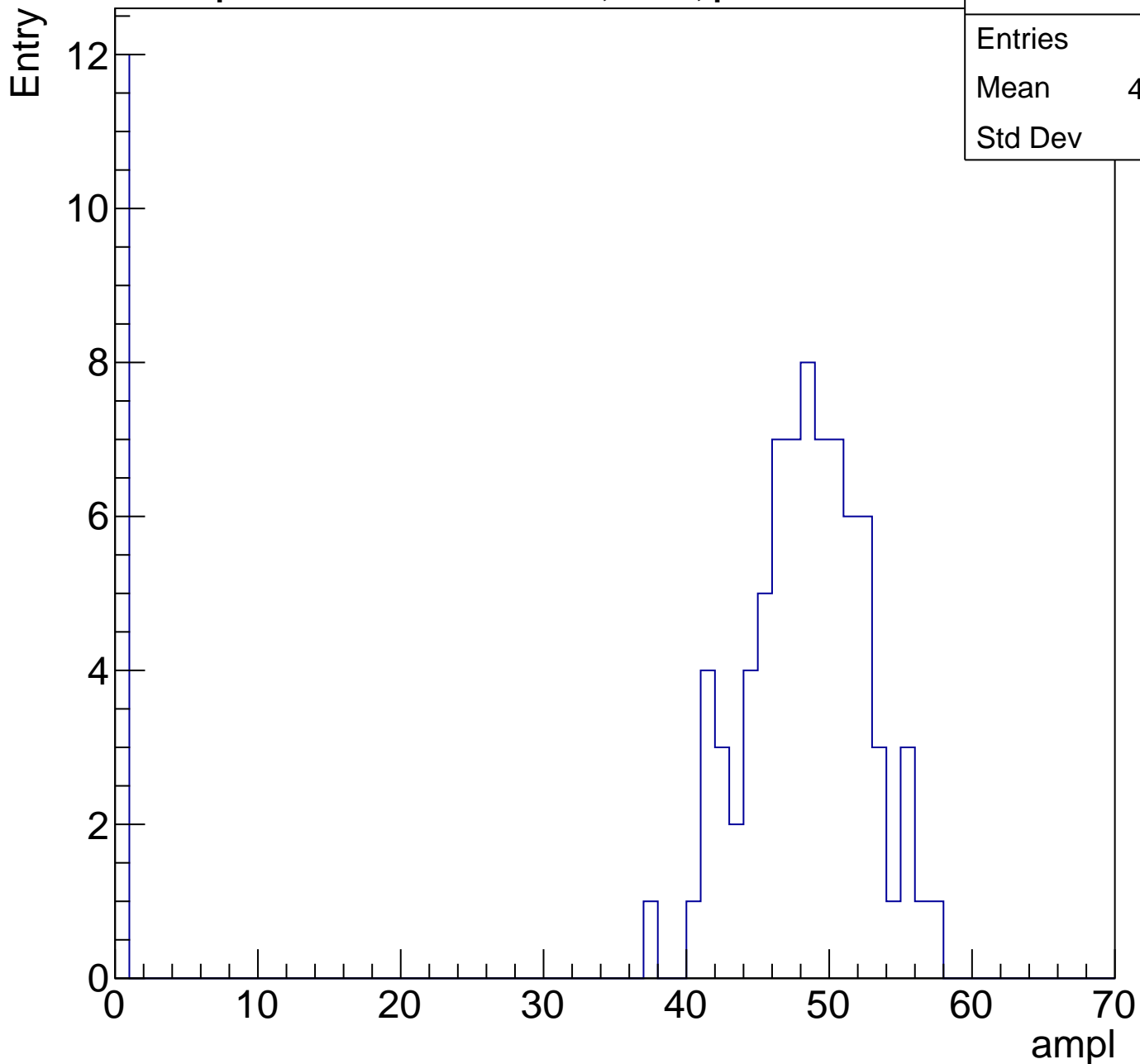
Gaus Width: 3.8766



B1L103S, U24-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	41.47
Std Dev	16.8

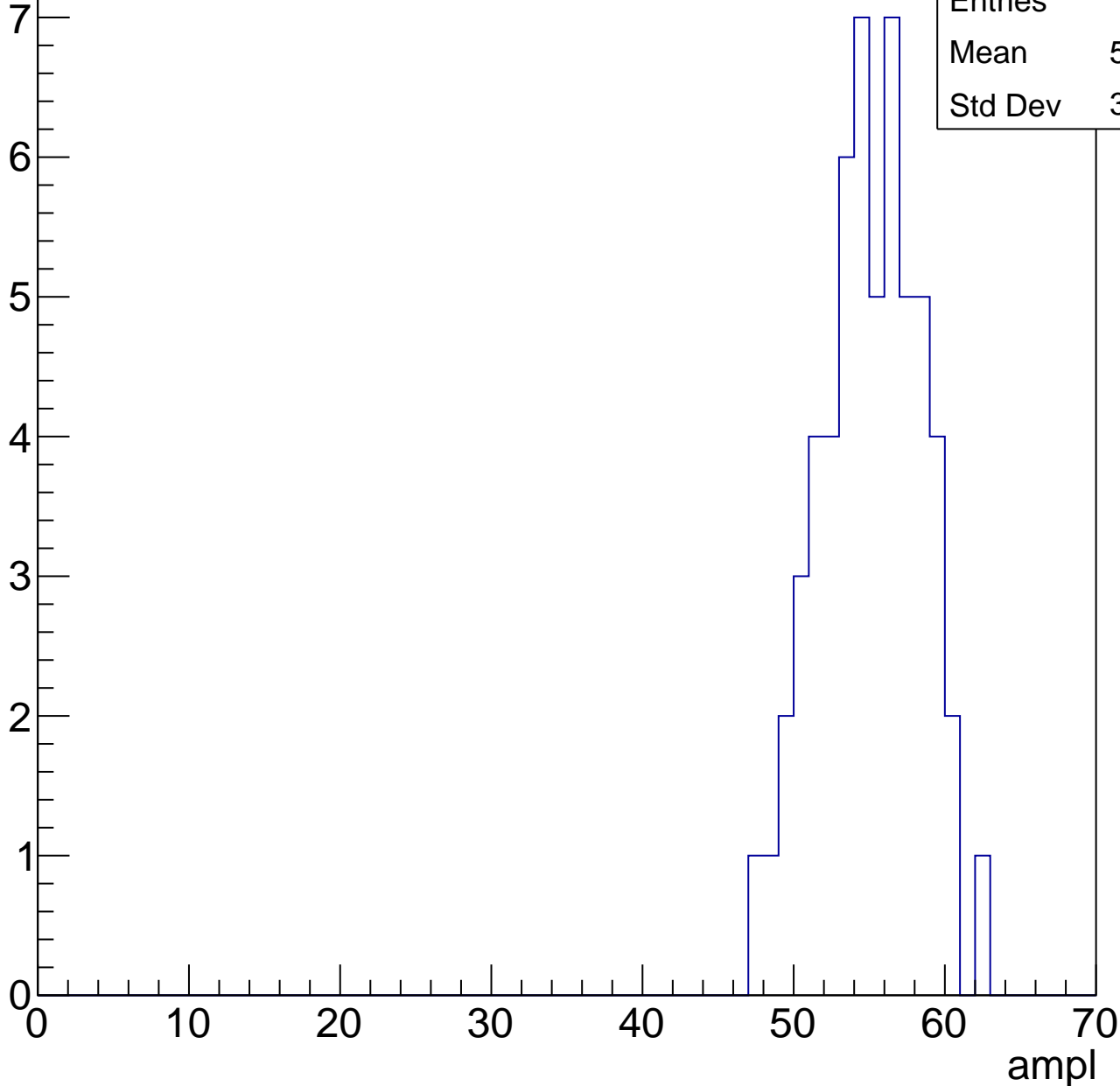


B1L103S, U24-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.58
Std Dev	3.287

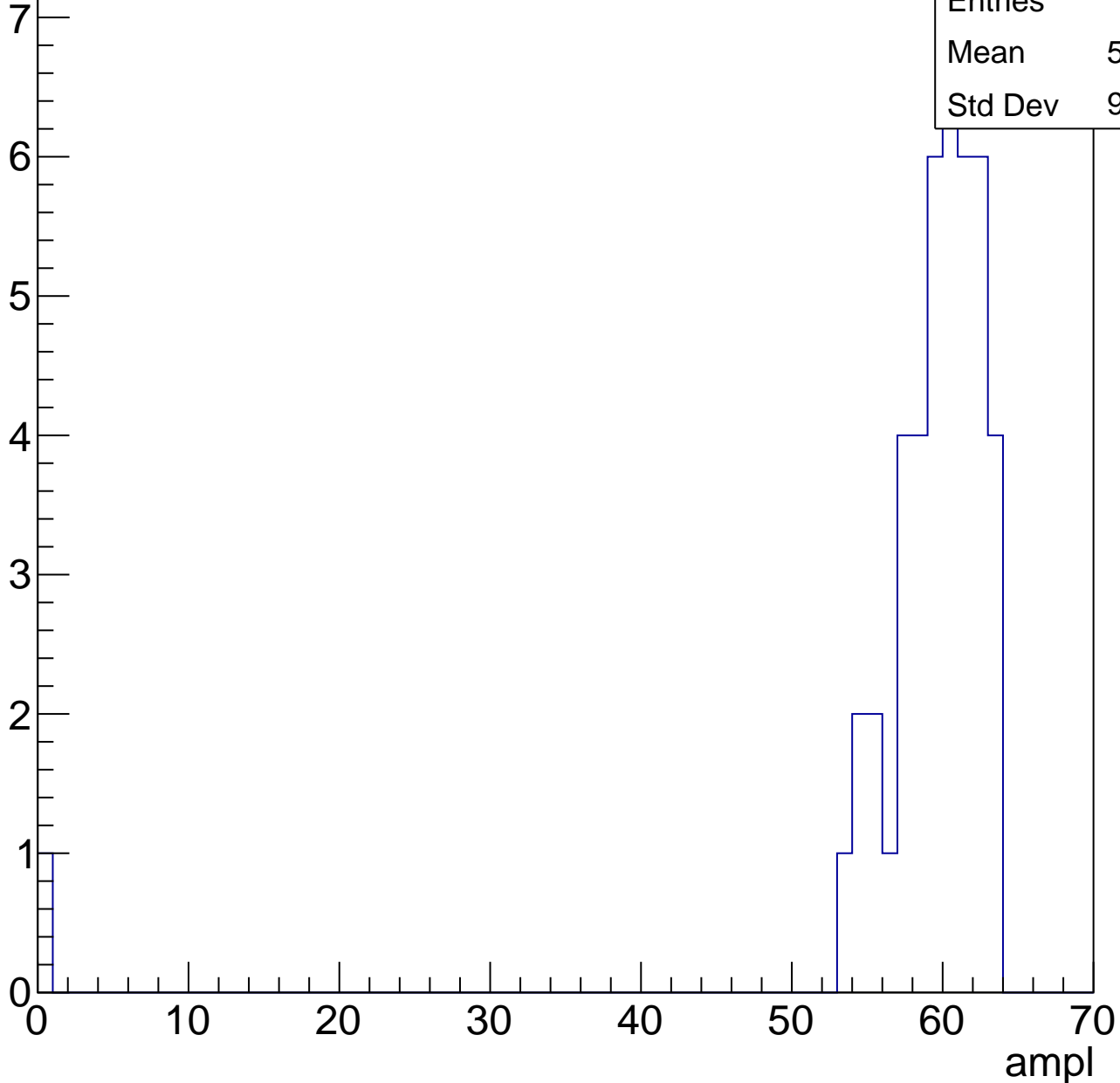


B1L103S, U24-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

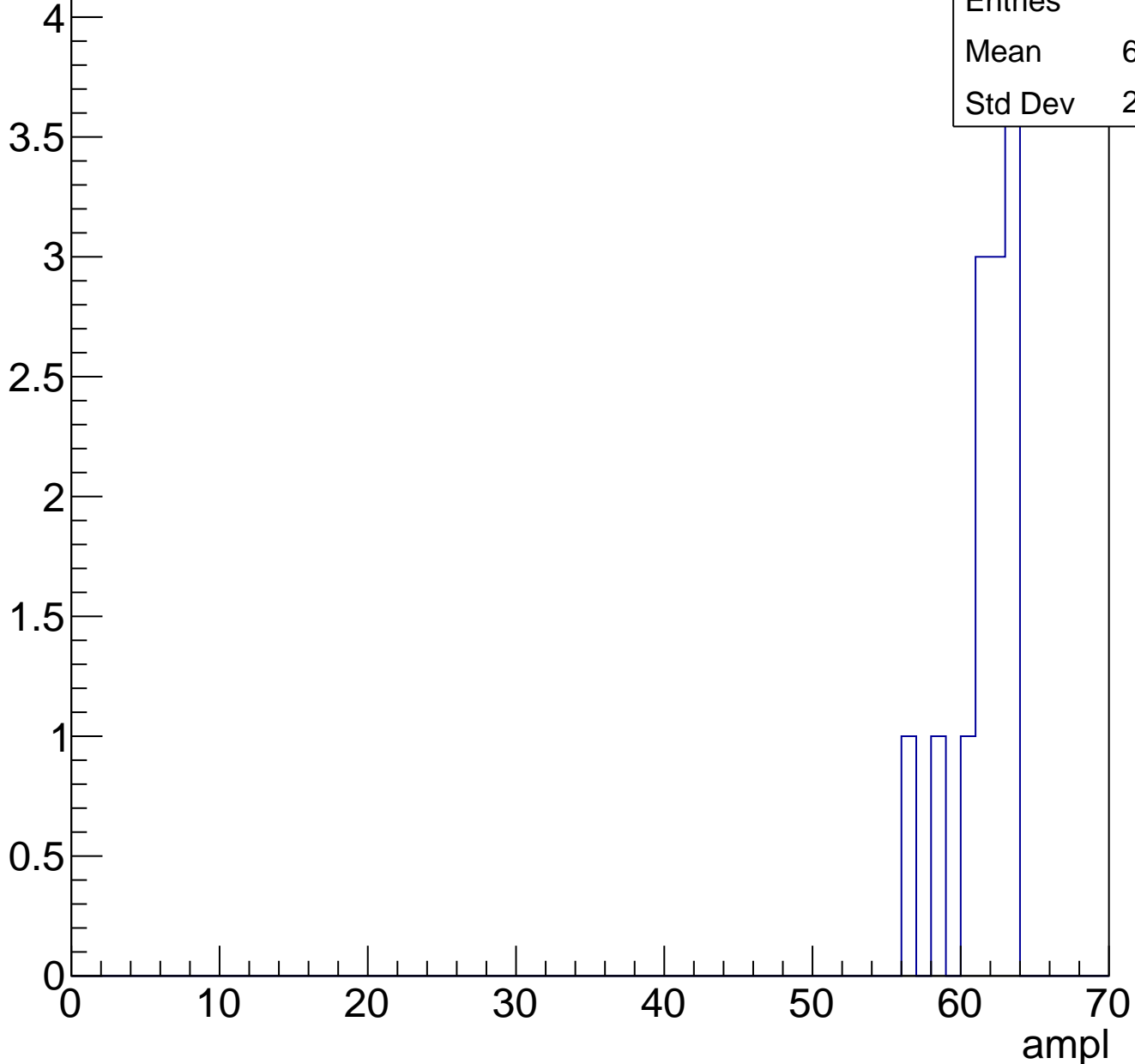
Entries	44
Mean	57.98
Std Dev	9.208



B1L103S, U24-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

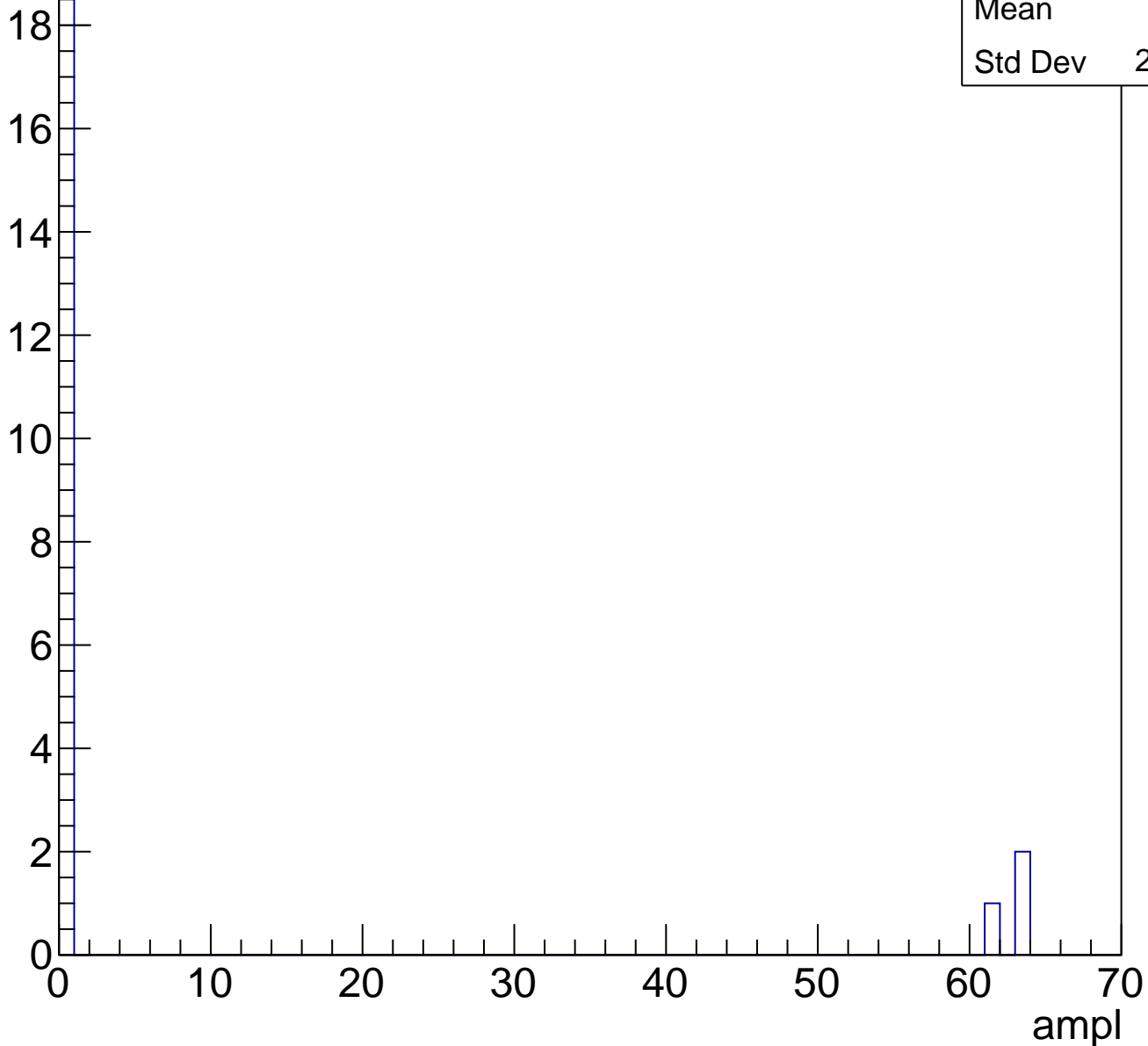


B1L103S, U24-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.5
Std Dev	21.39

Entry



B1L103S, U24-ch110, adc0

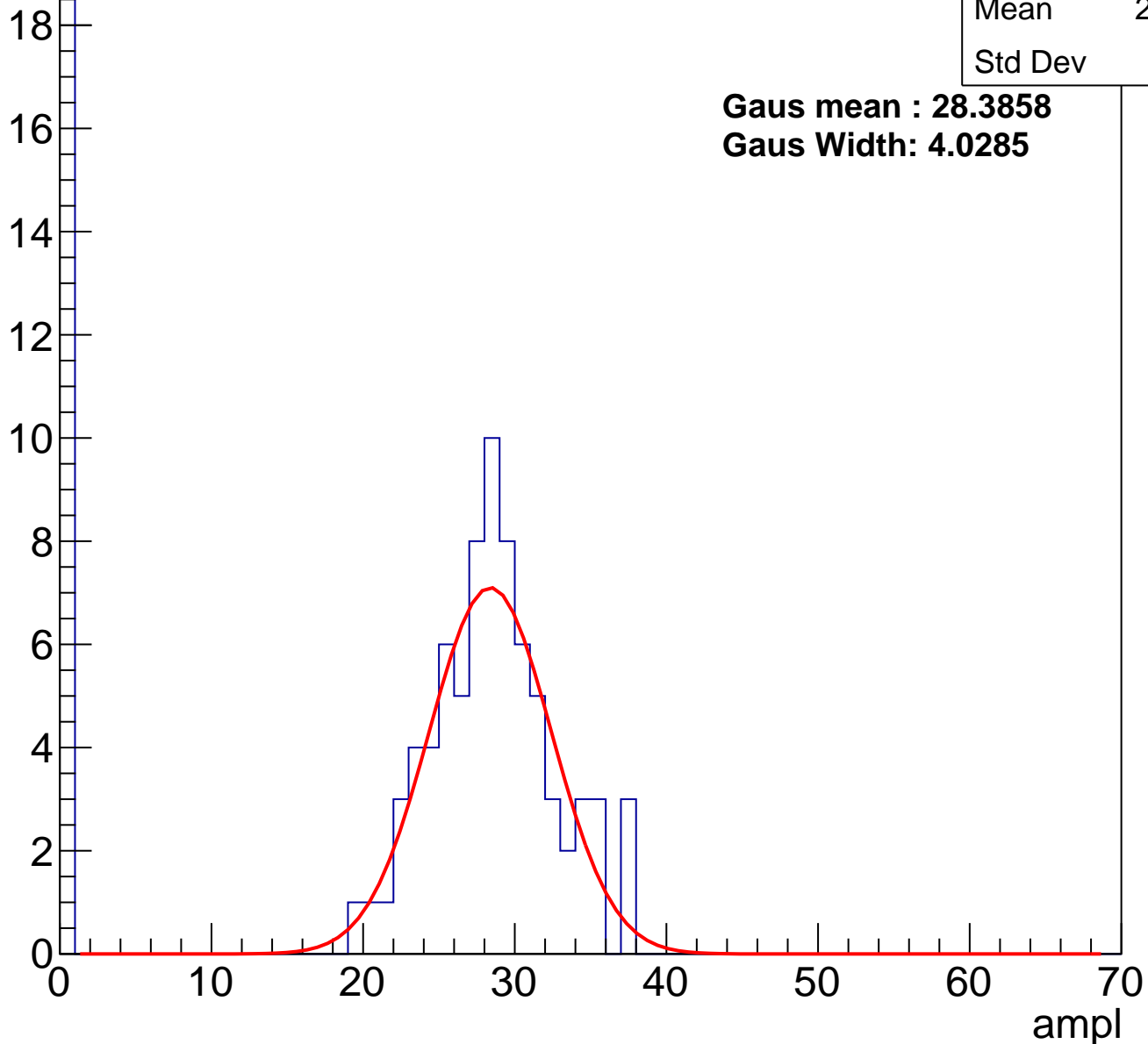
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	22.49
Std Dev	11.8

Gaus mean : 28.3858

Gaus Width: 4.0285

Entry



B1L103S, U24-ch110, adc1

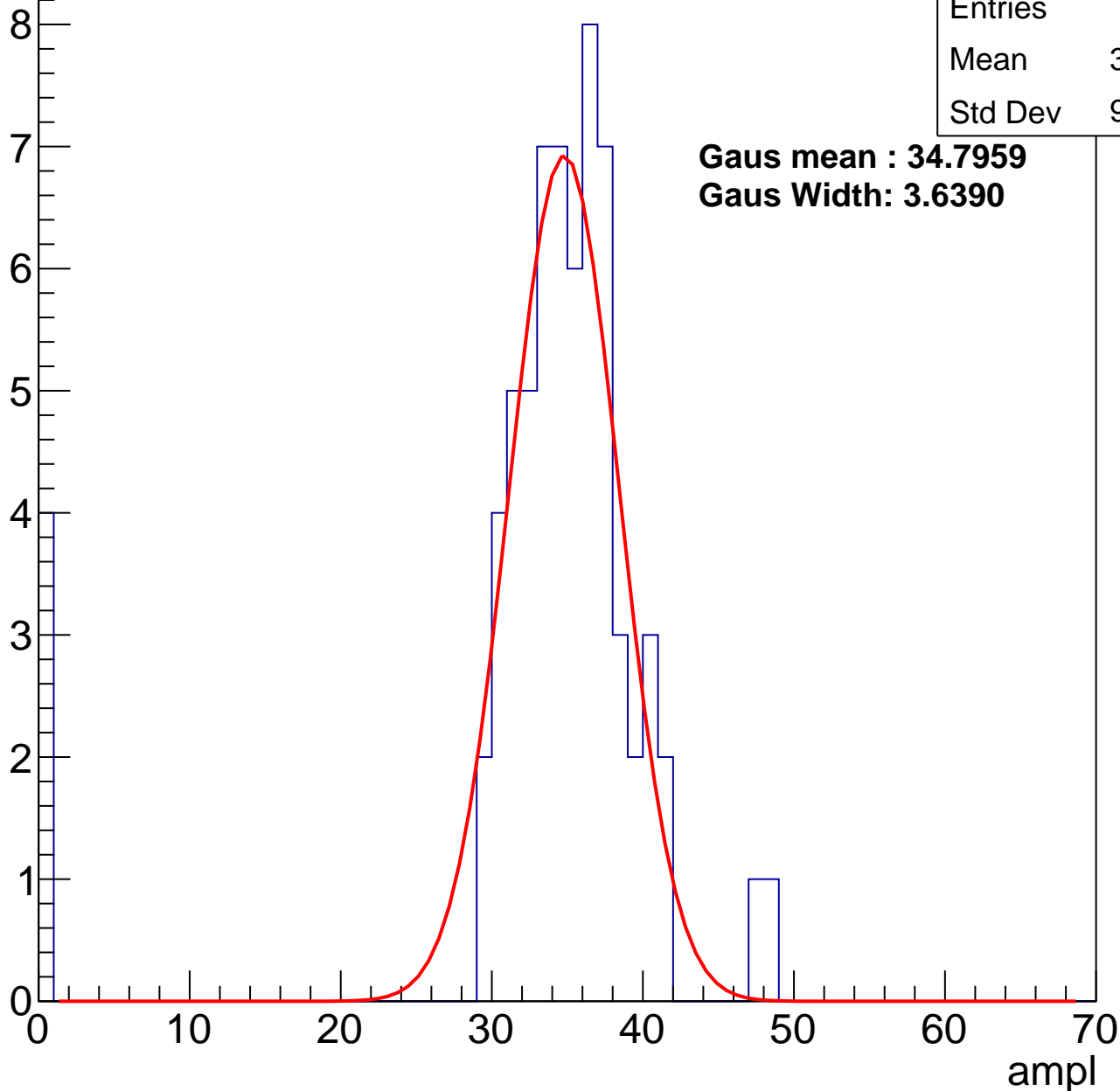
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.96
Std Dev	9.066

Gaus mean : 34.7959

Gaus Width: 3.6390



B1L103S, U24-ch110, adc2

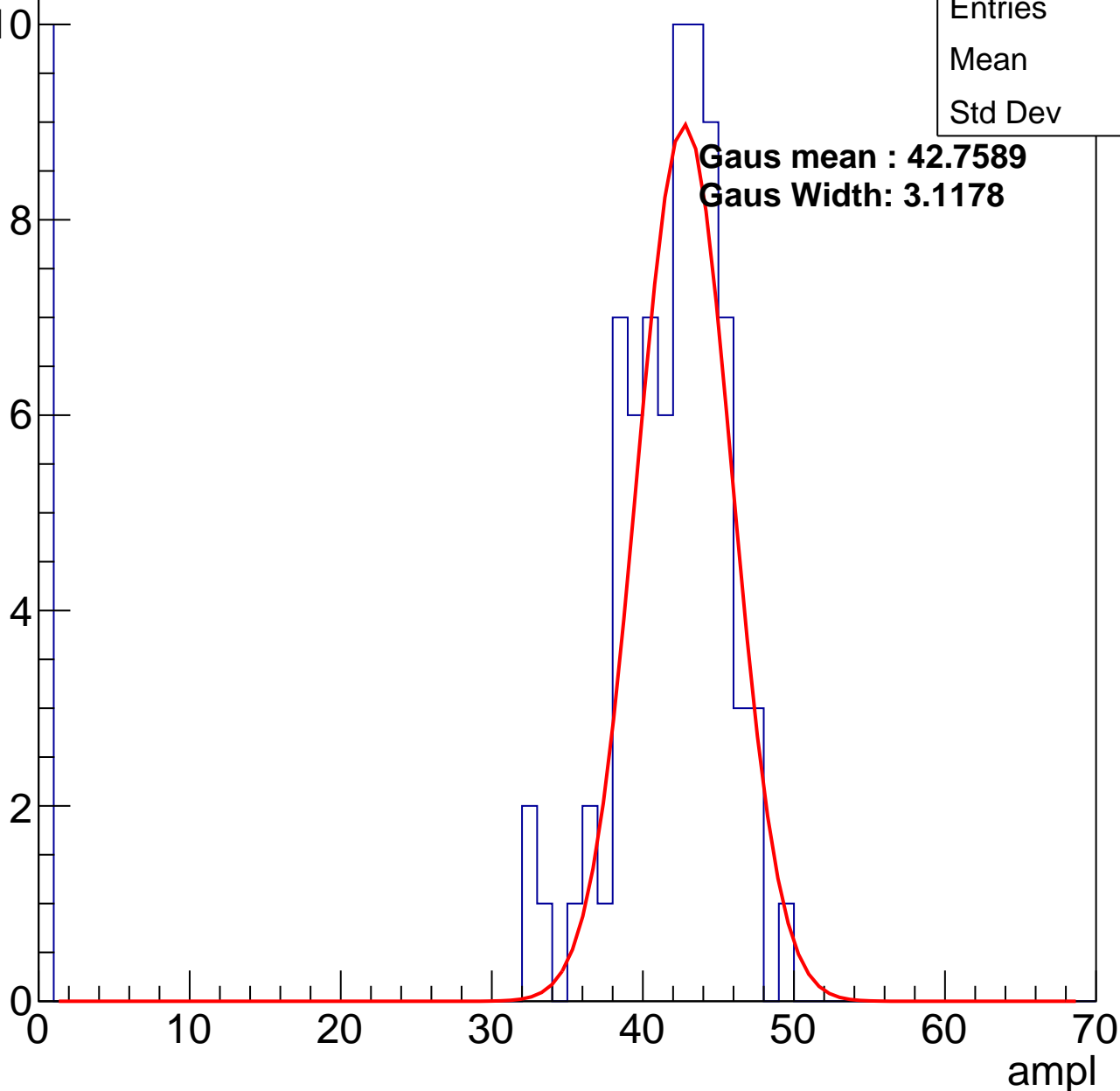
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	36.7
Std Dev	13.7

Gaus mean : 42.7589

Gaus Width: 3.1178

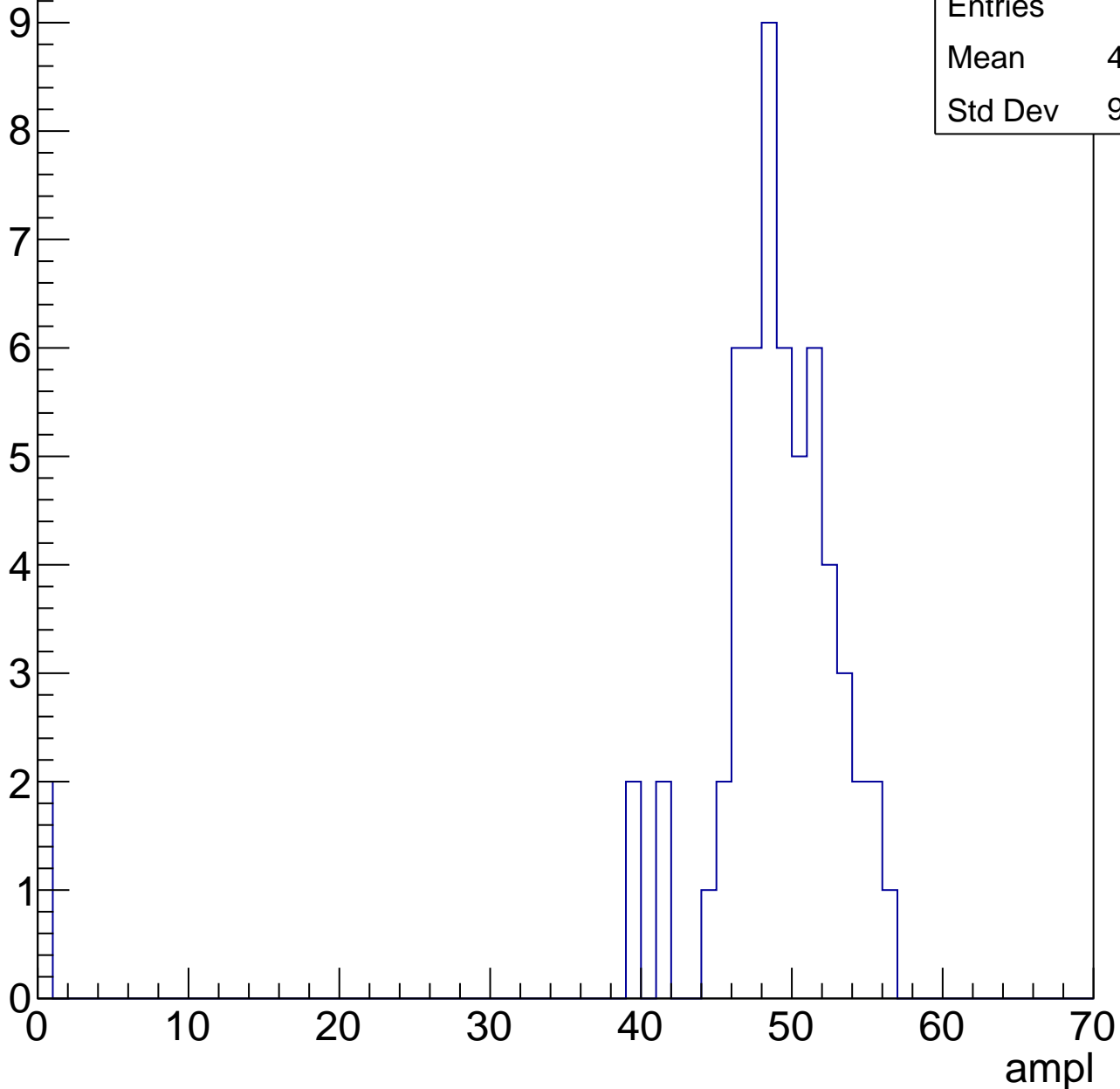


B1L103S, U24-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	47.03
Std Dev	9.505

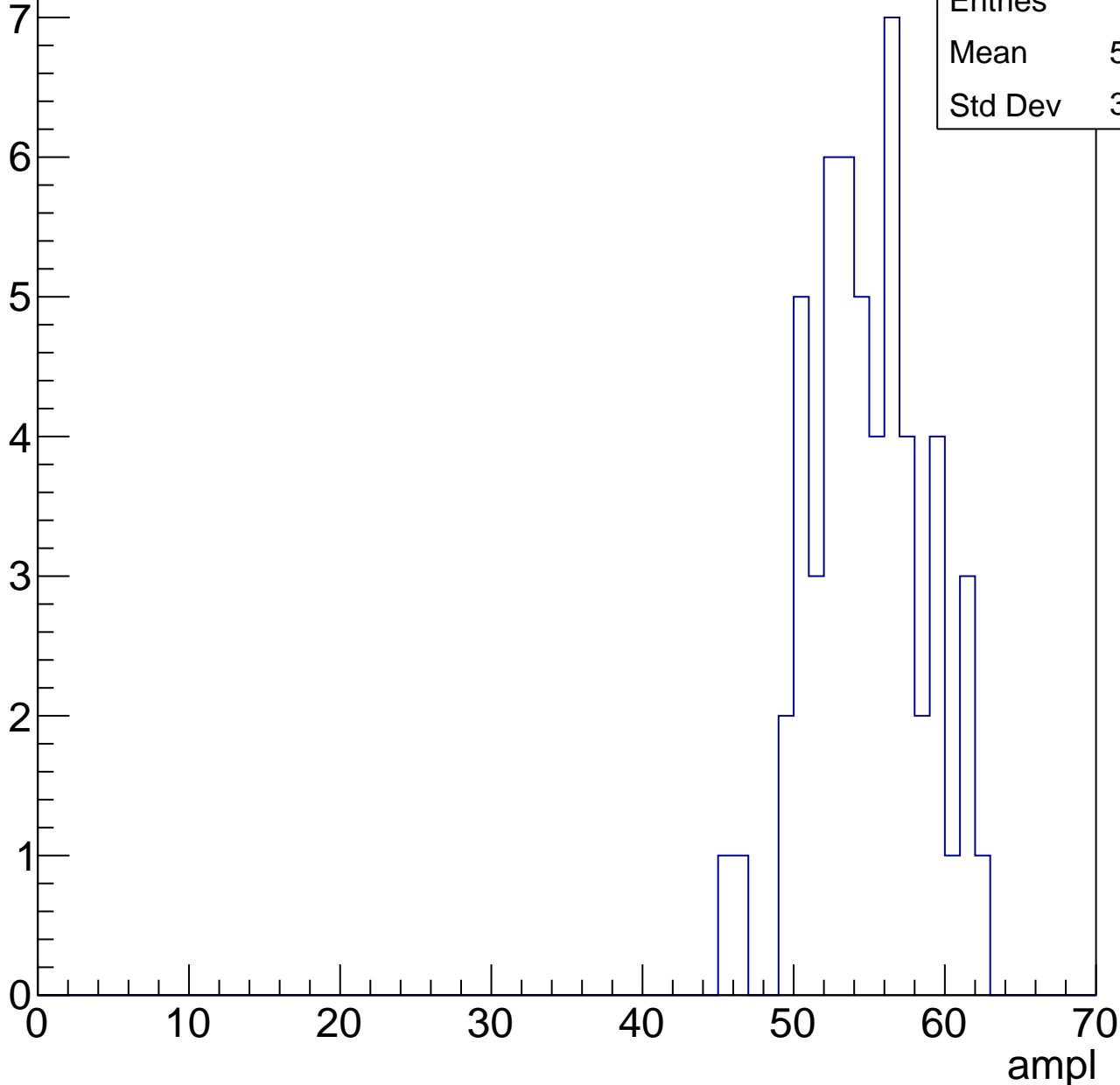


B1L103S, U24-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.35
Std Dev	3.748

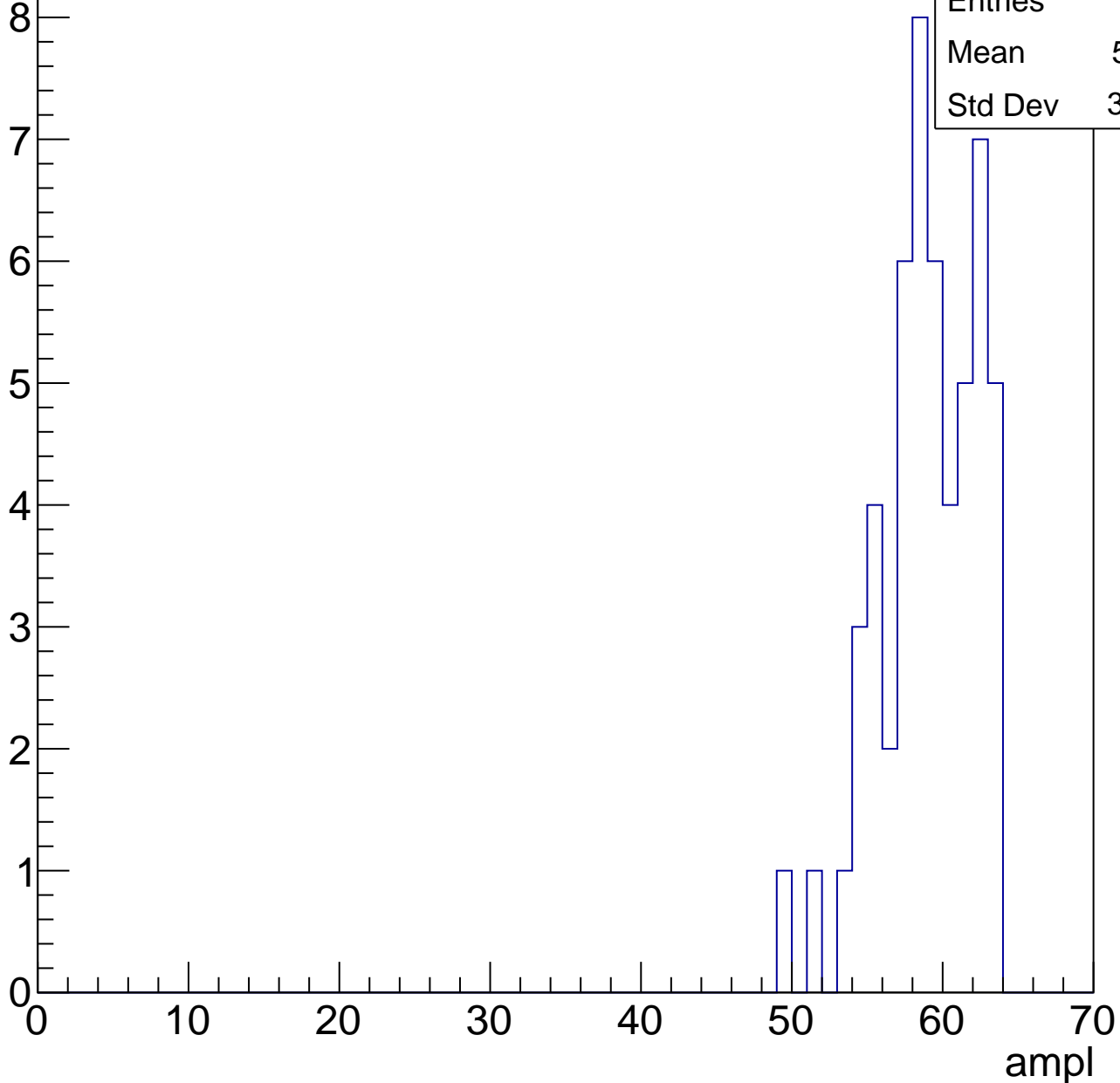


B1L103S, U24-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.51
Std Dev	3.202

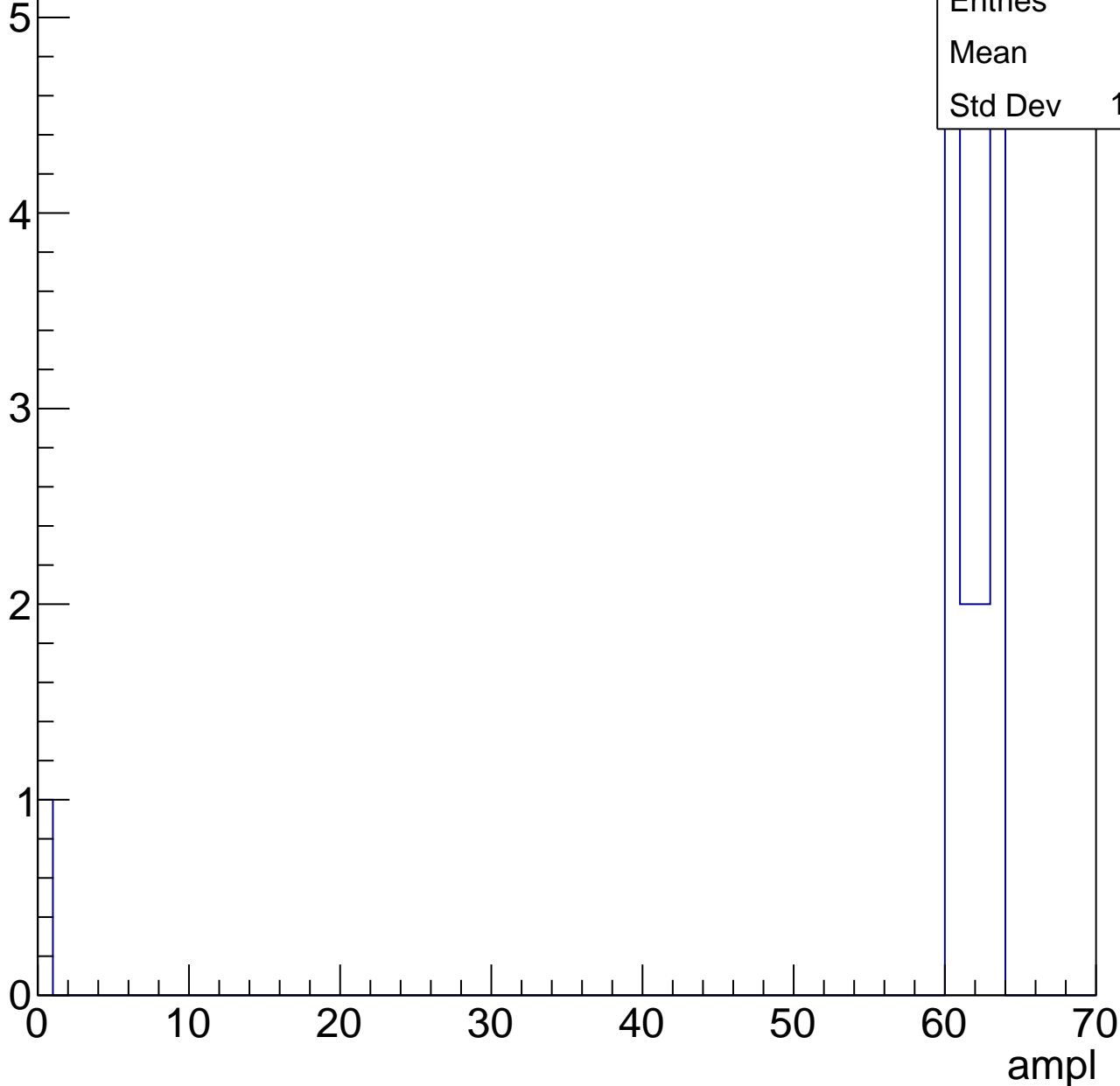


B1L103S, U24-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.4
Std Dev	15.39

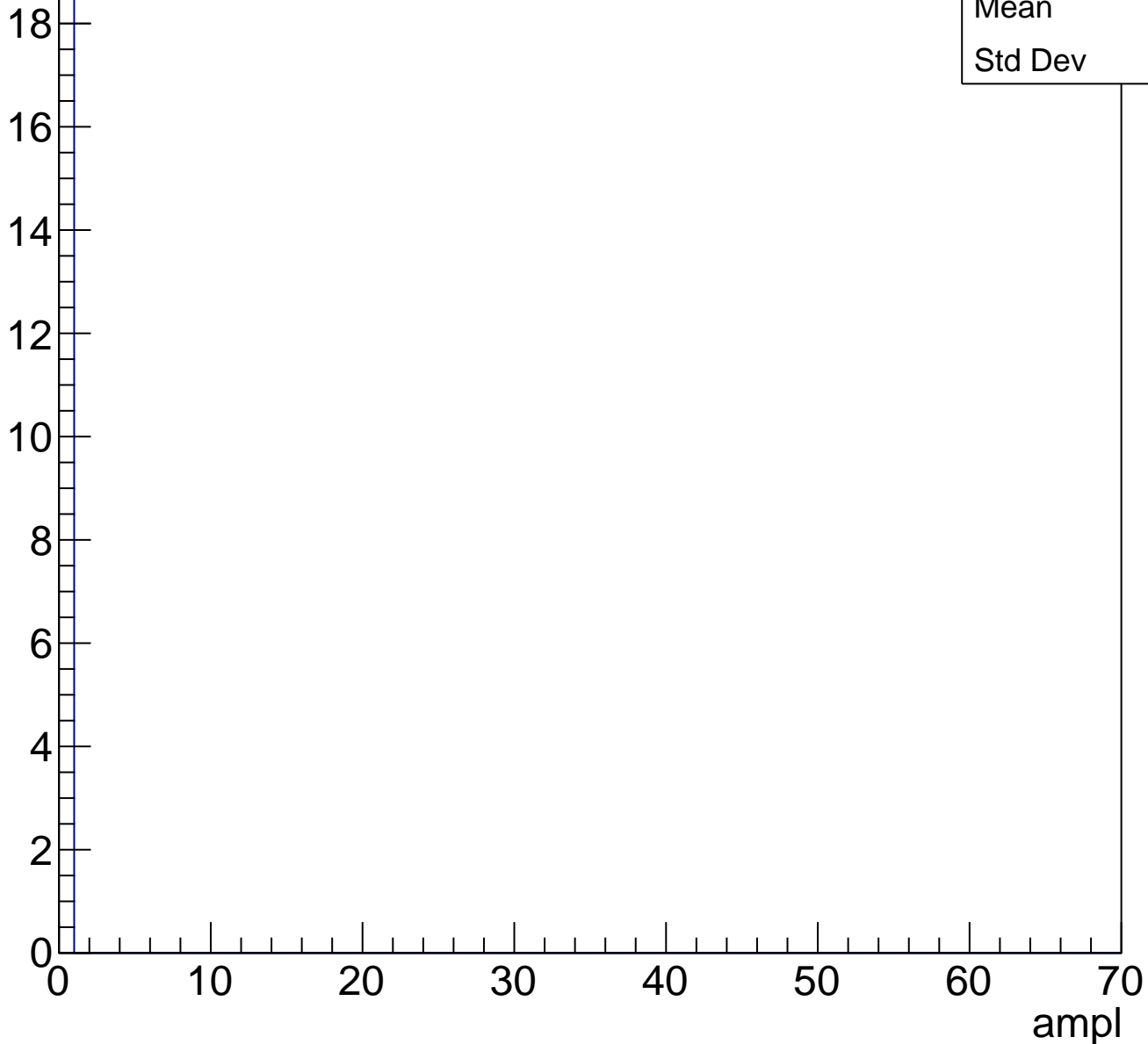


B1L103S, U24-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



B1L103S, U24-ch111, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	23.96
Std Dev	11.01

Gaus mean : 28.9339

Gaus Width: 4.9804

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70



B1L103S, U24-ch111, adc1

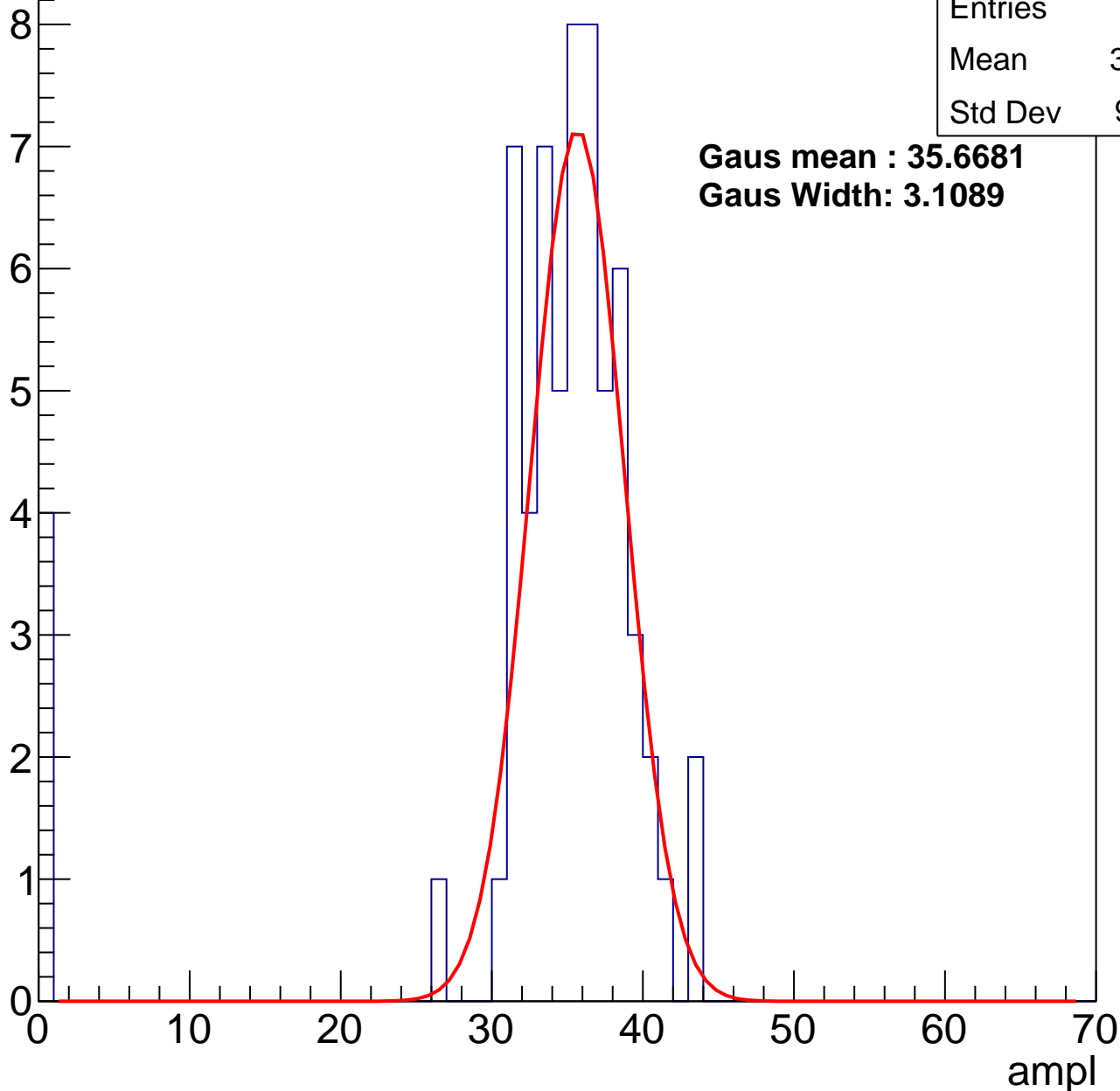
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	32.92
Std Dev	9.061

Gaus mean : 35.6681

Gaus Width: 3.1089



B1L103S, U24-ch111, adc2

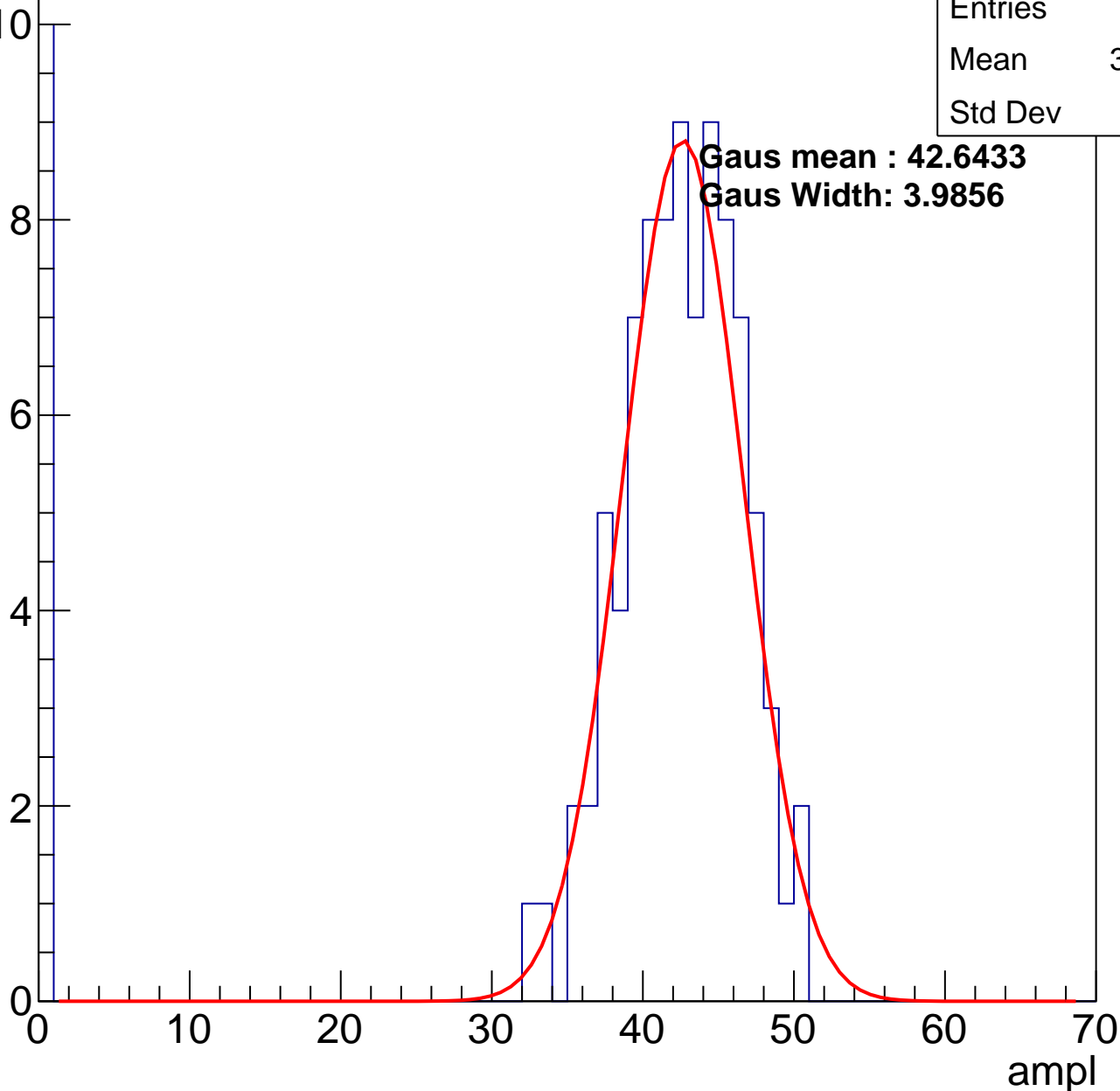
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	99
Mean	37.88
Std Dev	13.2

Gaus mean : 42.6433

Gaus Width: 3.9856

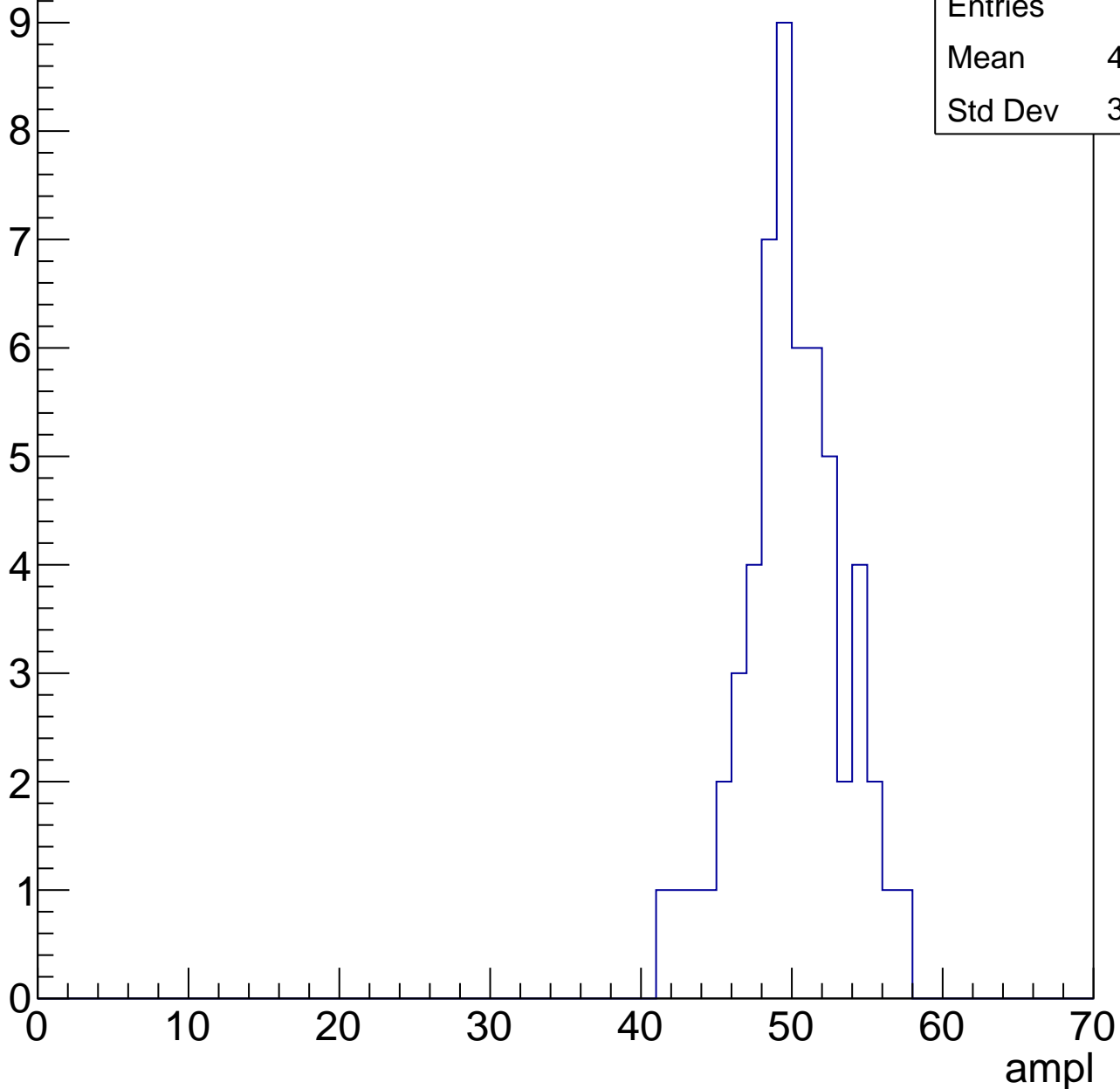


B1L103S, U24-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	49.54
Std Dev	3.359

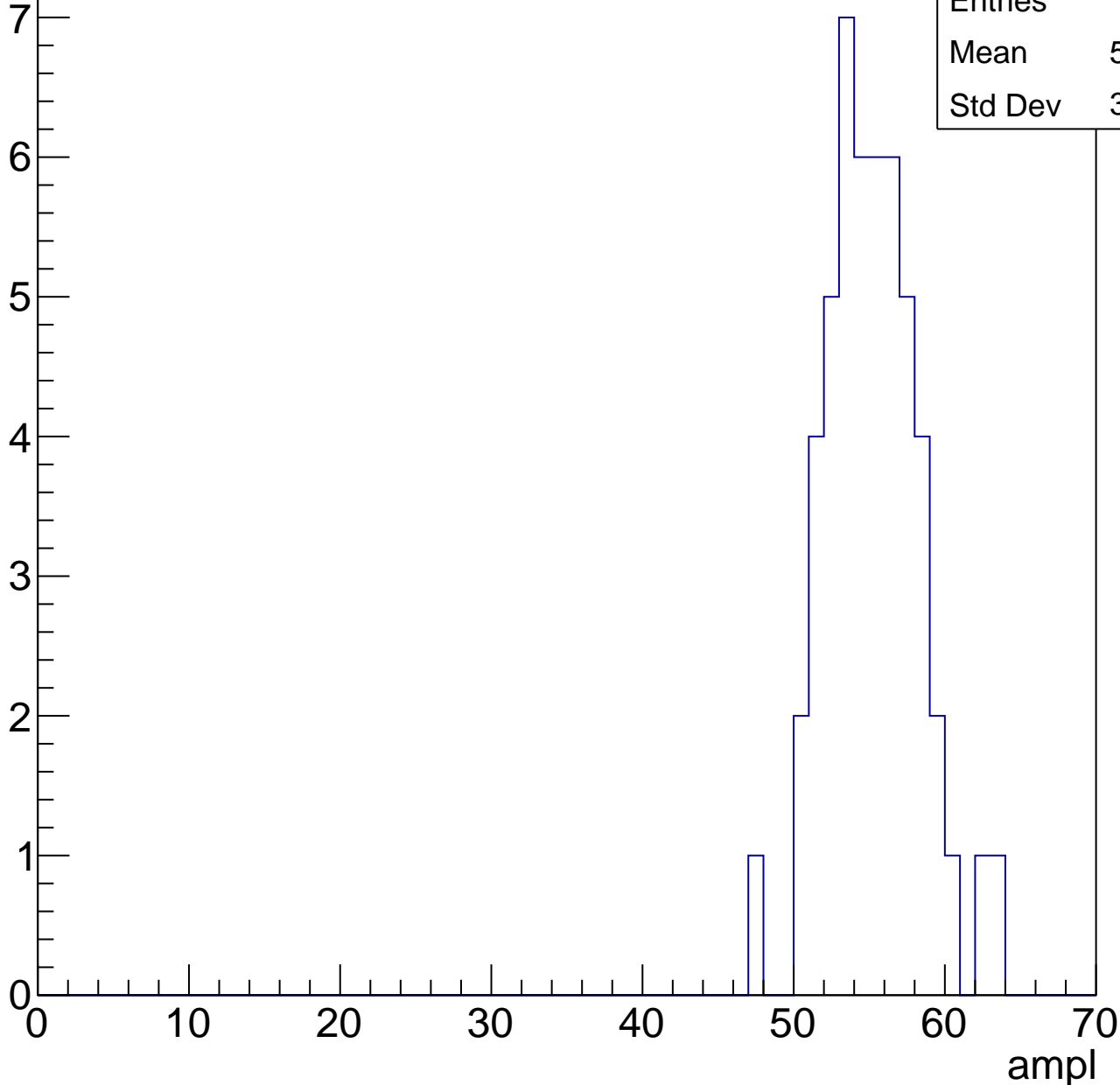


B1L103S, U24-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	54.75
Std Dev	3.086

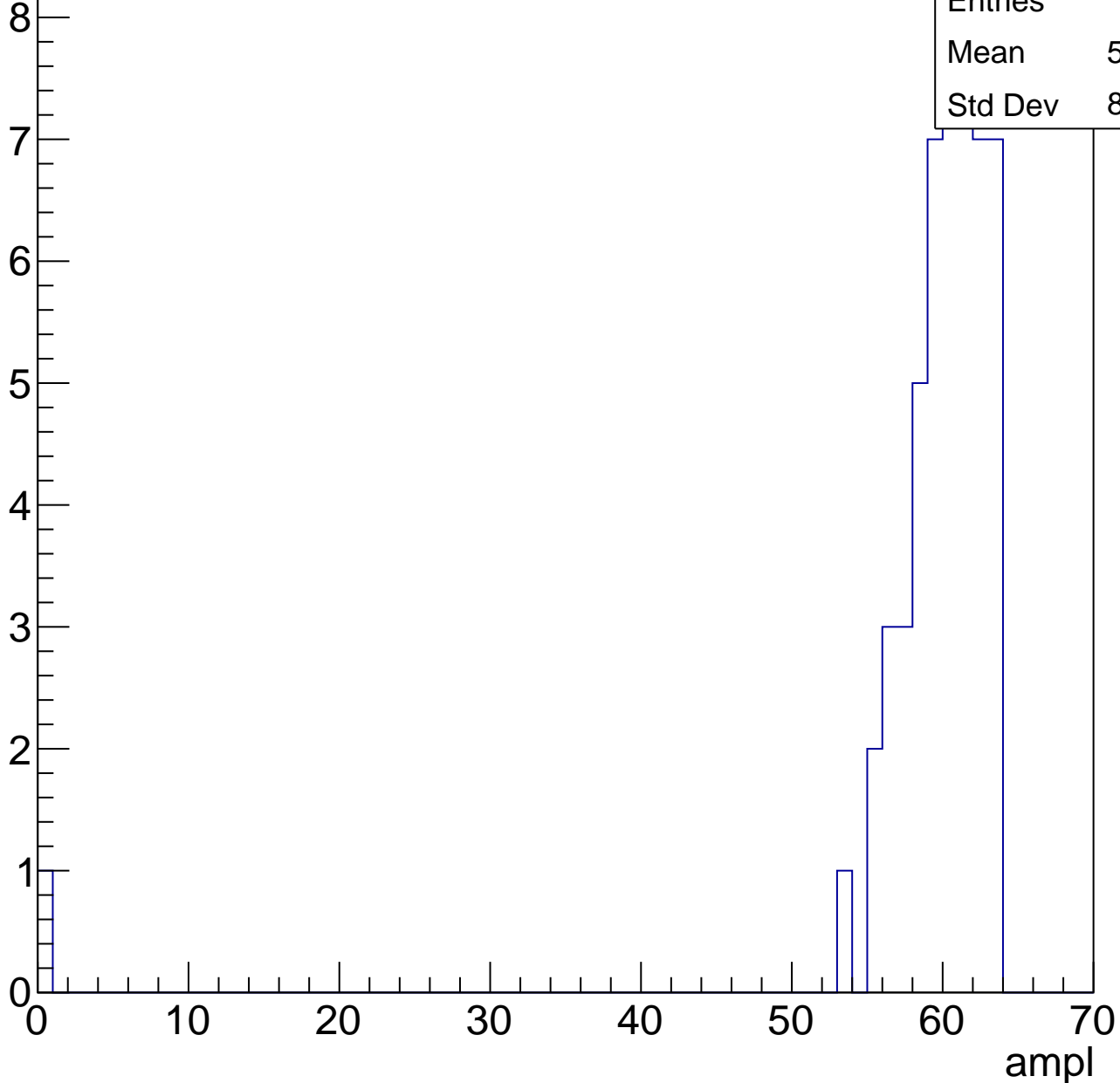


B1L103S, U24-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.62
Std Dev	8.549



B1L103S, U24-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

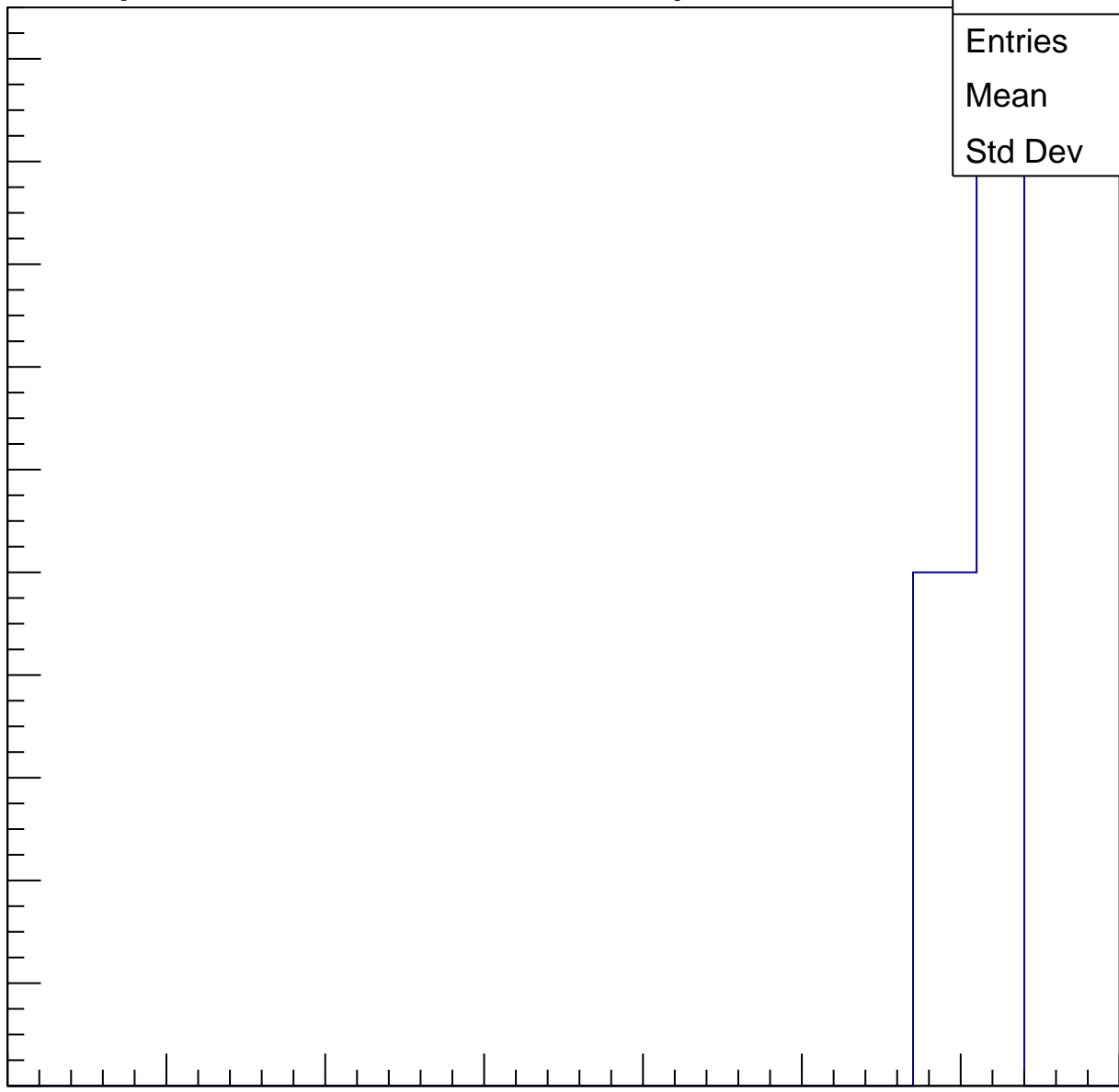
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	10
Mean	60.6
Std Dev	1.96

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U24-ch112, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	137
Mean	22.37
Std Dev	13.52

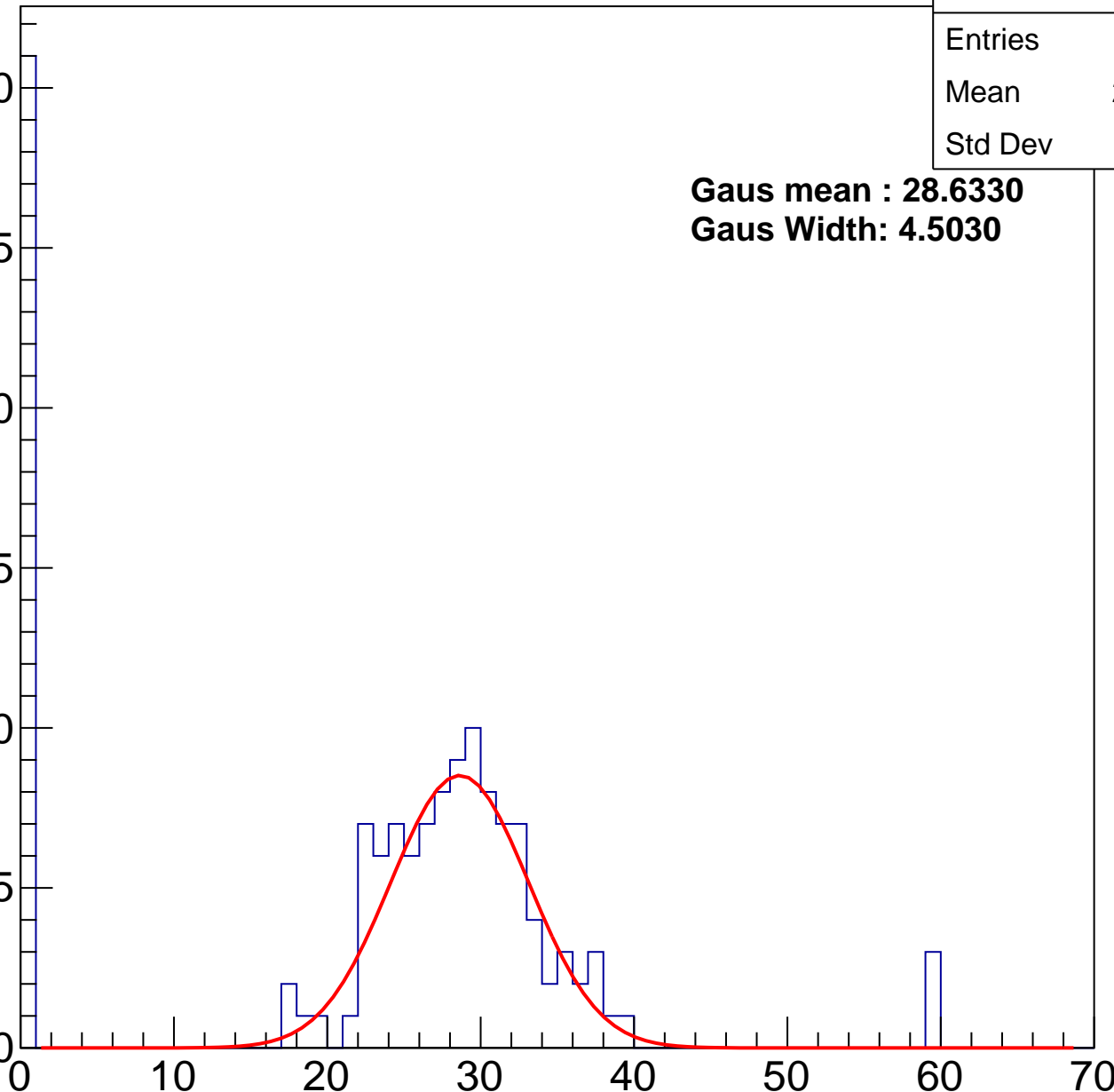
Gaus mean : 28.6330

Gaus Width: 4.5030

Entry

30
25
20
15
10
5
0

ampl



B1L103S, U24-ch112, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	30.57
Std Dev	13.77

Gaus mean : 36.5988

Gaus Width: 5.5124

Entry

10

8

6

4

2

0

0

10

20

30

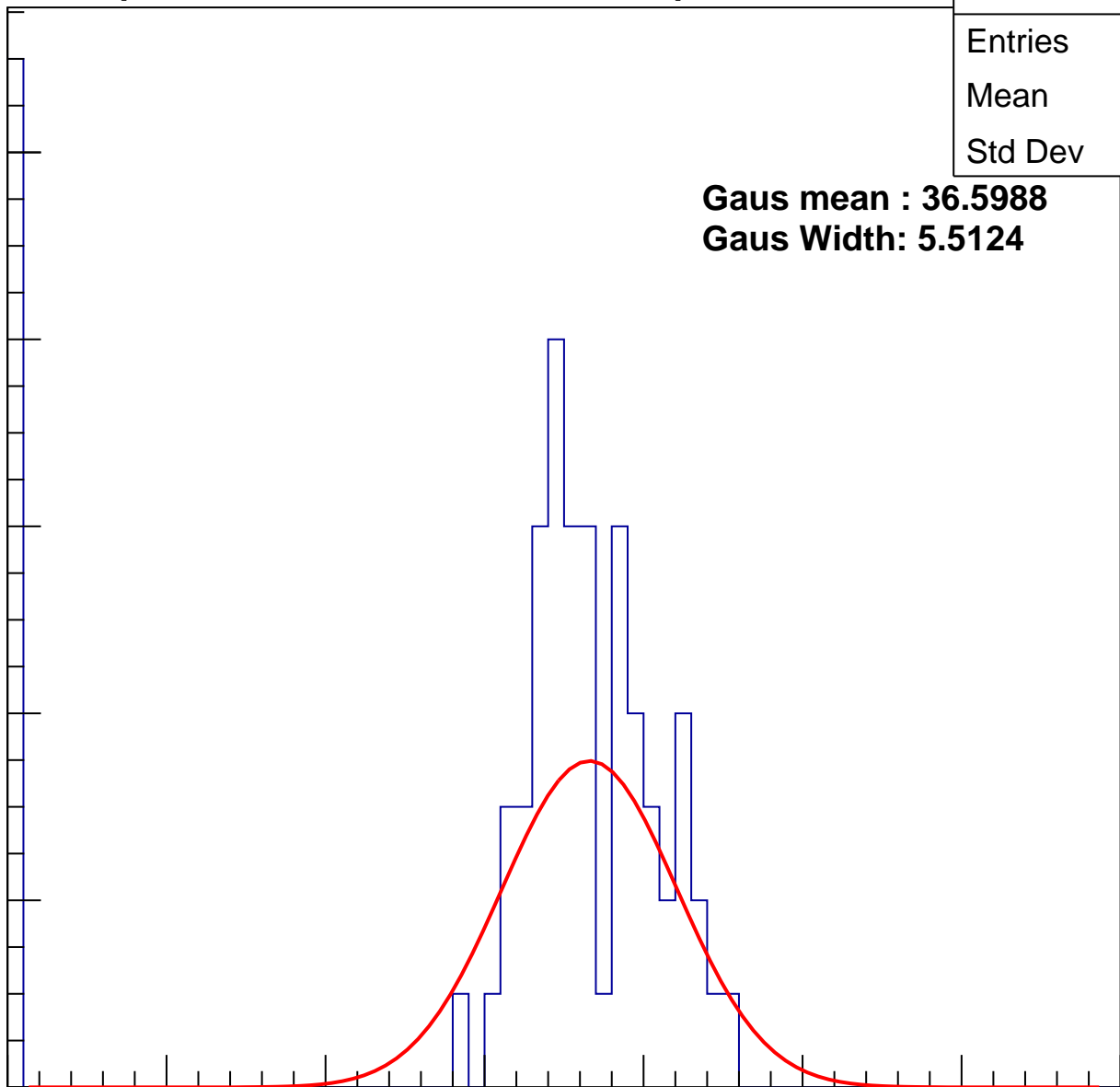
40

50

60

70

ampl



B1L103S, U24-ch112, adc2

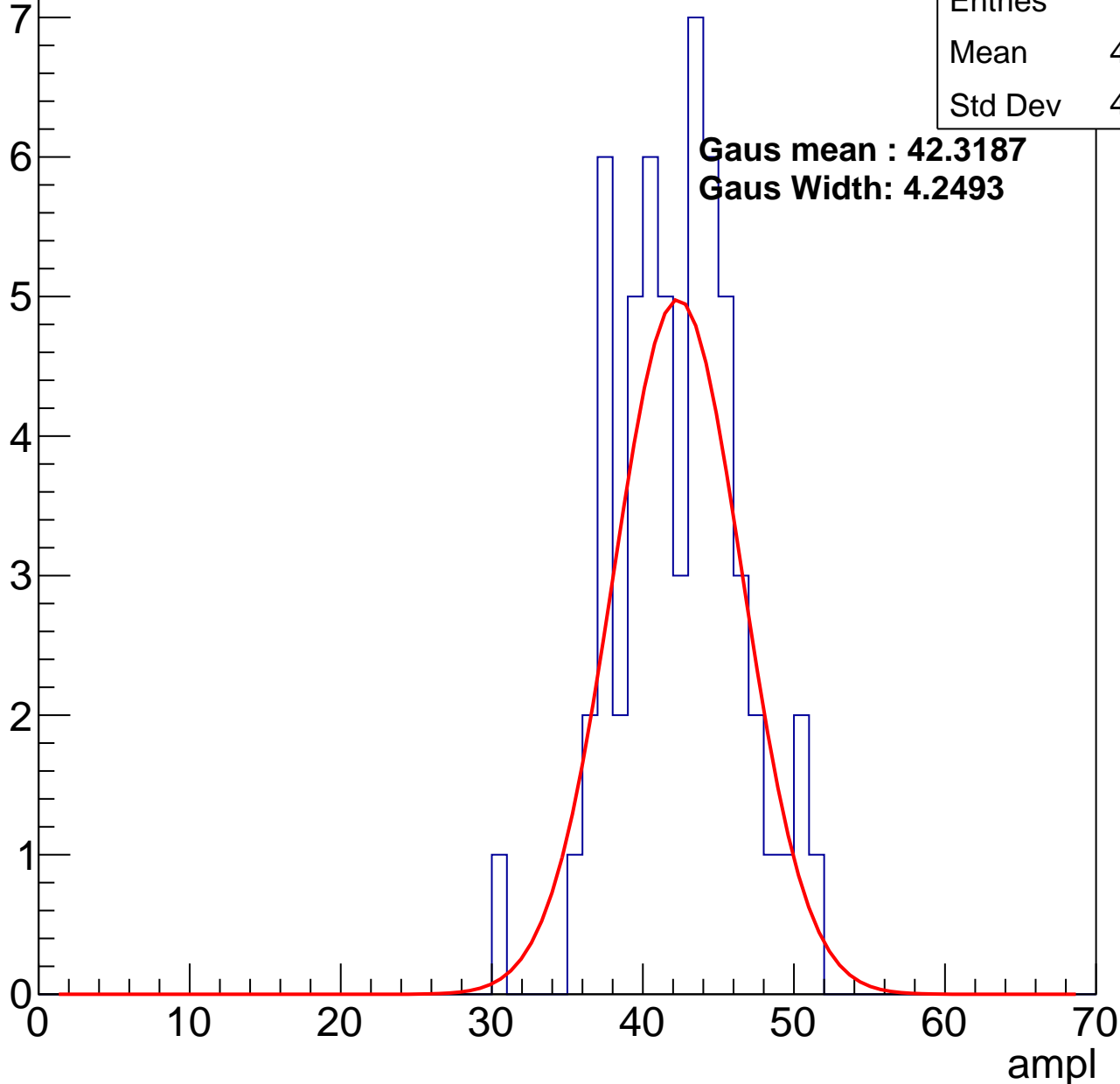
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	41.88
Std Dev	4.093

Gaus mean : 42.3187

Gaus Width: 4.2493

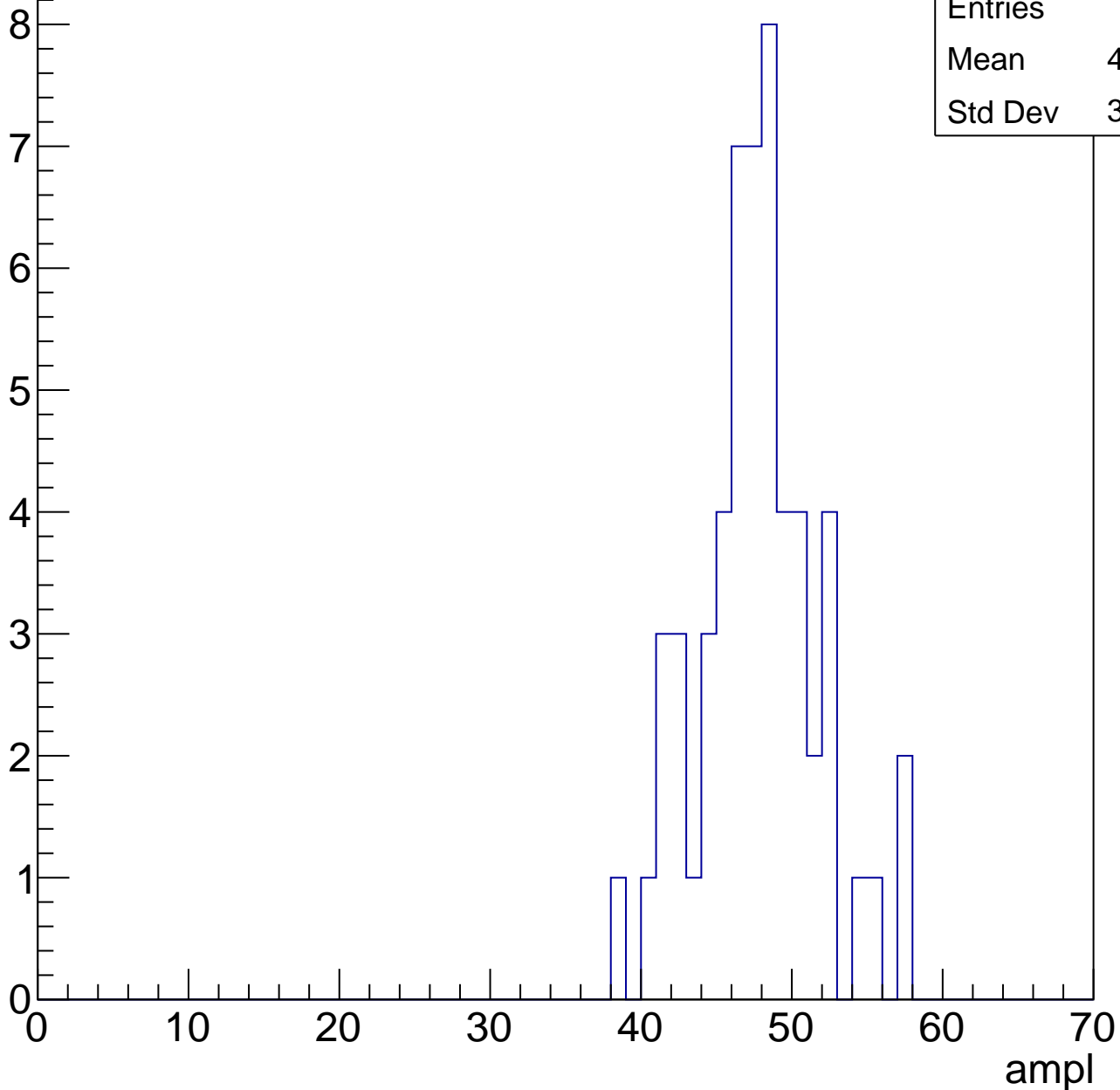


B1L103S, U24-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	47.25
Std Dev	3.965

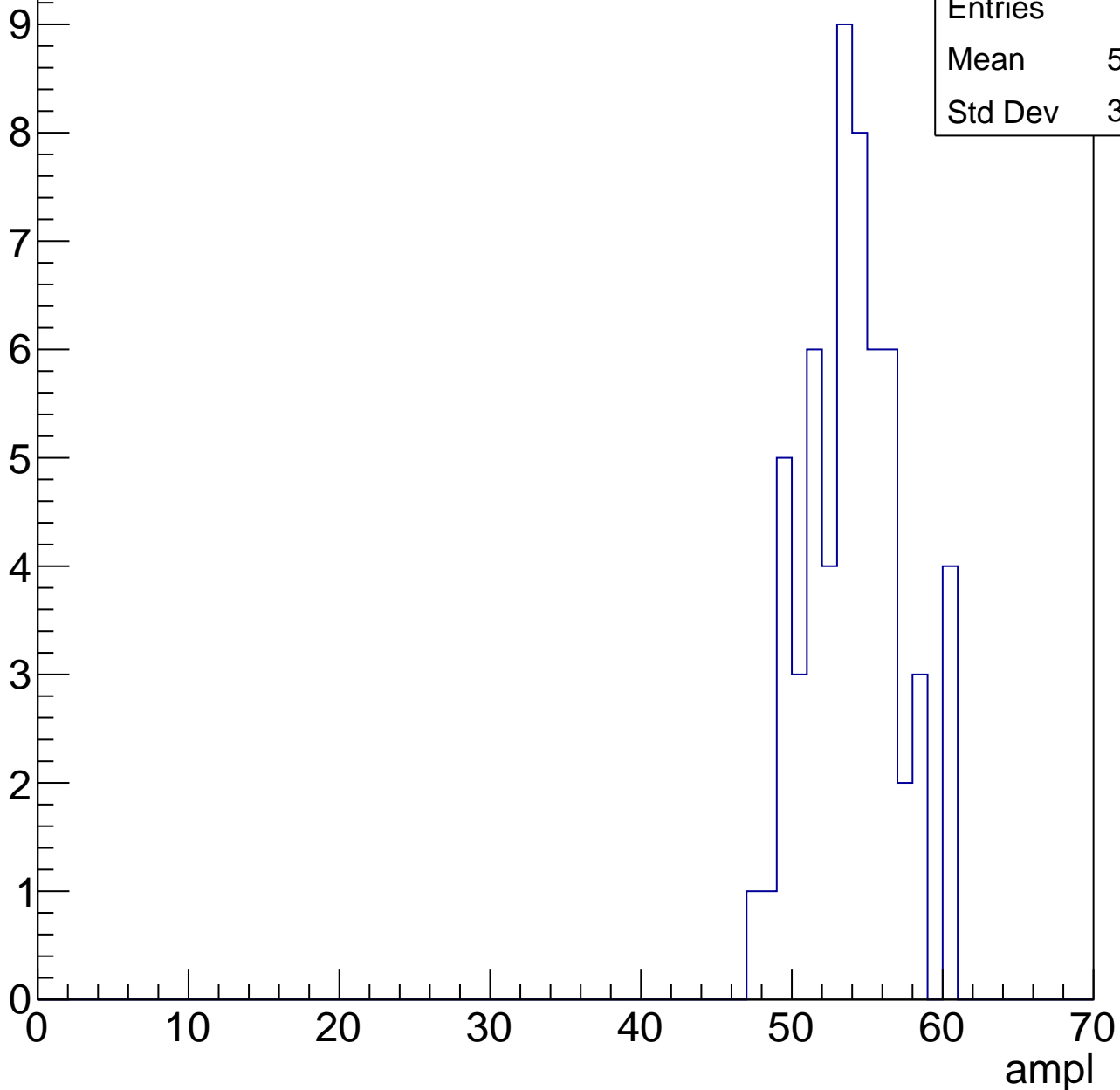


B1L103S, U24-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.57
Std Dev	3.124



B1L103S, U24-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

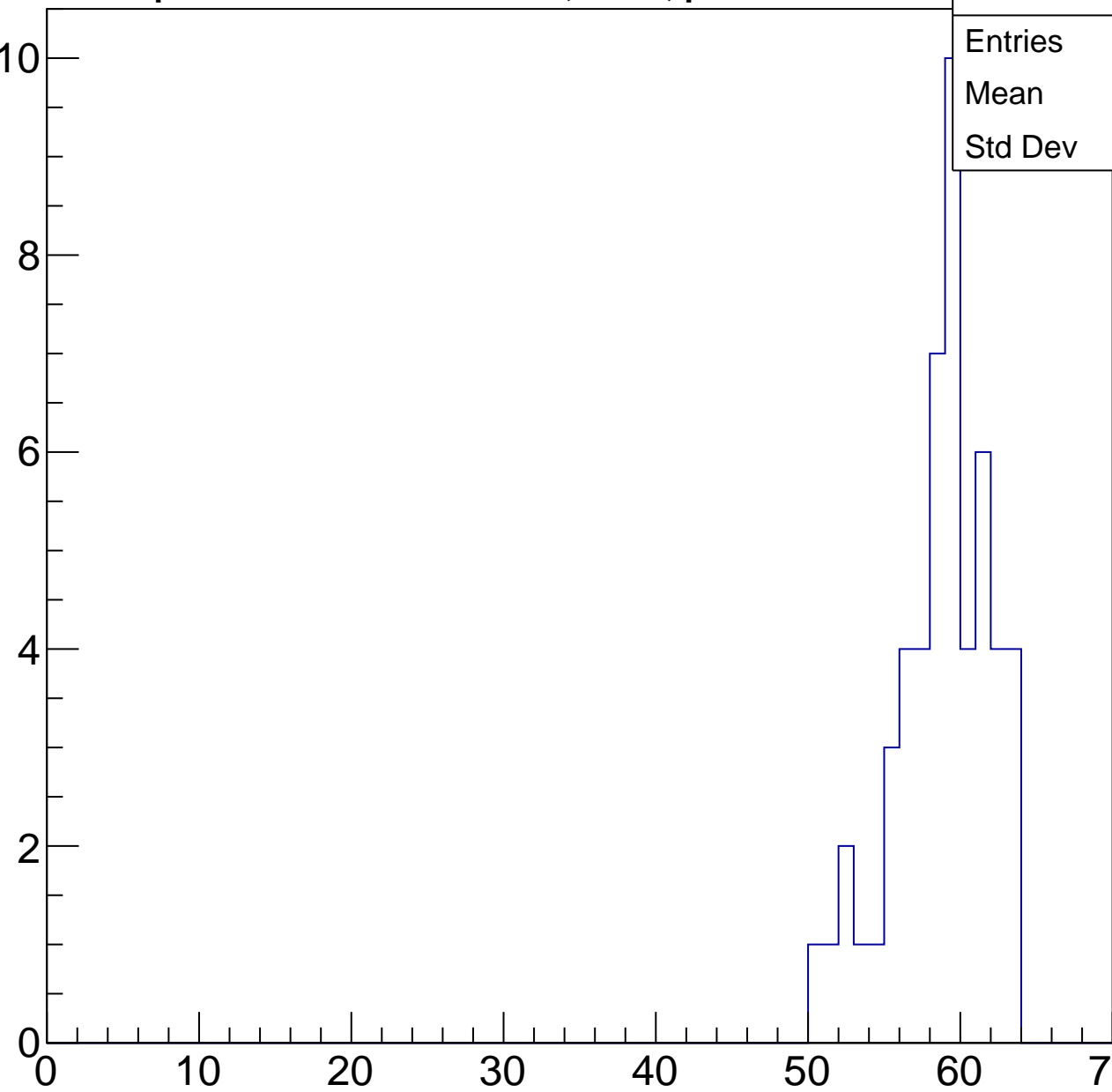
Entries	52
Mean	58.29
Std Dev	3.14

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

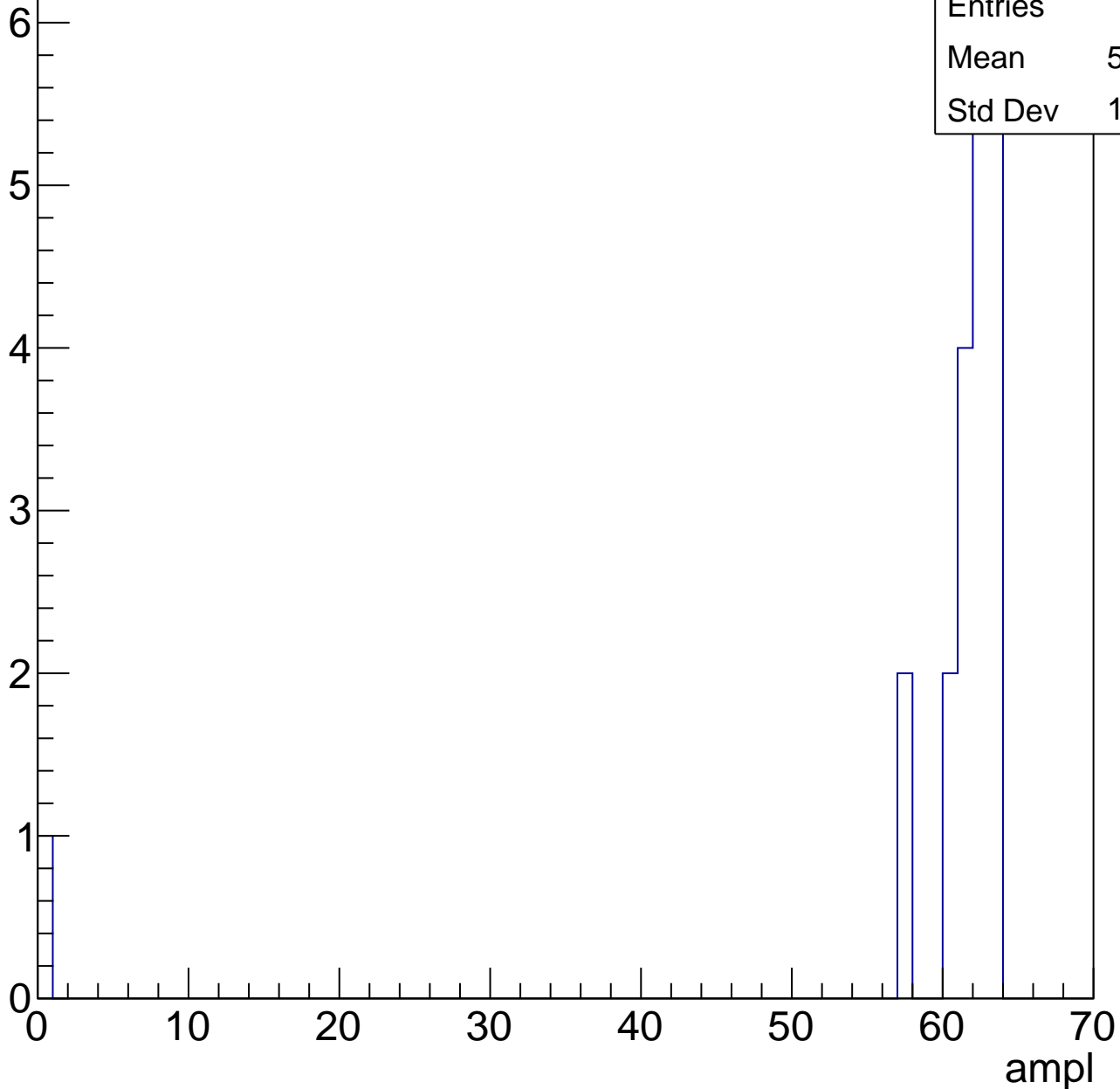


B1L103S, U24-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	58.48
Std Dev	13.19

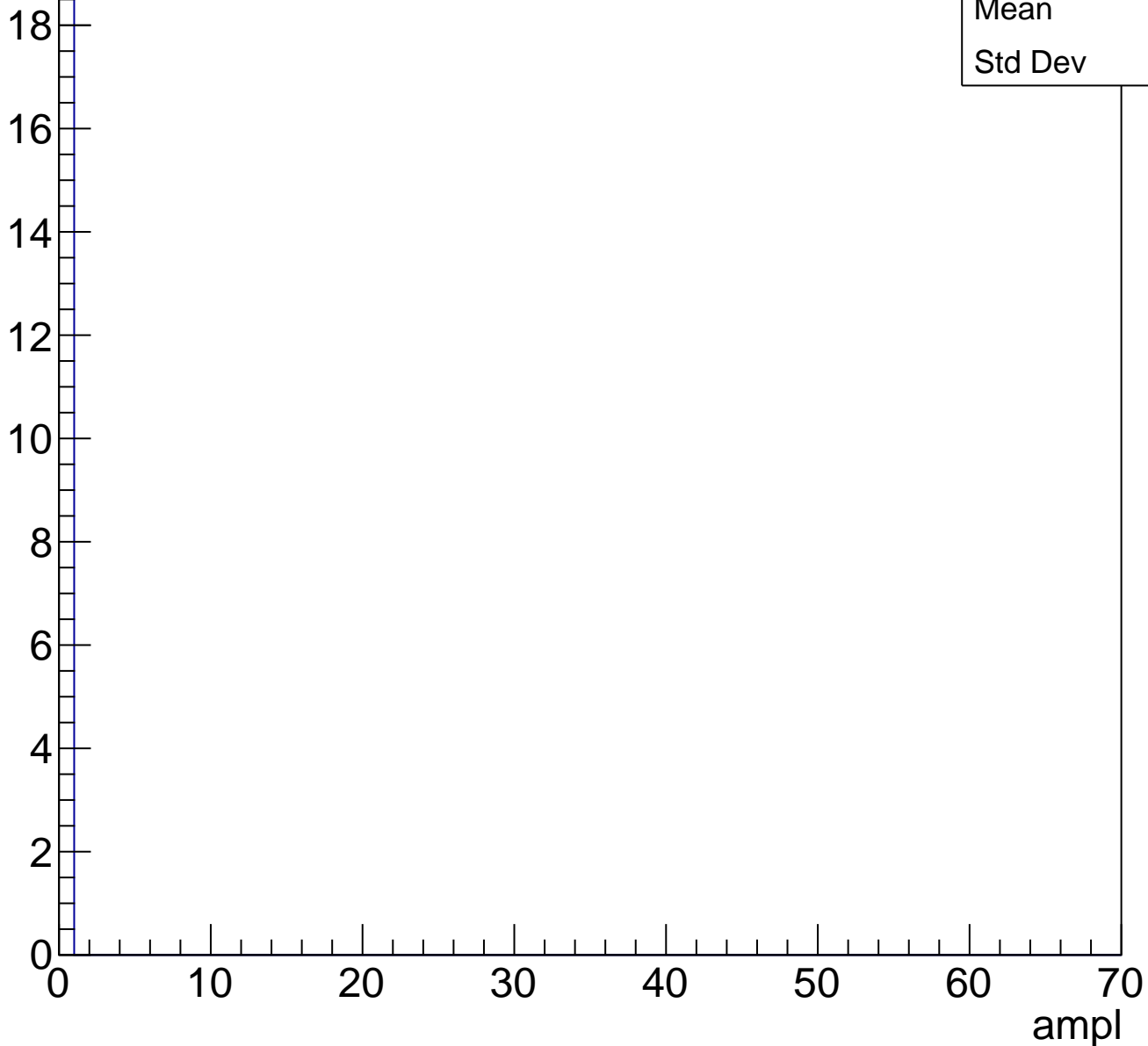


B1L103S, U24-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



B1L103S, U24-ch113, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	22.11
Std Dev	12.47

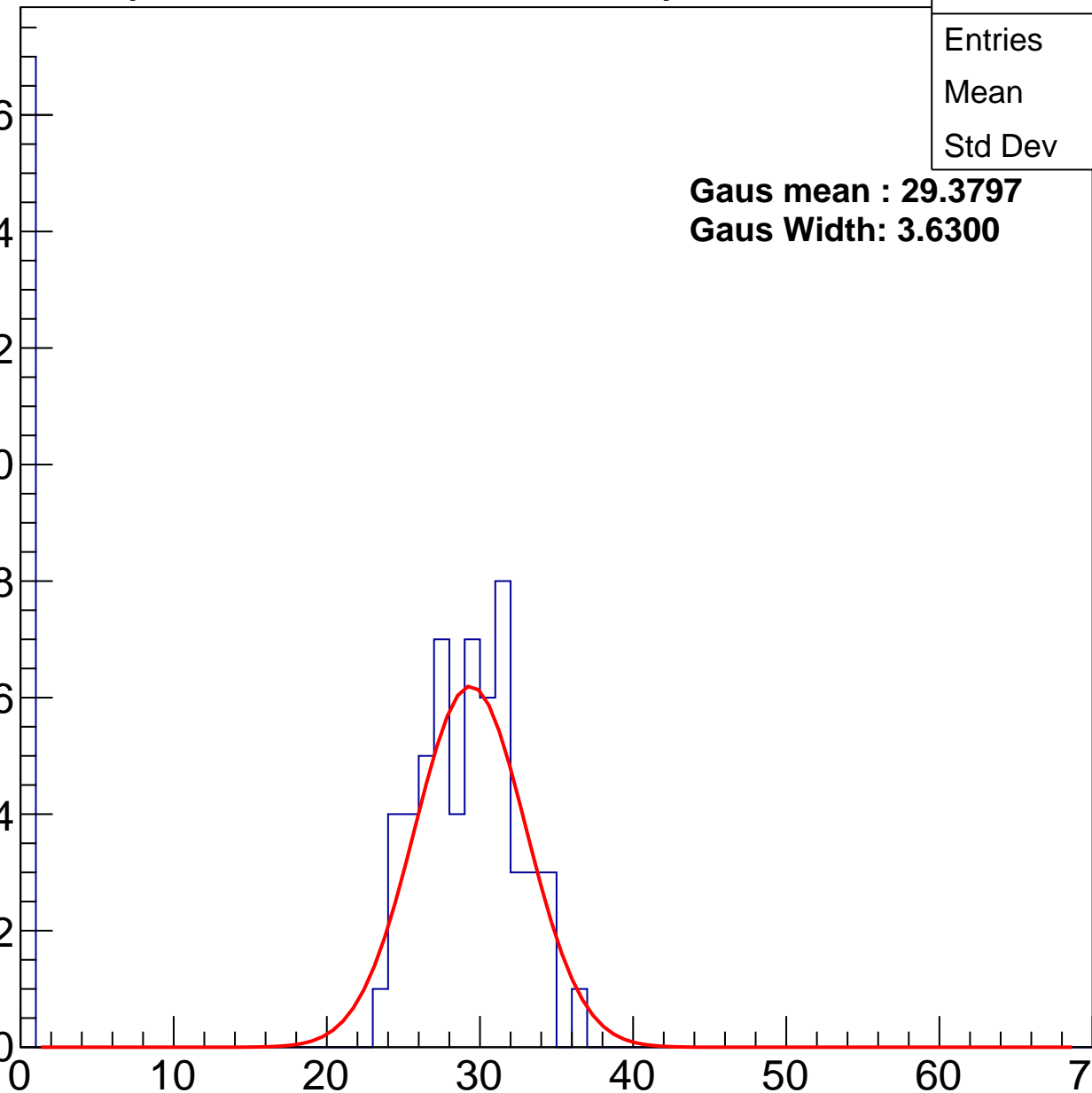
Gaus mean : 29.3797

Gaus Width: 3.6300

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L103S, U24-ch113, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	30.31
Std Dev	13.06

Gaus mean : 36.1494

Gaus Width: 3.7405

Entry

12

10

8

6

4

2

0

0

10

20

30

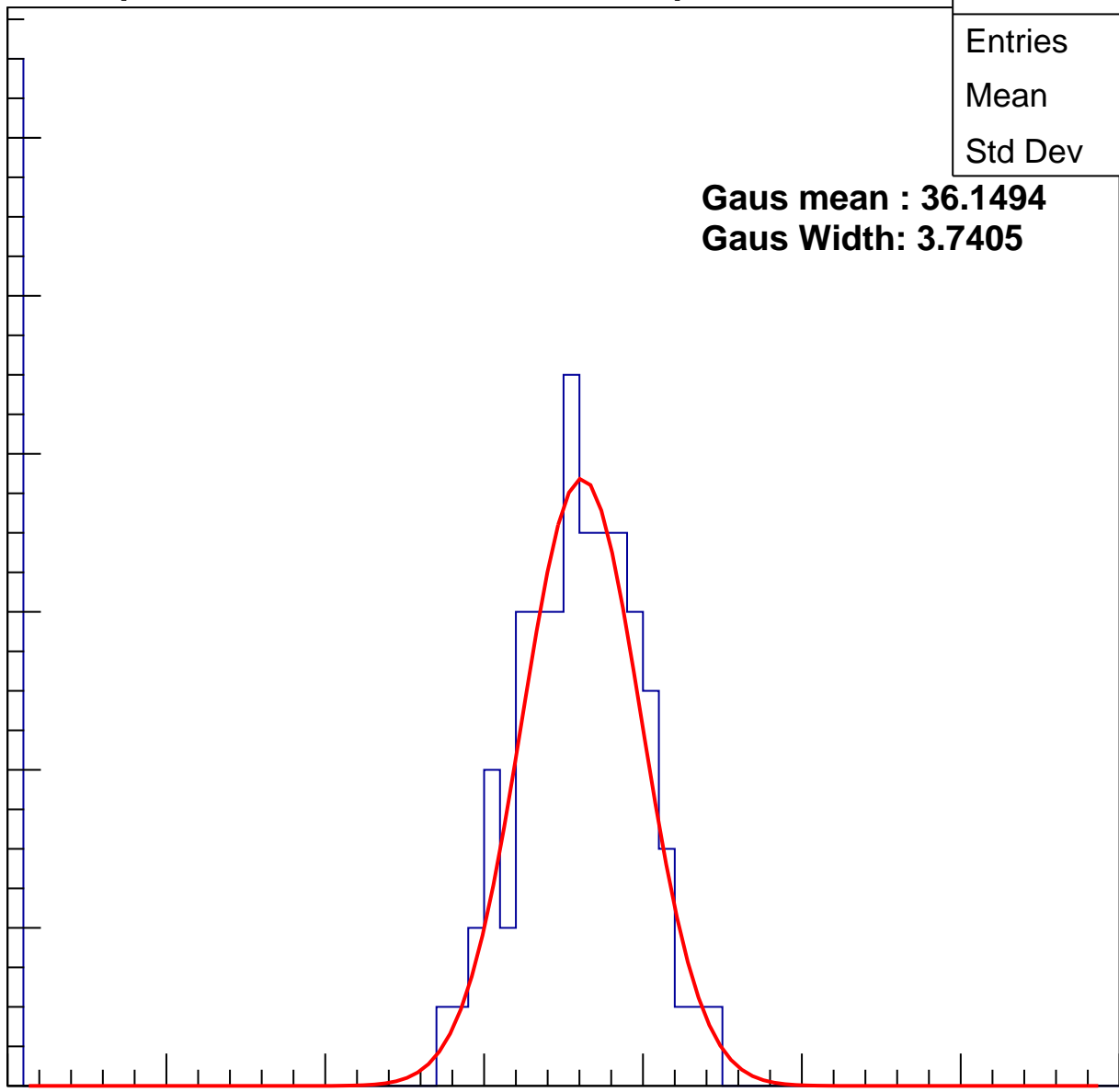
40

50

60

70

ampl



B1L103S, U24-ch113, adc2

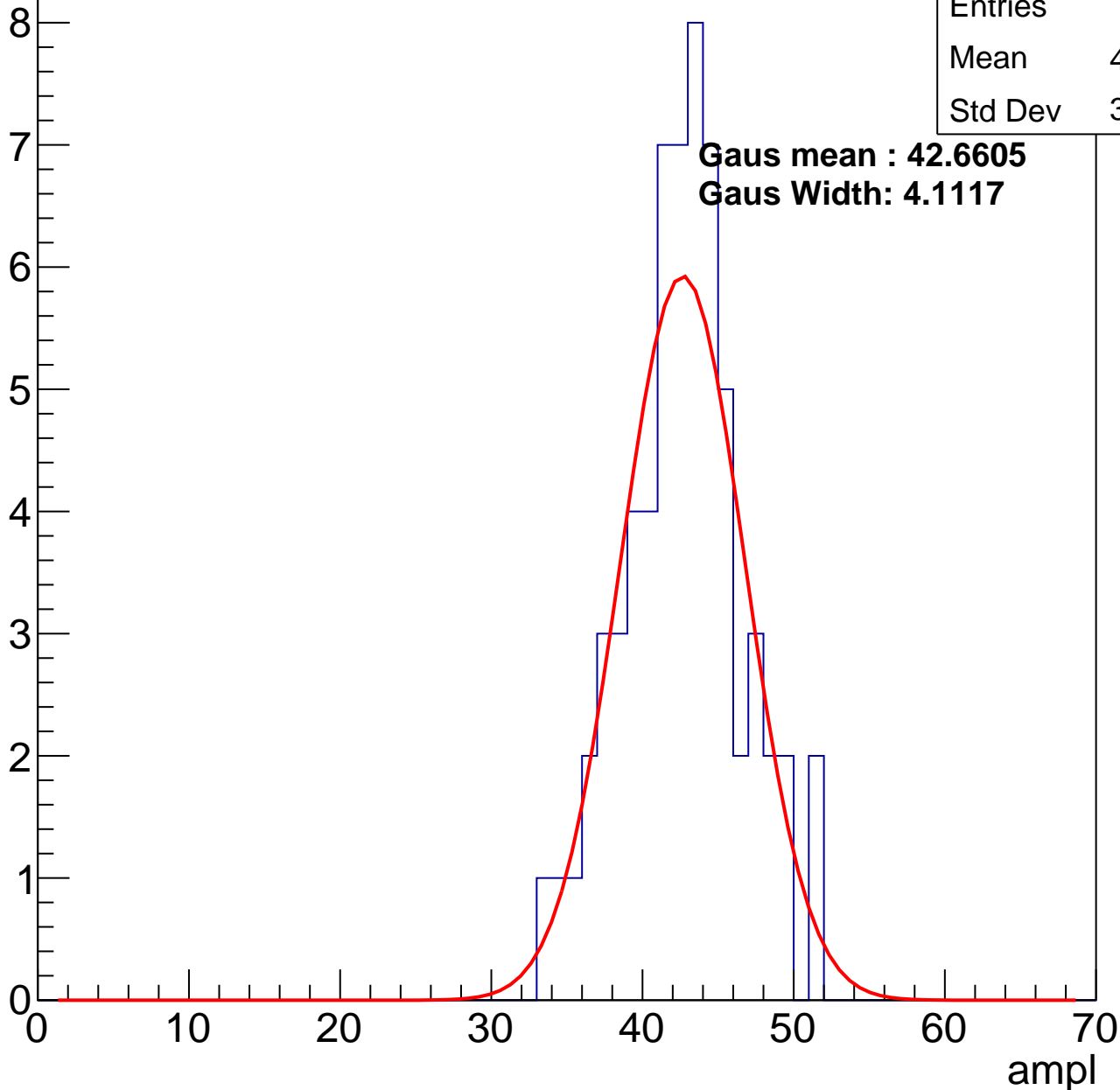
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	42.22
Std Dev	3.883

Gaus mean : 42.6605

Gaus Width: 4.1117

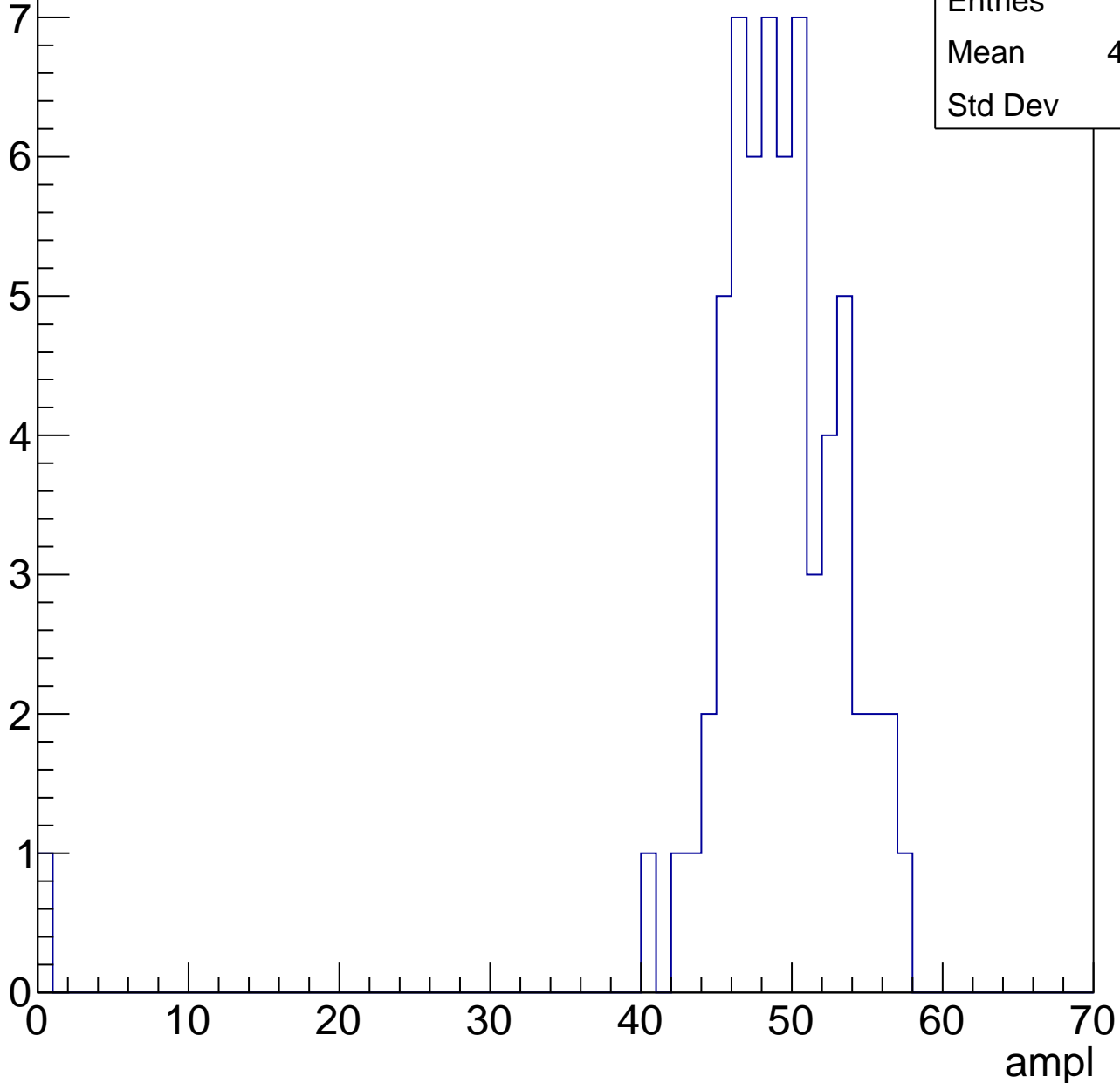


B1L103S, U24-ch113, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	48.17
Std Dev	7.09

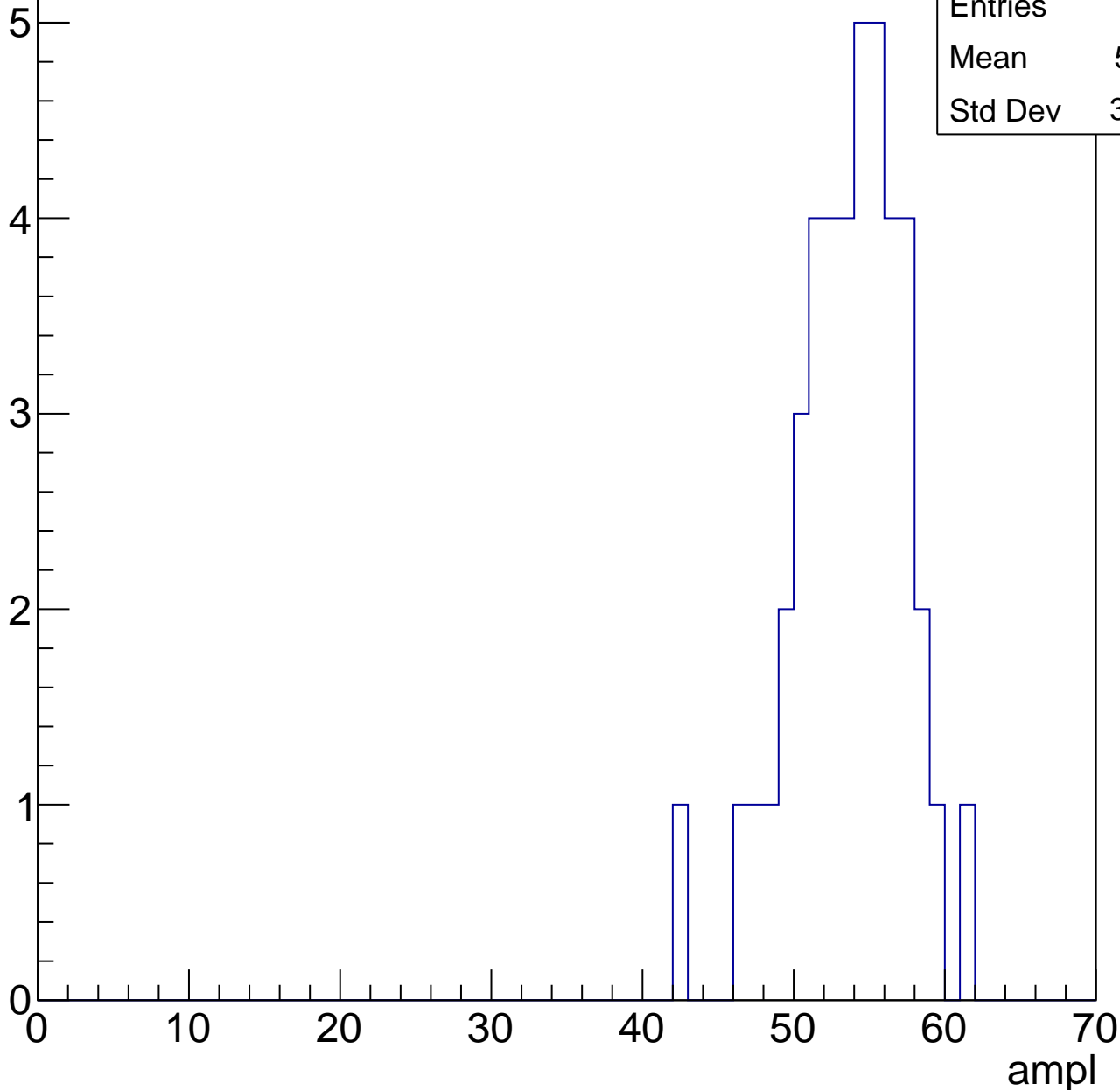


B1L103S, U24-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	53.21
Std Dev	3.683

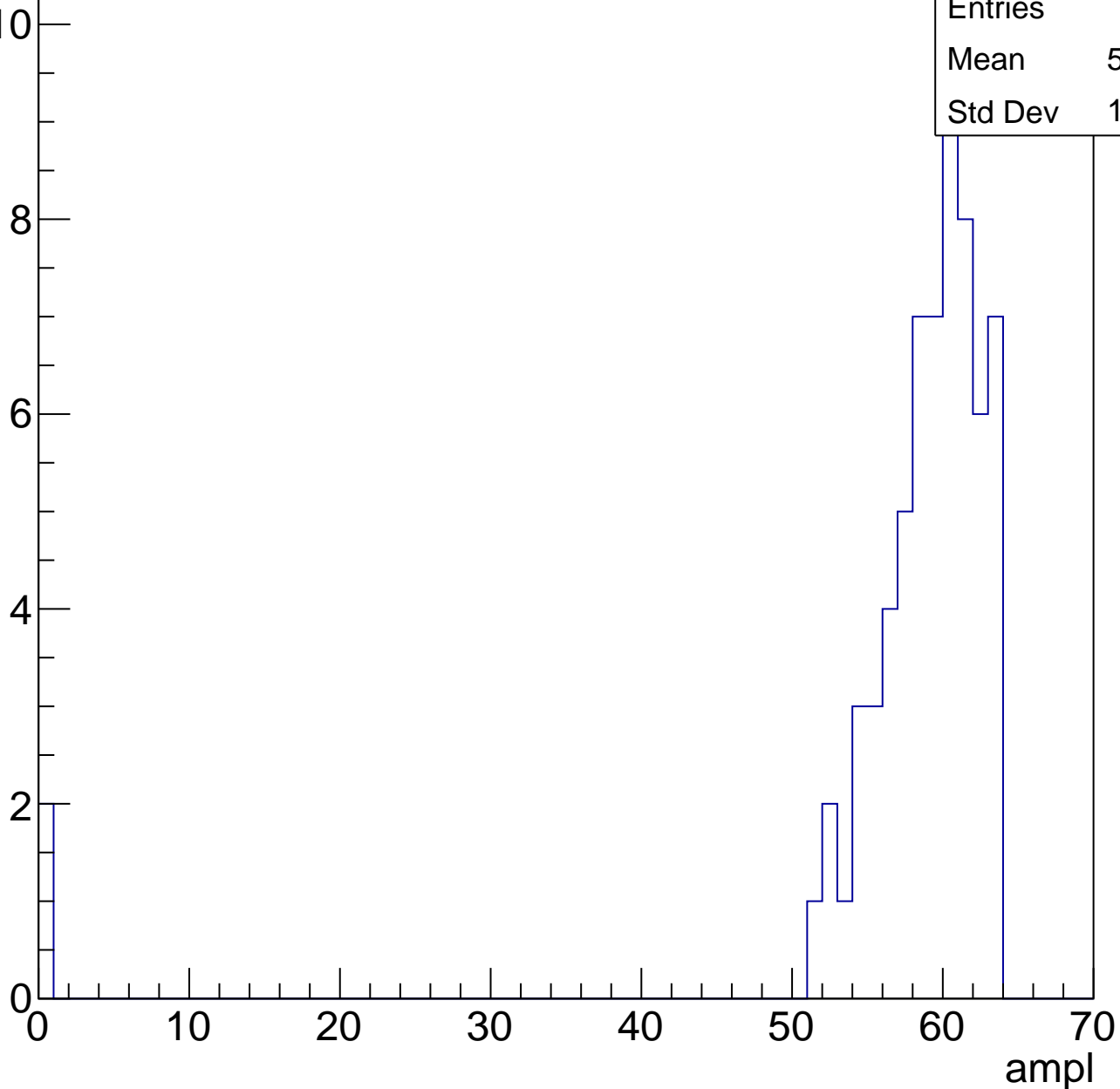


B1L103S, U24-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

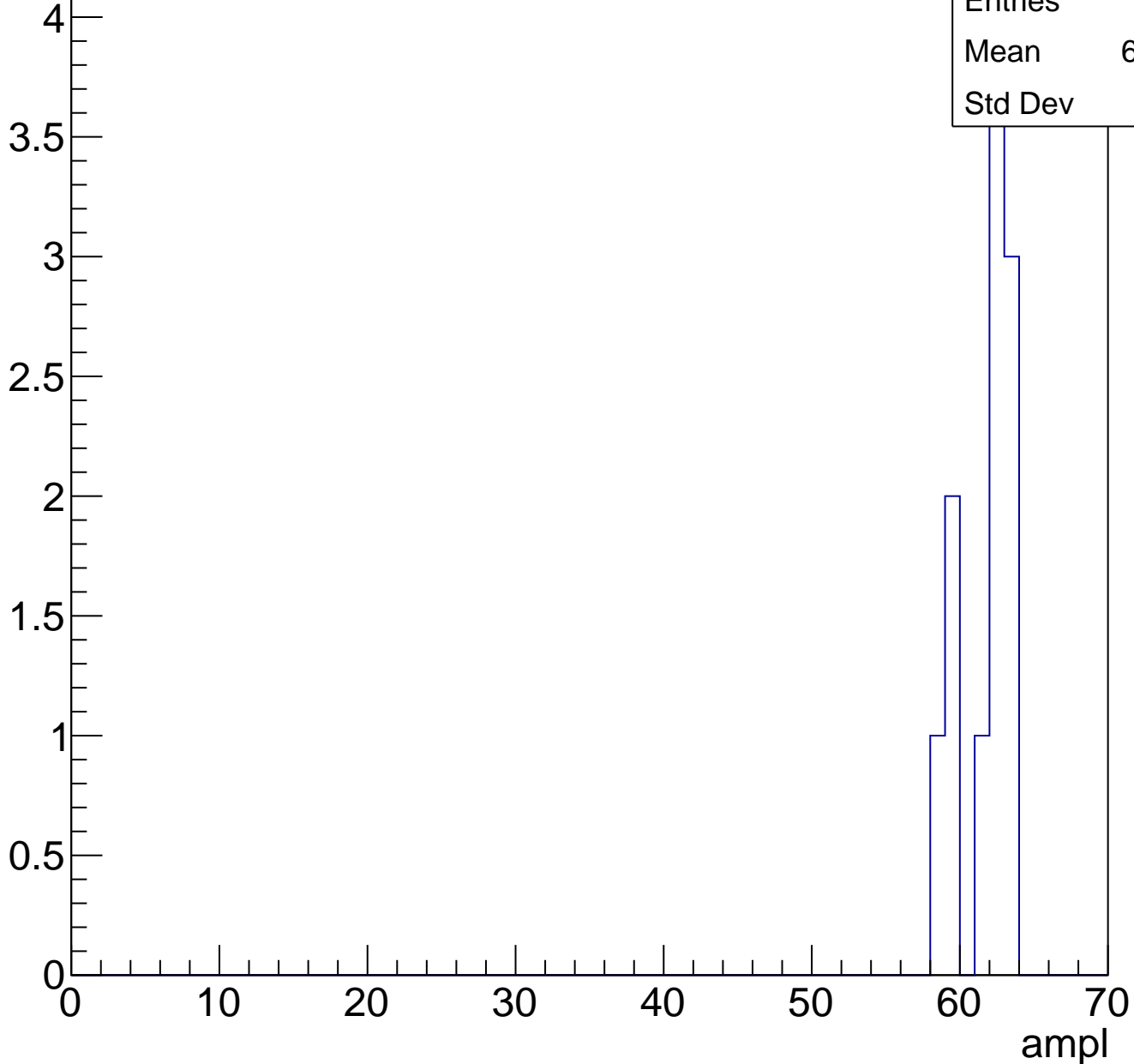
Entries	66
Mean	57.03
Std Dev	10.52



B1L103S, U24-ch113, adc6

calib_packv5_041523_1651.root, FC#0, port C2

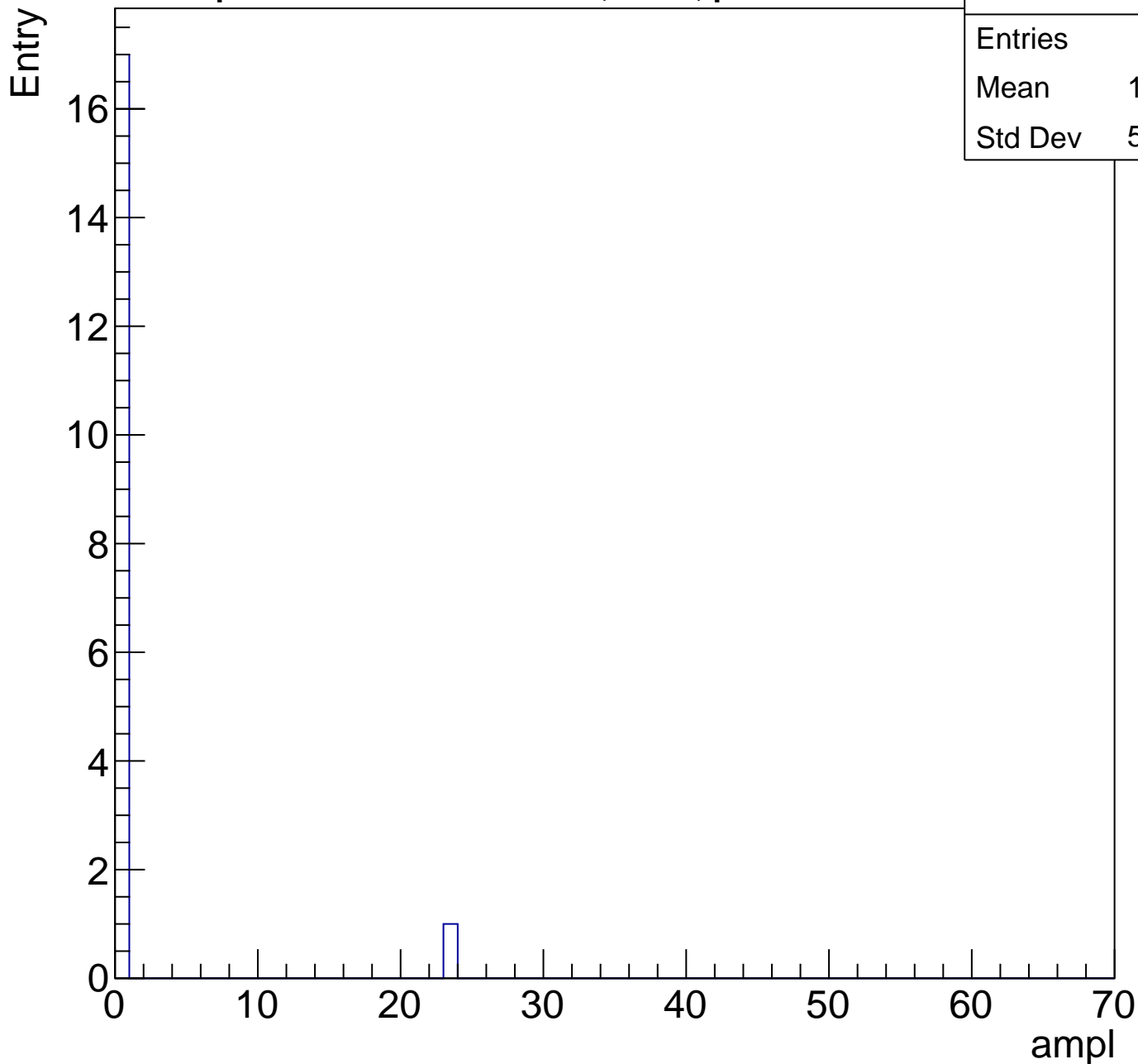
Entry



B1L103S, U24-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.278
Std Dev	5.268



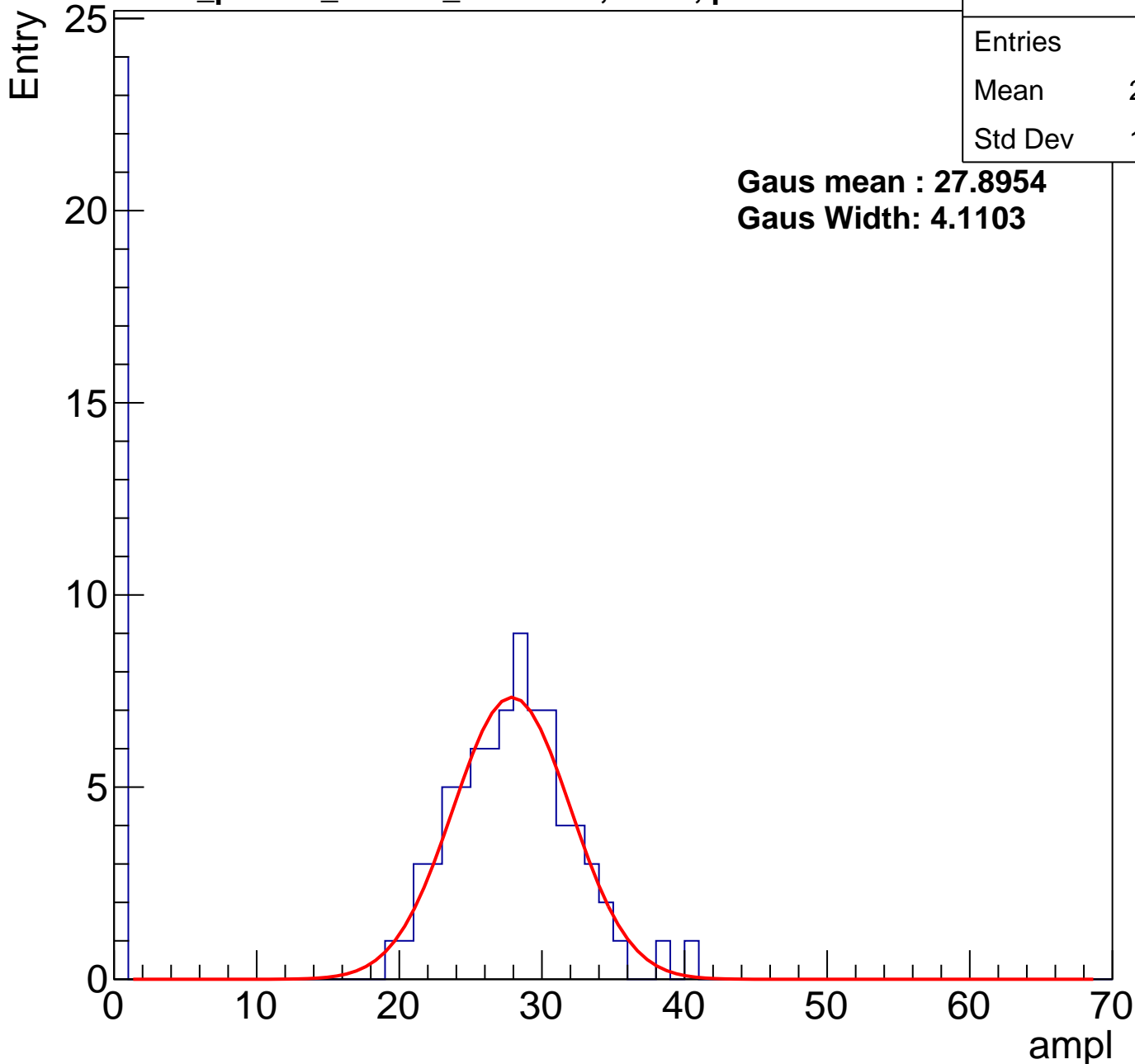
B1L103S, U24-ch114, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	20.95
Std Dev	12.29

Gaus mean : 27.8954

Gaus Width: 4.1103



B1L103S, U24-ch114, adc1

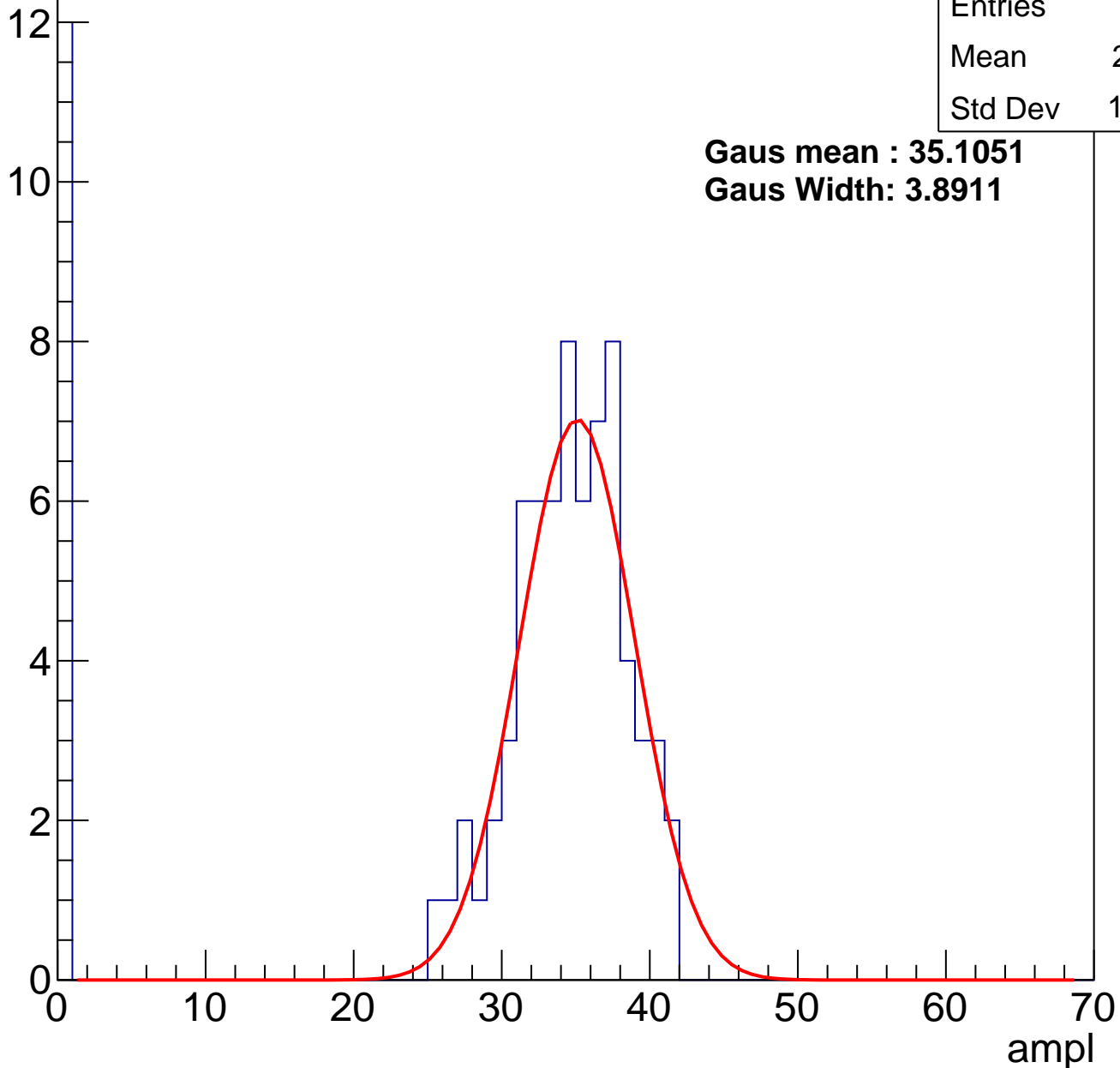
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	29.11
Std Dev	12.59

Gaus mean : 35.1051

Gaus Width: 3.8911

Entry



B1L103S, U24-ch114, adc2

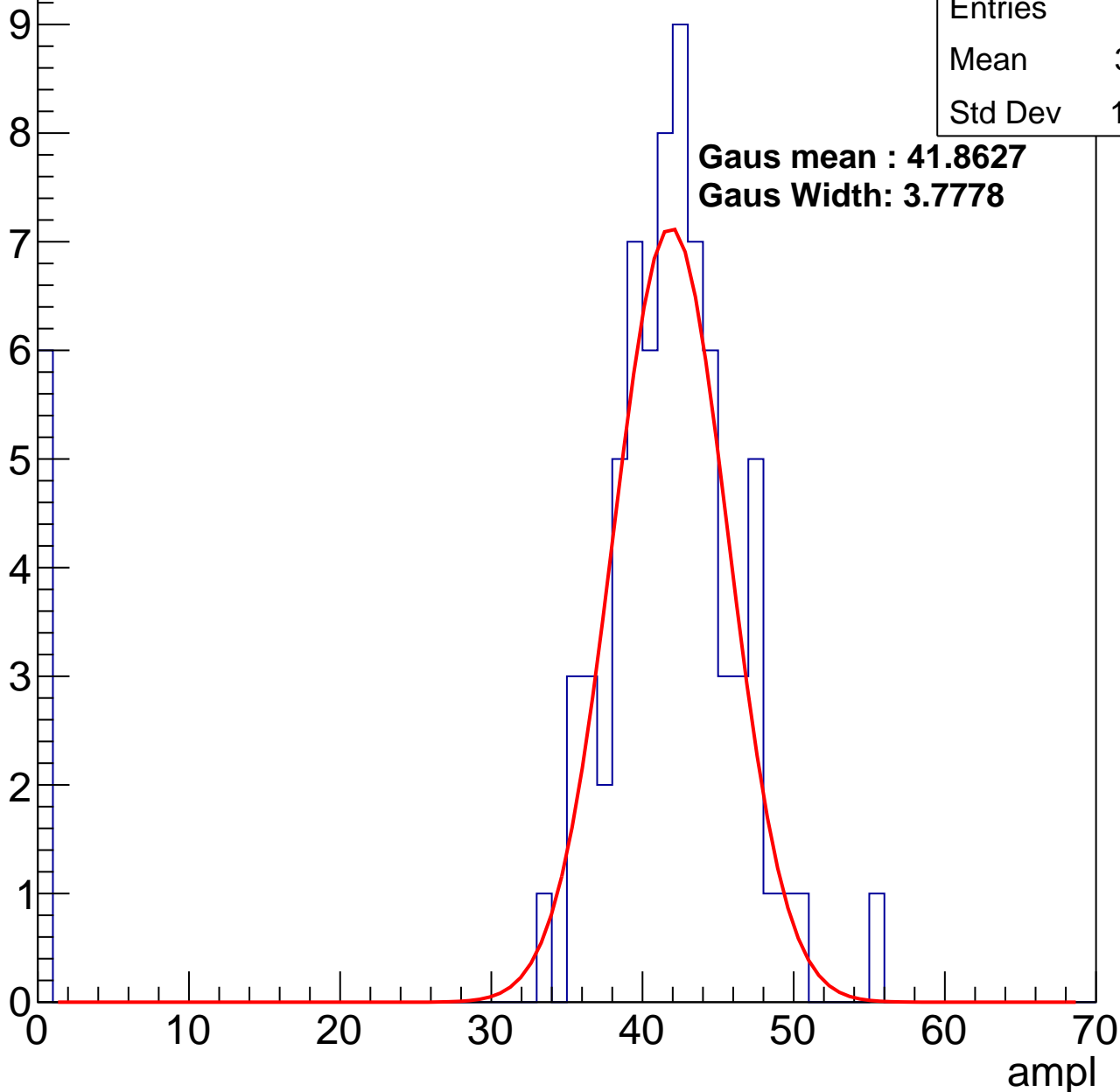
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	38.51
Std Dev	11.74

Gaus mean : 41.8627

Gaus Width: 3.7778

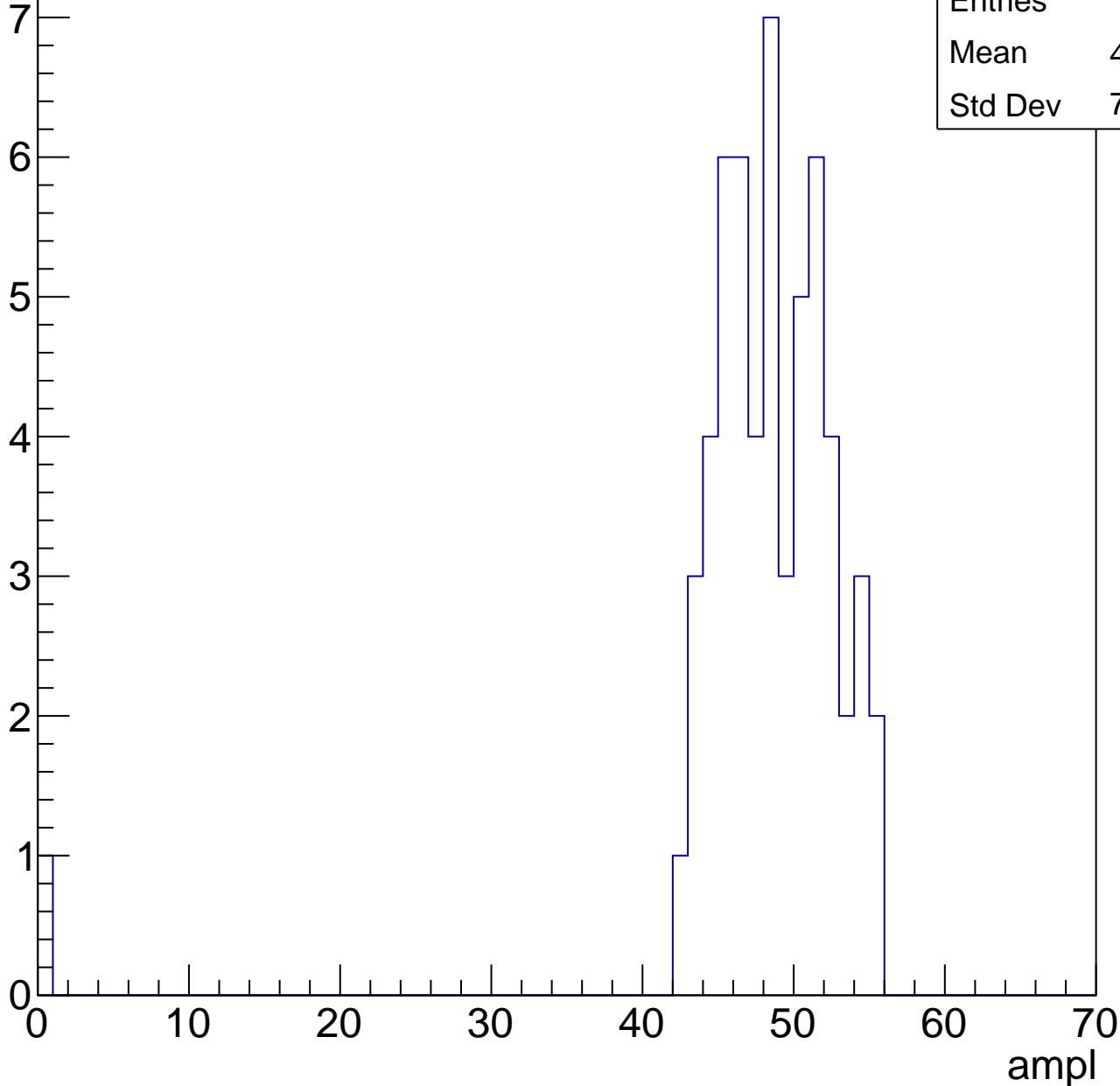


B1L103S, U24-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	47.47
Std Dev	7.185

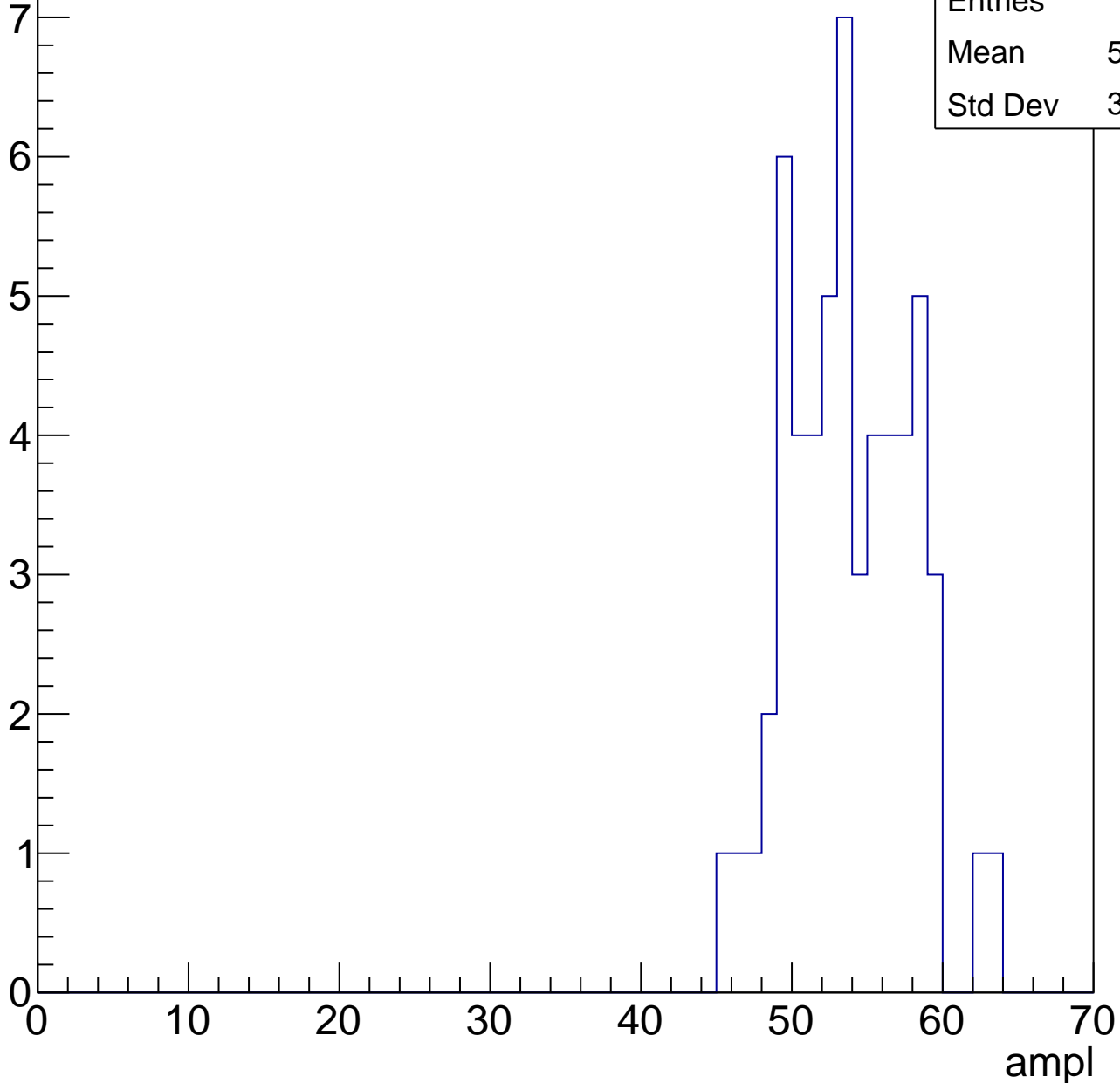


B1L103S, U24-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	53.38
Std Dev	3.958

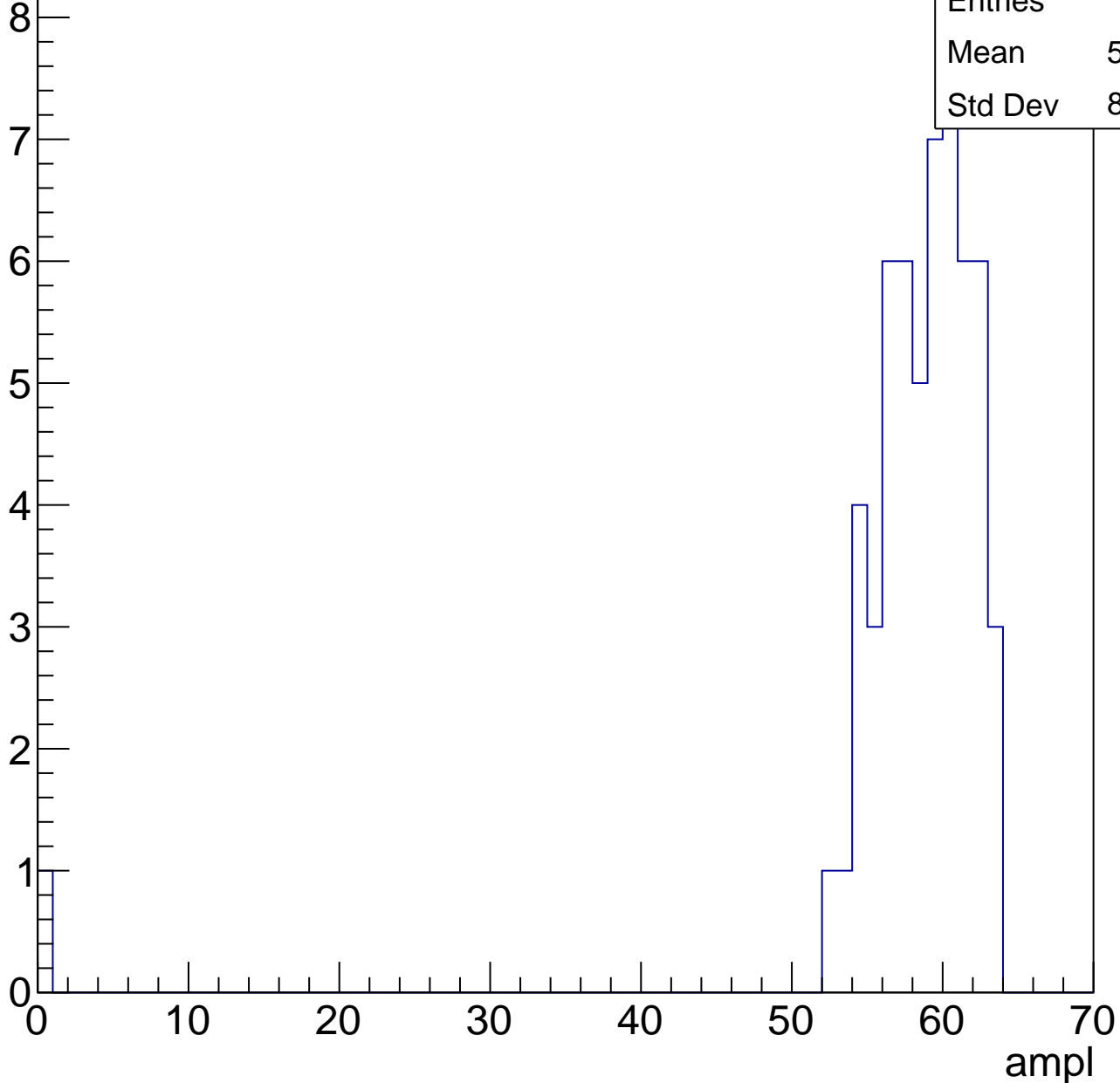


B1L103S, U24-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	57.44
Std Dev	8.156

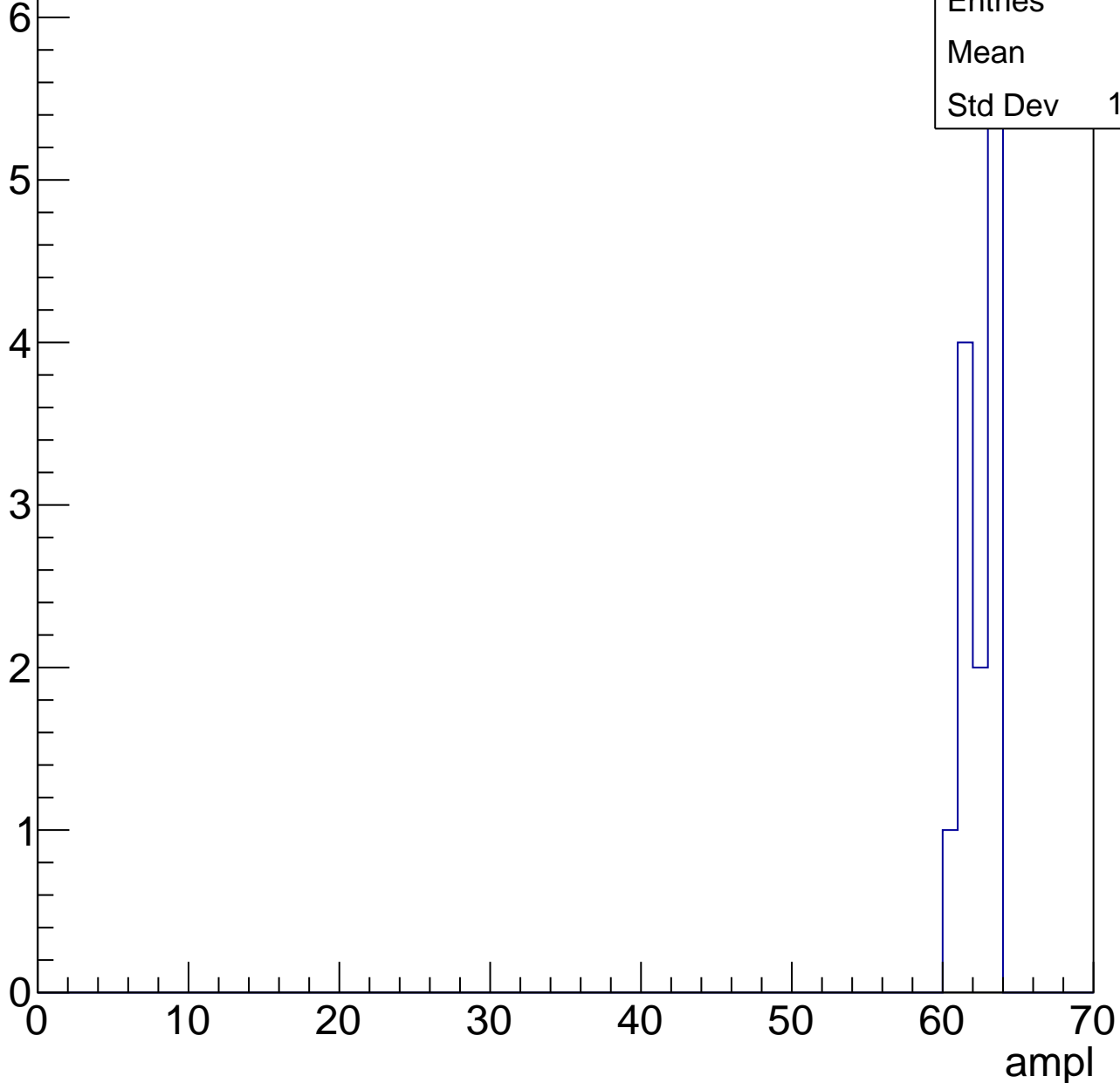


B1L103S, U24-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

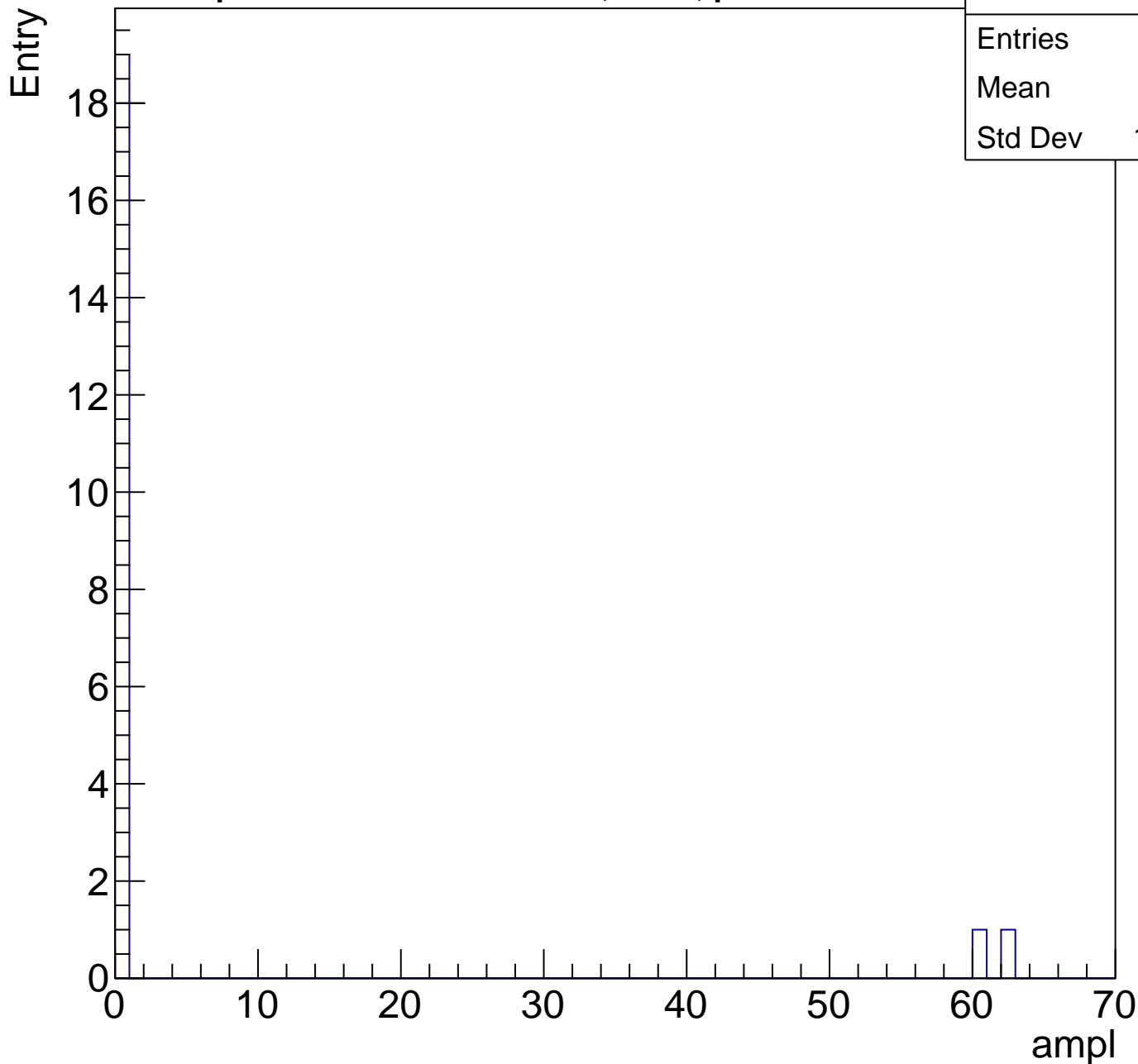
Entries	13
Mean	62
Std Dev	1.038



B1L103S, U24-ch114, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.91



B1L103S, U24-ch115, adc0

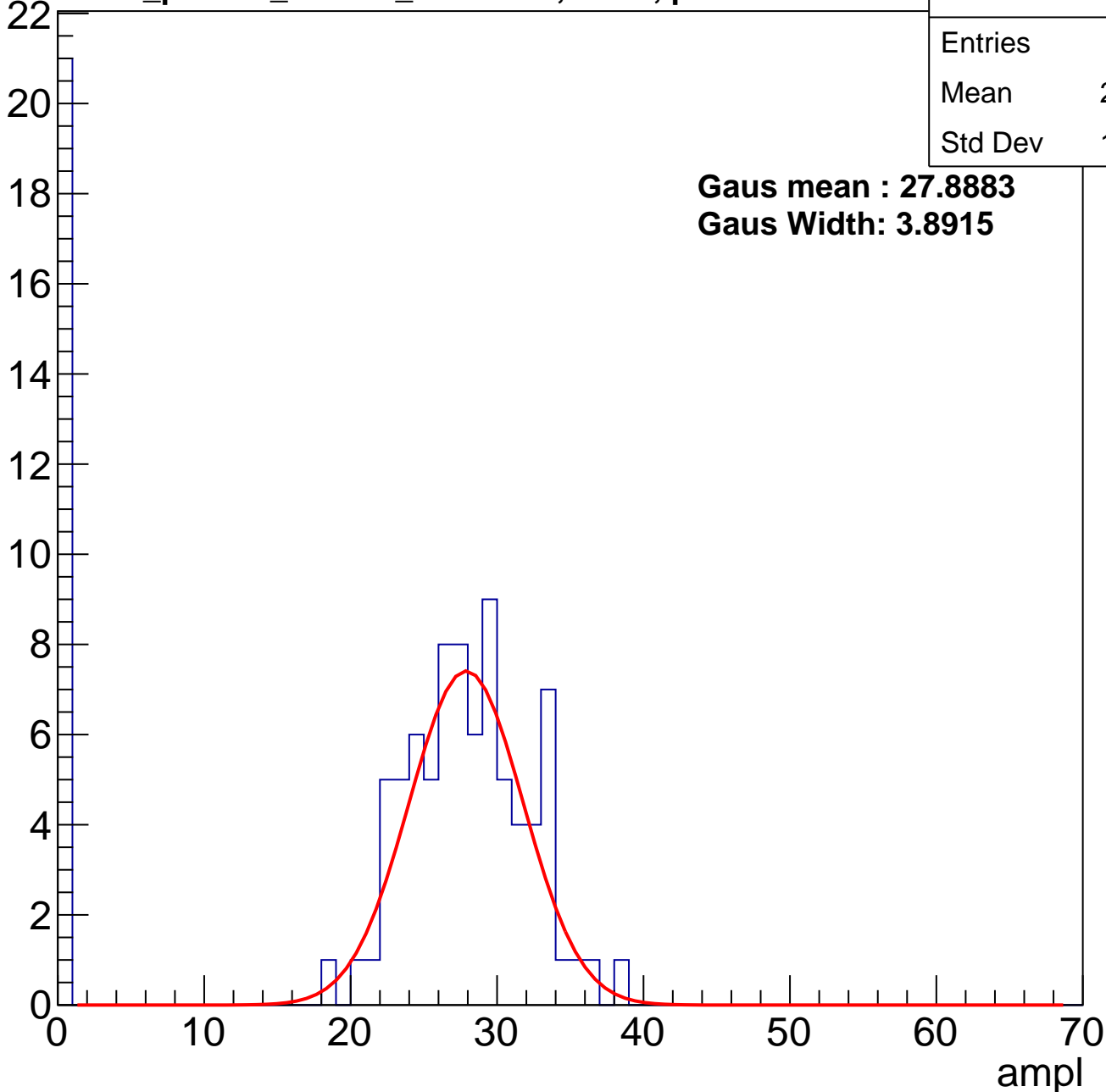
calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	21.82
Std Dev	11.79

Gaus mean : 27.8883

Gaus Width: 3.8915

Entry



B1L103S, U24-ch115, adc1

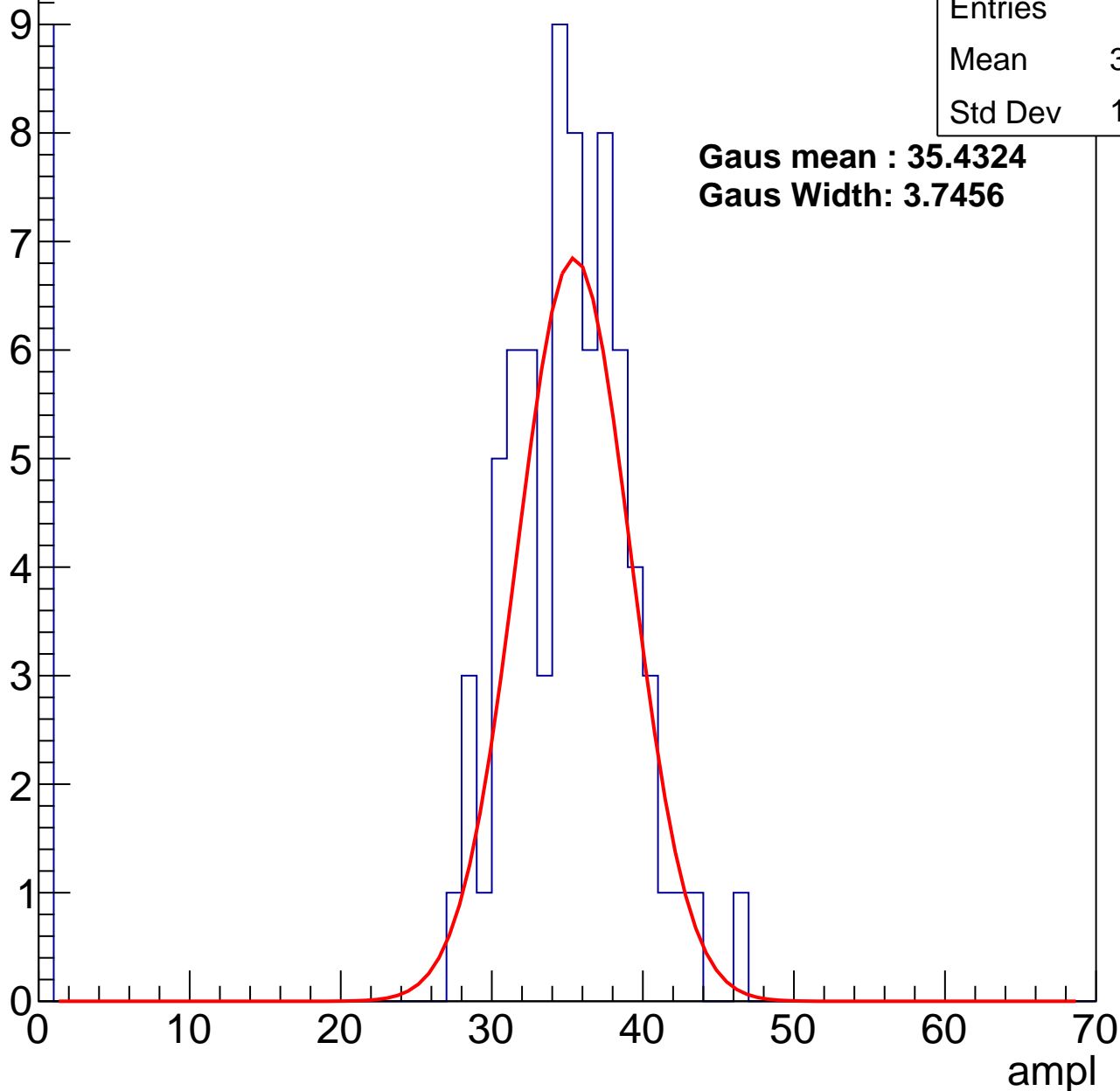
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	30.99
Std Dev	11.45

Gaus mean : 35.4324

Gaus Width: 3.7456



B1L103S, U24-ch115, adc2

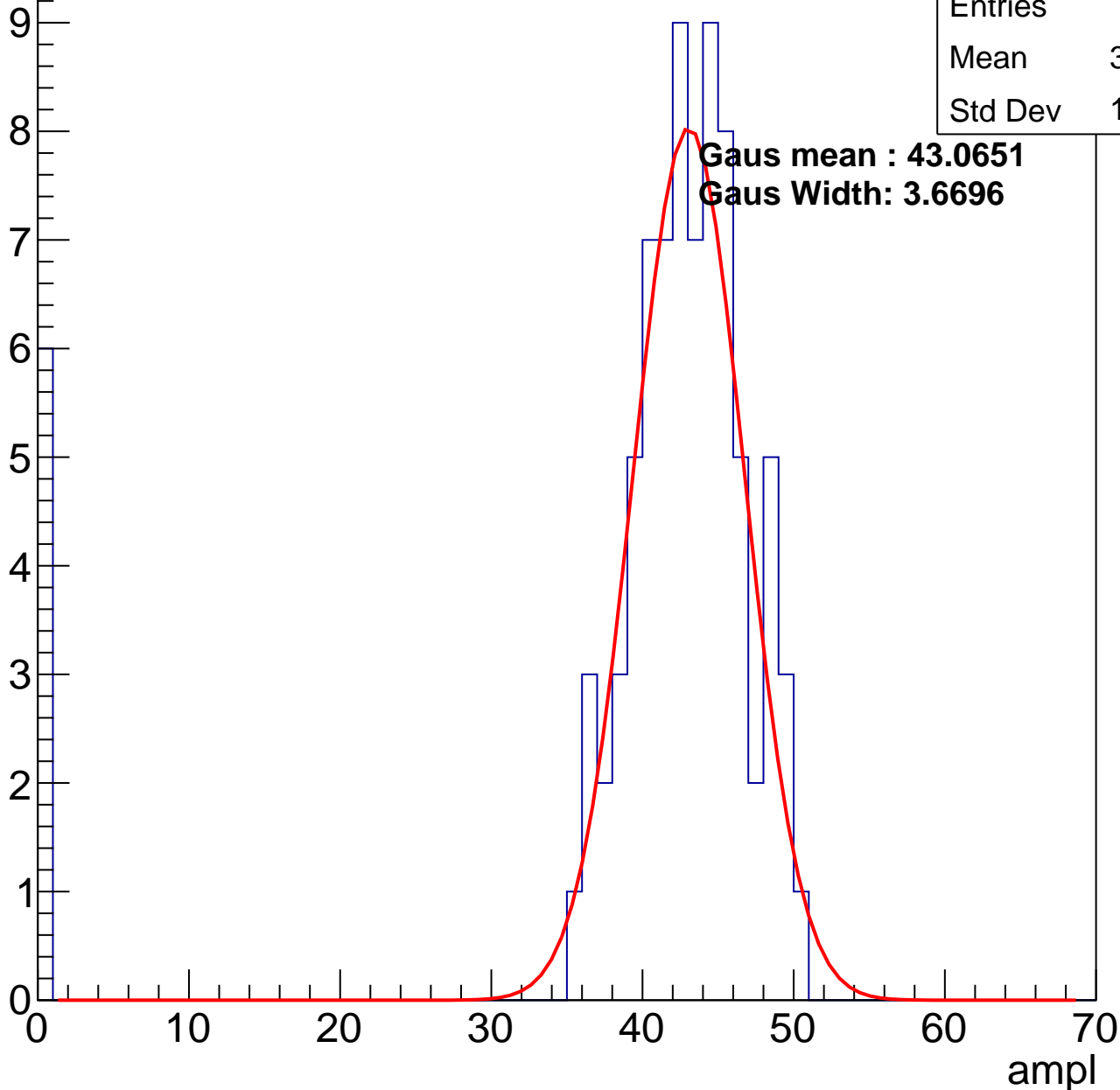
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	39.63
Std Dev	11.56

Gaus mean : 43.0651

Gaus Width: 3.6696

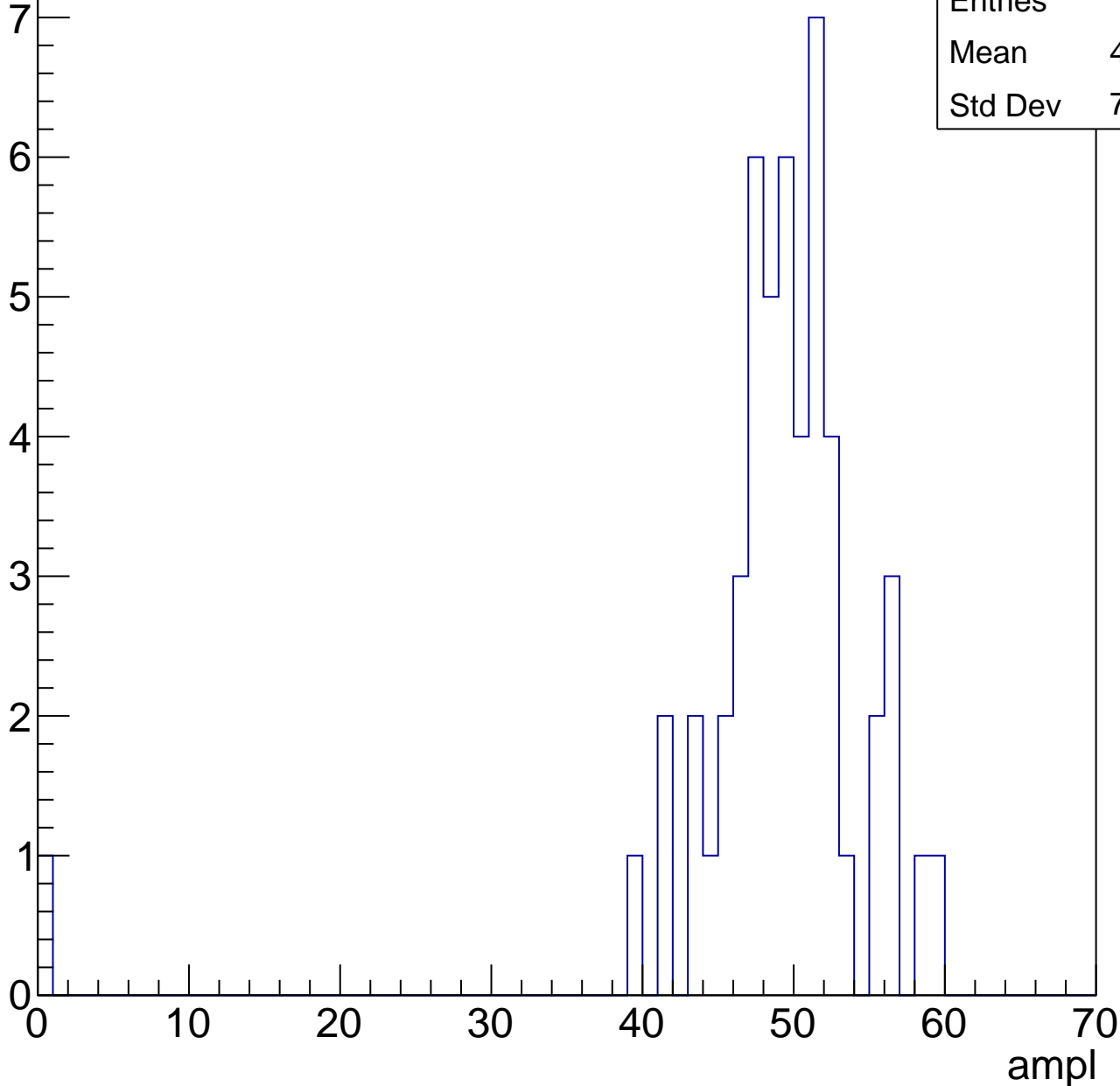


B1L103S, U24-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	48.23
Std Dev	7.927

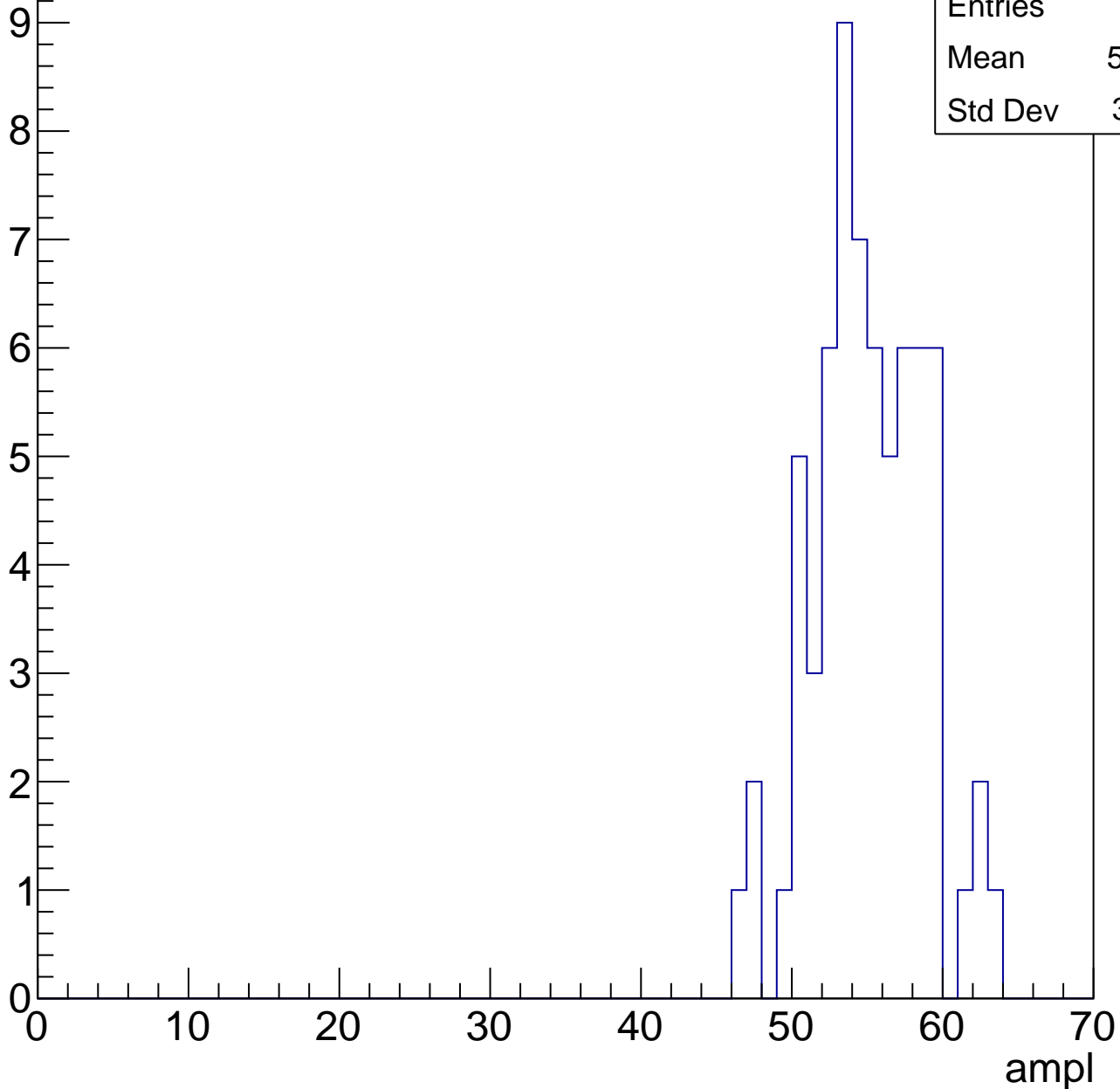


B1L103S, U24-ch115, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	54.64
Std Dev	3.631



B1L103S, U24-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 48

Mean 59.65

Std Dev 2.602

8

6

4

2

0

0

10

20

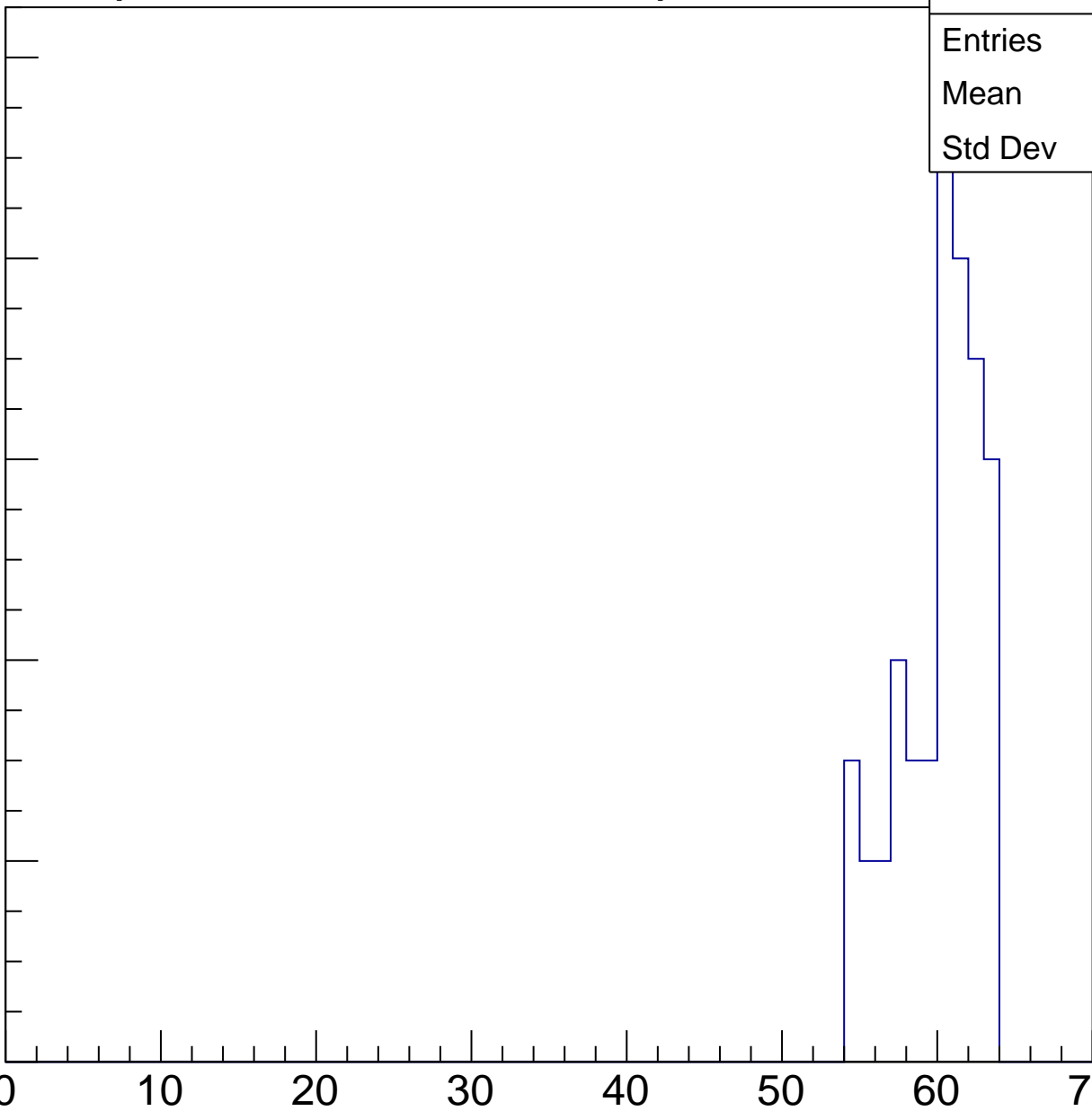
30

40

50

60

ampl



B1L103S, U24-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U24-ch116, adc0

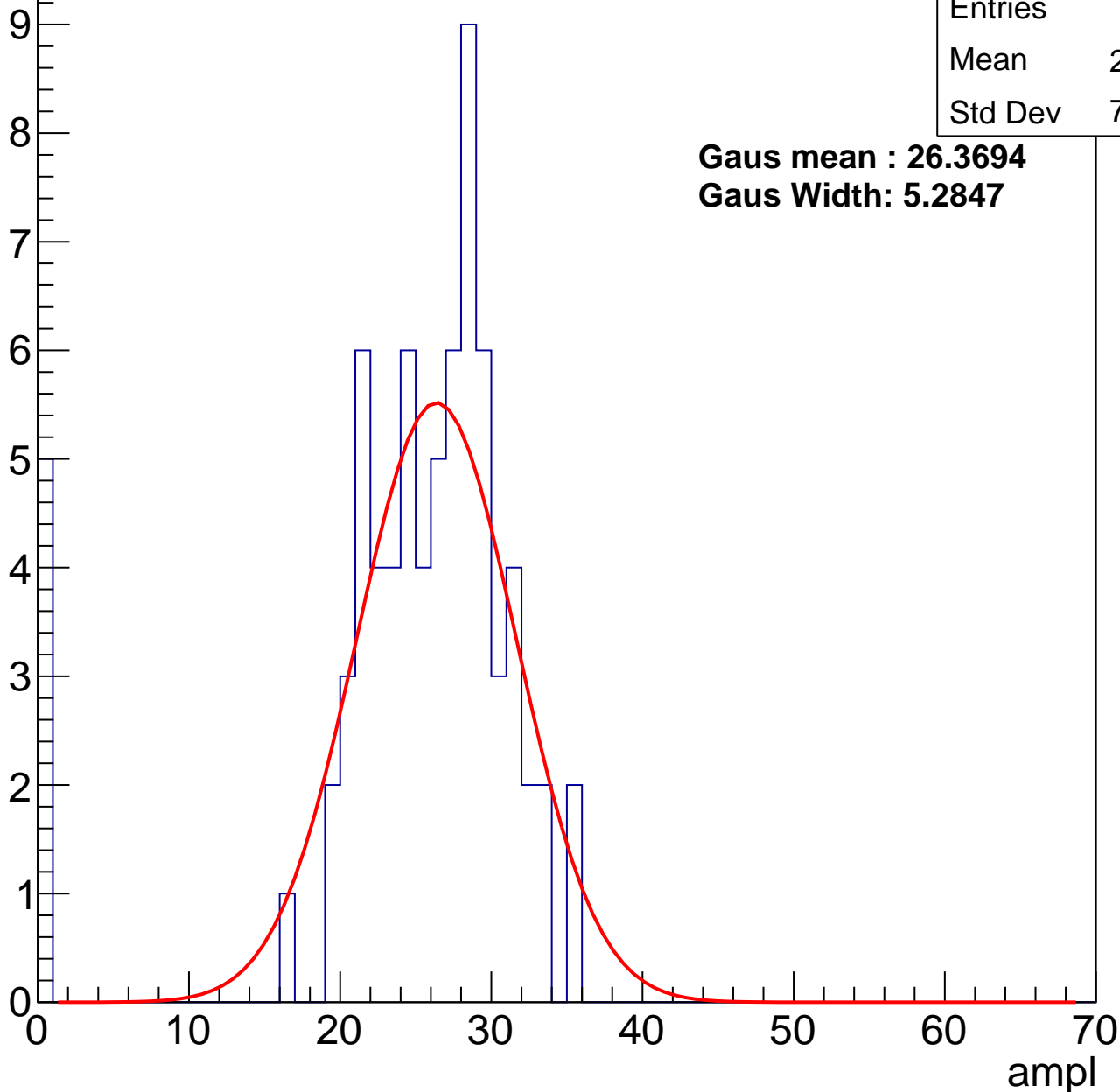
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	24.27
Std Dev	7.639

Gaus mean : 26.3694

Gaus Width: 5.2847



B1L103S, U24-ch116, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	32.77
Std Dev	5.381

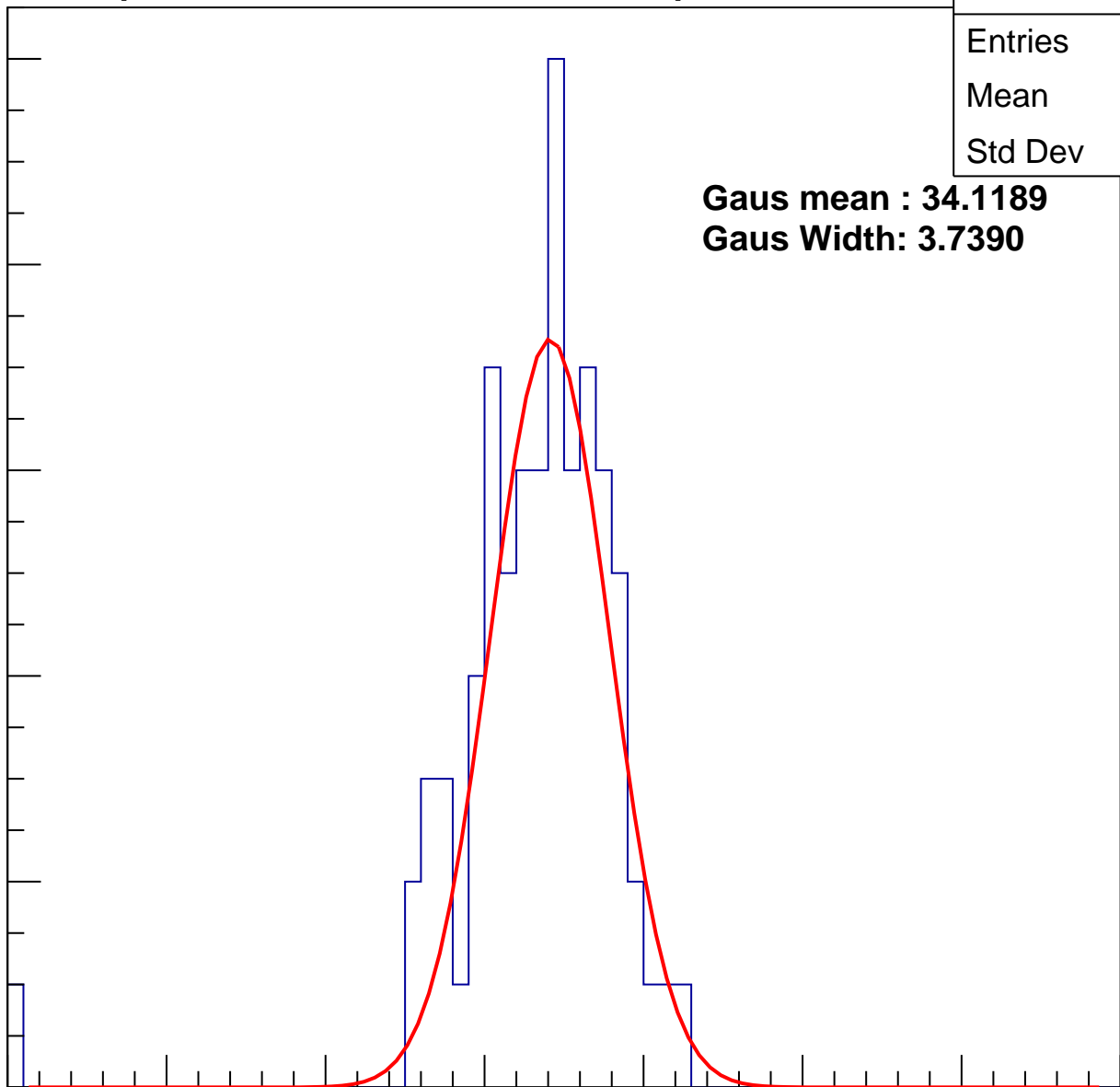
Gaus mean : 34.1189

Gaus Width: 3.7390

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch116, adc2

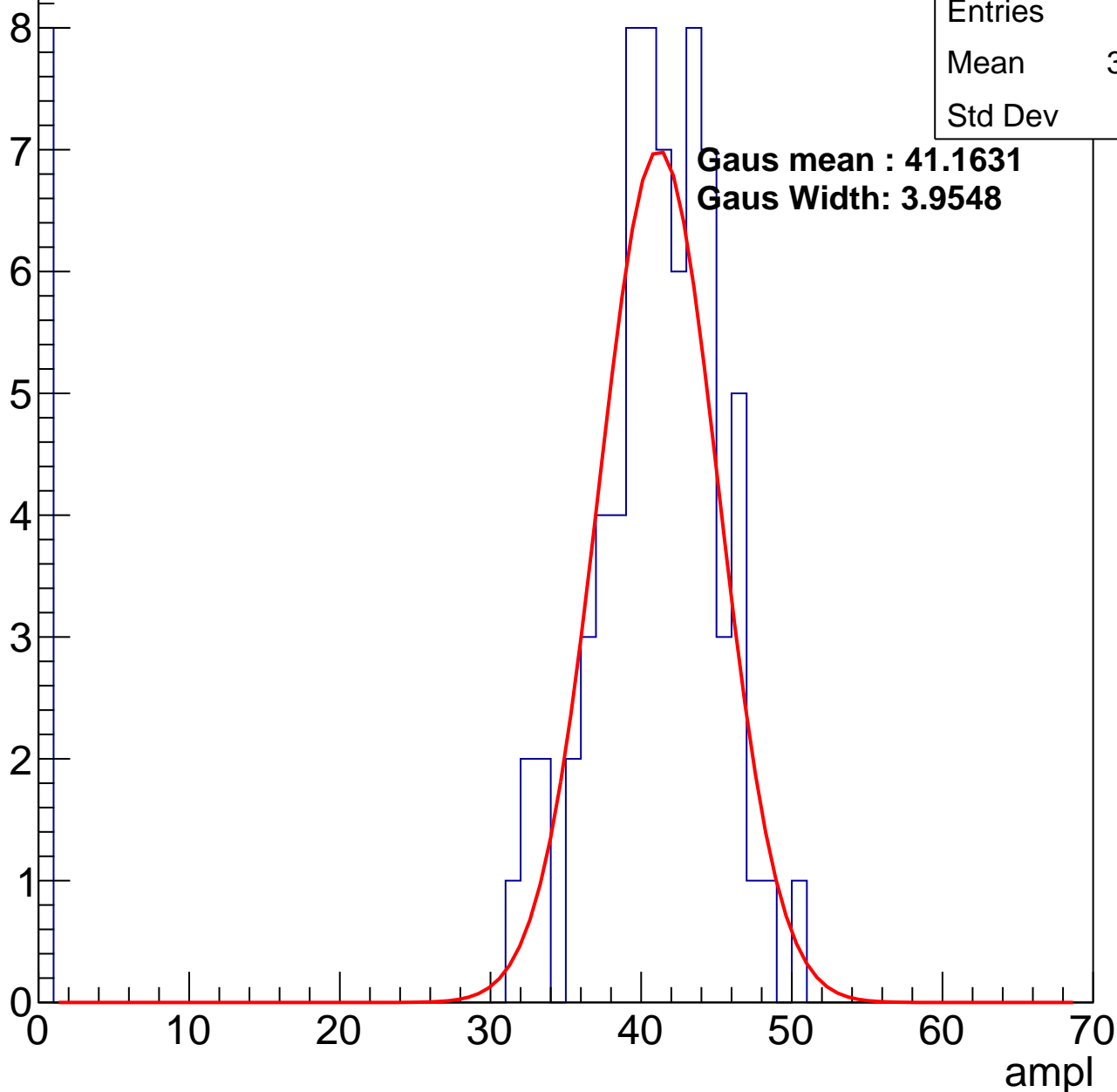
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	36.69
Std Dev	12.7

Gaus mean : 41.1631

Gaus Width: 3.9548



B1L103S, U24-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	46.21
Std Dev	9.994

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

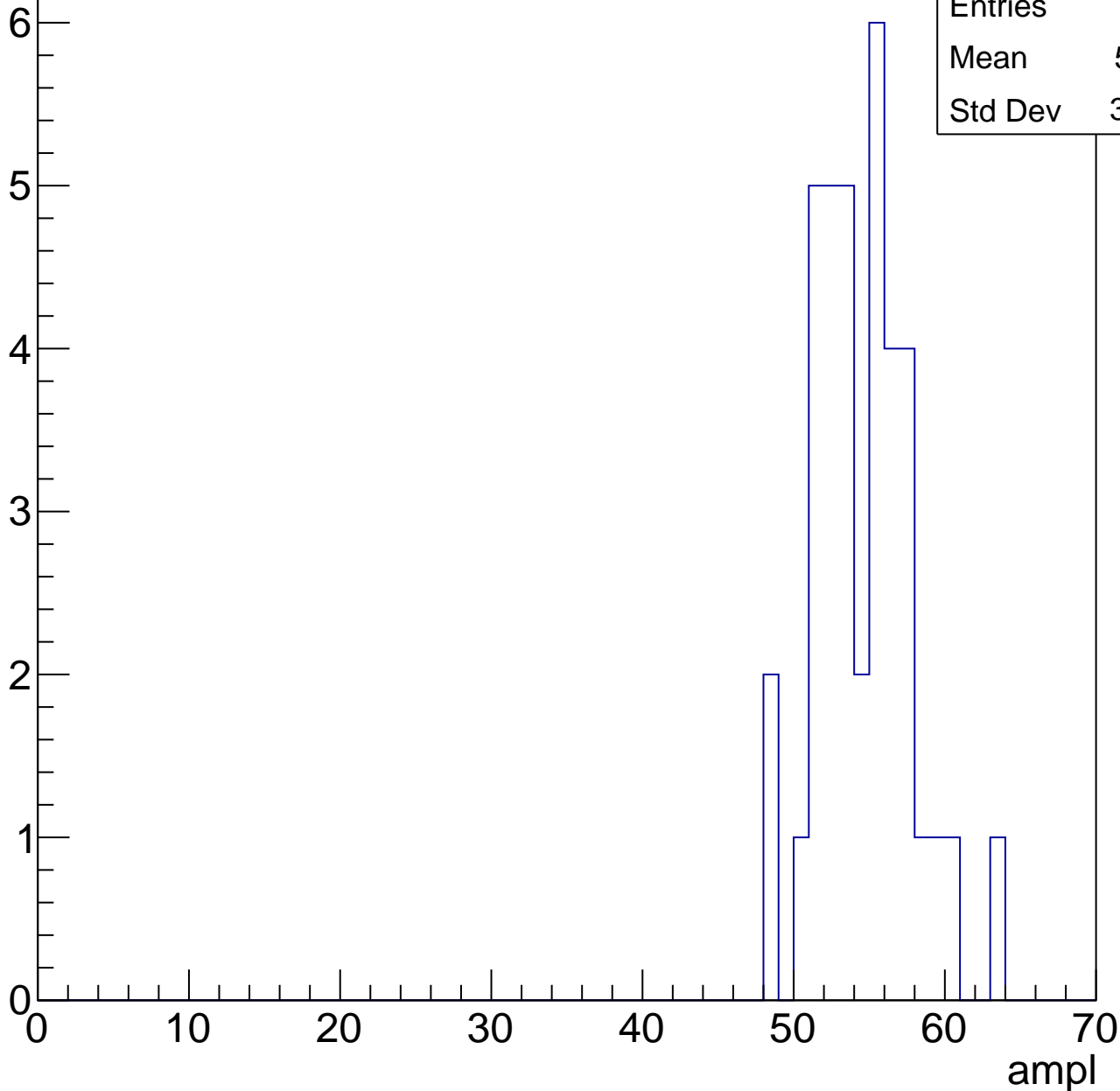
70

B1L103S, U24-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	54.11
Std Dev	3.135

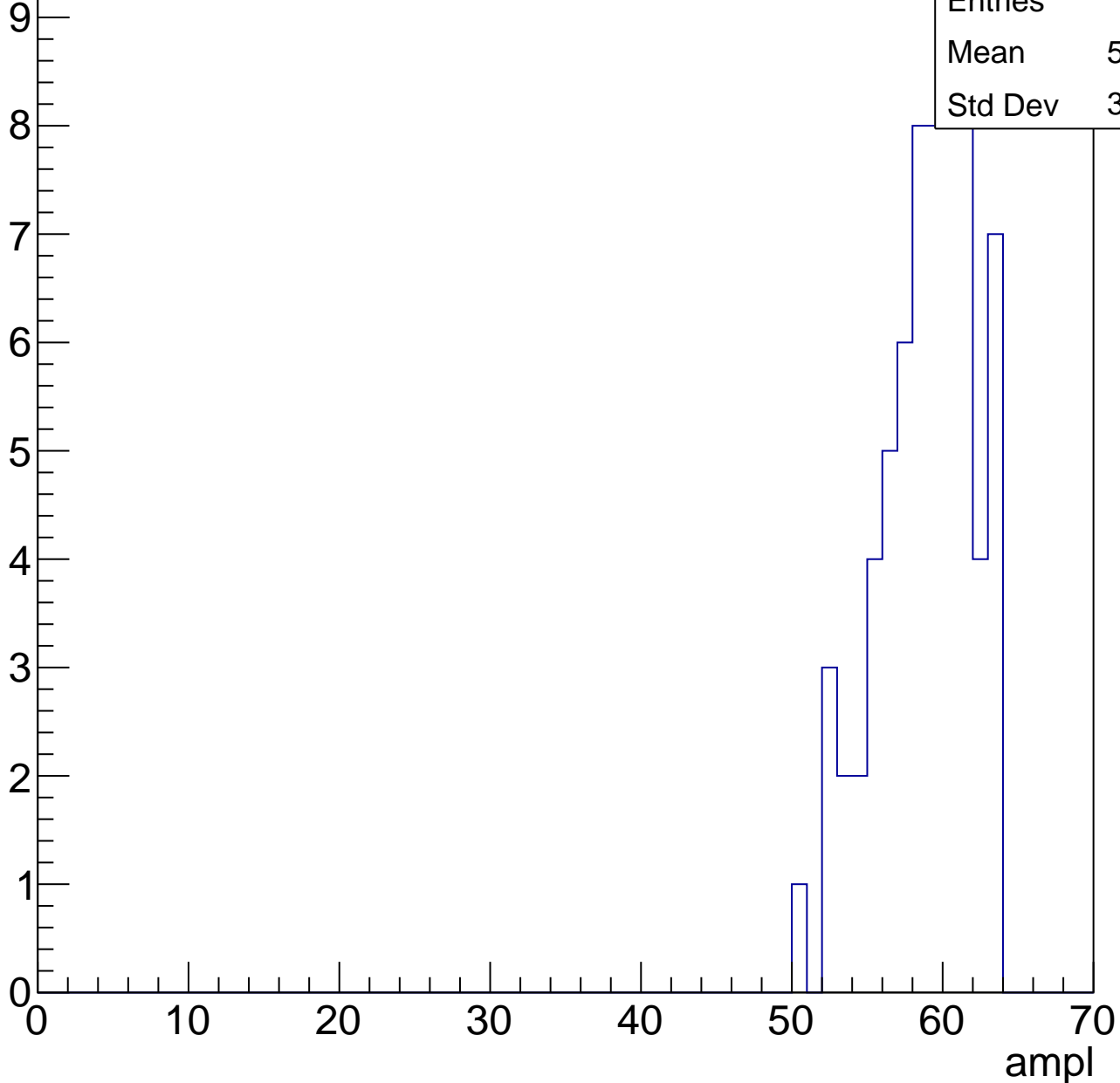


B1L103S, U24-ch116, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	58.43
Std Dev	3.135

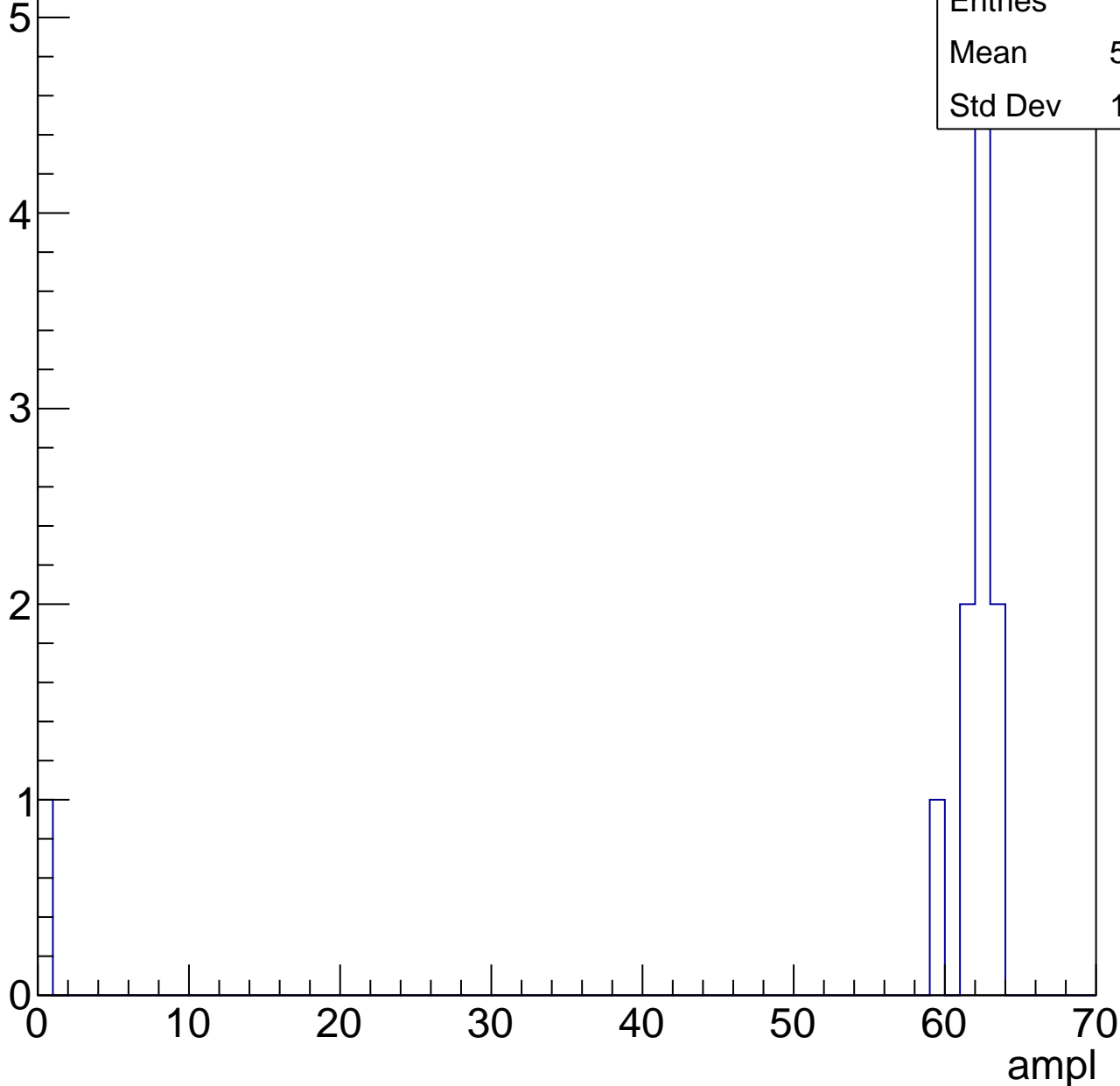


B1L103S, U24-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	56.09
Std Dev	17.77

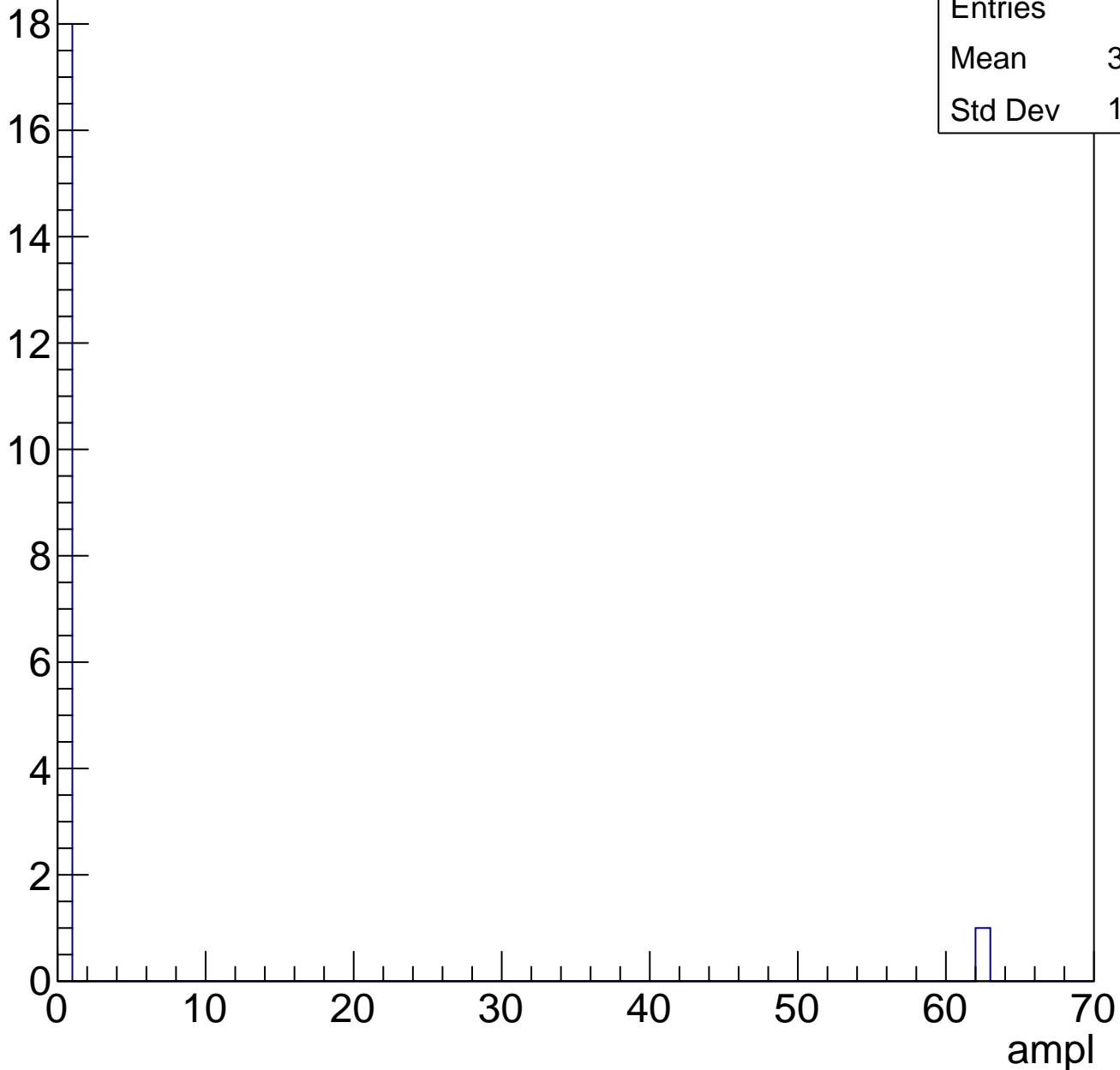


B1L103S, U24-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U24-ch117, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	23.84
Std Dev	12.19

Gaus mean : 30.2832

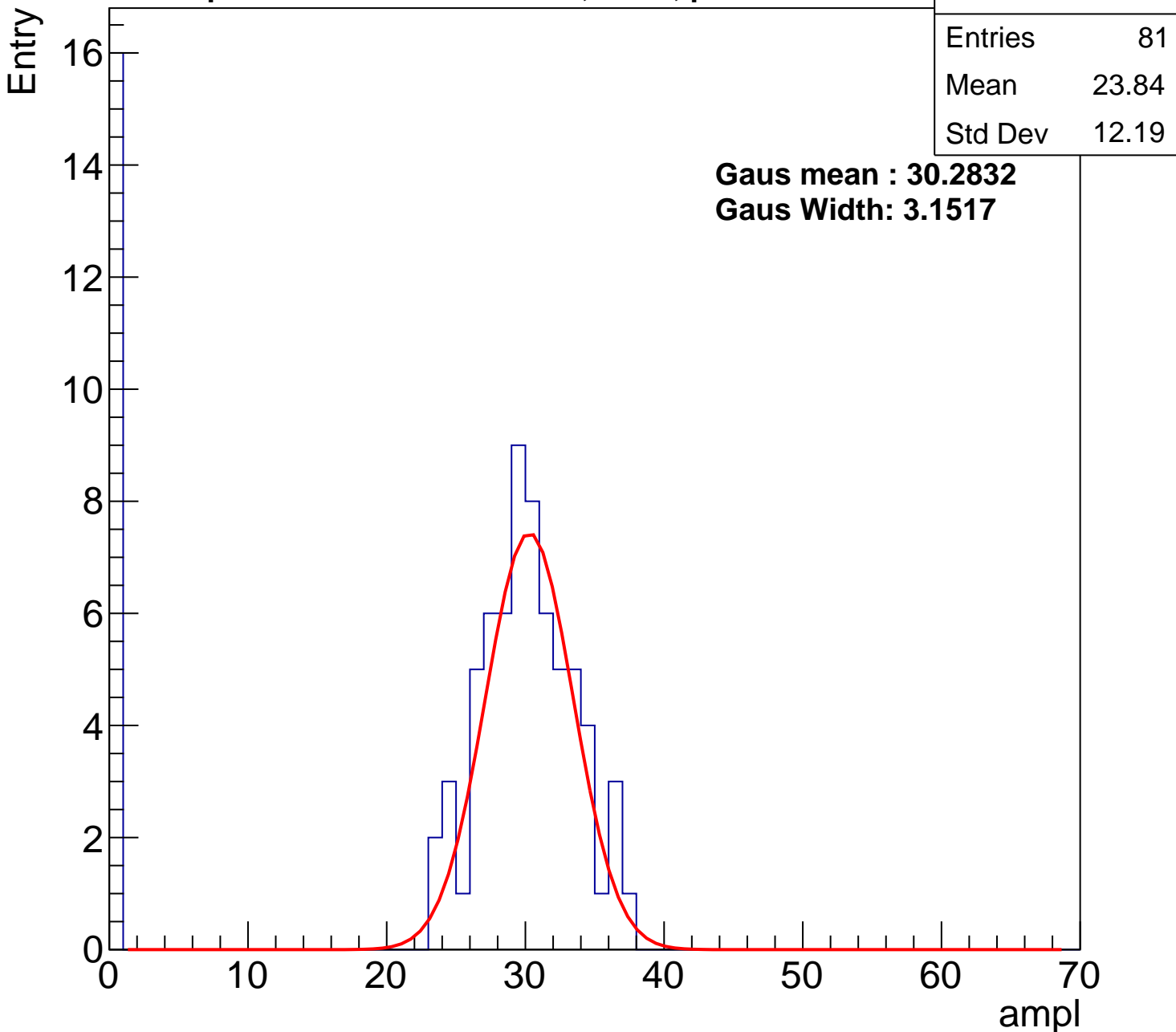
Gaus Width: 3.1517

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch117, adc1

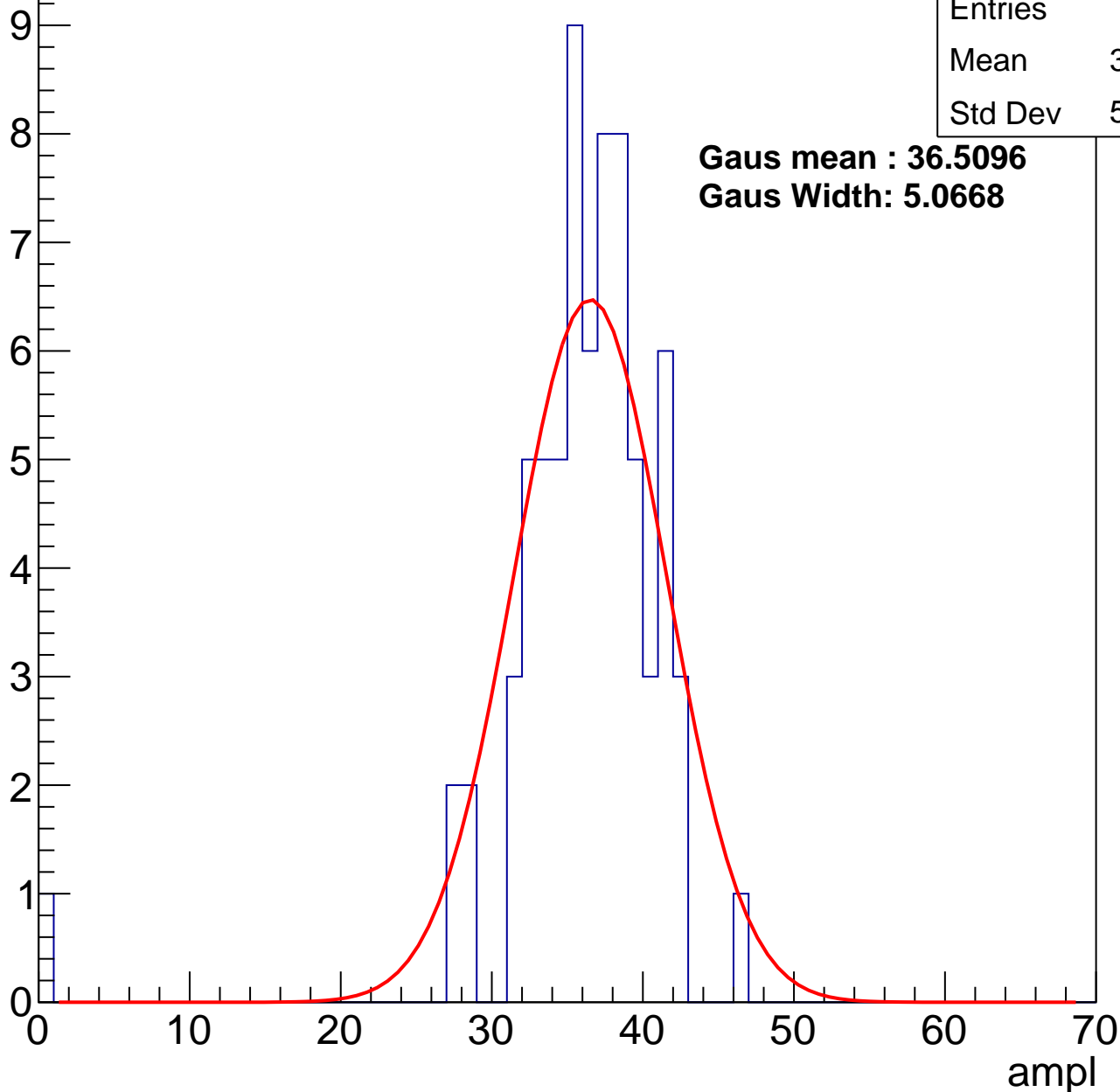
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	35.58
Std Dev	5.644

Gaus mean : 36.5096

Gaus Width: 5.0668



B1L103S, U24-ch117, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	35.19
Std Dev	16.25

Gaus mean : 42.5713

Gaus Width: 2.9423

Entry

10

8

6

4

2

0

0

10

20

30

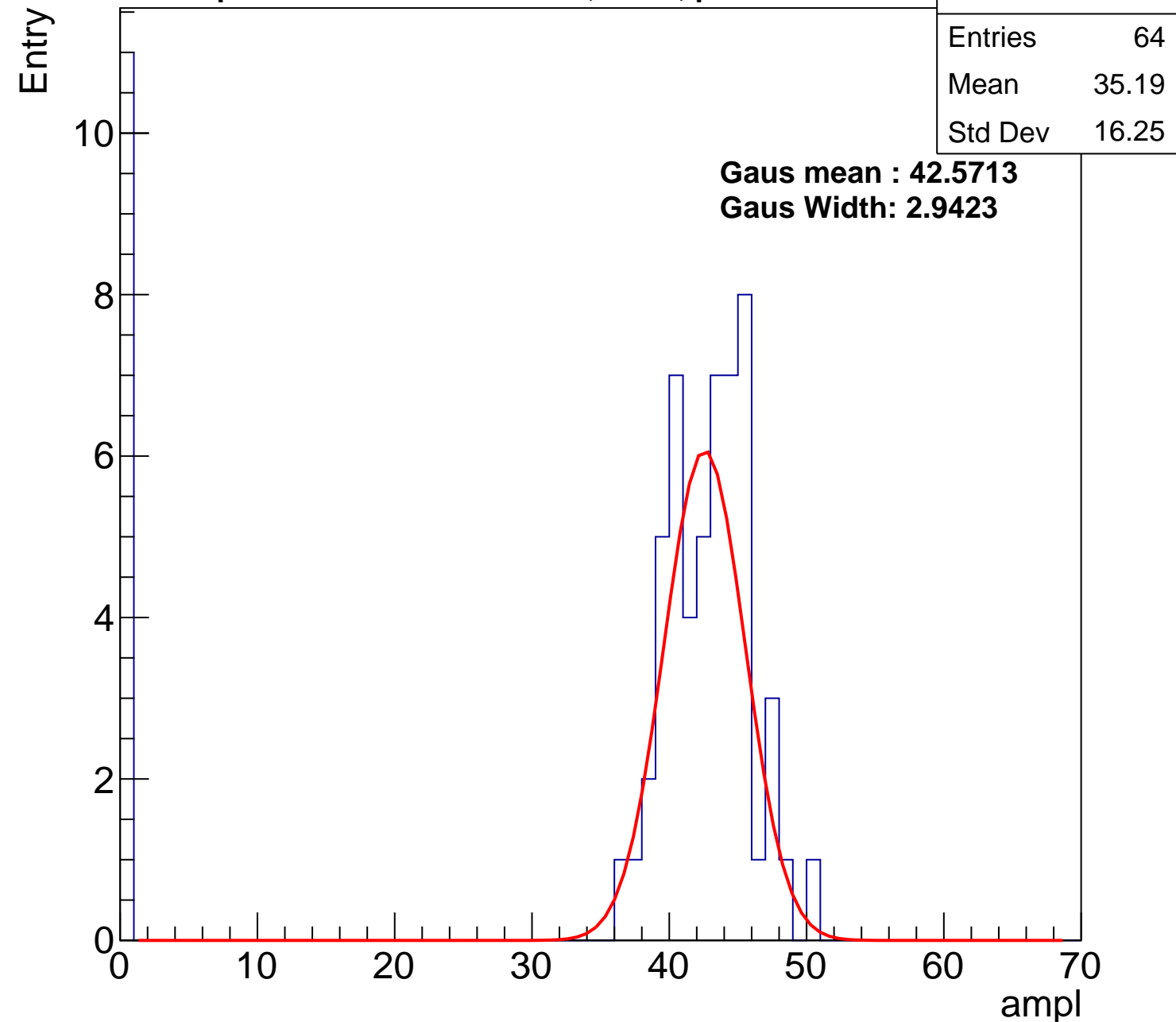
40

50

60

70

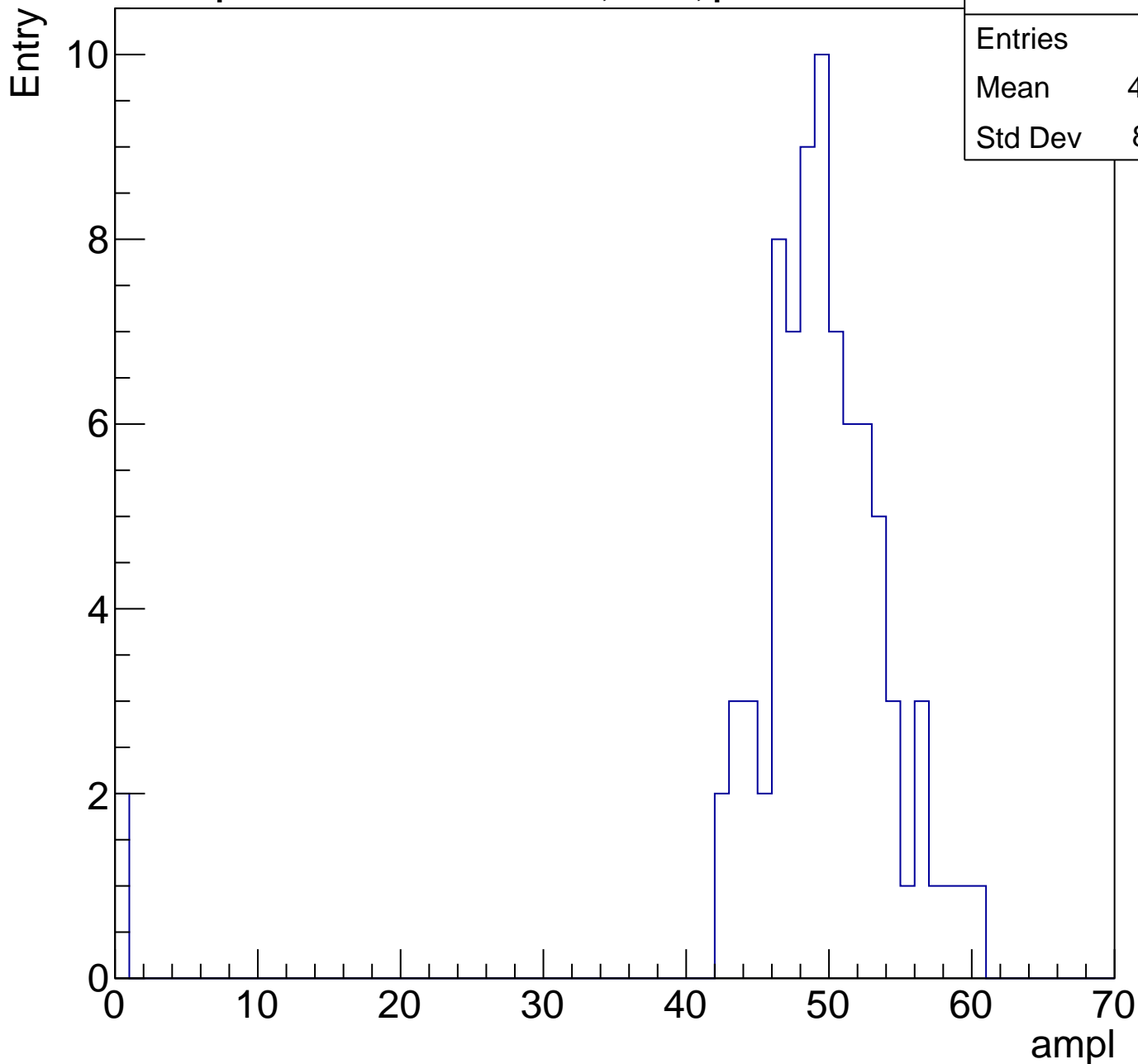
ampl



B1L103S, U24-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	48.22
Std Dev	8.581

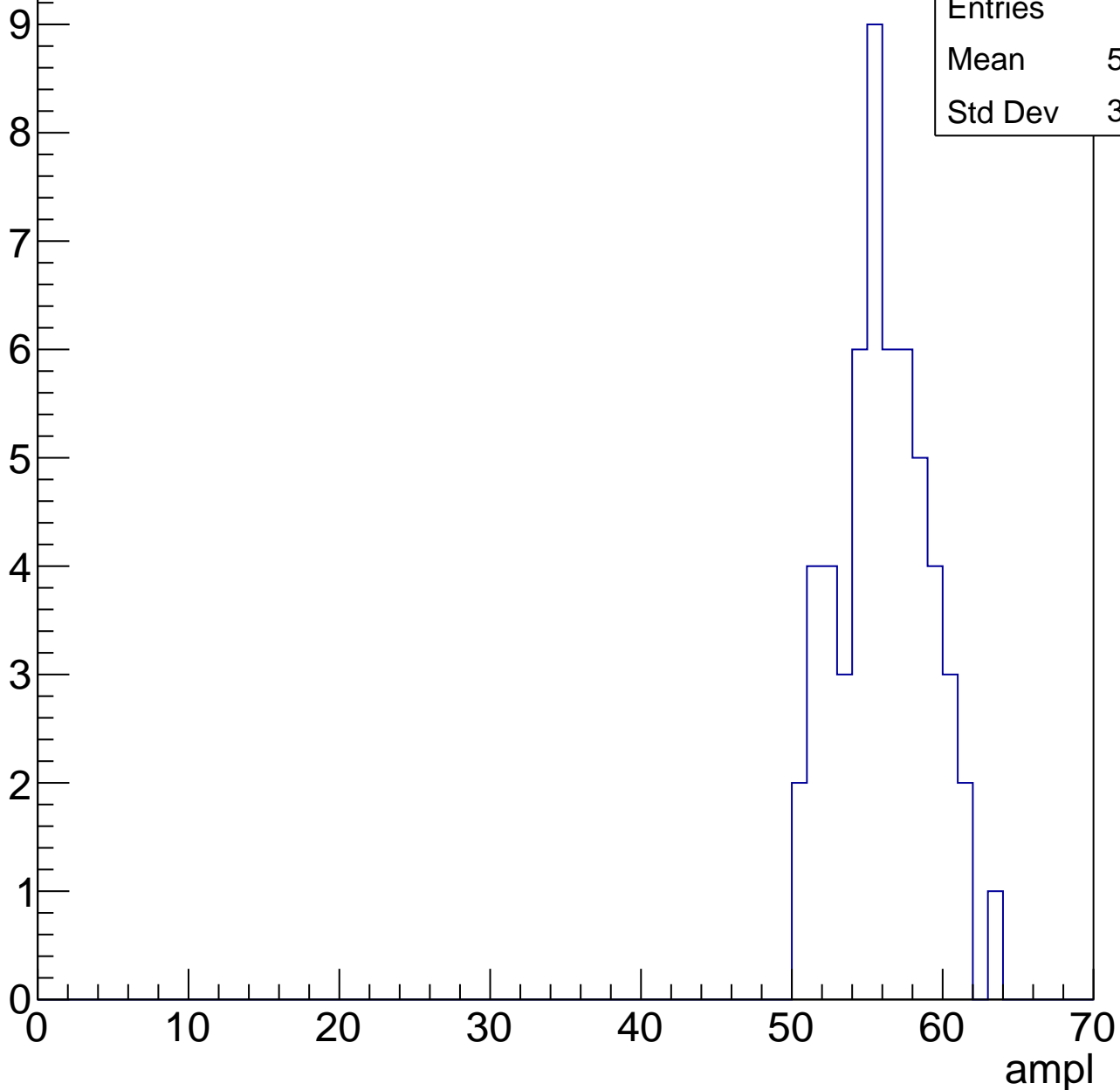


B1L103S, U24-ch117, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.62
Std Dev	3.006



B1L103S, U24-ch117, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	45
Mean	60.07
Std Dev	2.542

Entry

10

8

6

4

2

0

0

10

20

30

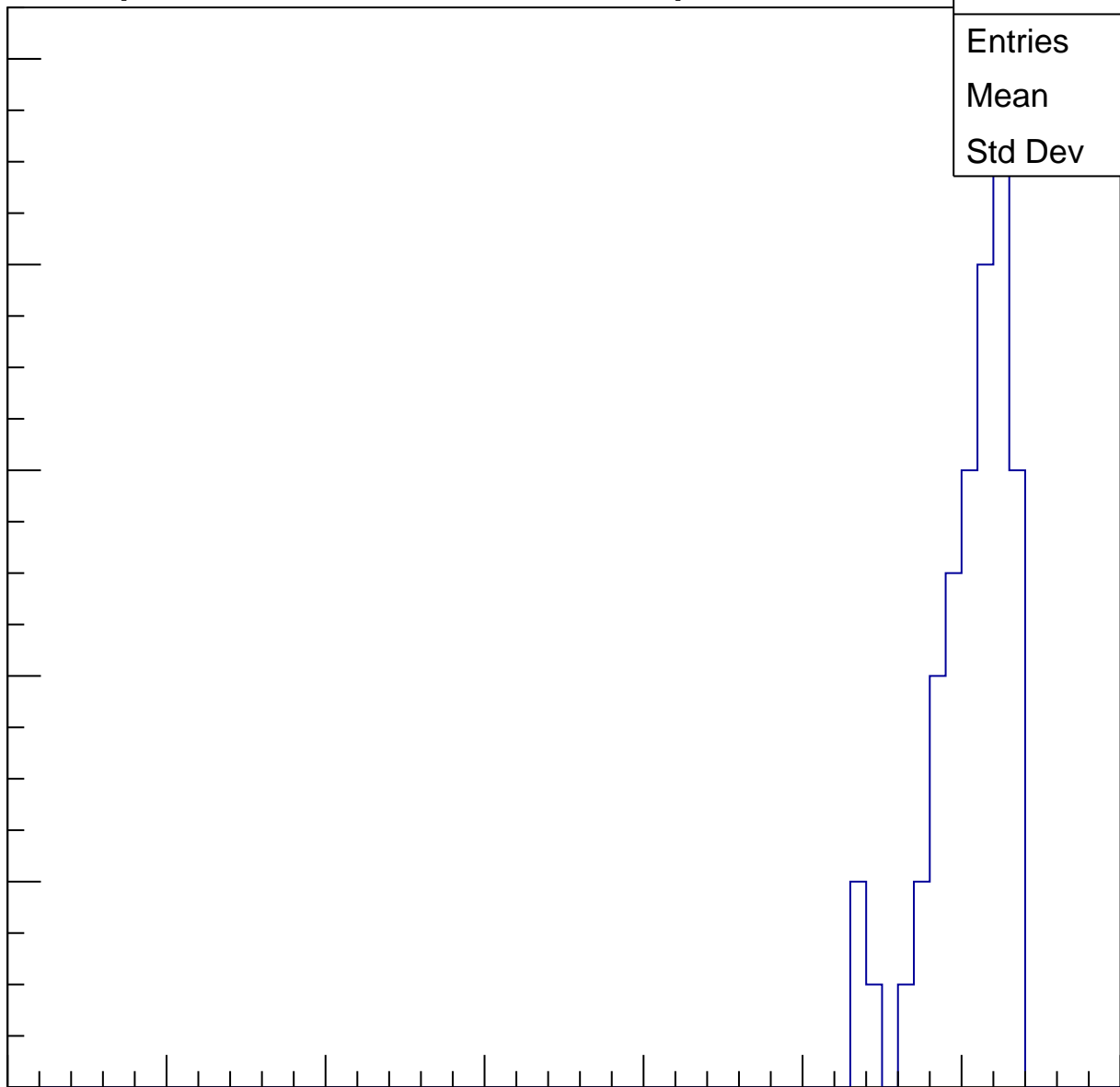
40

50

60

70

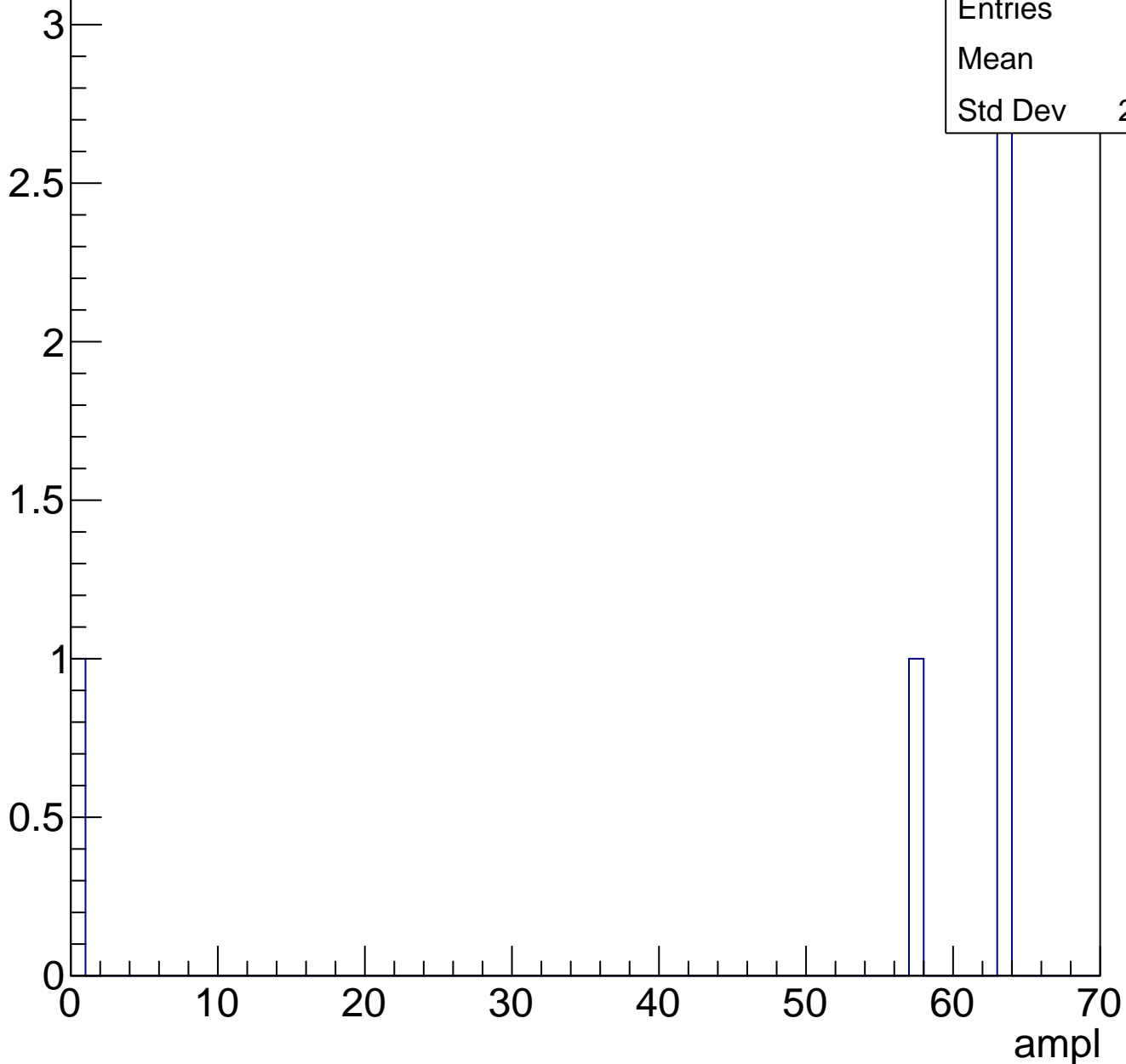
ampl



B1L103S, U24-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

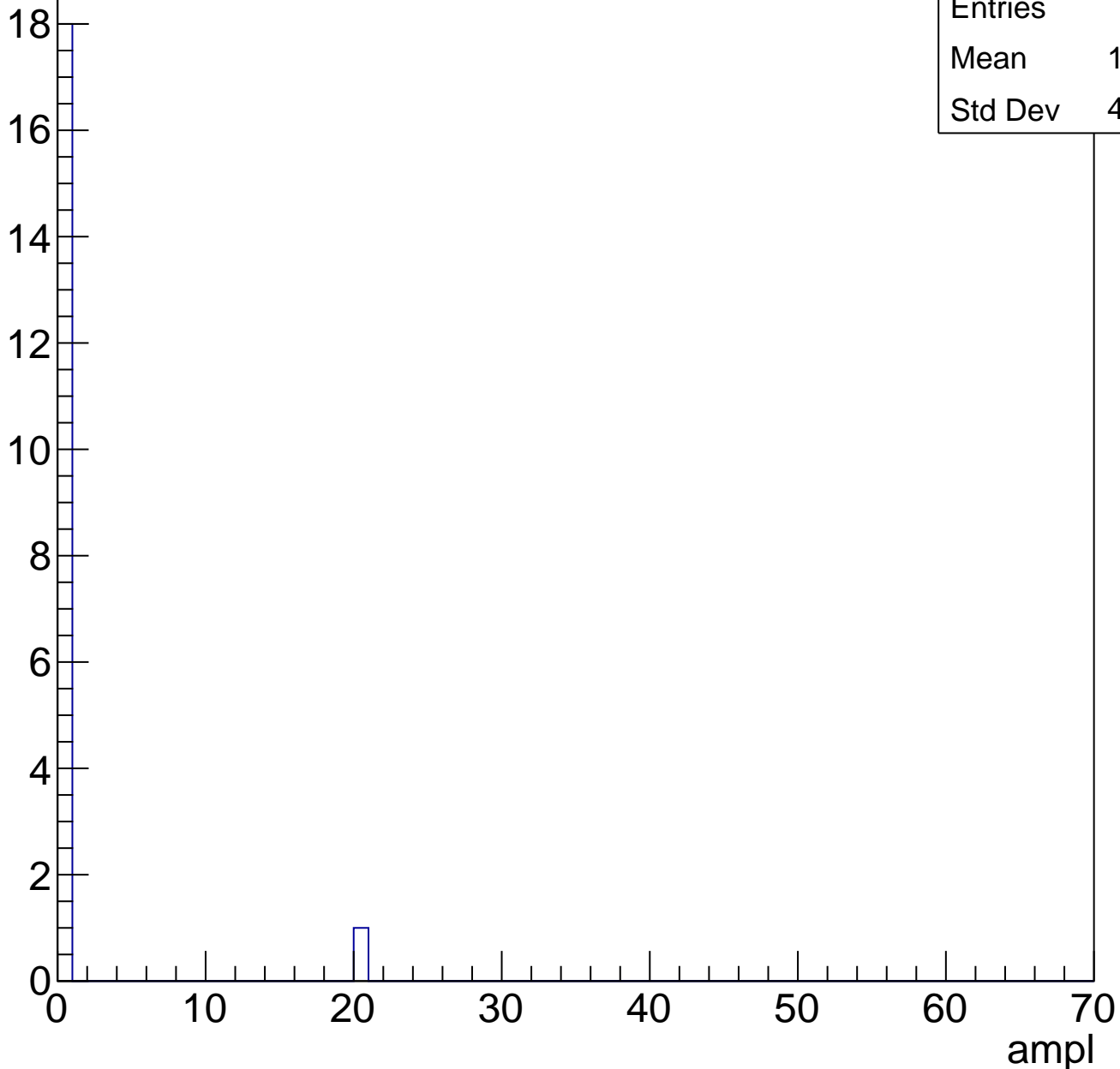


B1L103S, U24-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



B1L103S, U24-ch118, adc0

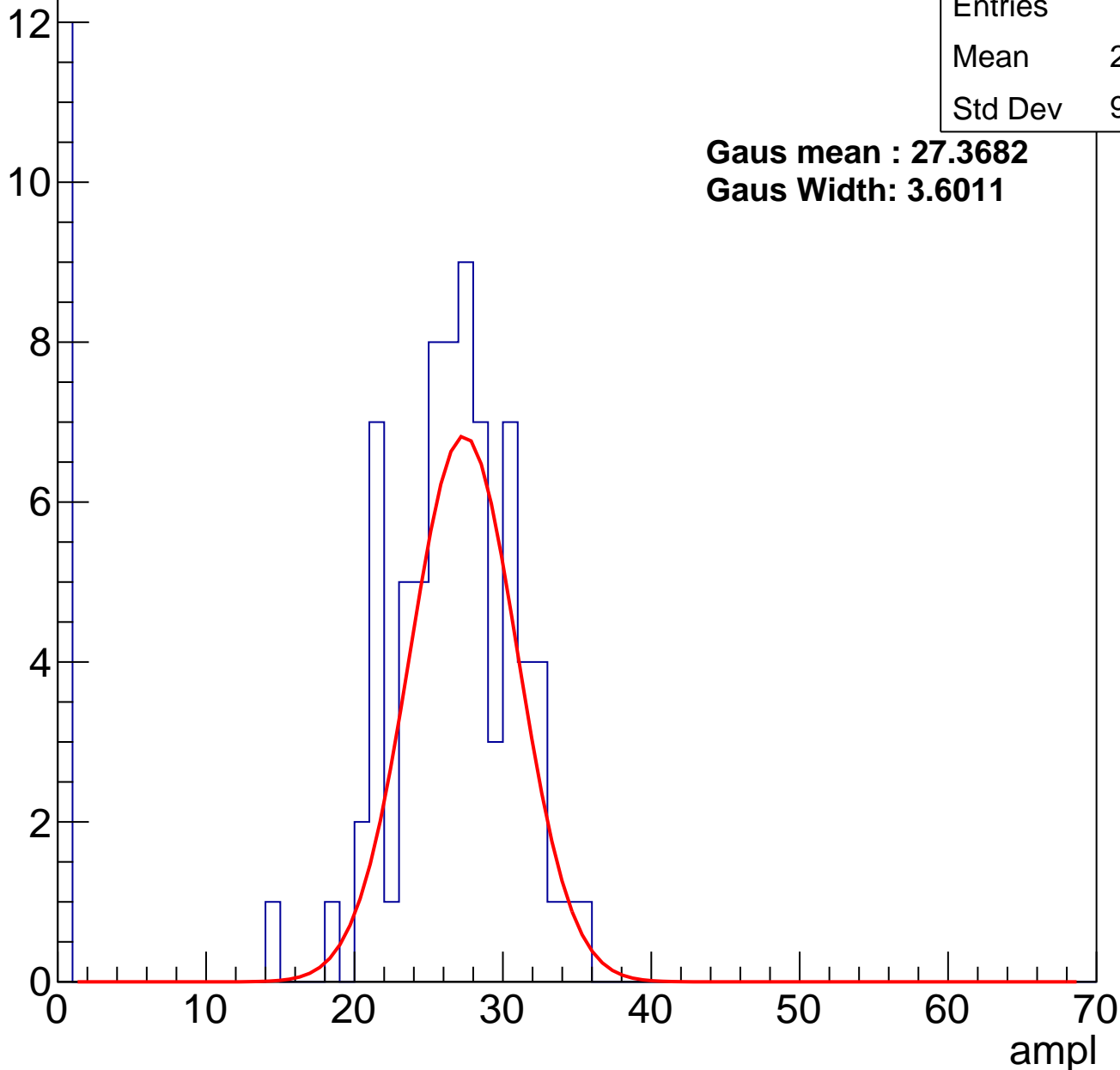
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	22.69
Std Dev	9.777

Gaus mean : 27.3682

Gaus Width: 3.6011

Entry



B1L103S, U24-ch118, adc1

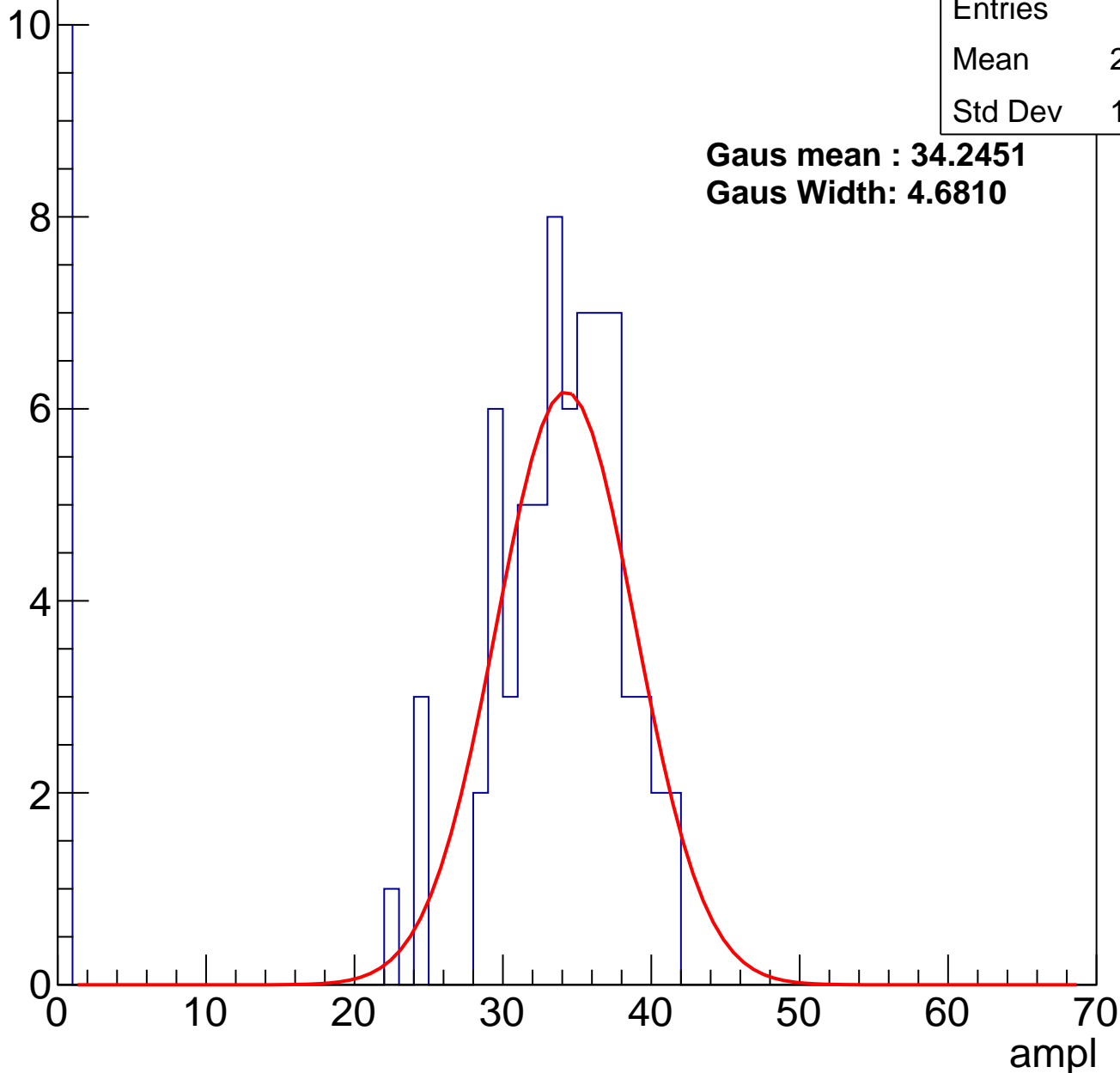
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	29.32
Std Dev	11.72

Gaus mean : 34.2451

Gaus Width: 4.6810

Entry

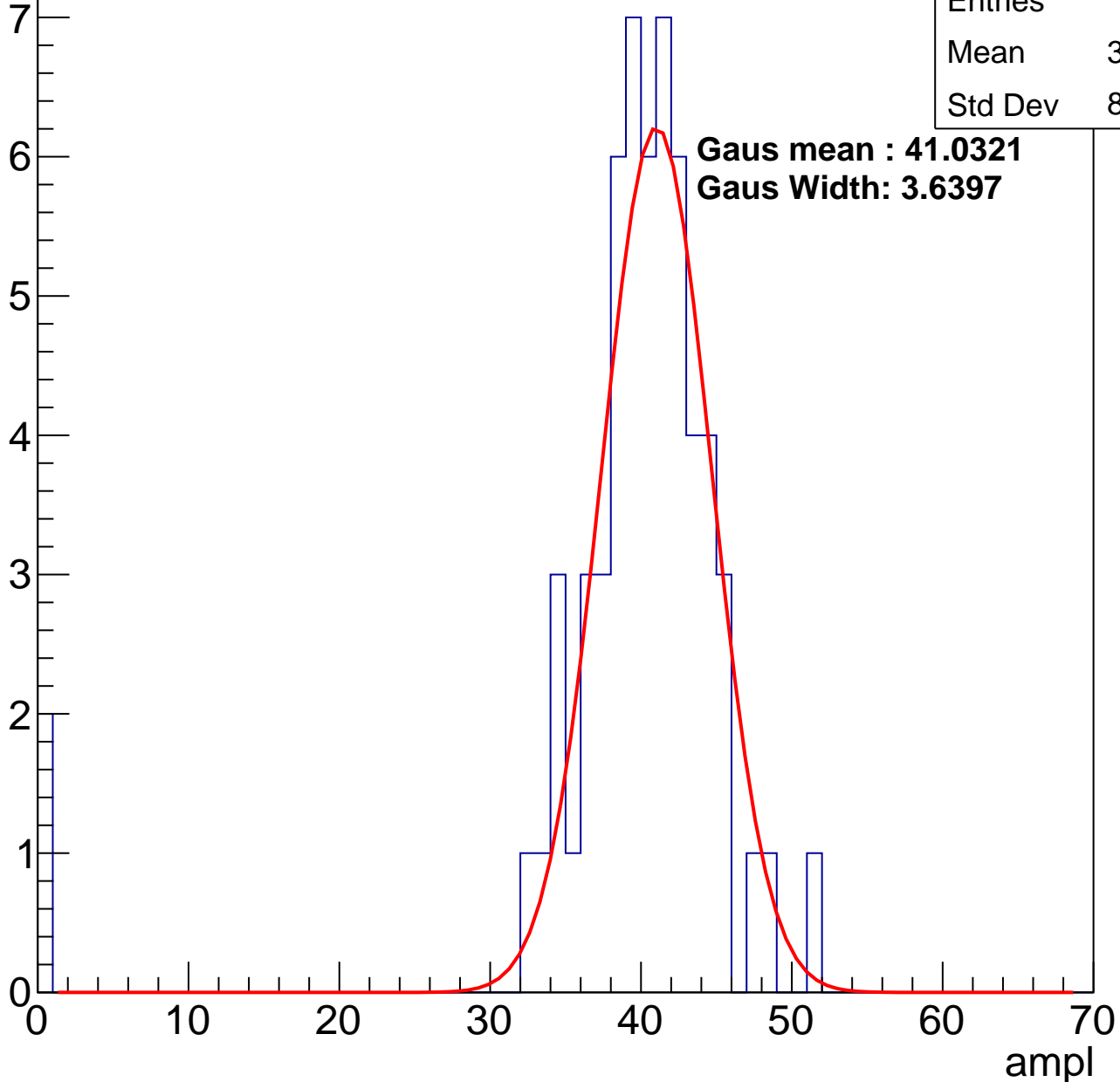


B1L103S, U24-ch118, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	38.83
Std Dev	8.073

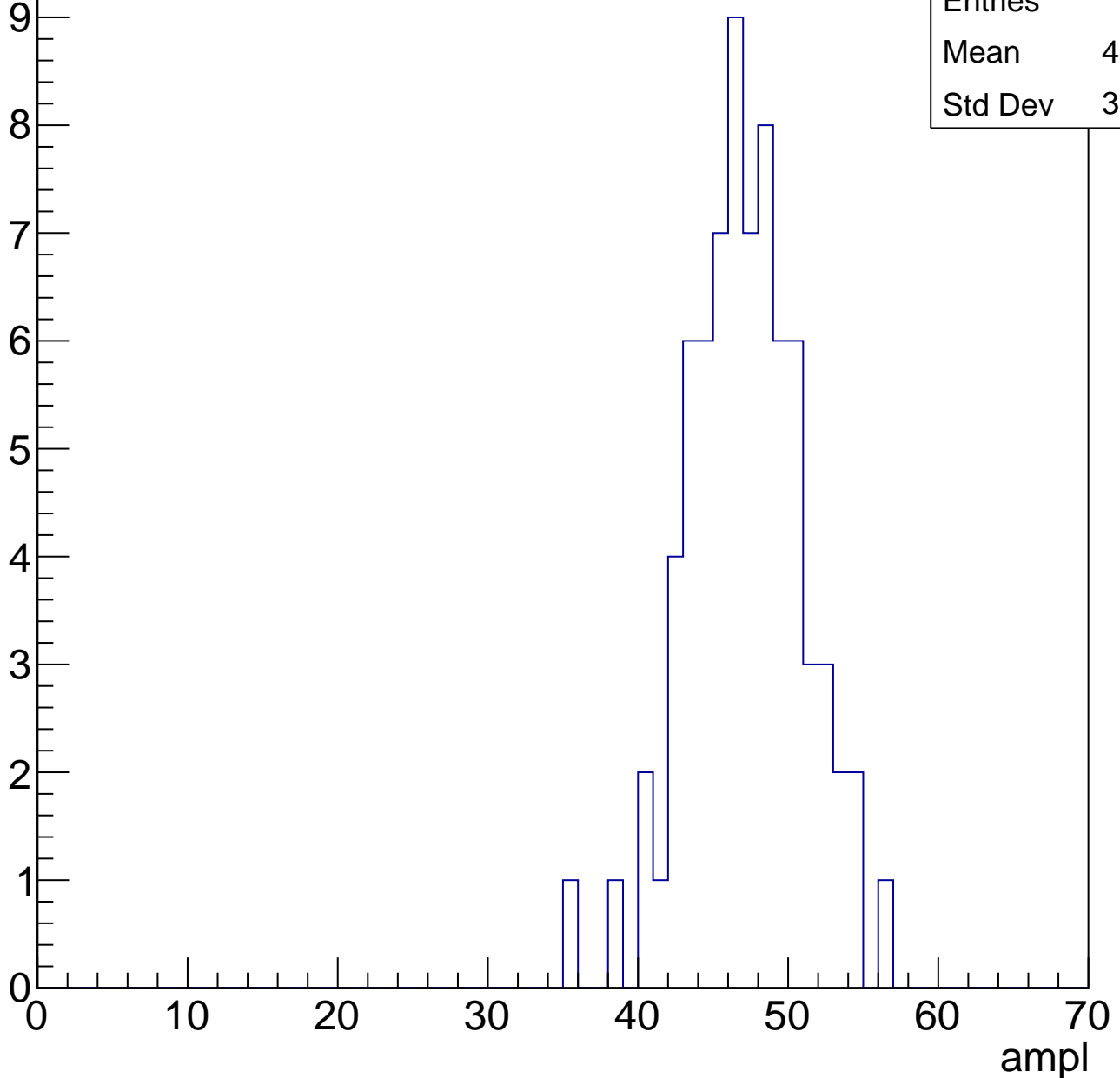


B1L103S, U24-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	46.65
Std Dev	3.824

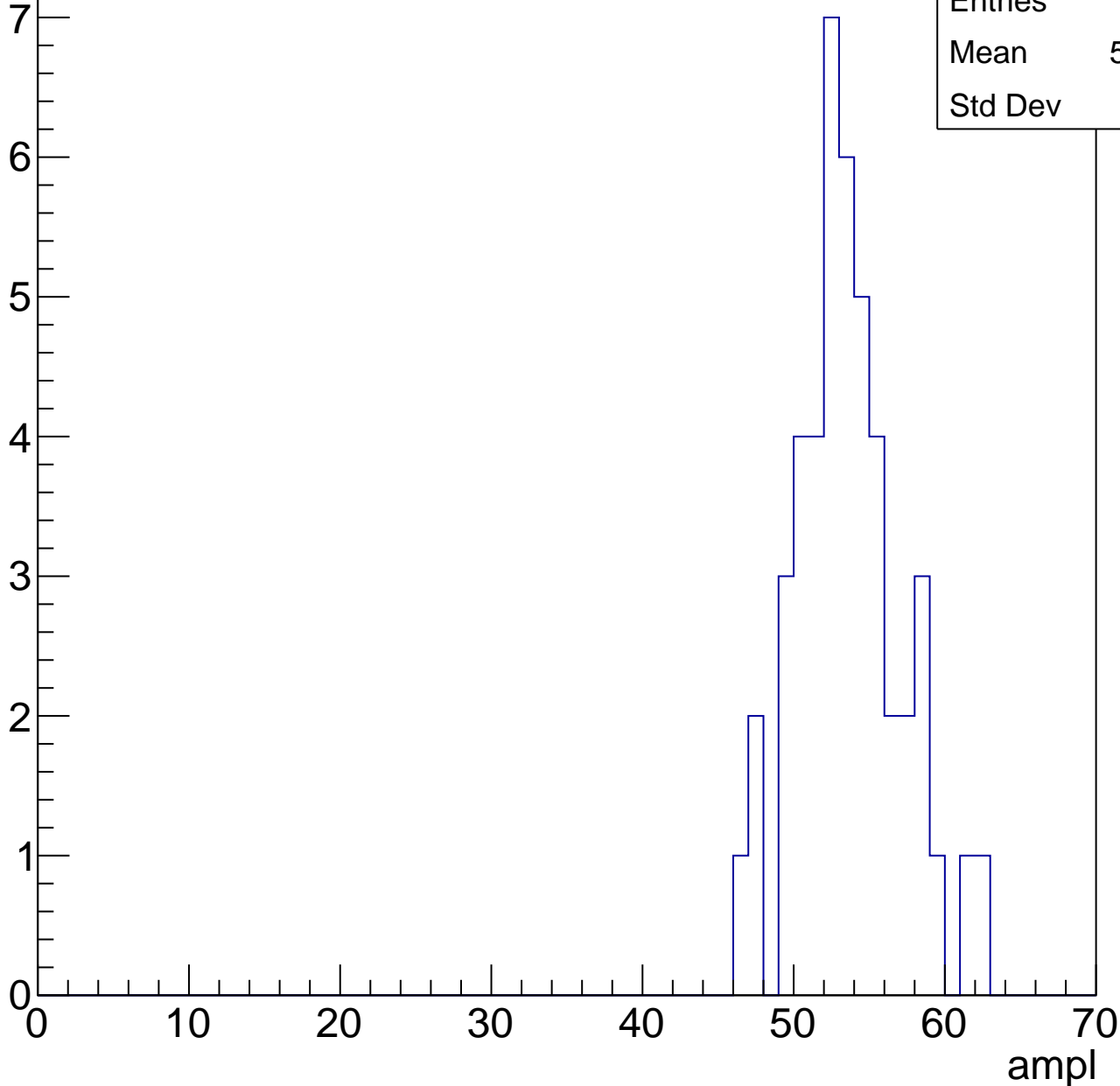


B1L103S, U24-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	53.15
Std Dev	3.47

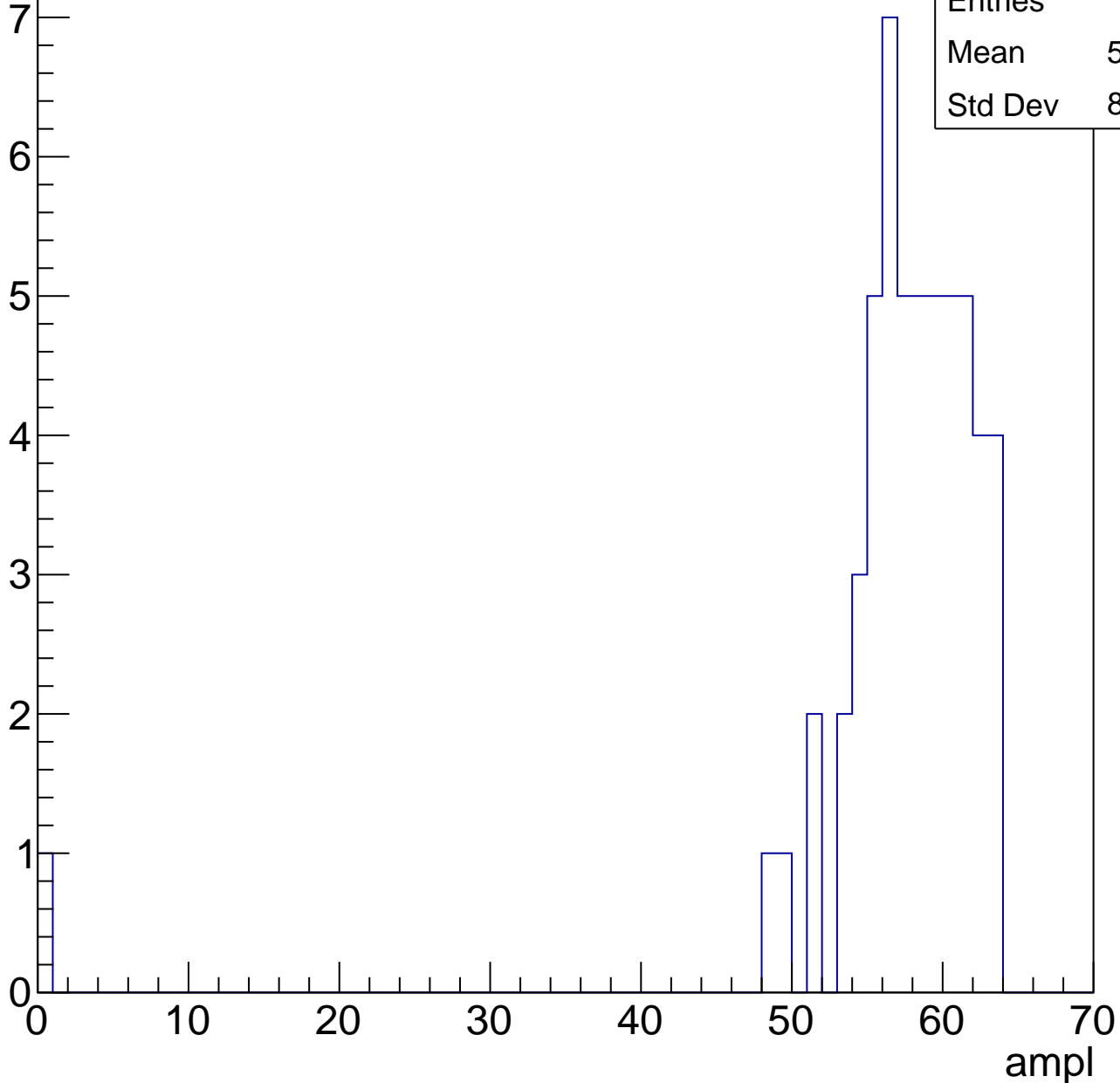


B1L103S, U24-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	56.53
Std Dev	8.457

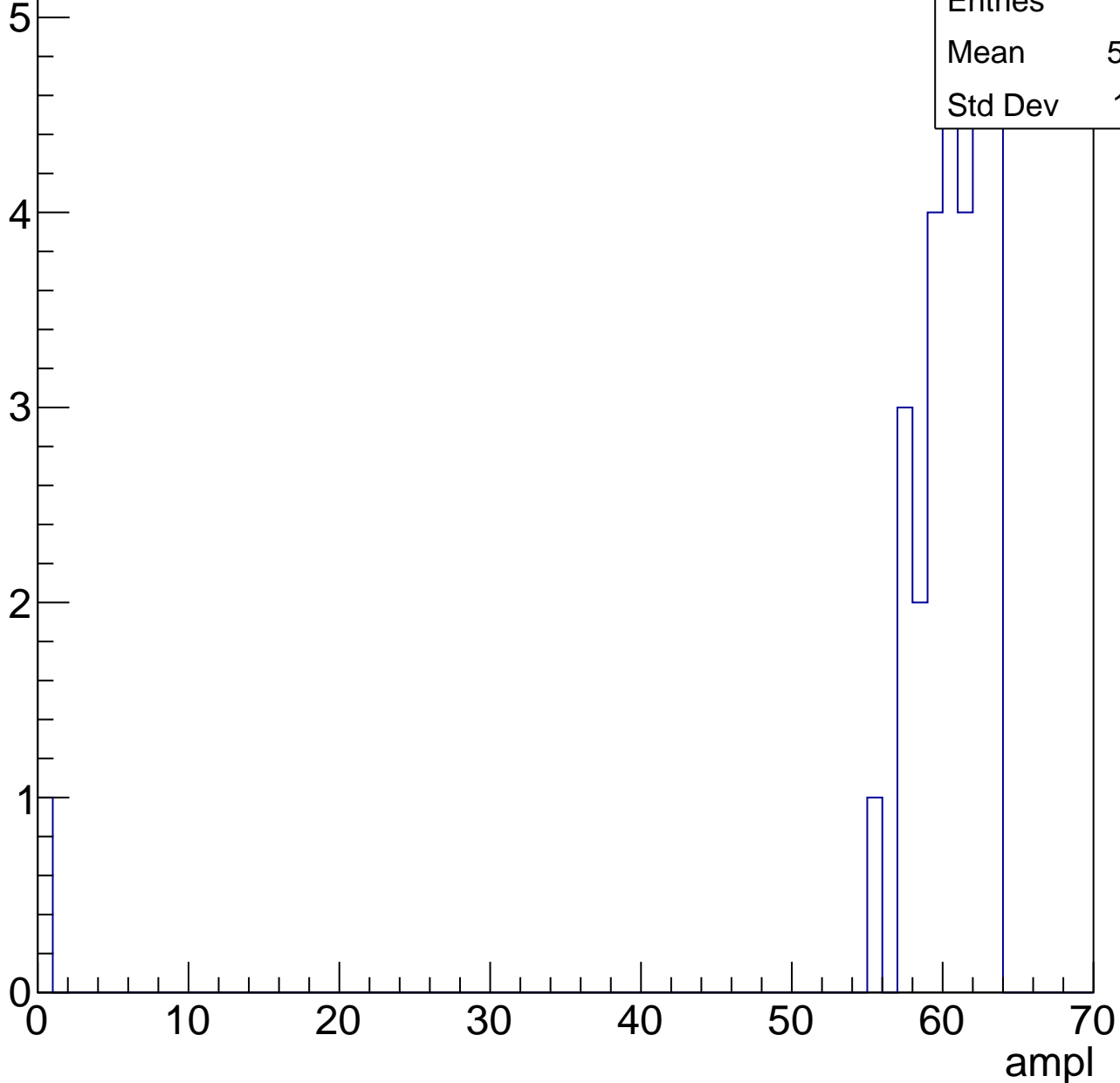


B1L103S, U24-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	30
Mean	58.23
Std Dev	11.01



B1L103S, U24-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U24-ch119, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	21.74
Std Dev	10.99

Gaus mean : 27.4643

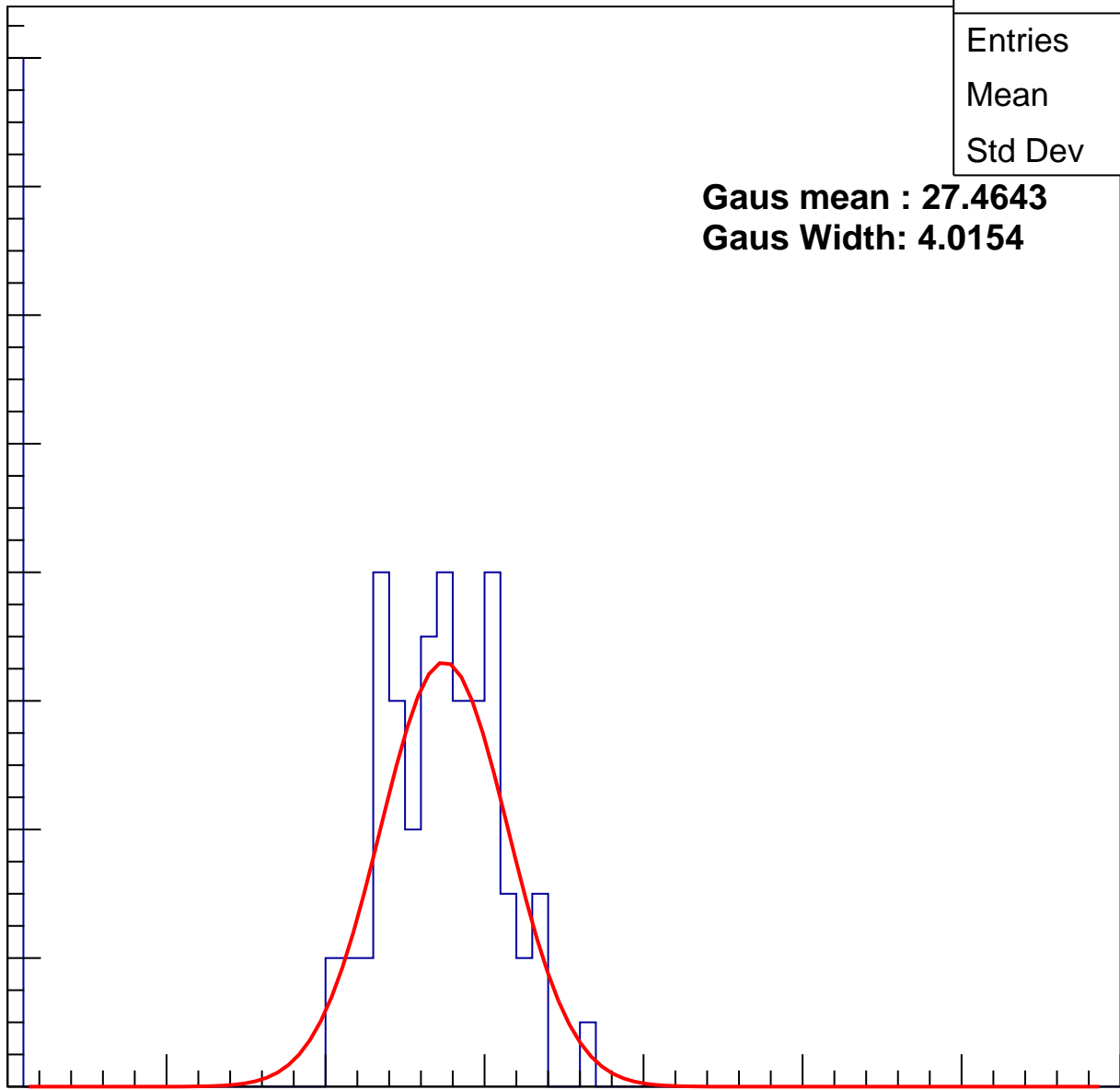
Gaus Width: 4.0154

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch119, adc1

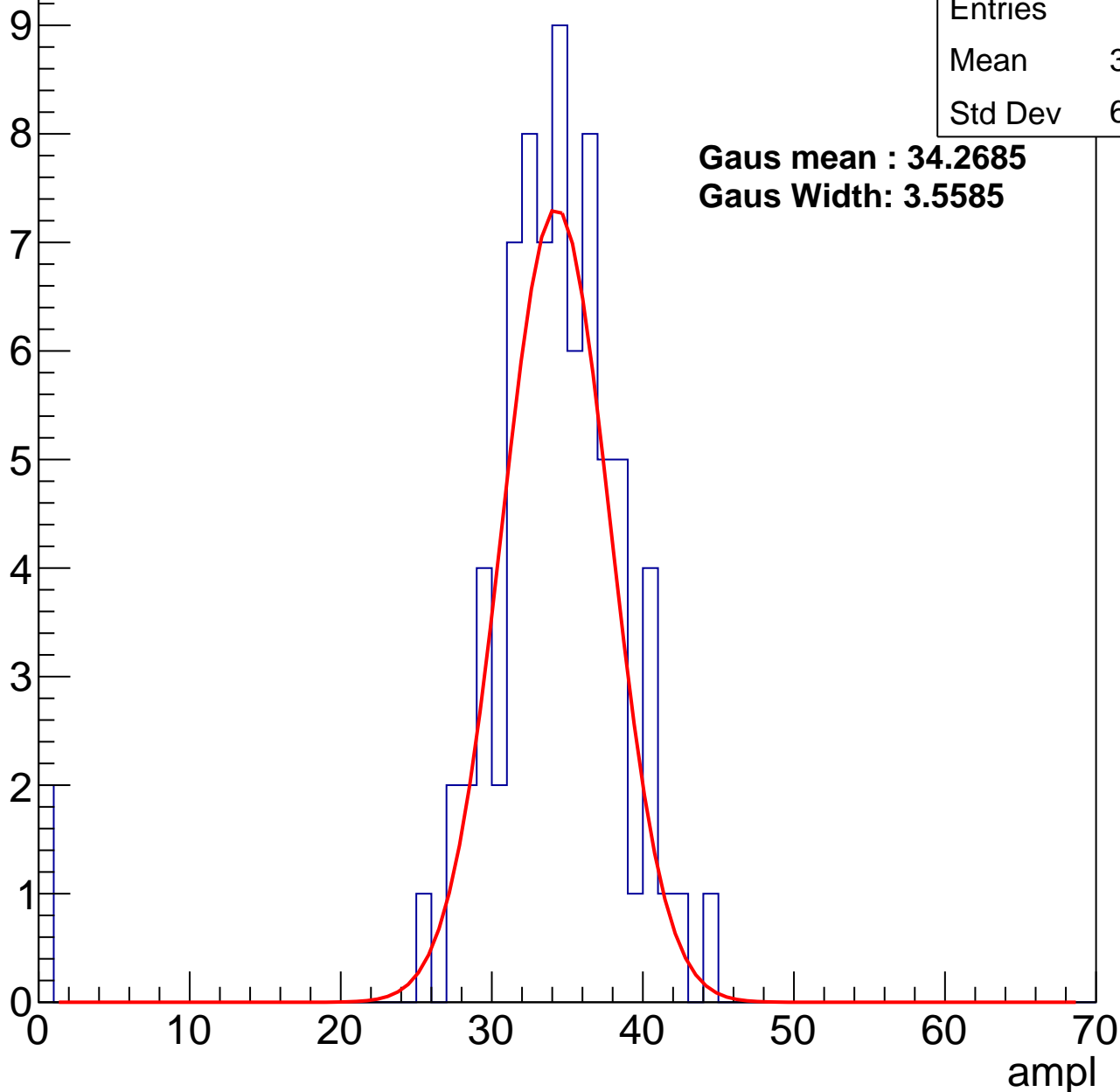
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	33.16
Std Dev	6.578

Gaus mean : 34.2685

Gaus Width: 3.5585



B1L103S, U24-ch119, adc2

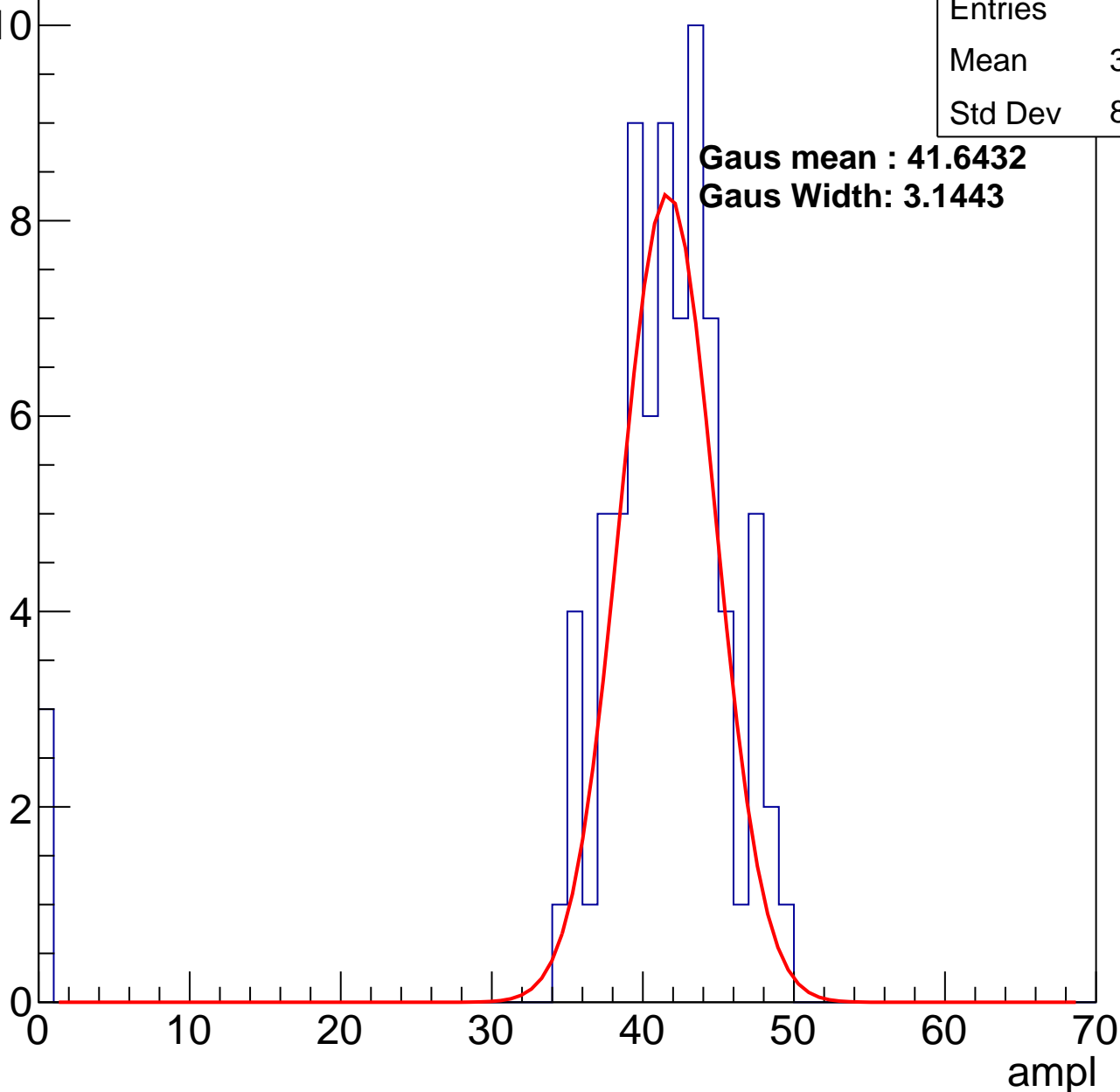
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	39.79
Std Dev	8.552

Gaus mean : 41.6432

Gaus Width: 3.1443

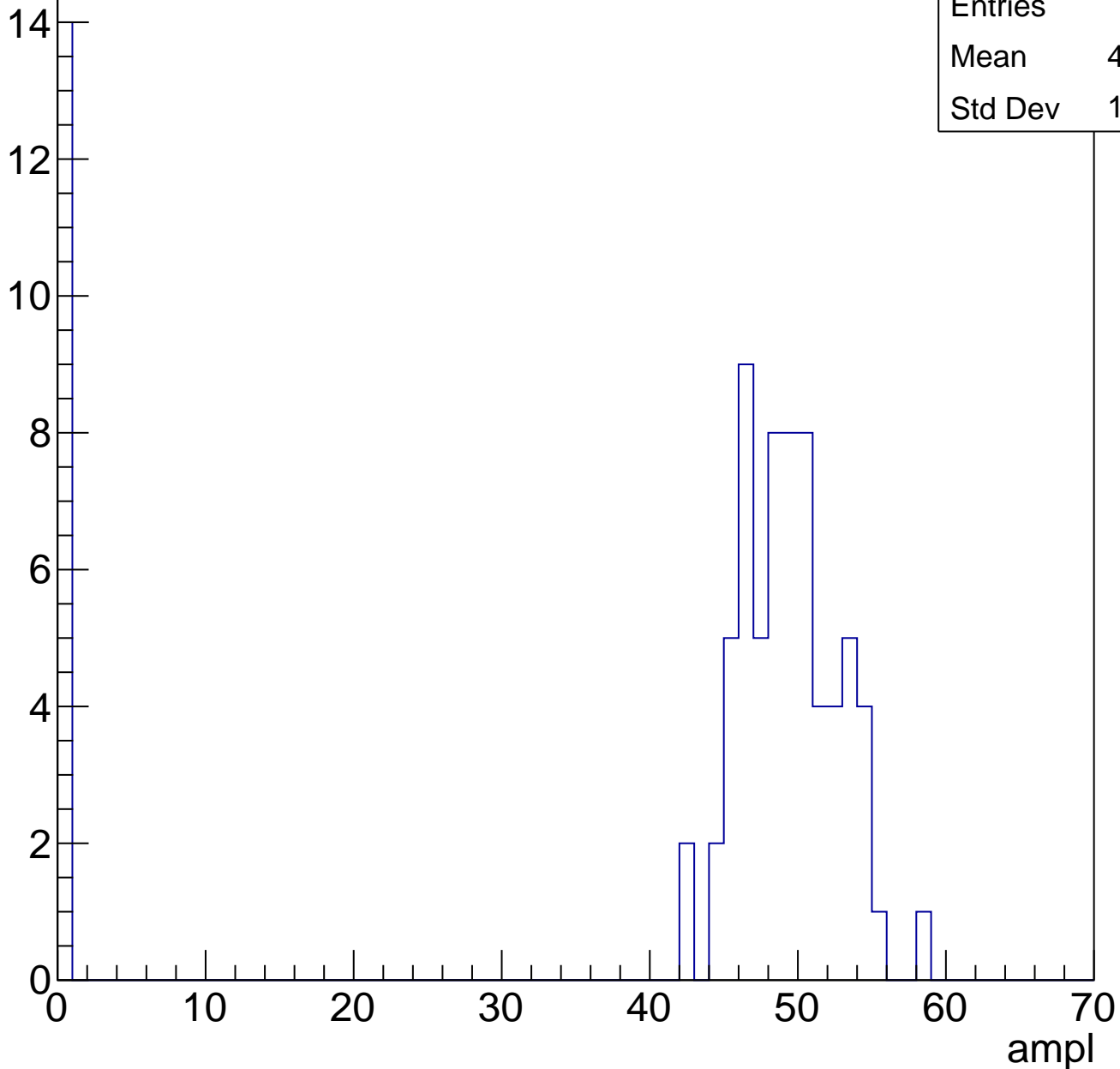


B1L103S, U24-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	40.35
Std Dev	18.82

Entry

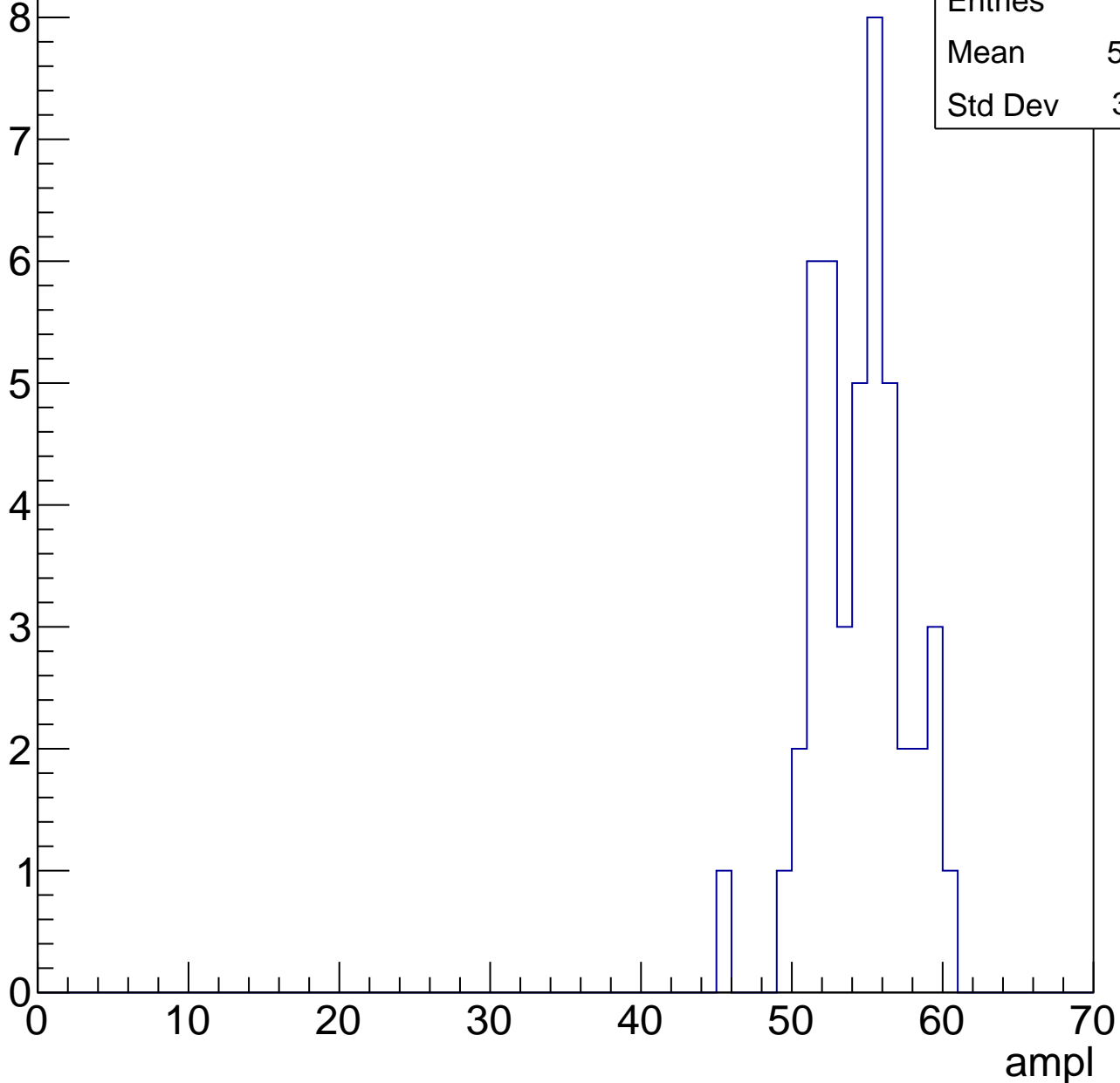


B1L103S, U24-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

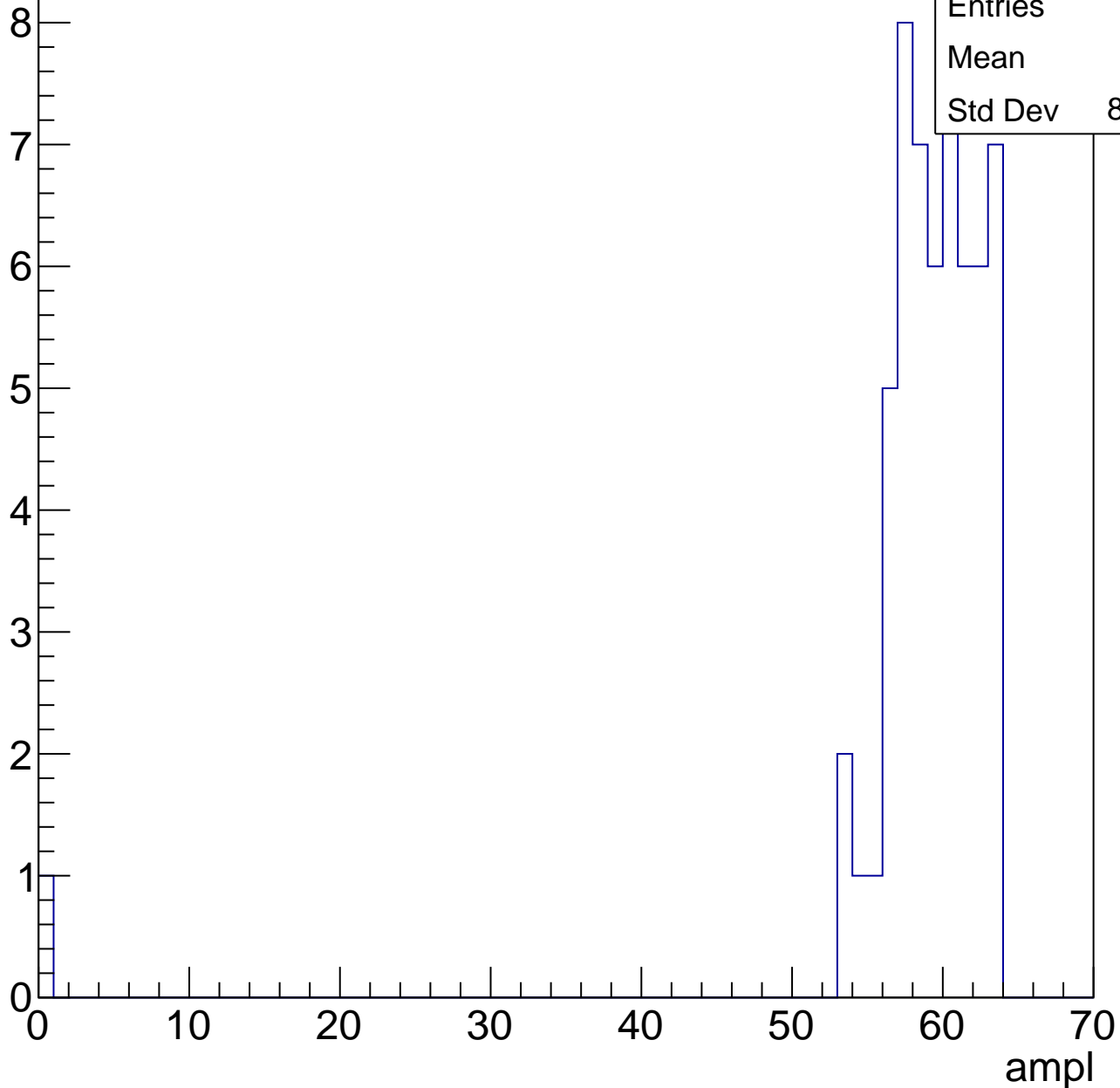
Entries	45
Mean	53.96
Std Dev	3.011



B1L103S, U24-ch119, adc5

calib_packv5_041523_1651.root, FC#0, port C2

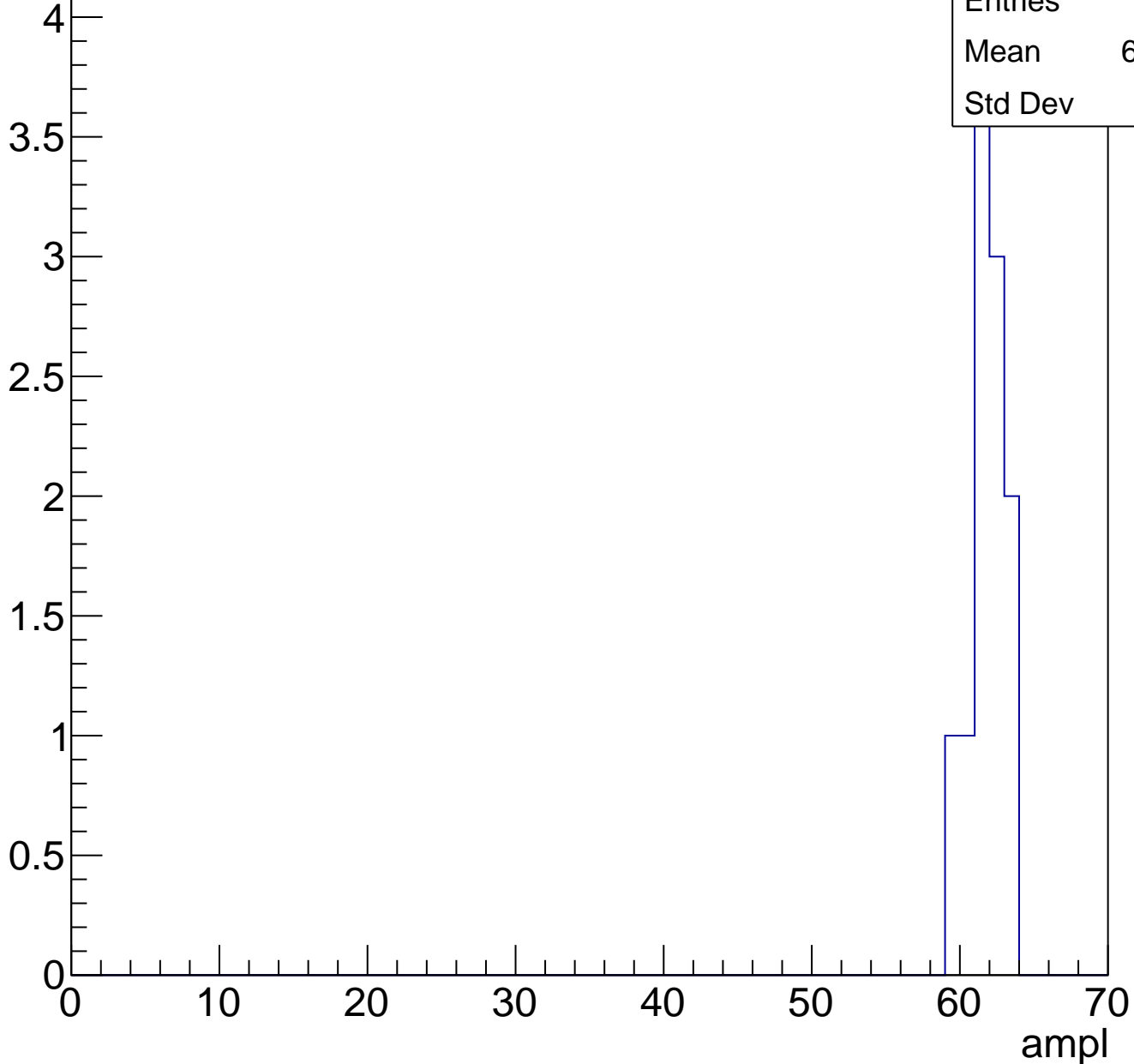
Entry



B1L103S, U24-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

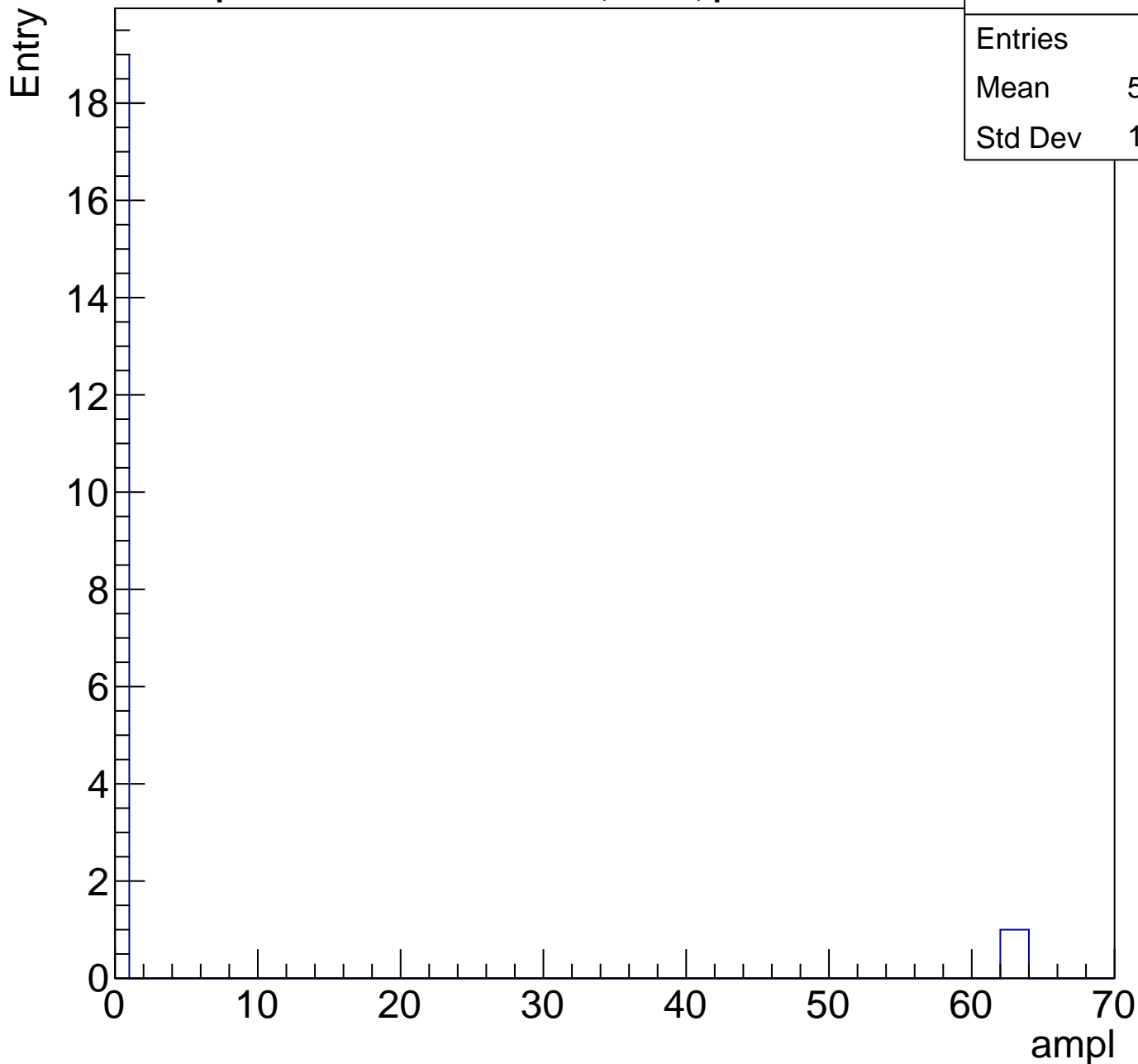
Entry



B1L103S, U24-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



B1L103S, U24-ch120, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	19.3
Std Dev	12.5

Gaus mean : 27.4830

Gaus Width: 3.9288

Entry

25

20

15

10

5

0

0

10

20

30

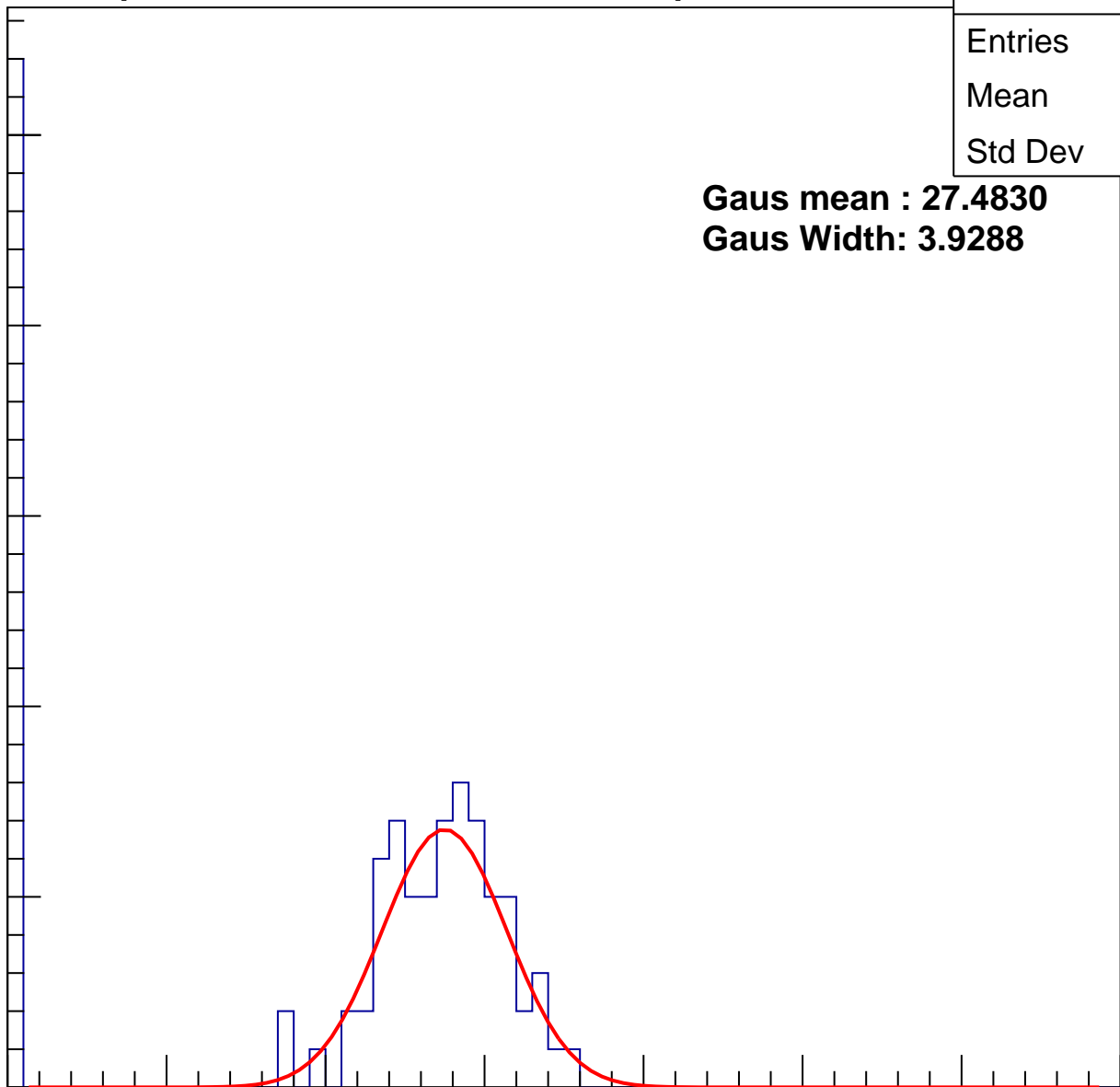
40

50

60

70

ampl



B1L103S, U24-ch120, adc1

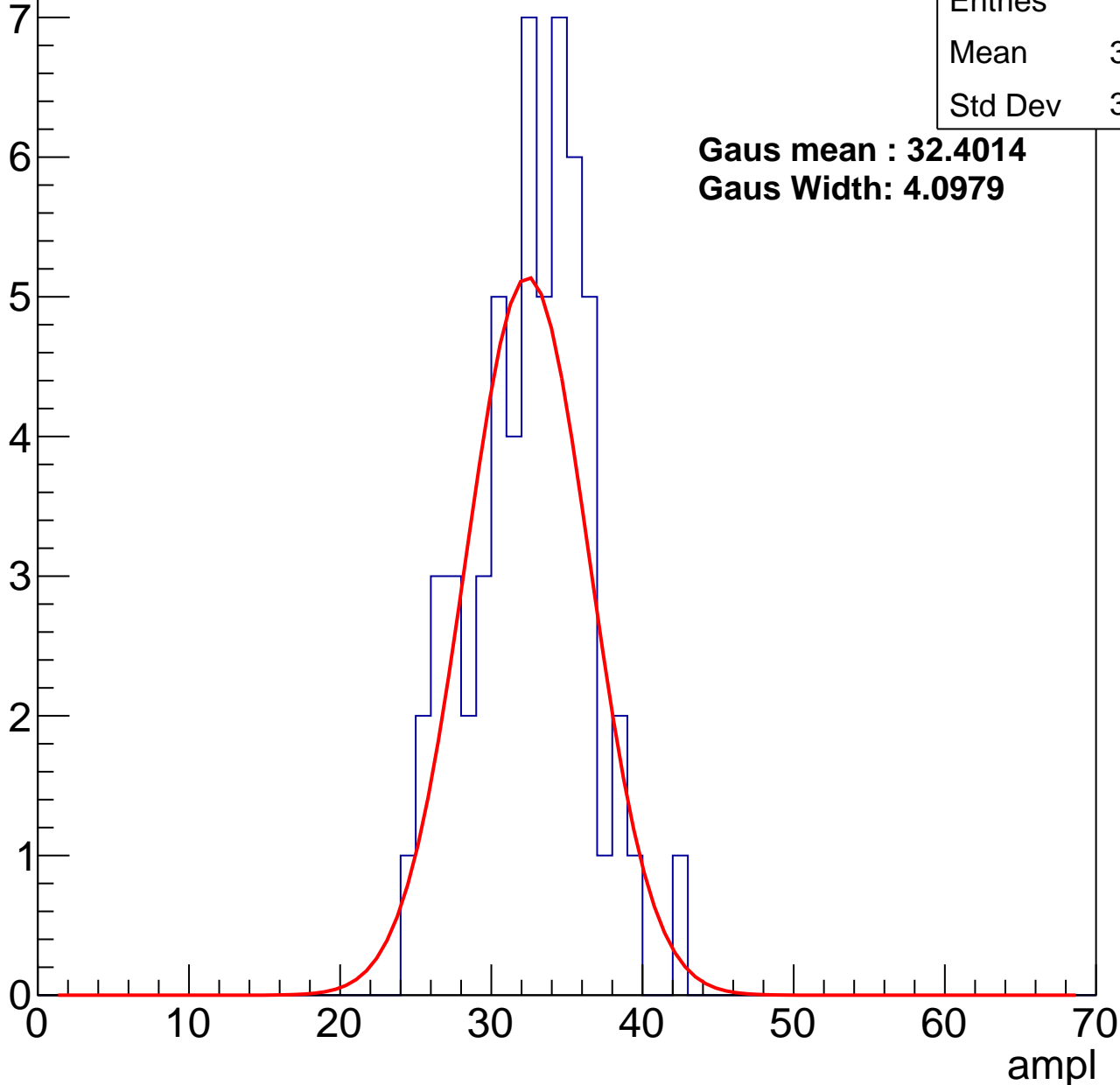
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	32.09
Std Dev	3.807

Gaus mean : 32.4014

Gaus Width: 4.0979



B1L103S, U24-ch120, adc2

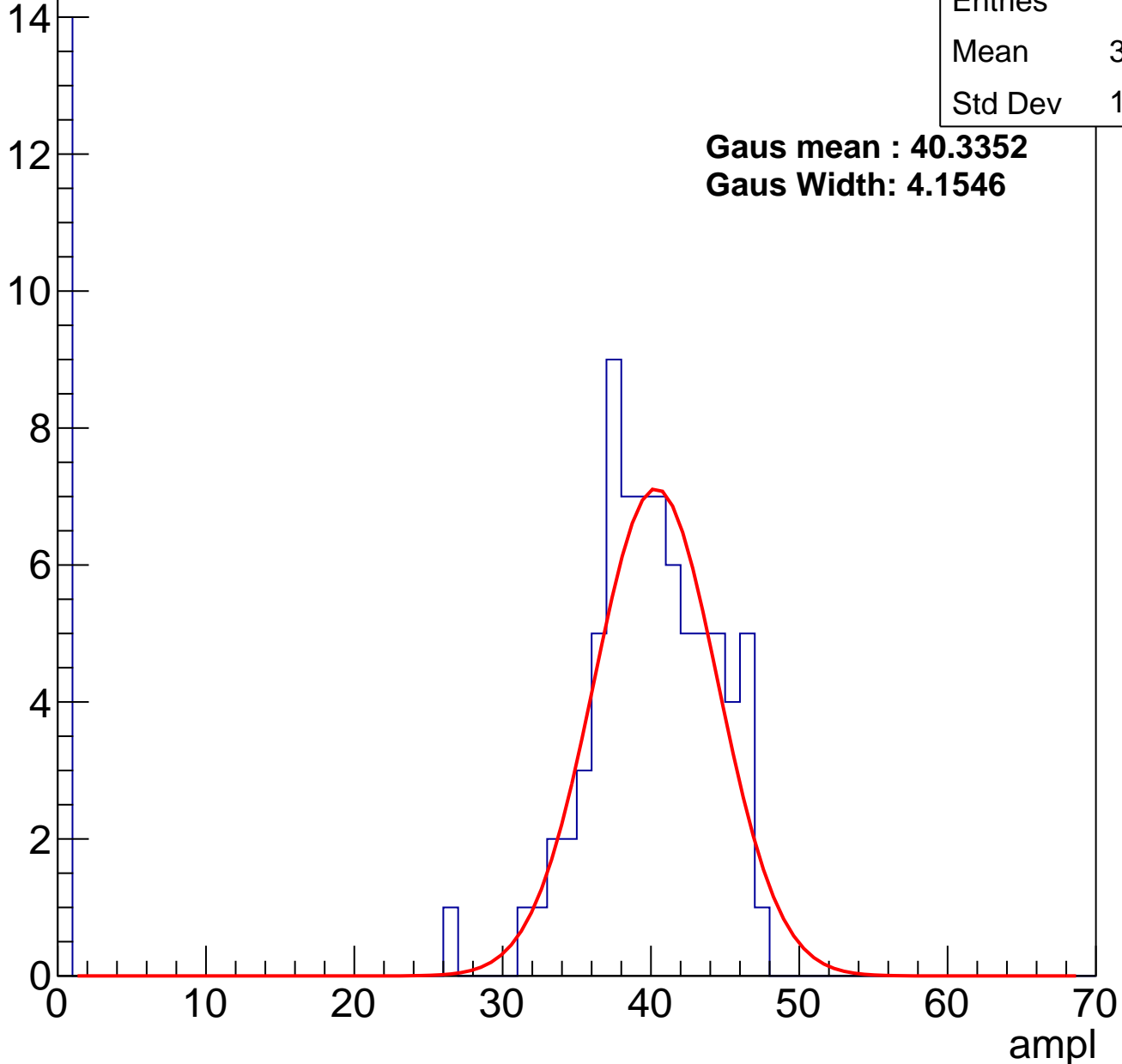
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	33.42
Std Dev	14.82

Gaus mean : 40.3352

Gaus Width: 4.1546

Entry

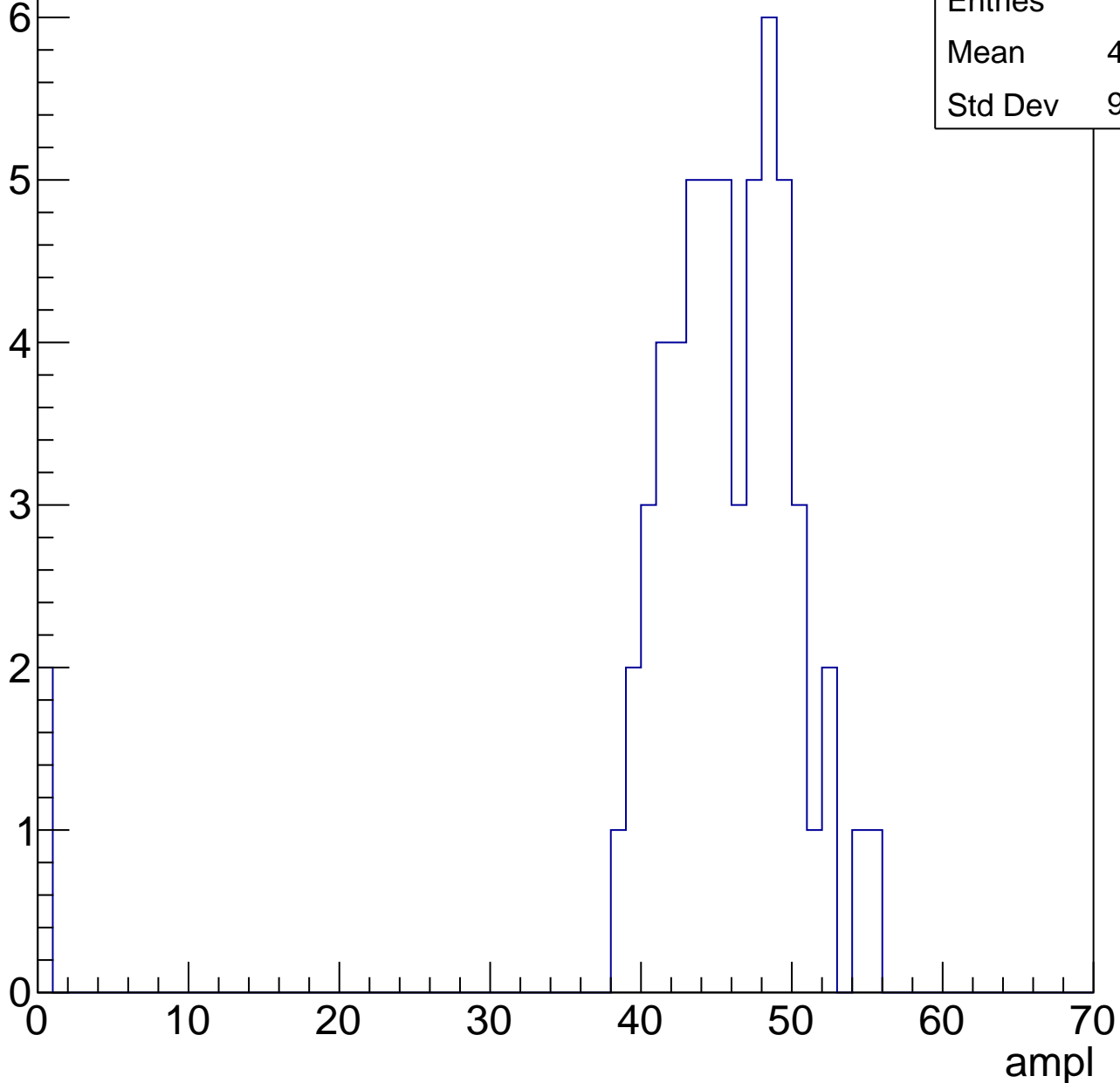


B1L103S, U24-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	43.93
Std Dev	9.146

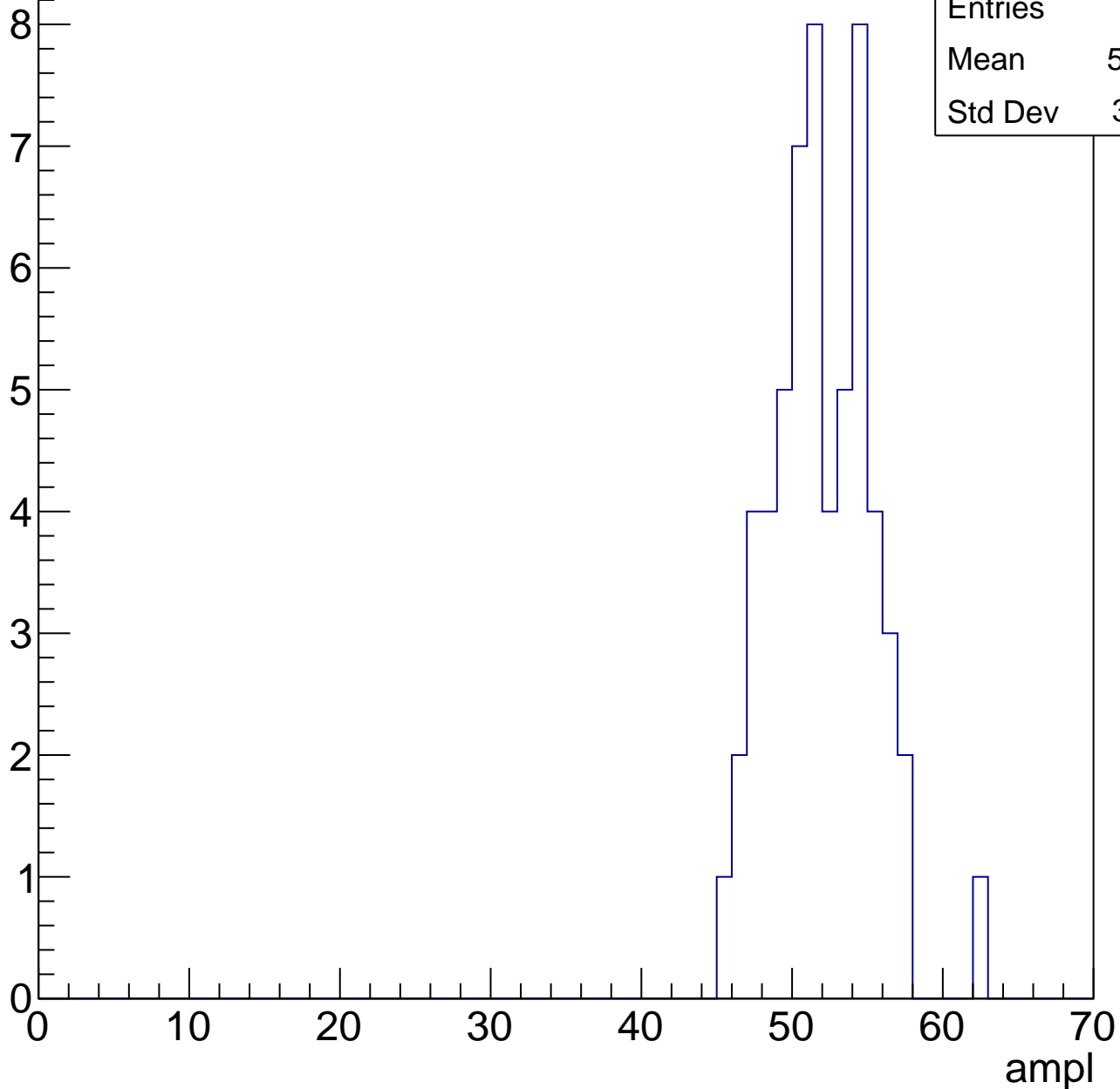


B1L103S, U24-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	51.53
Std Dev	3.281

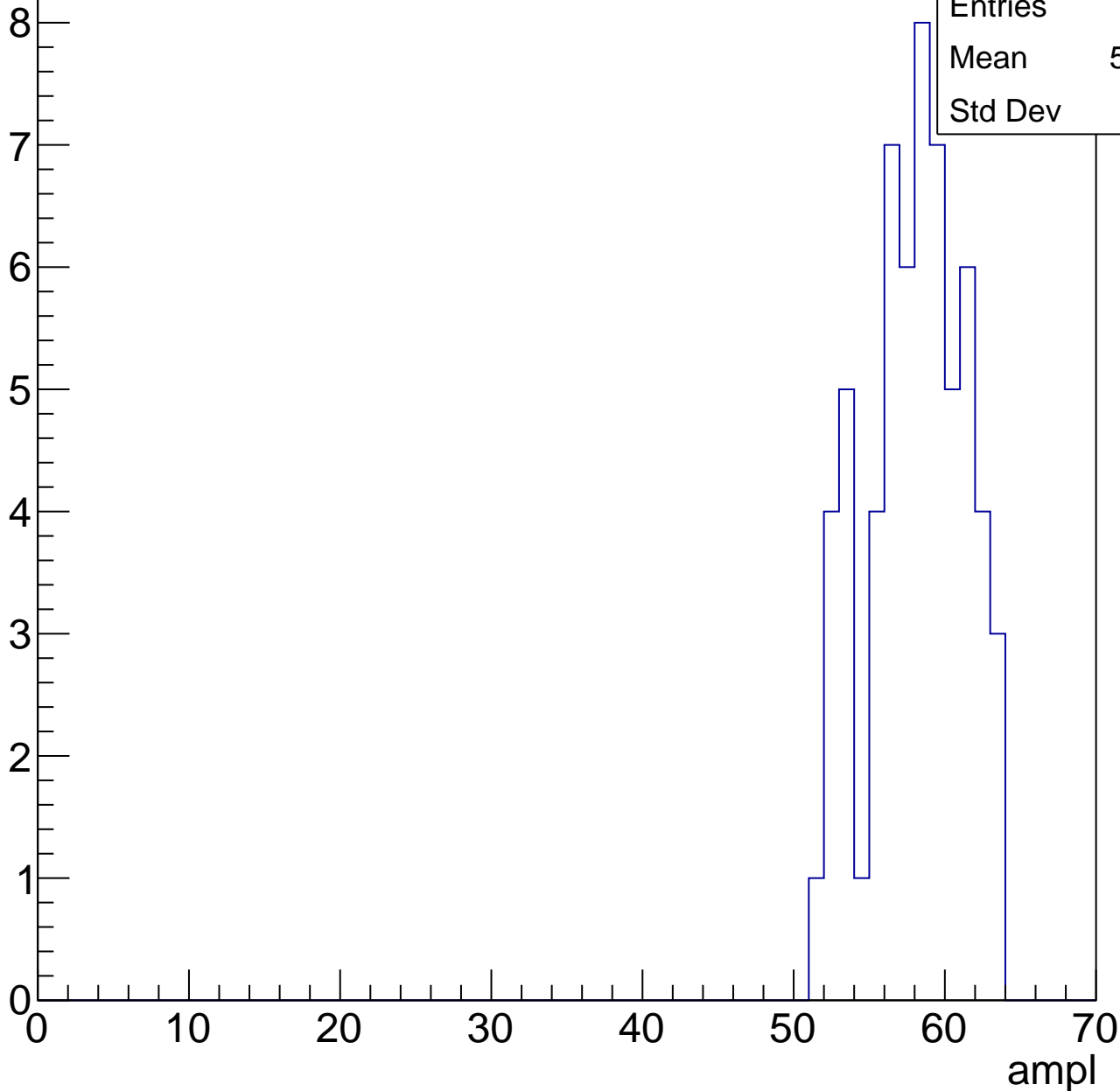


B1L103S, U24-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.57
Std Dev	3.17

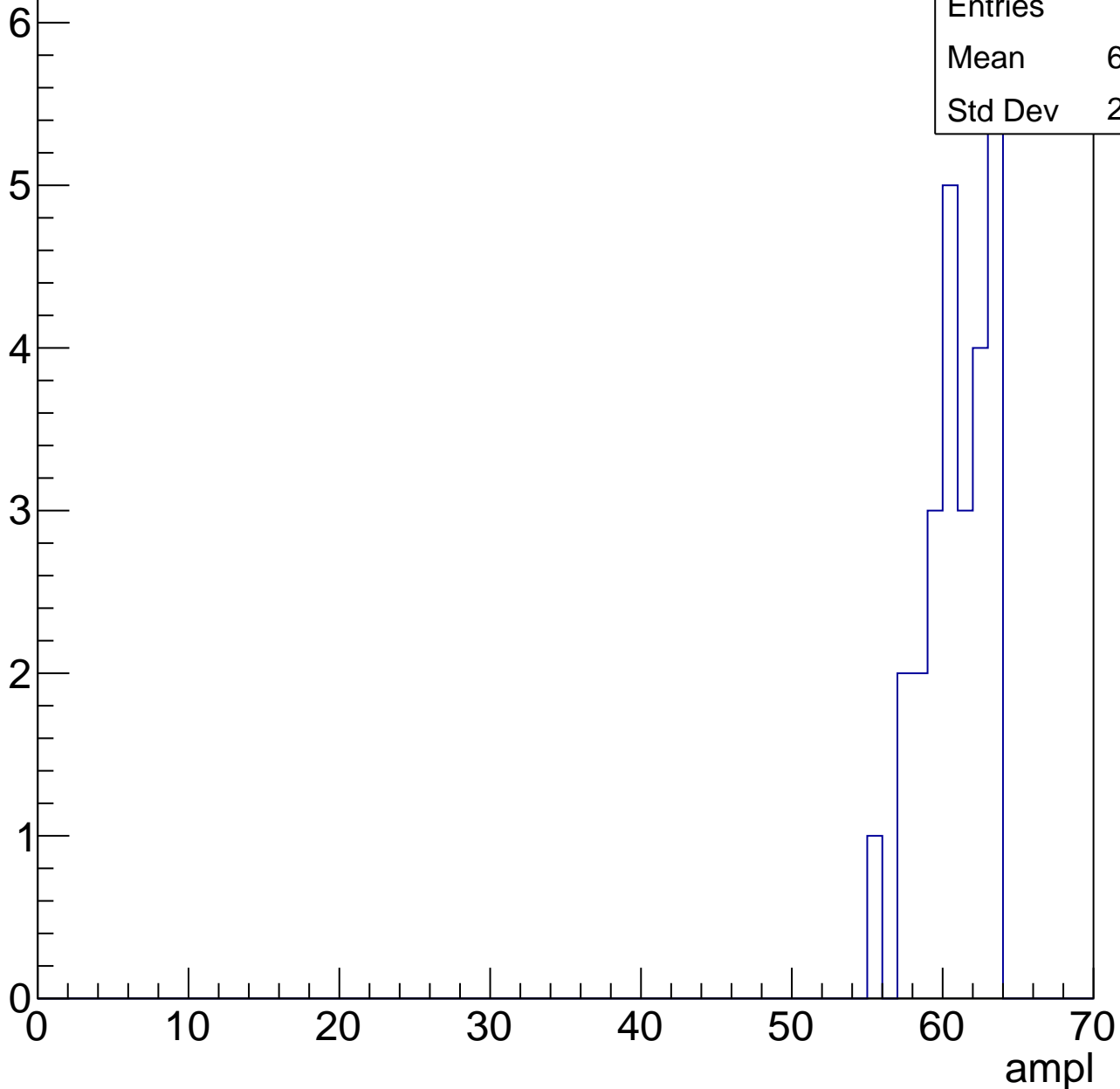


B1L103S, U24-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	26
Mean	60.42
Std Dev	2.169



B1L103S, U24-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

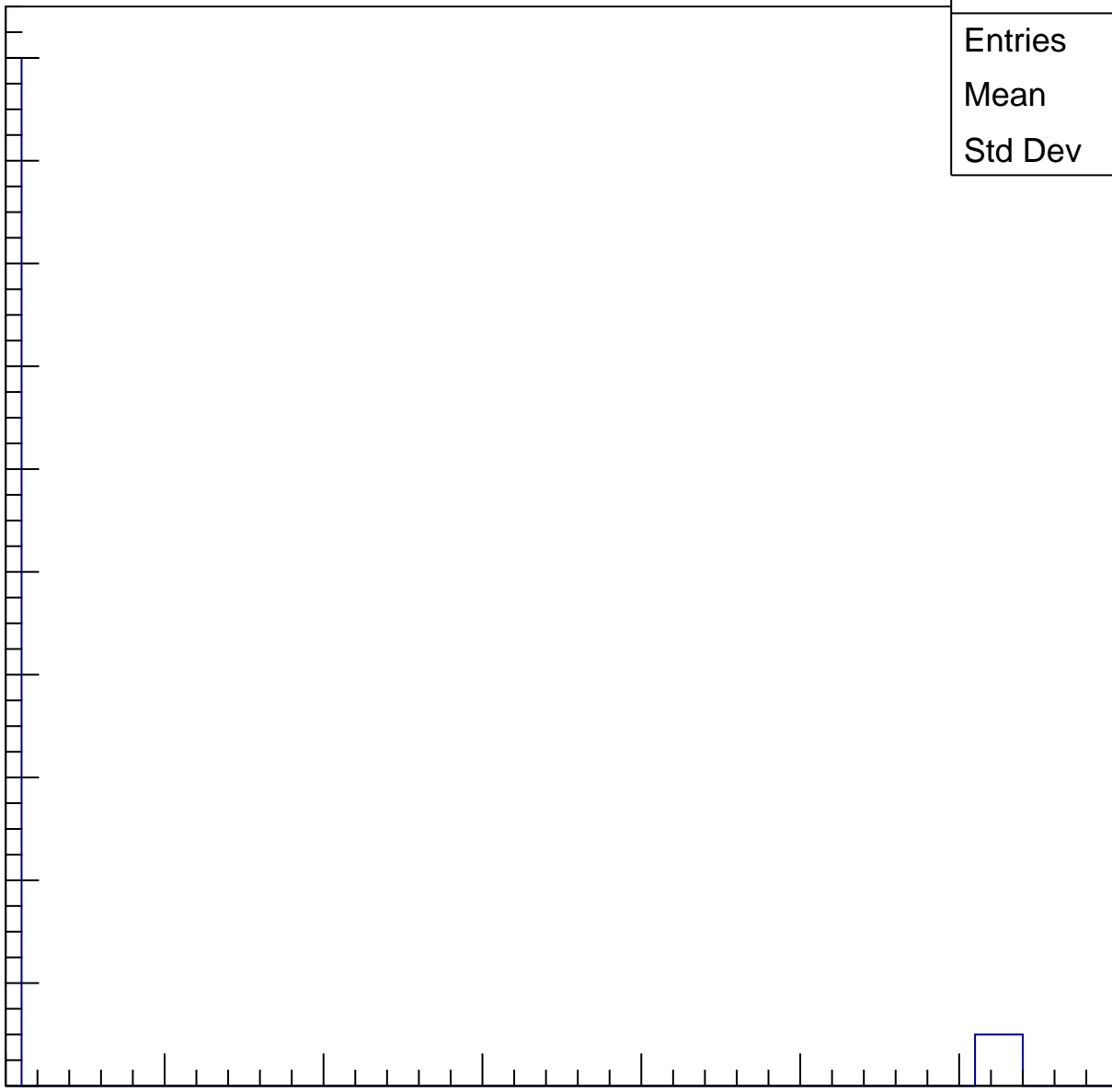
Entries	23
Mean	8.087
Std Dev	20.88

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch121, adc0

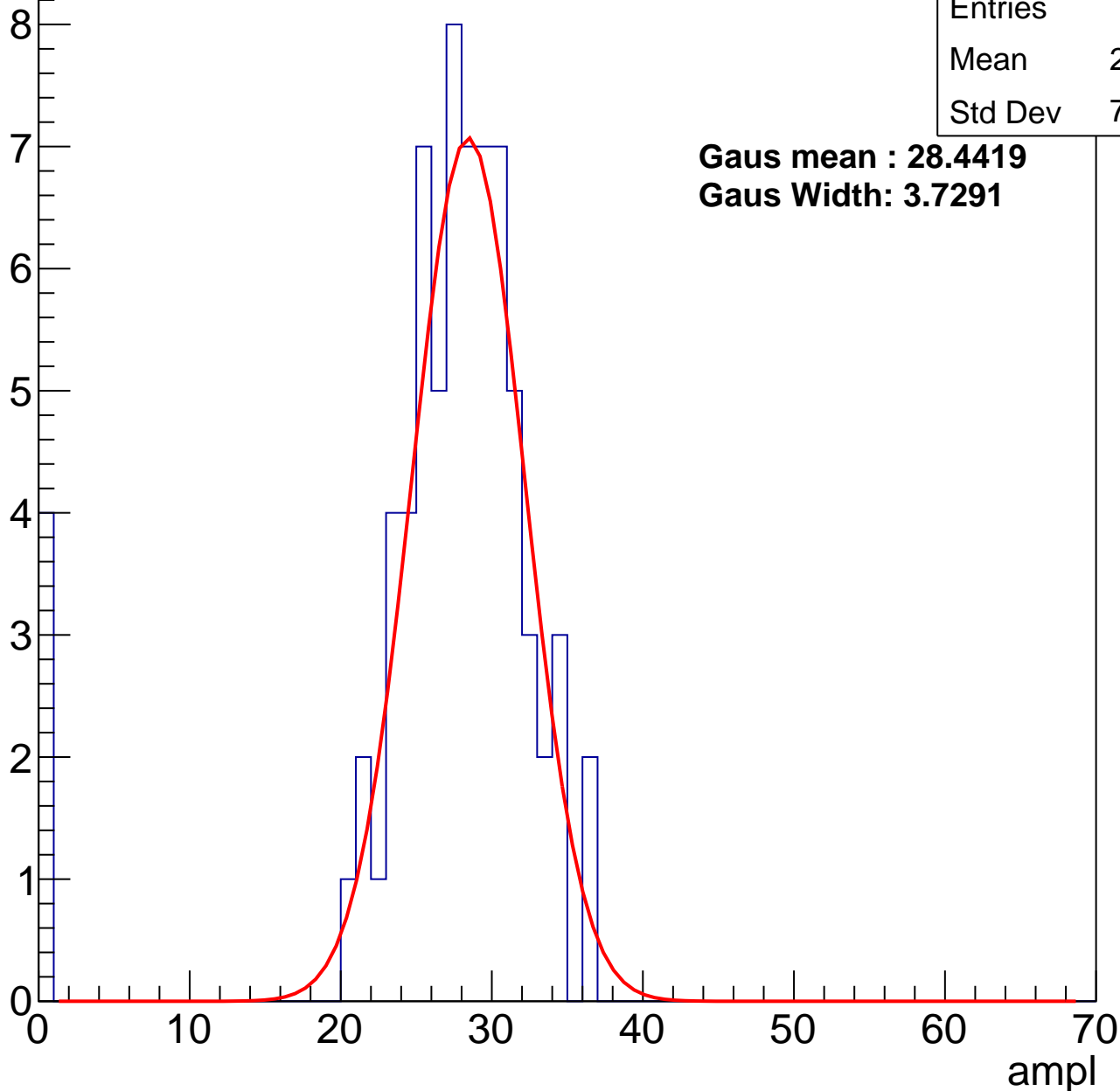
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	26.29
Std Dev	7.258

Gaus mean : 28.4419

Gaus Width: 3.7291



B1L103S, U24-ch121, adc1

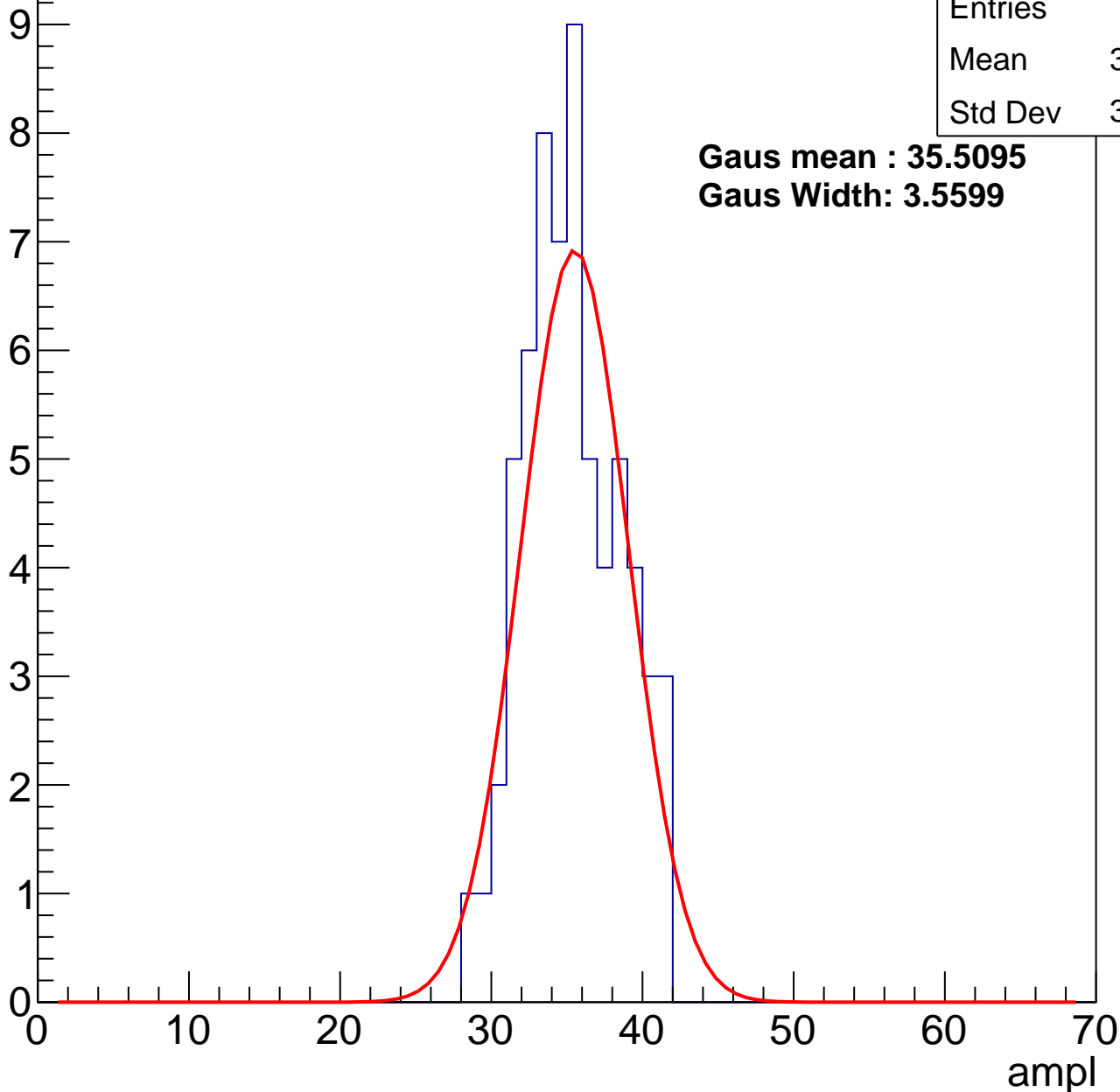
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.89
Std Dev	3.138

Gaus mean : 35.5095

Gaus Width: 3.5599



B1L103S, U24-ch121, adc2

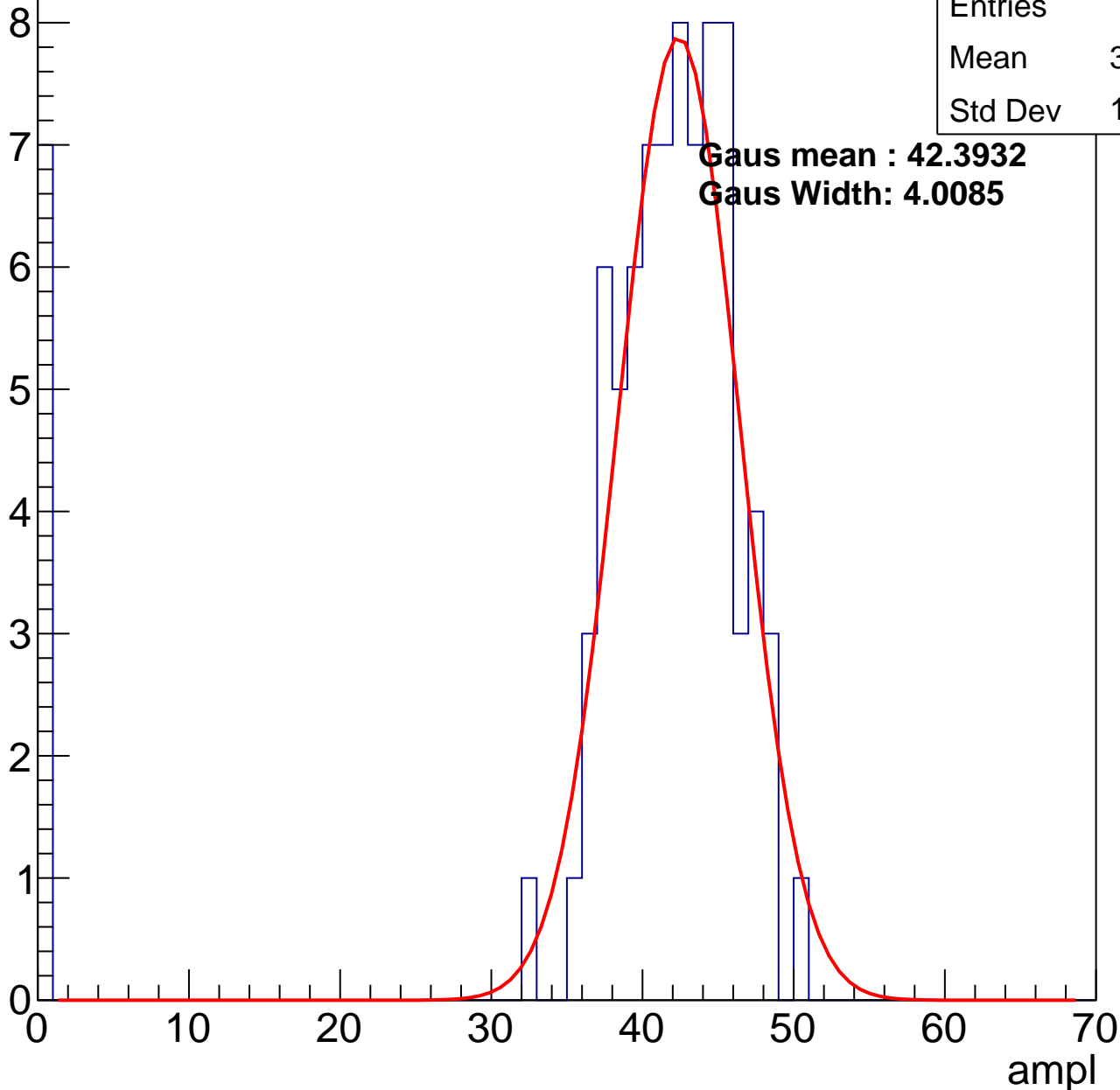
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	38.32
Std Dev	11.98

Gaus mean : 42.3932

Gaus Width: 4.0085

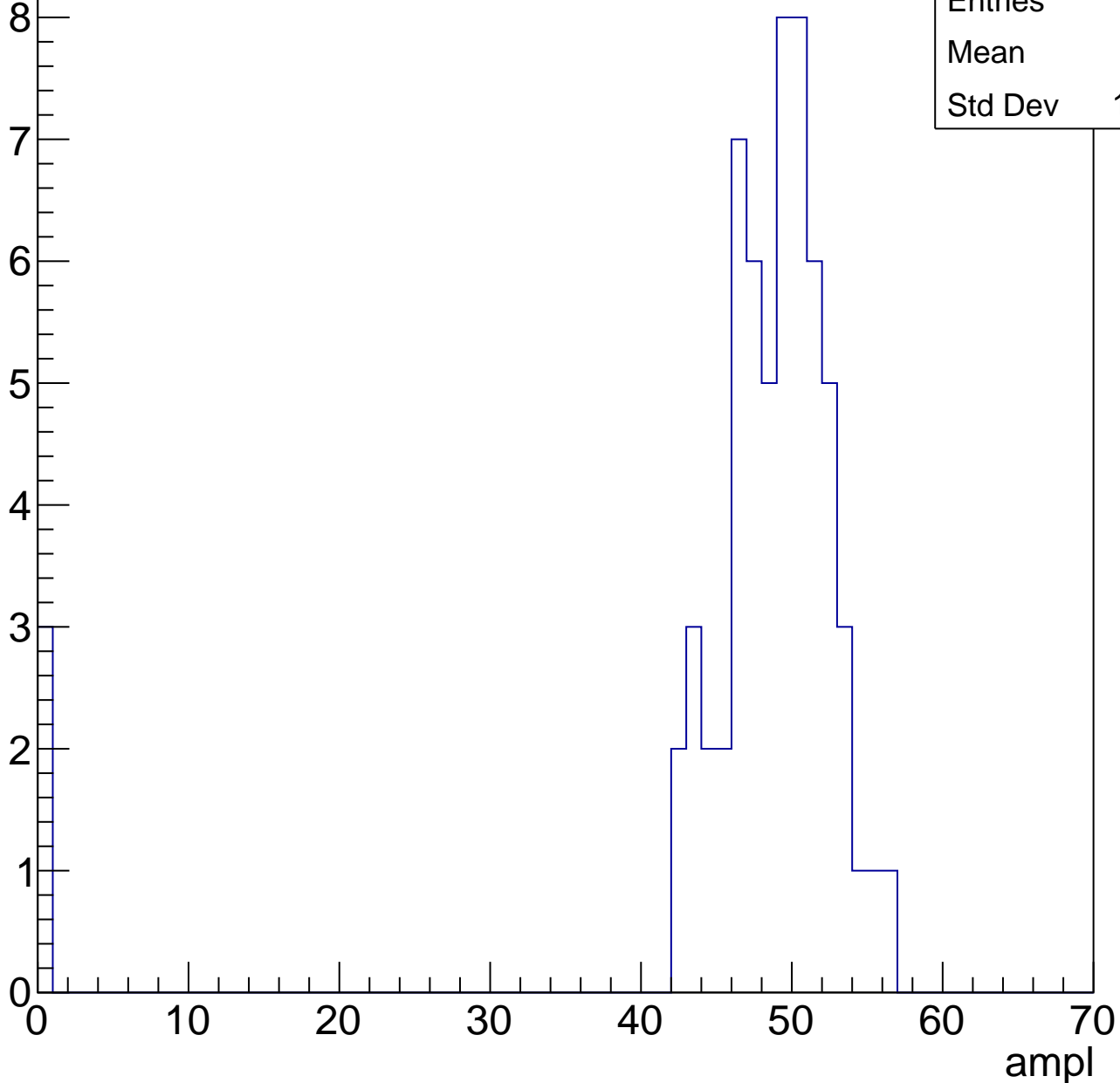


B1L103S, U24-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	46.3
Std Dev	10.81

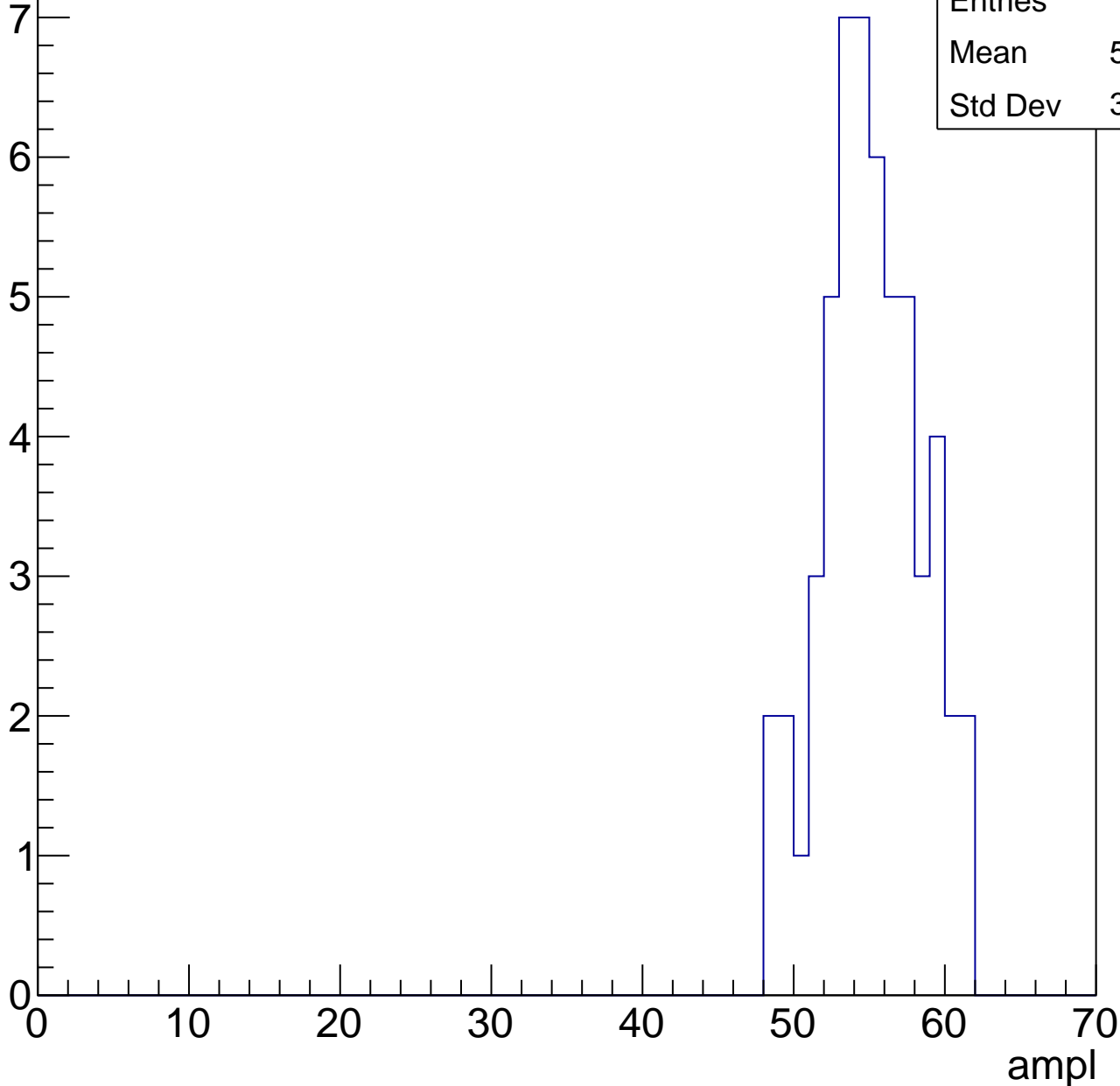


B1L103S, U24-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	54.69
Std Dev	3.208

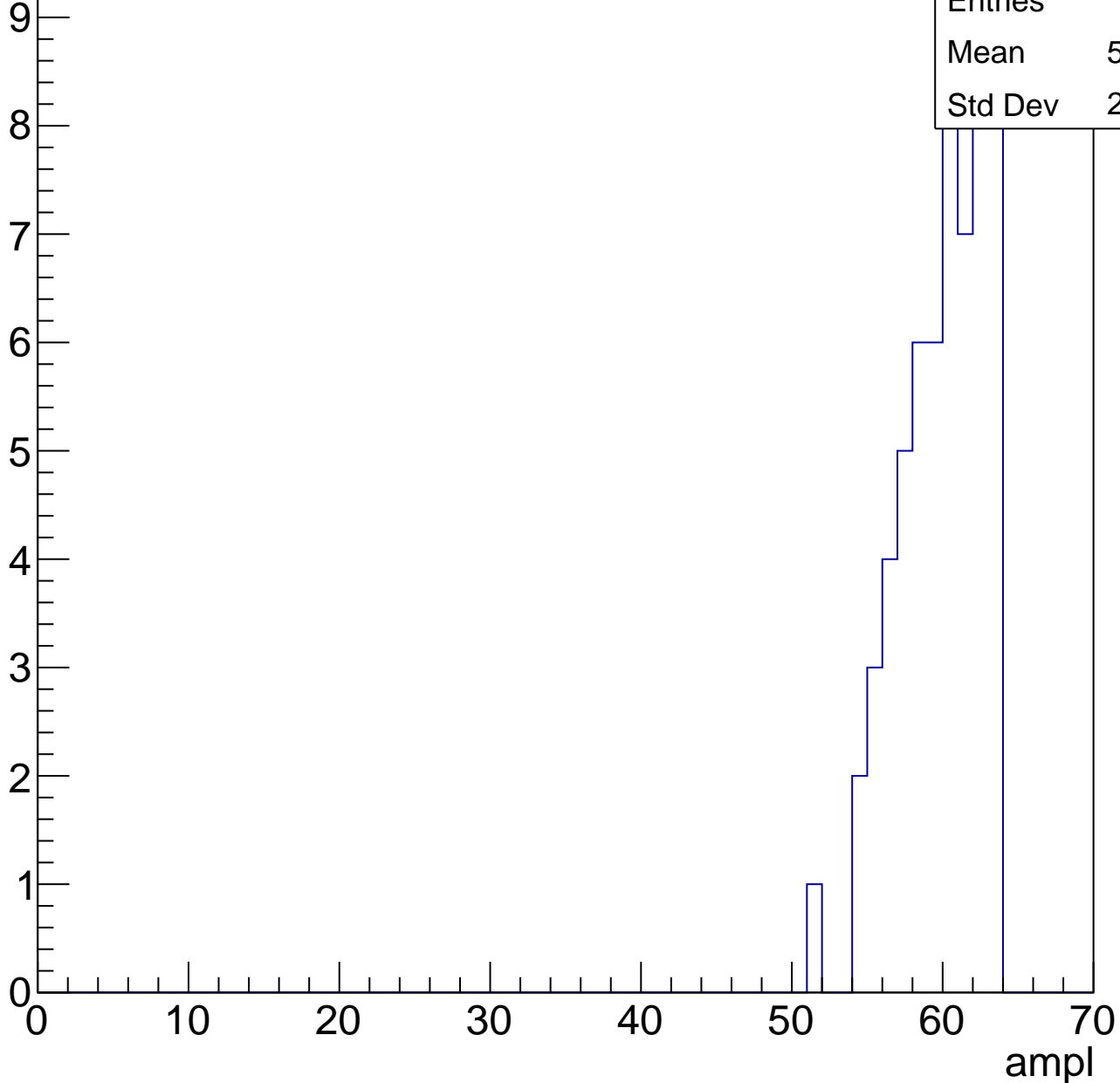


B1L103S, U24-ch121, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	59.39
Std Dev	2.792



B1L103S, U24-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

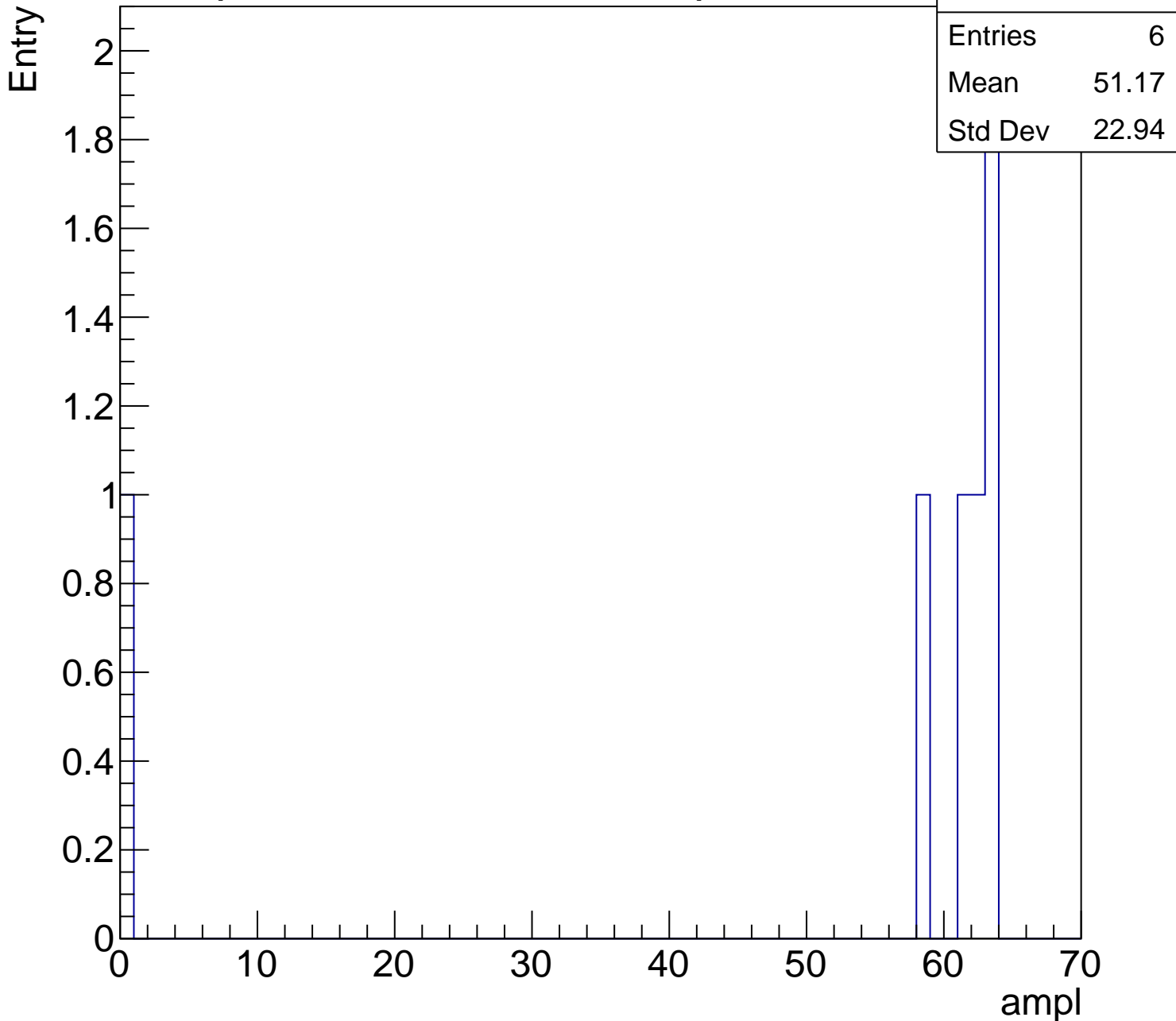
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.94

0 10 20 30 40 50 60 70

ampl



B1L103S, U24-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	18
Mean	1.056
Std Dev	4.352

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

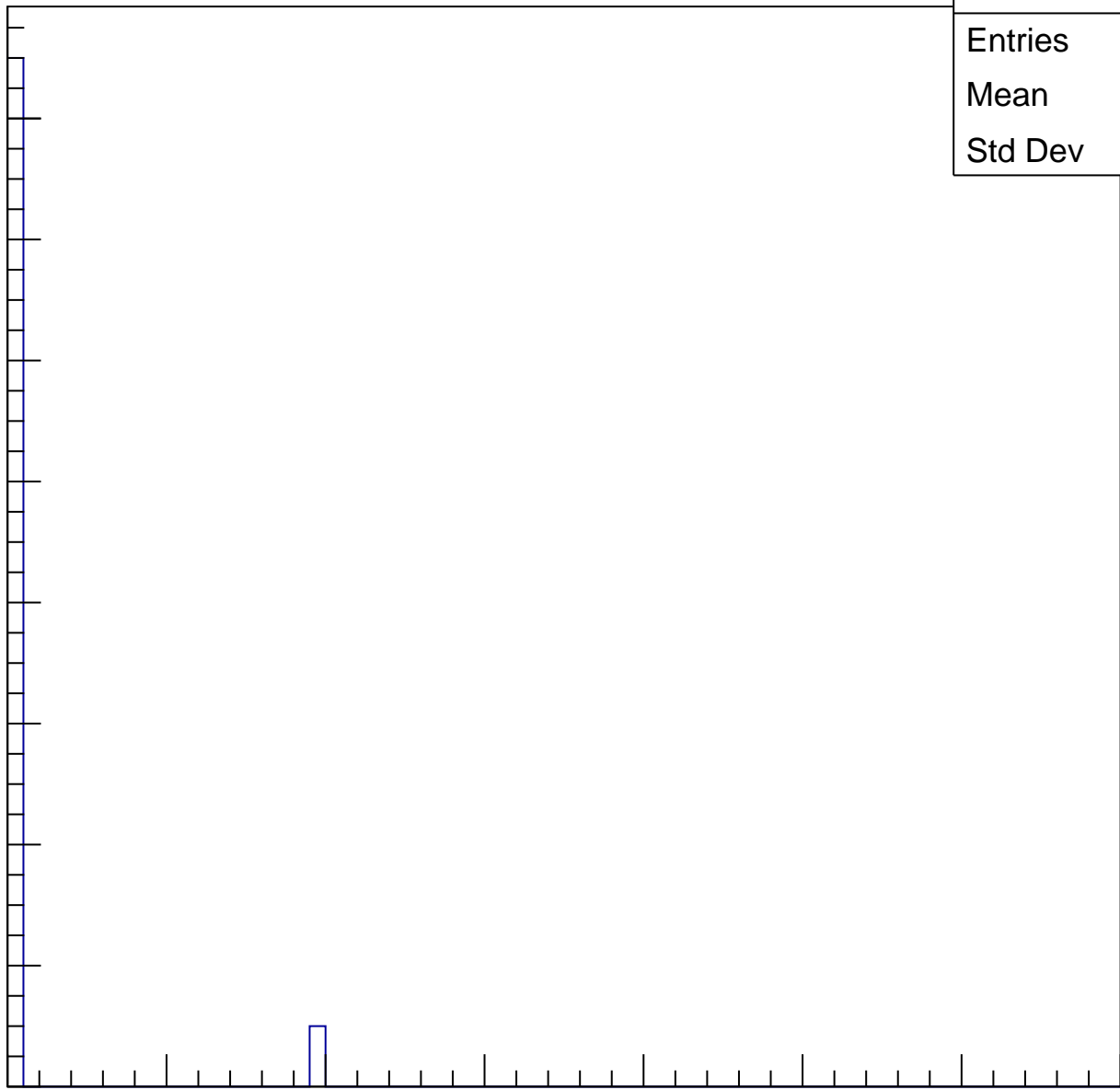
40

50

60

70

ampl



B1L103S, U24-ch122, adc0

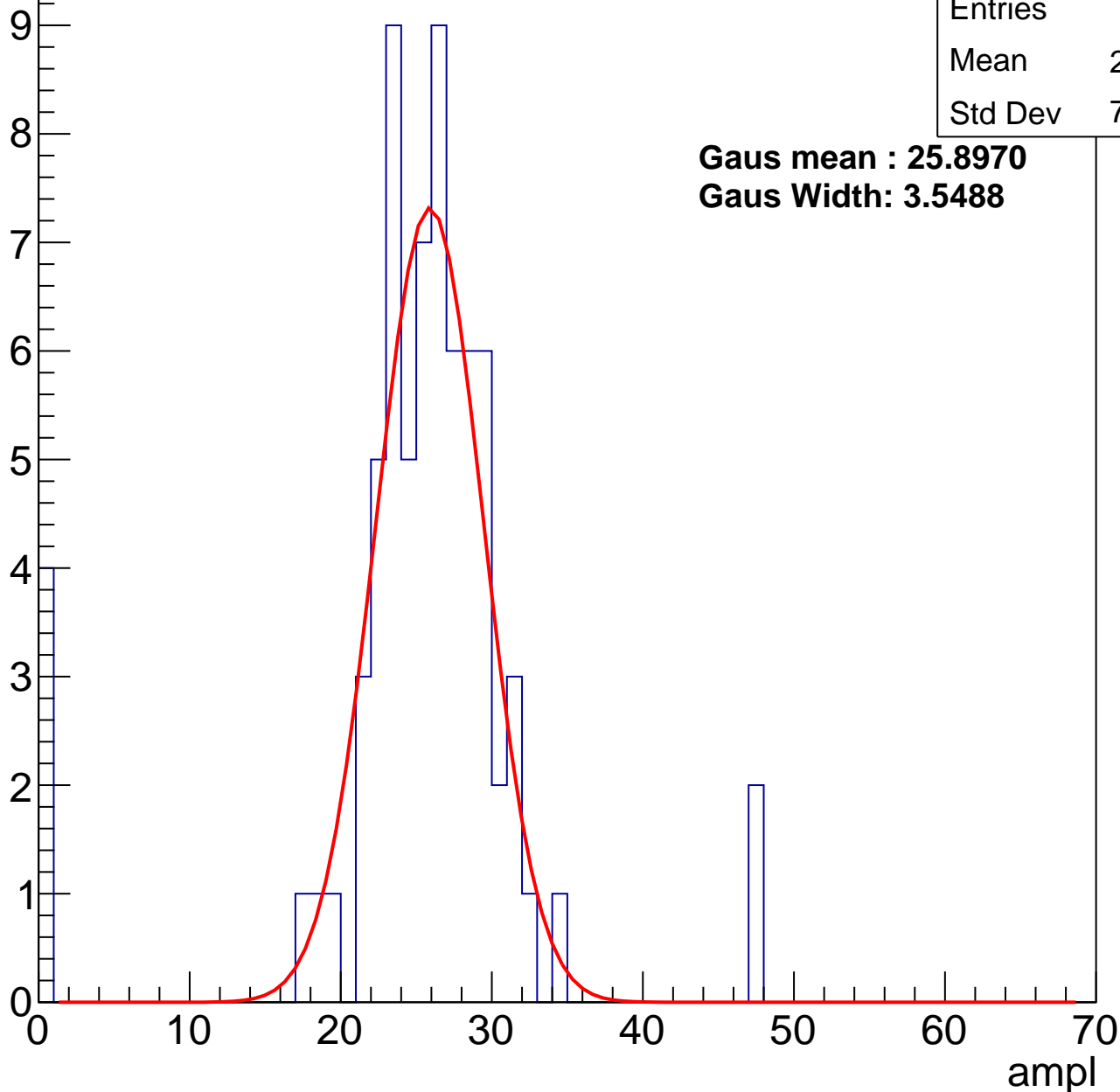
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	24.72
Std Dev	7.653

Gaus mean : 25.8970

Gaus Width: 3.5488



B1L103S, U24-ch122, adc1

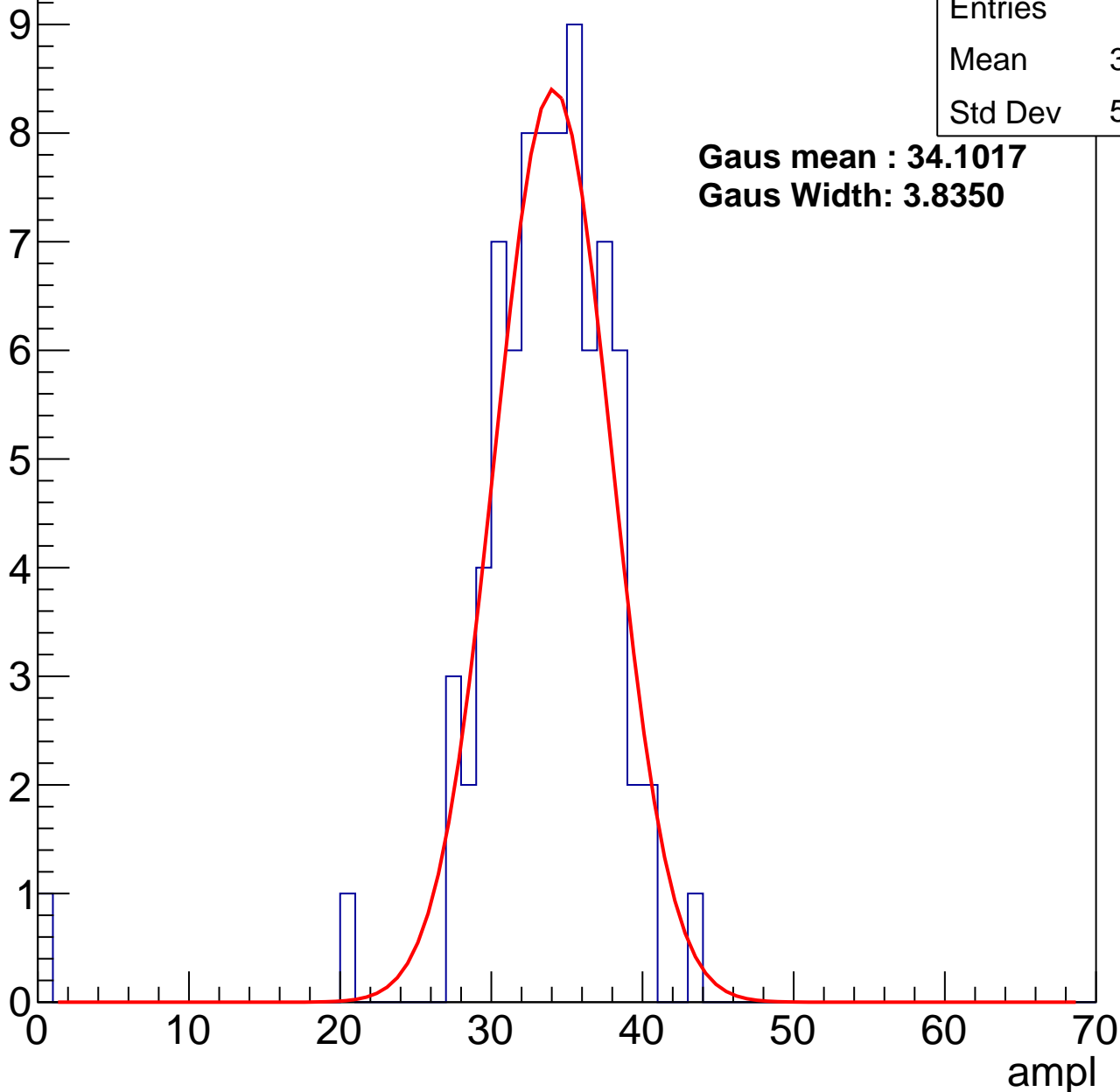
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	33.09
Std Dev	5.217

Gaus mean : 34.1017

Gaus Width: 3.8350



B1L103S, U24-ch122, adc2

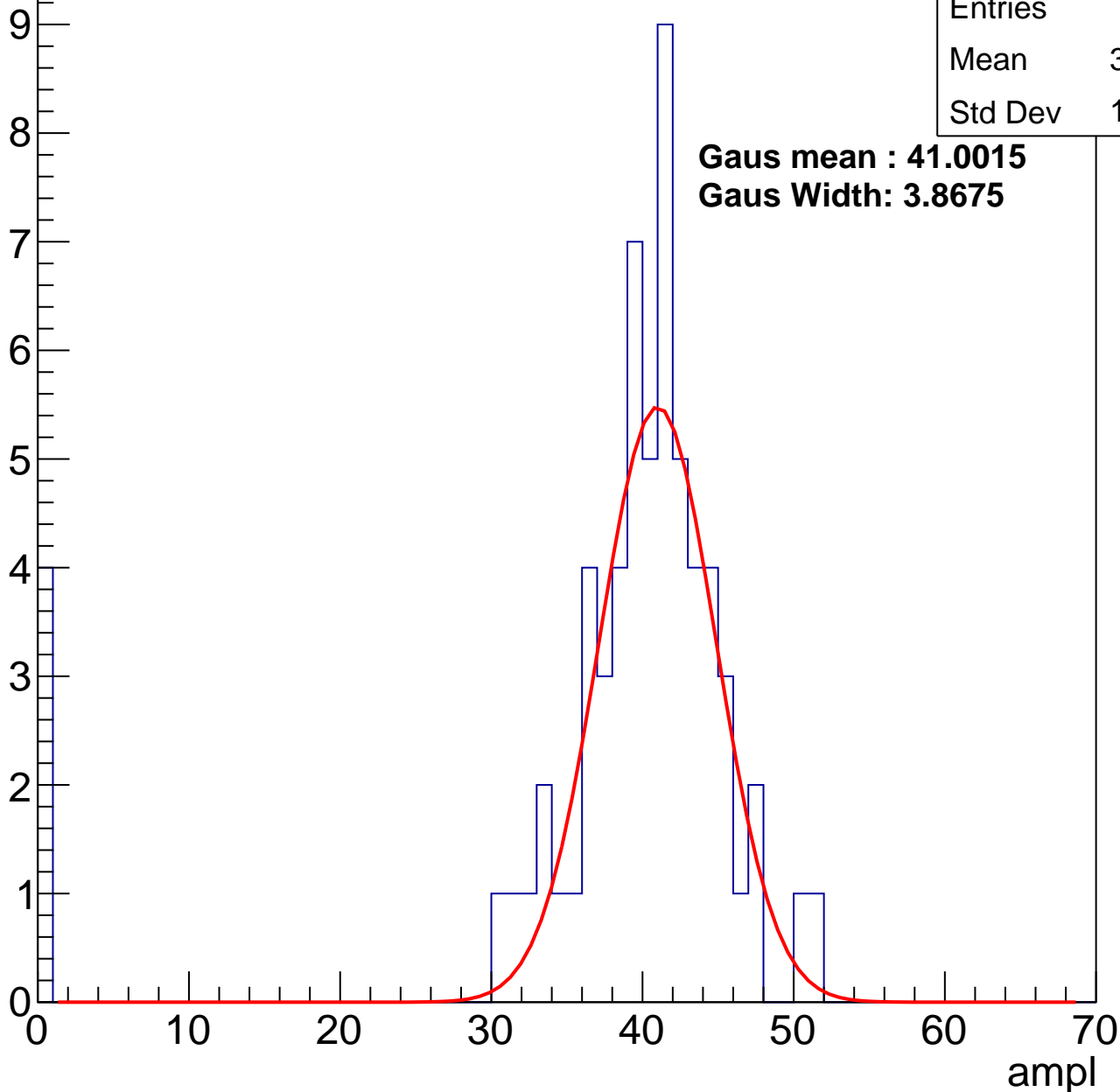
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	37.67
Std Dev	10.55

Gaus mean : 41.0015

Gaus Width: 3.8675

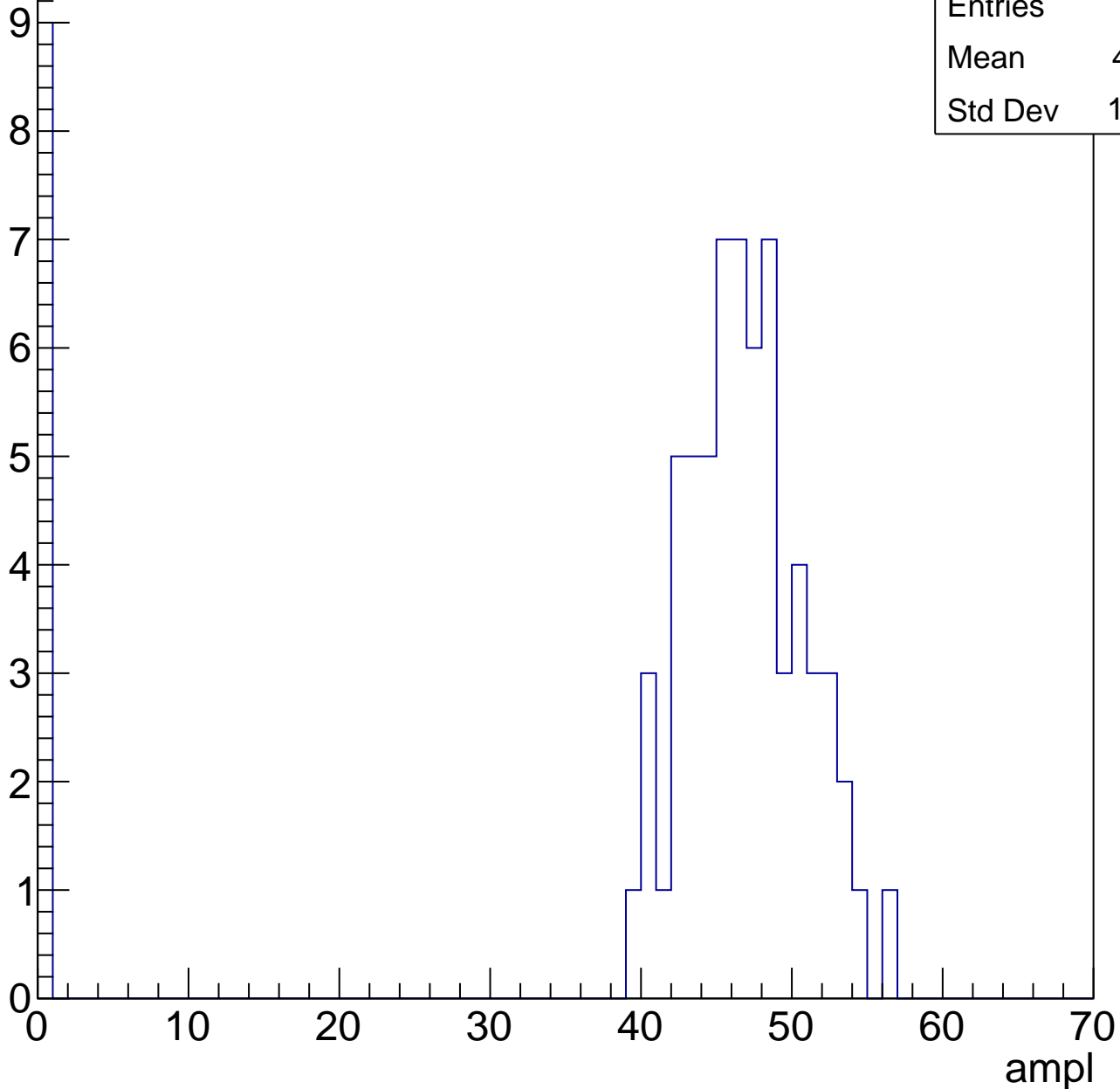


B1L103S, U24-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	40.71
Std Dev	15.66

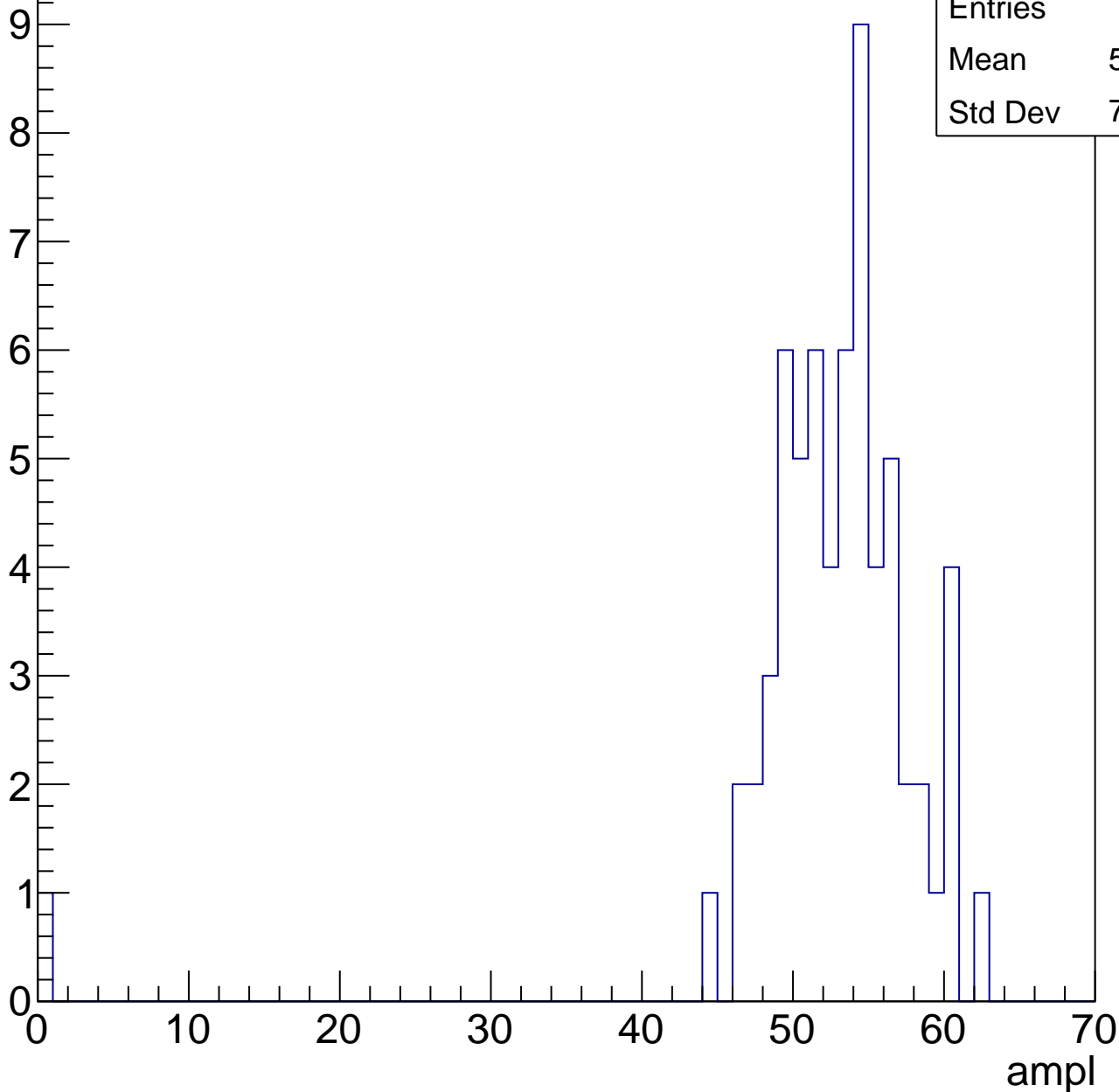


B1L103S, U24-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	51.98
Std Dev	7.607

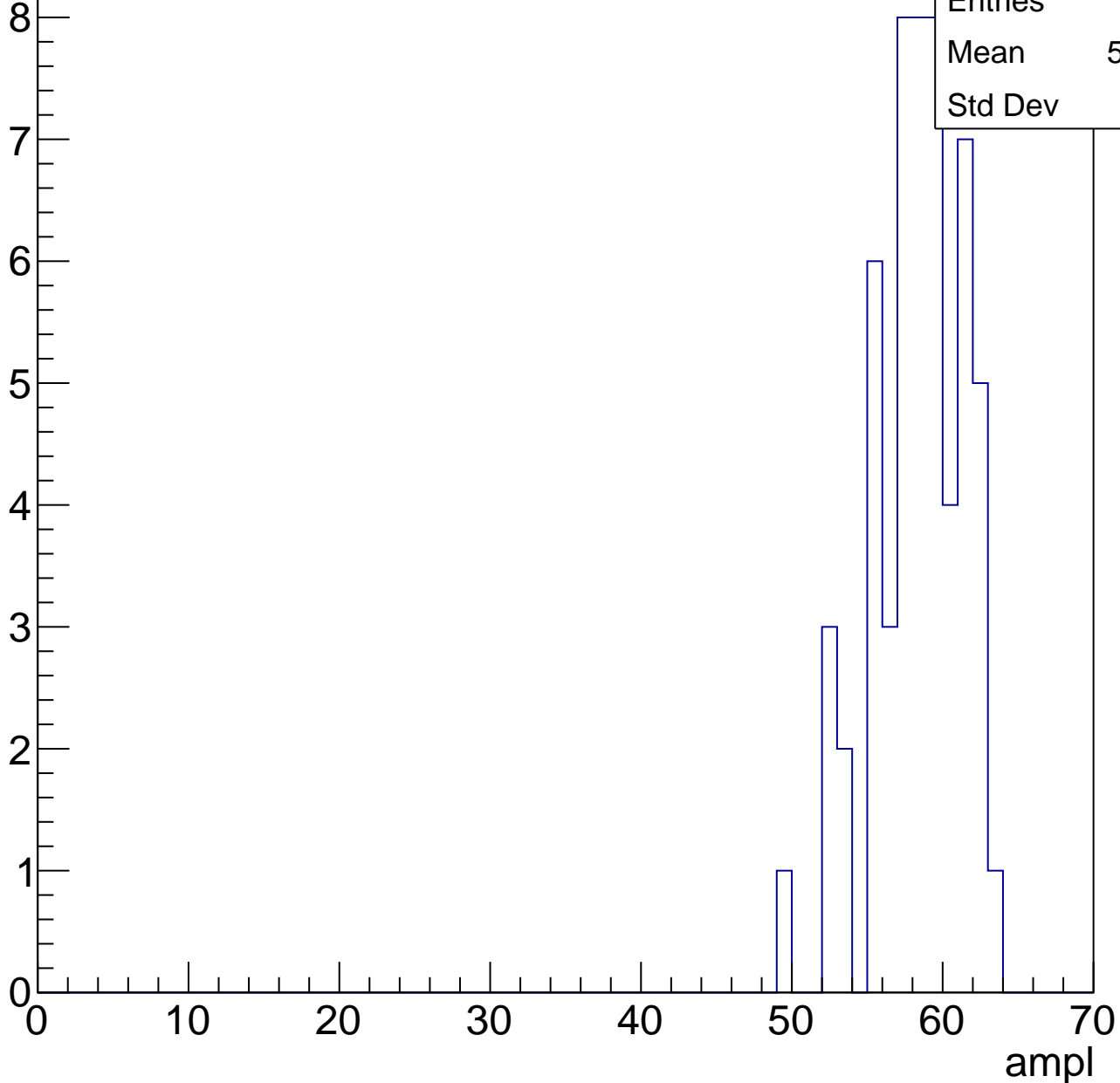


B1L103S, U24-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	57.88
Std Dev	3

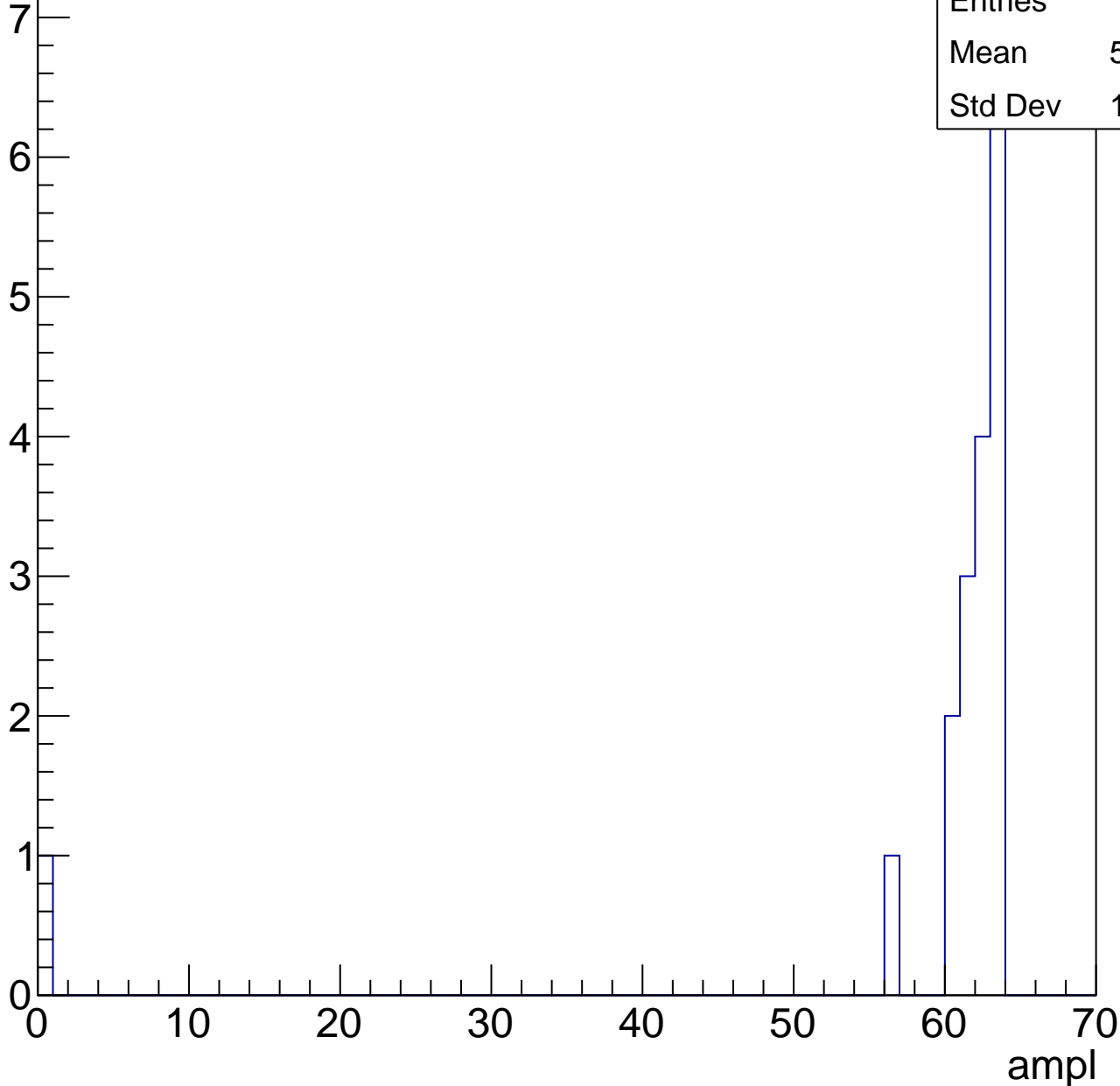


B1L103S, U24-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

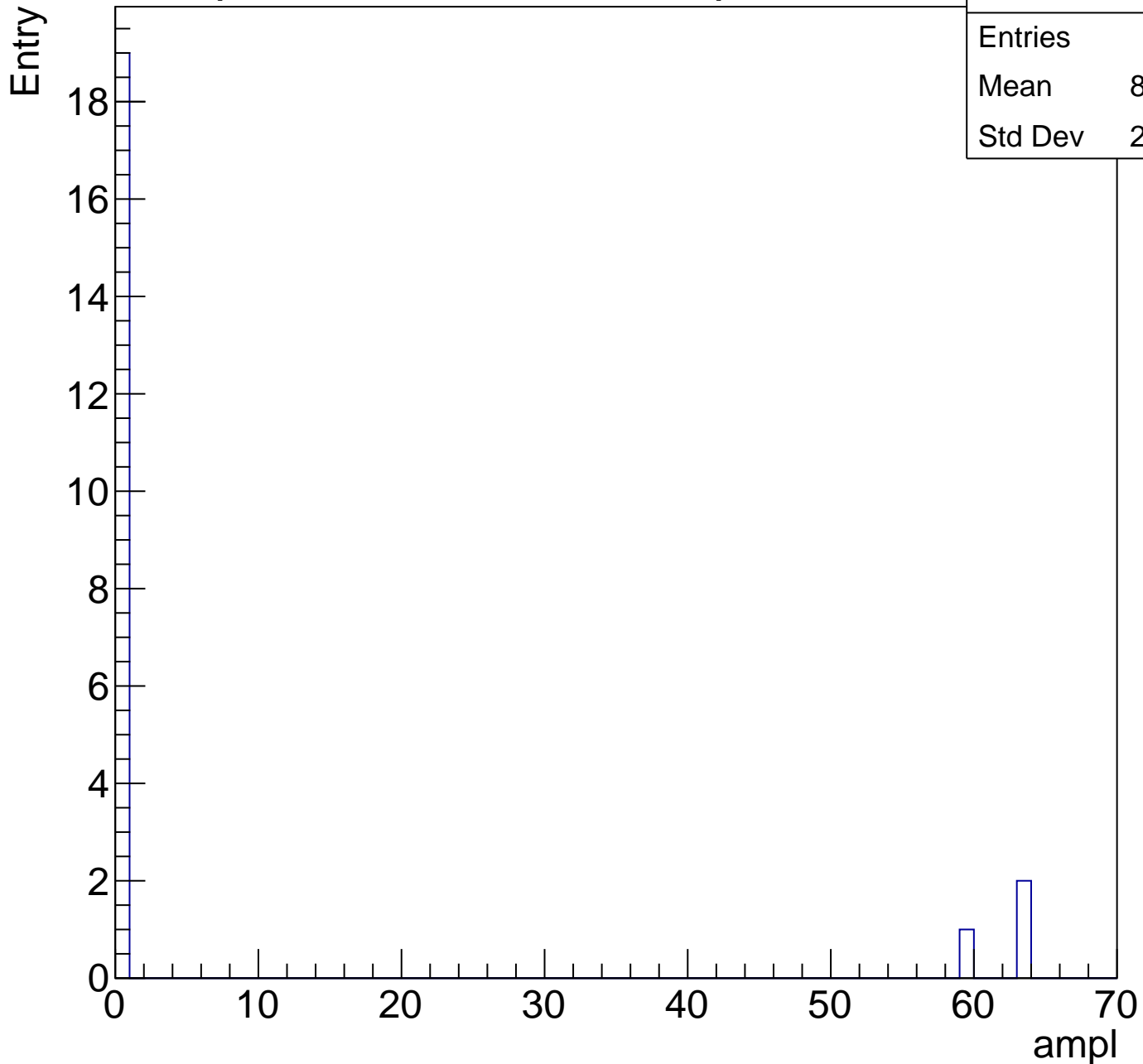
Entries	18
Mean	58.22
Std Dev	14.22



B1L103S, U24-ch122, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.409
Std Dev	21.17



B1L103S, U24-ch123, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	23.72
Std Dev	10.11

Gaus mean : 28.5648

Gaus Width: 5.5582

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L103S, U24-ch123, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	35.02
Std Dev	4.594

Gaus mean : 35.4058

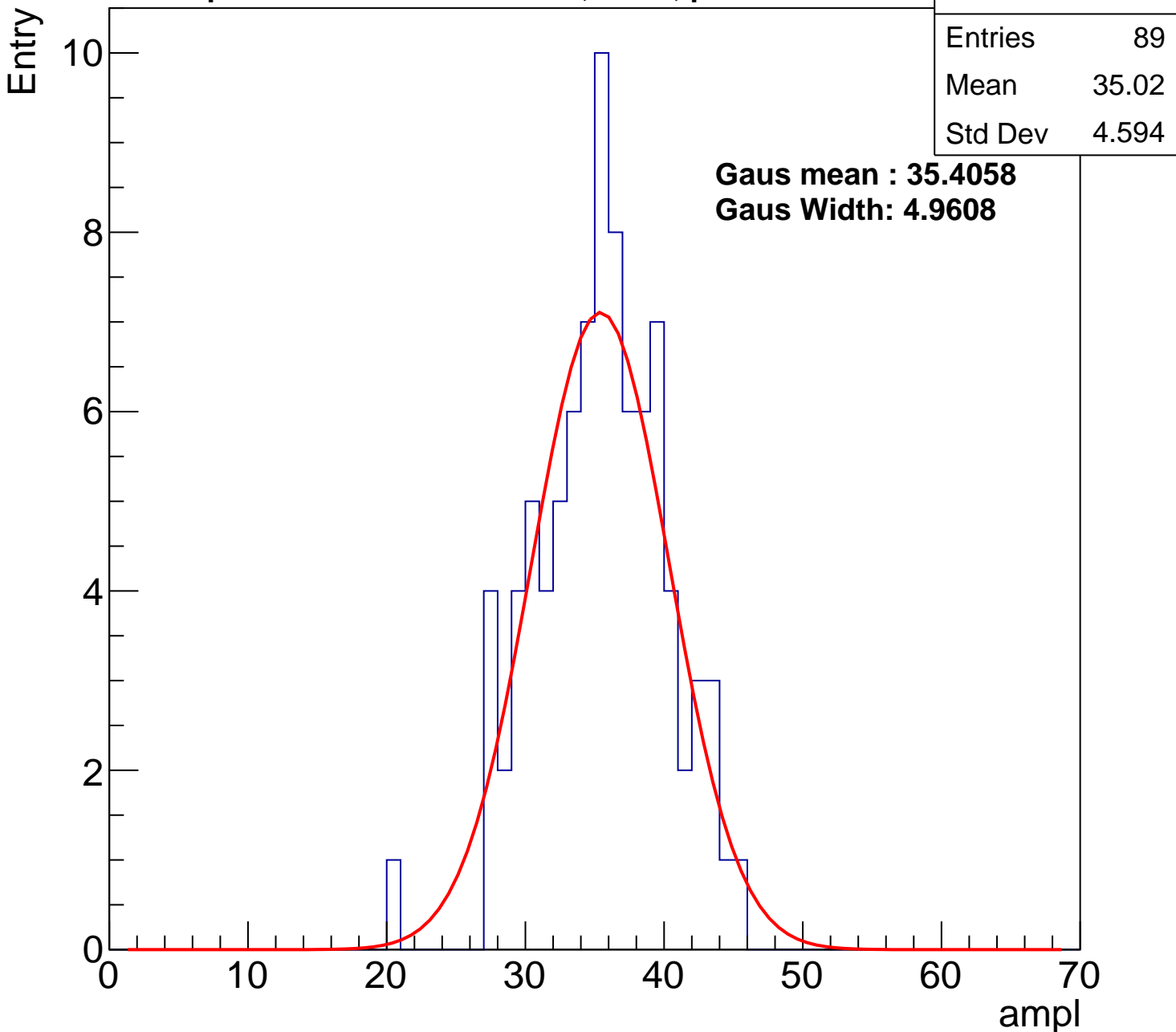
Gaus Width: 4.9608

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch123, adc2

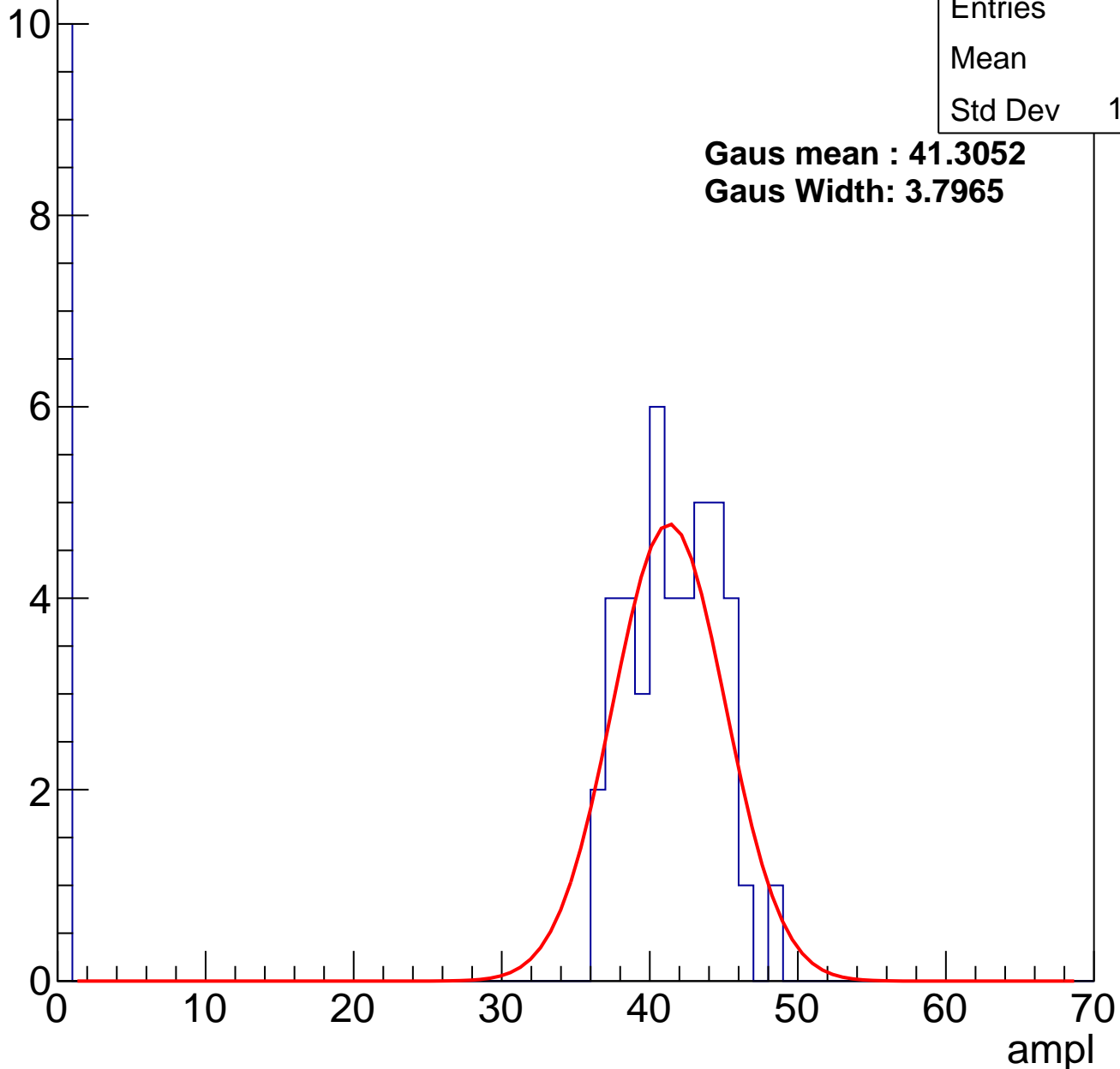
calib_packv5_041523_1651.root, FC#0, port C2

Entries	53
Mean	33.4
Std Dev	16.32

Gaus mean : 41.3052

Gaus Width: 3.7965

Entry

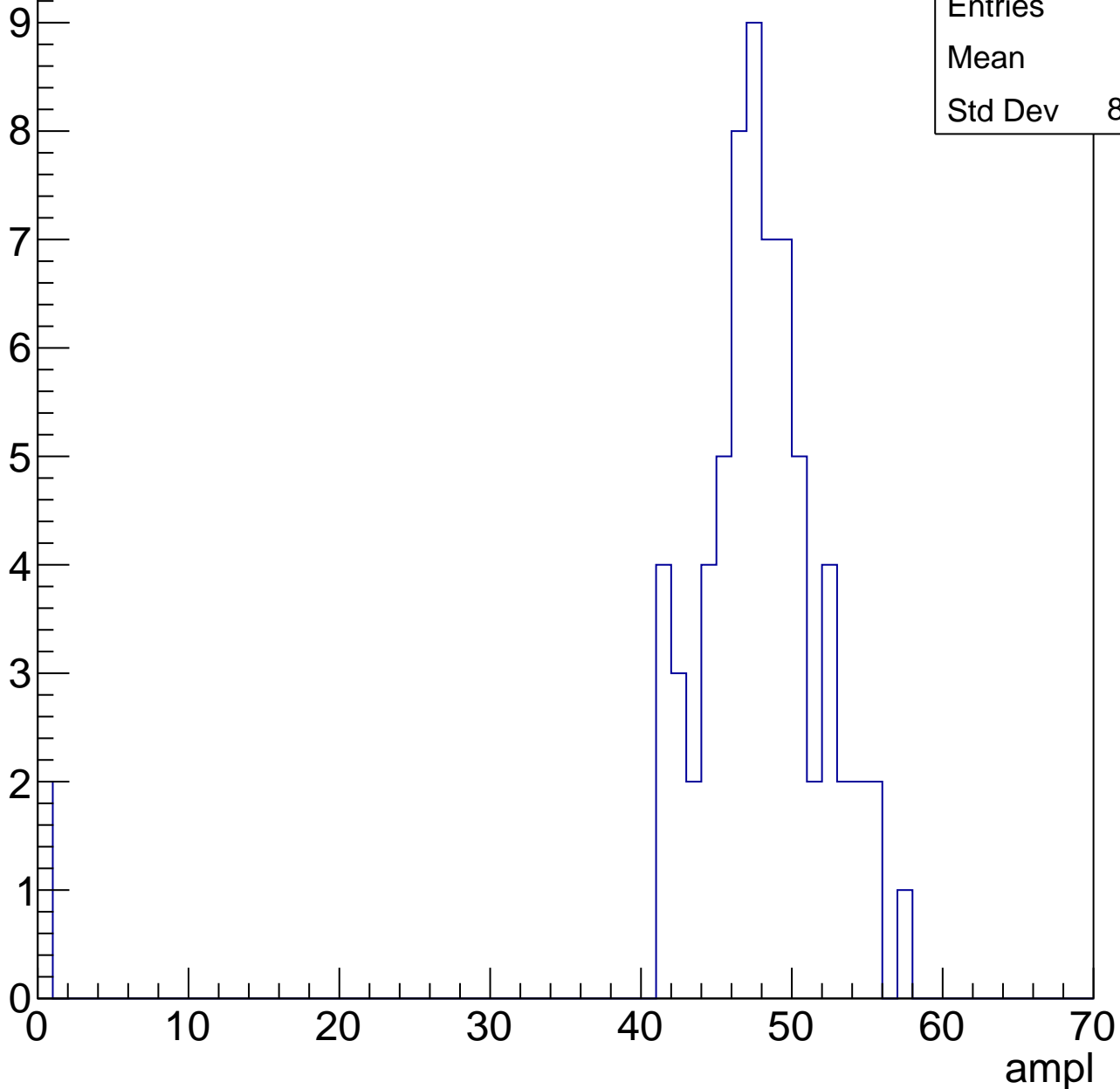


B1L103S, U24-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	46.2
Std Dev	8.764

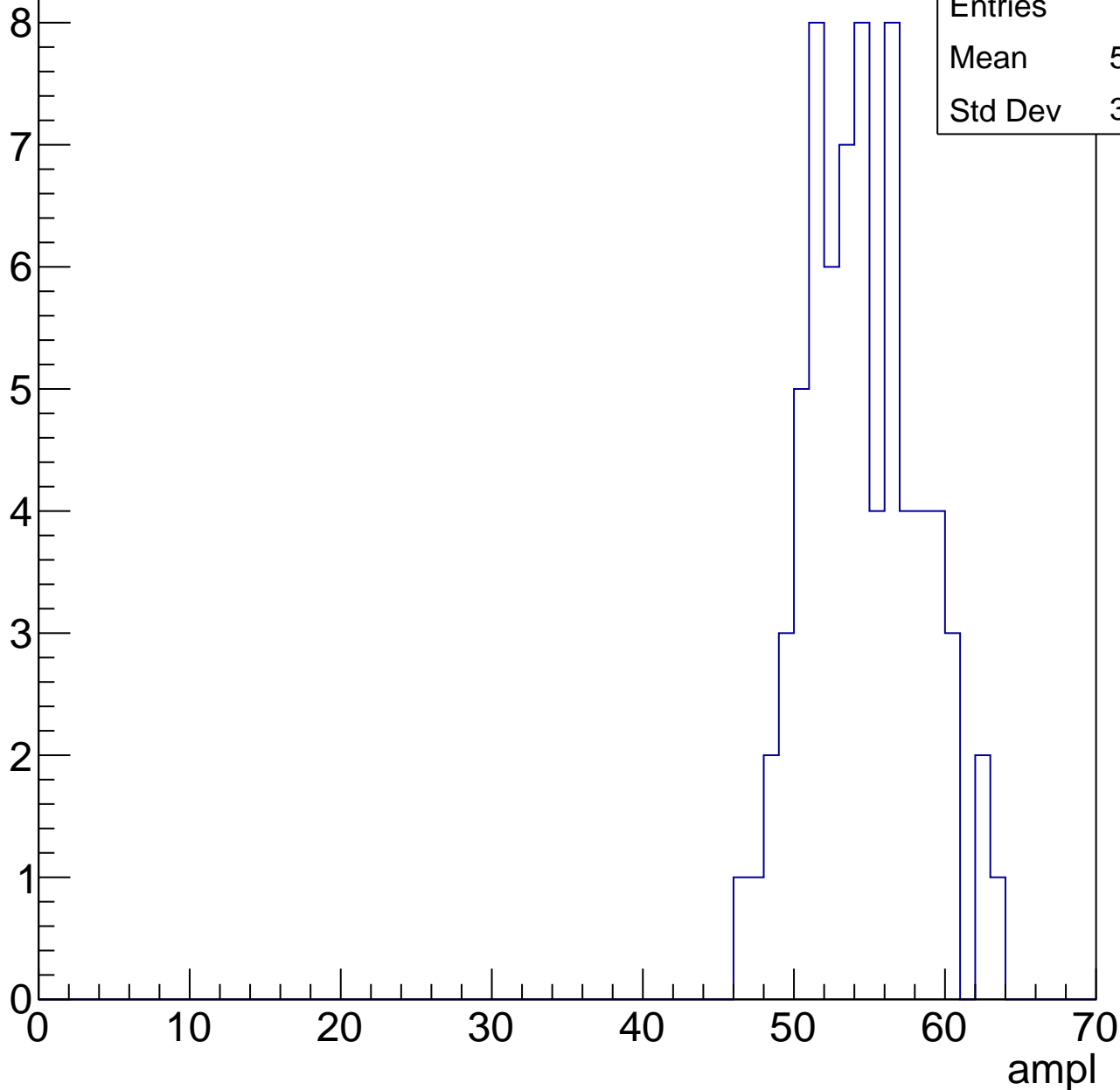


B1L103S, U24-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	54.08
Std Dev	3.744



B1L103S, U24-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

6

5

4

3

2

1

0

Entries

47

Mean

59.36

Std Dev

2.596

ampl

0

10

20

30

40

50

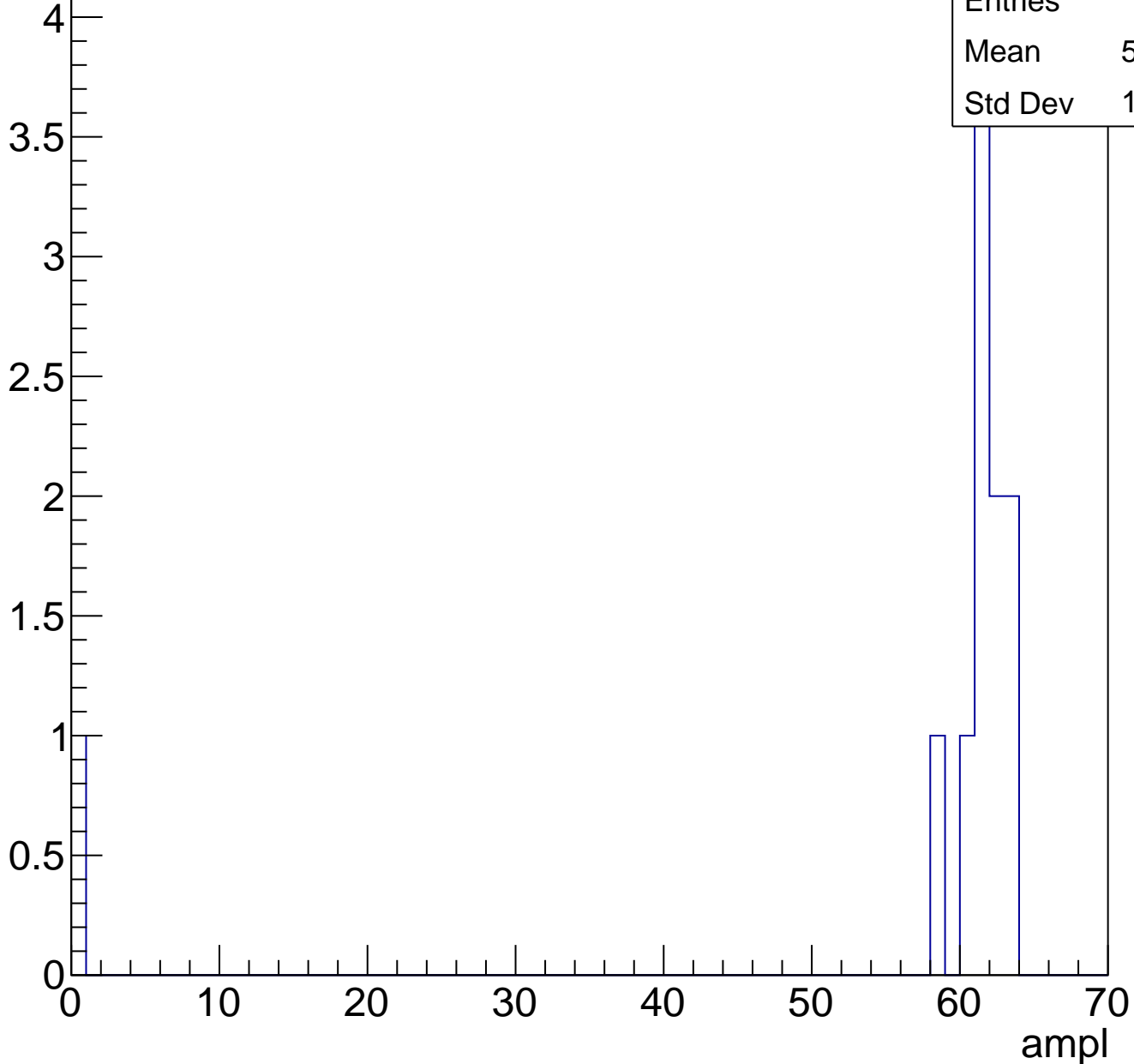
60

70

B1L103S, U24-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U24-ch124, adc0

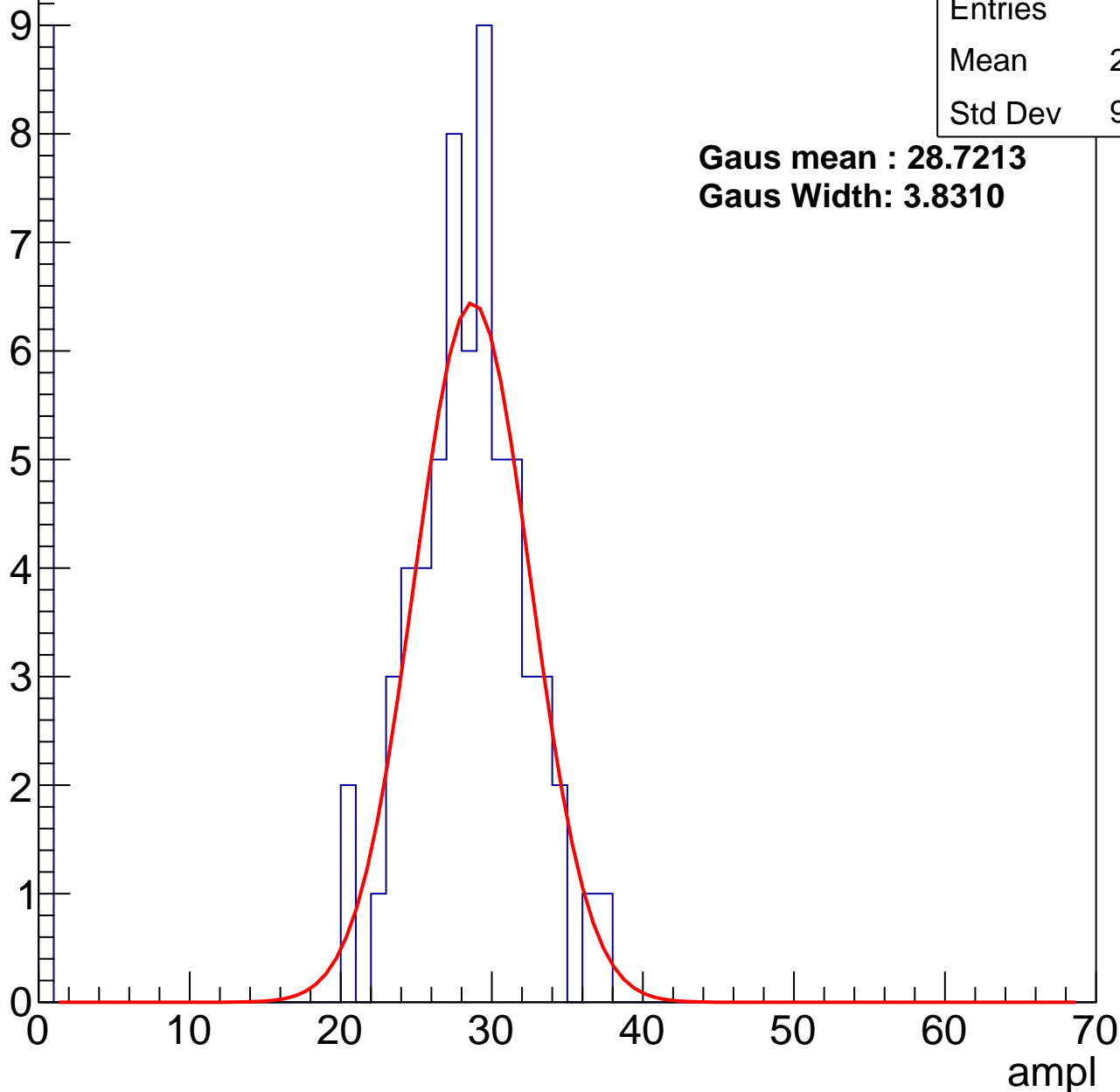
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	24.55
Std Dev	9.923

Gaus mean : 28.7213

Gaus Width: 3.8310



B1L103S, U24-ch124, adc1

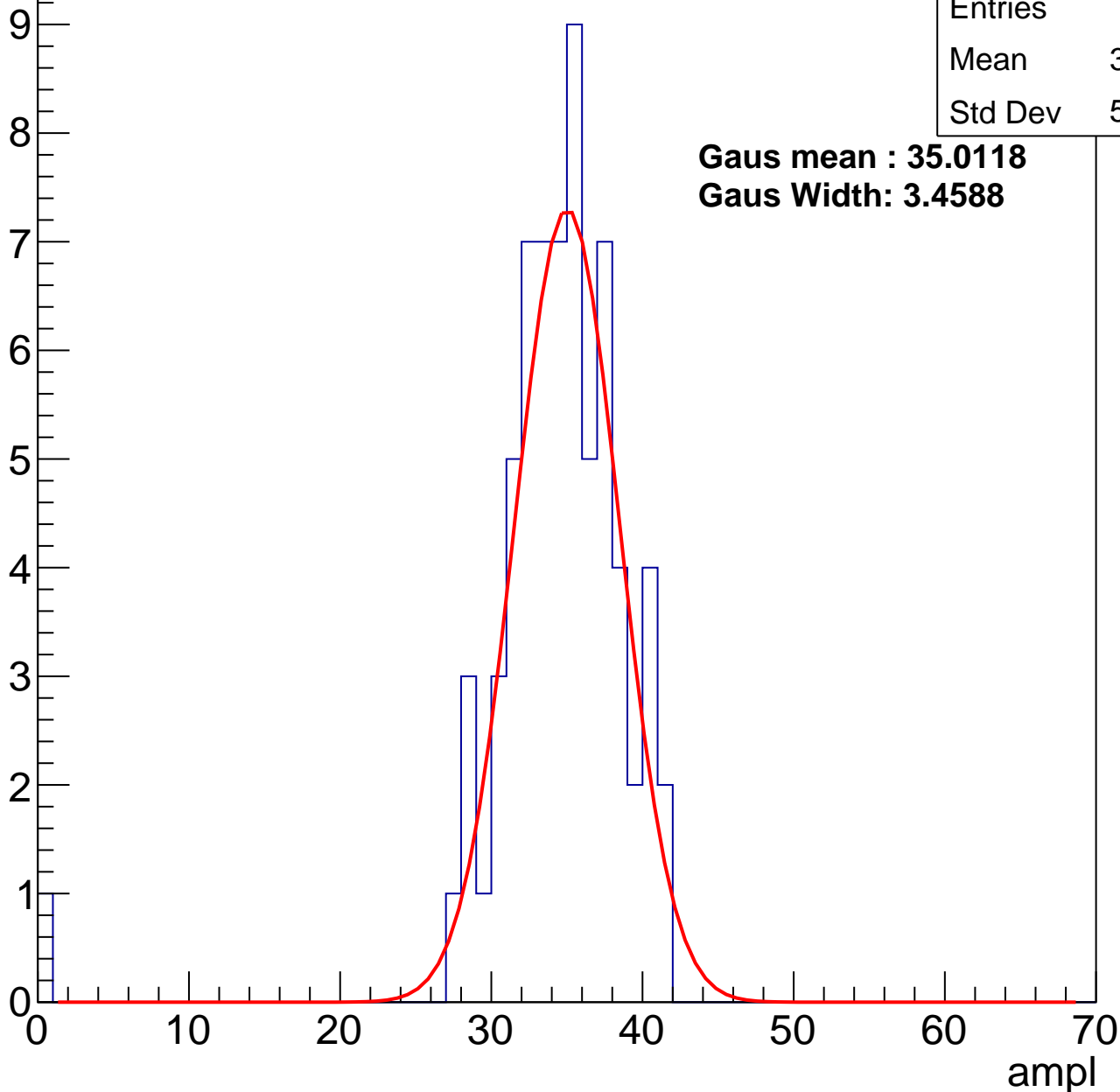
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	33.88
Std Dev	5.304

Gaus mean : 35.0118

Gaus Width: 3.4588



B1L103S, U24-ch124, adc2

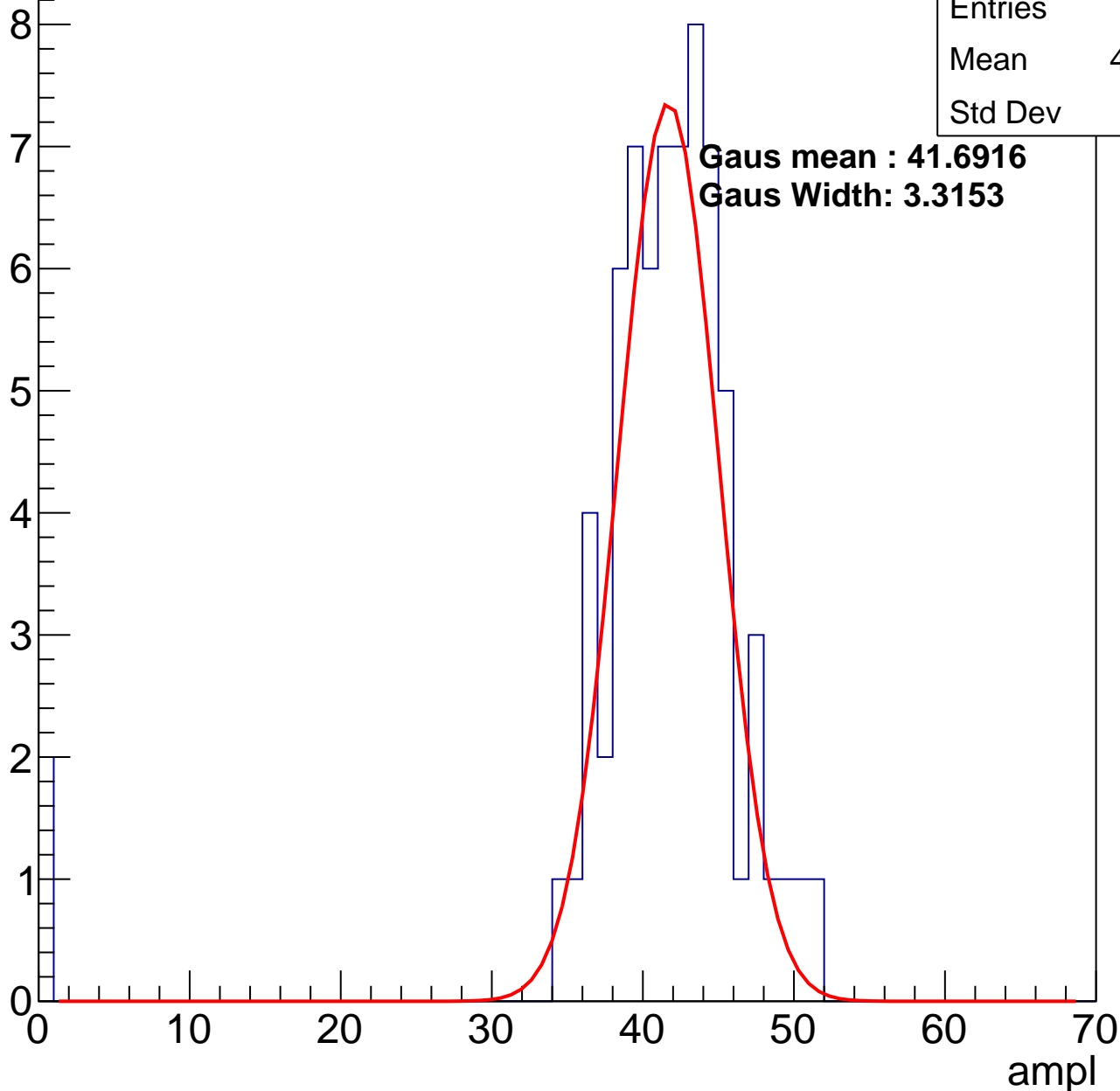
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	40.44
Std Dev	7.74

Gaus mean : 41.6916

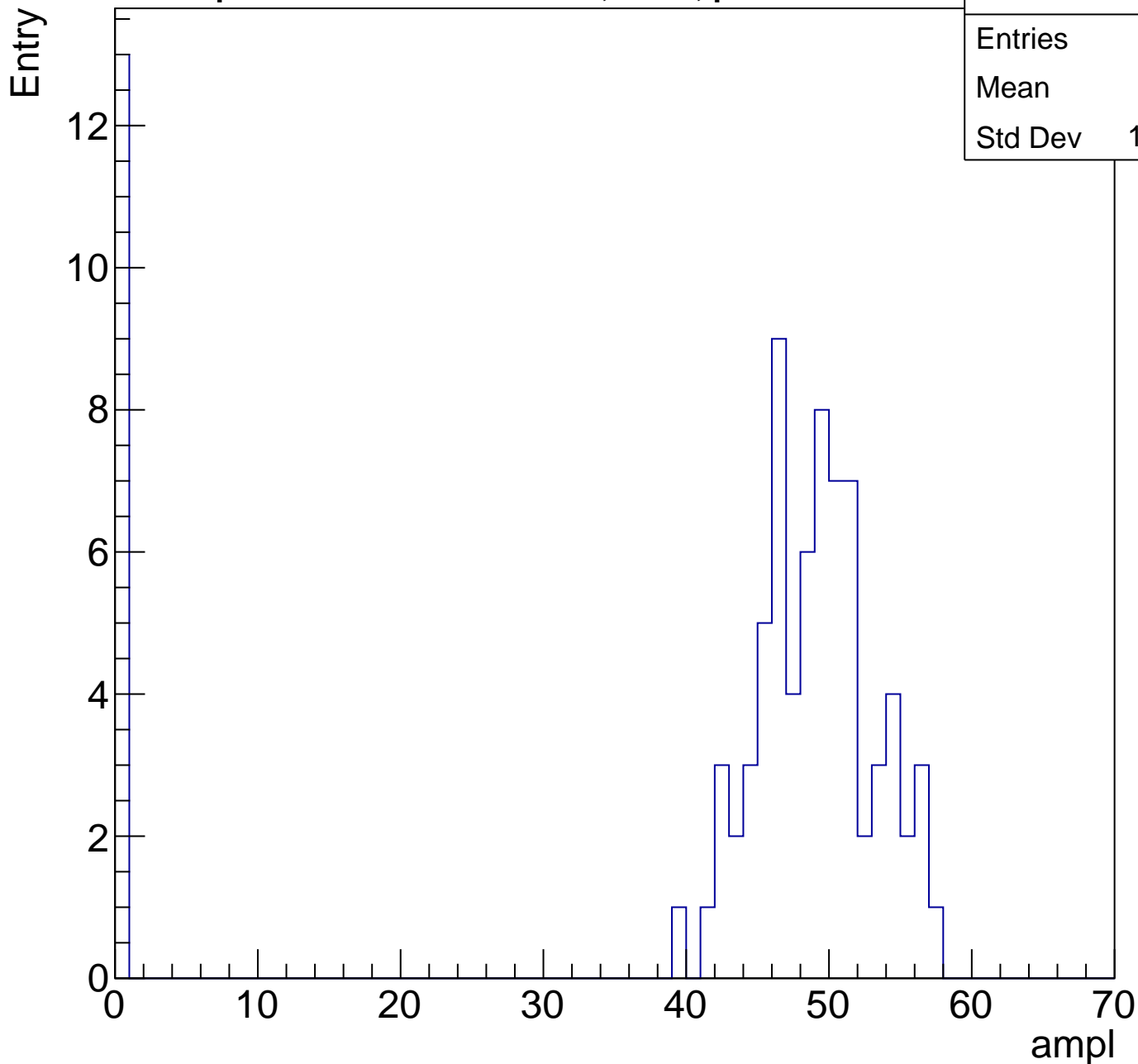
Gaus Width: 3.3153



B1L103S, U24-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	41.1
Std Dev	17.96

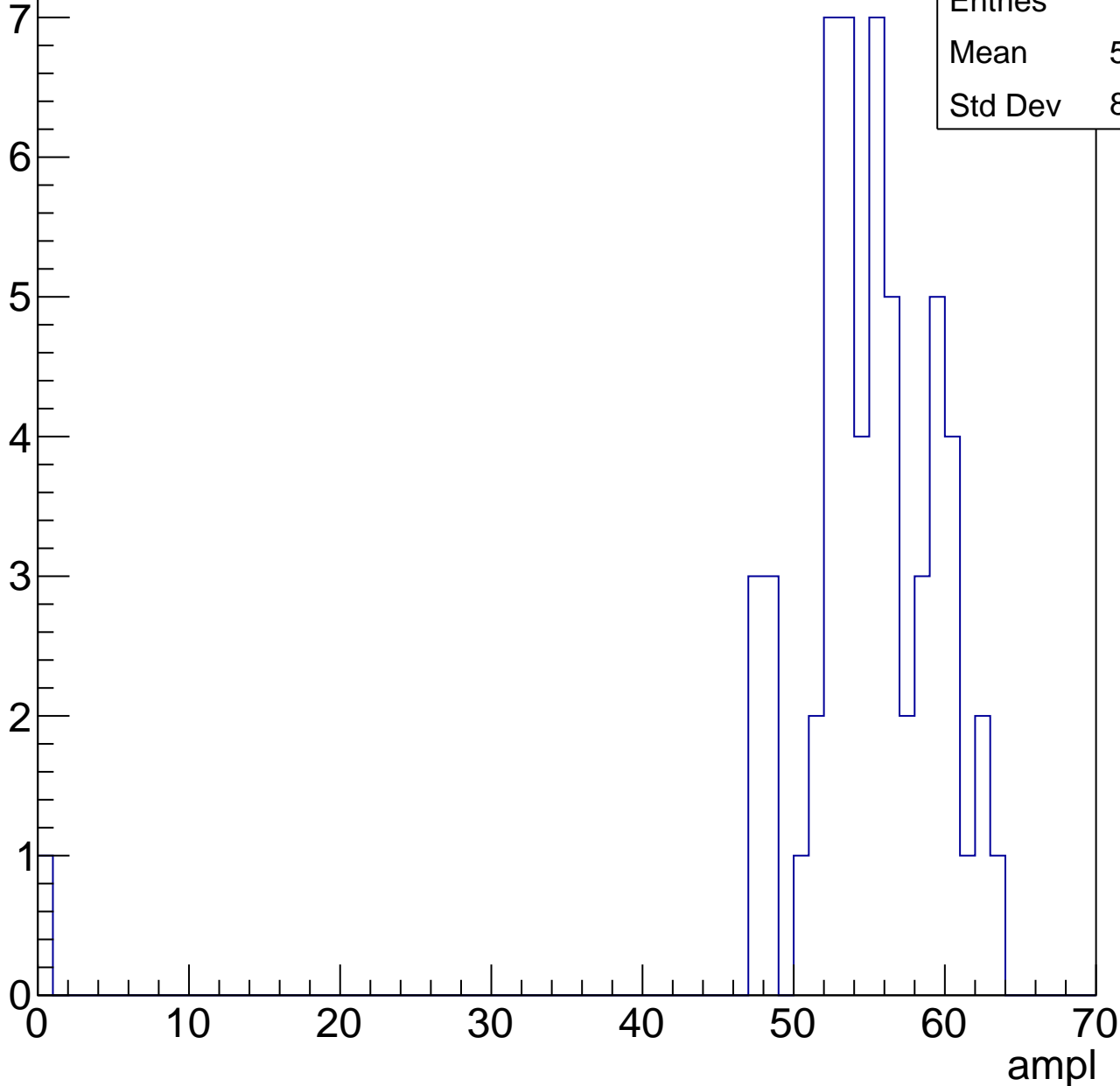


B1L103S, U24-ch124, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.86
Std Dev	8.157

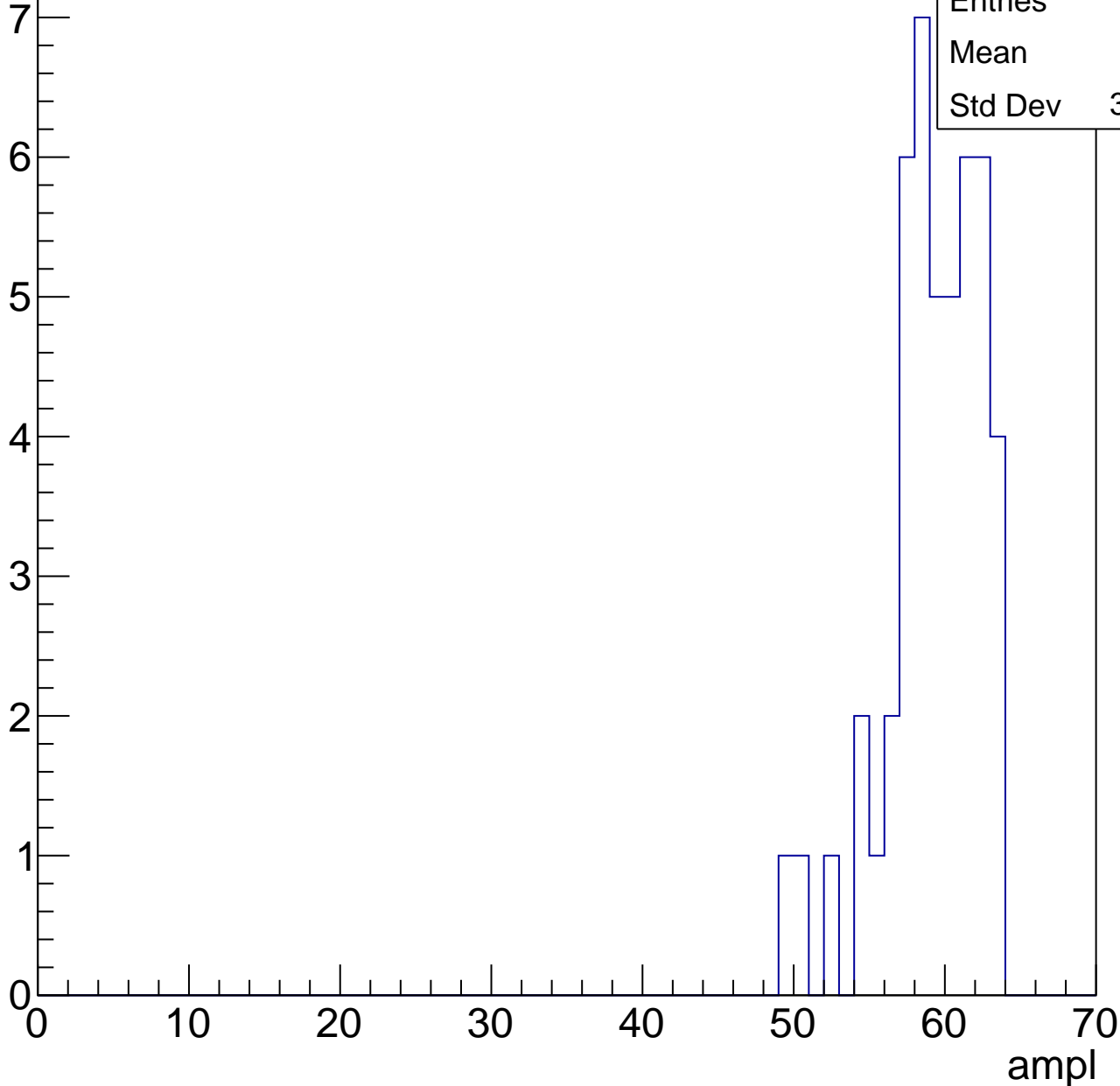


B1L103S, U24-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

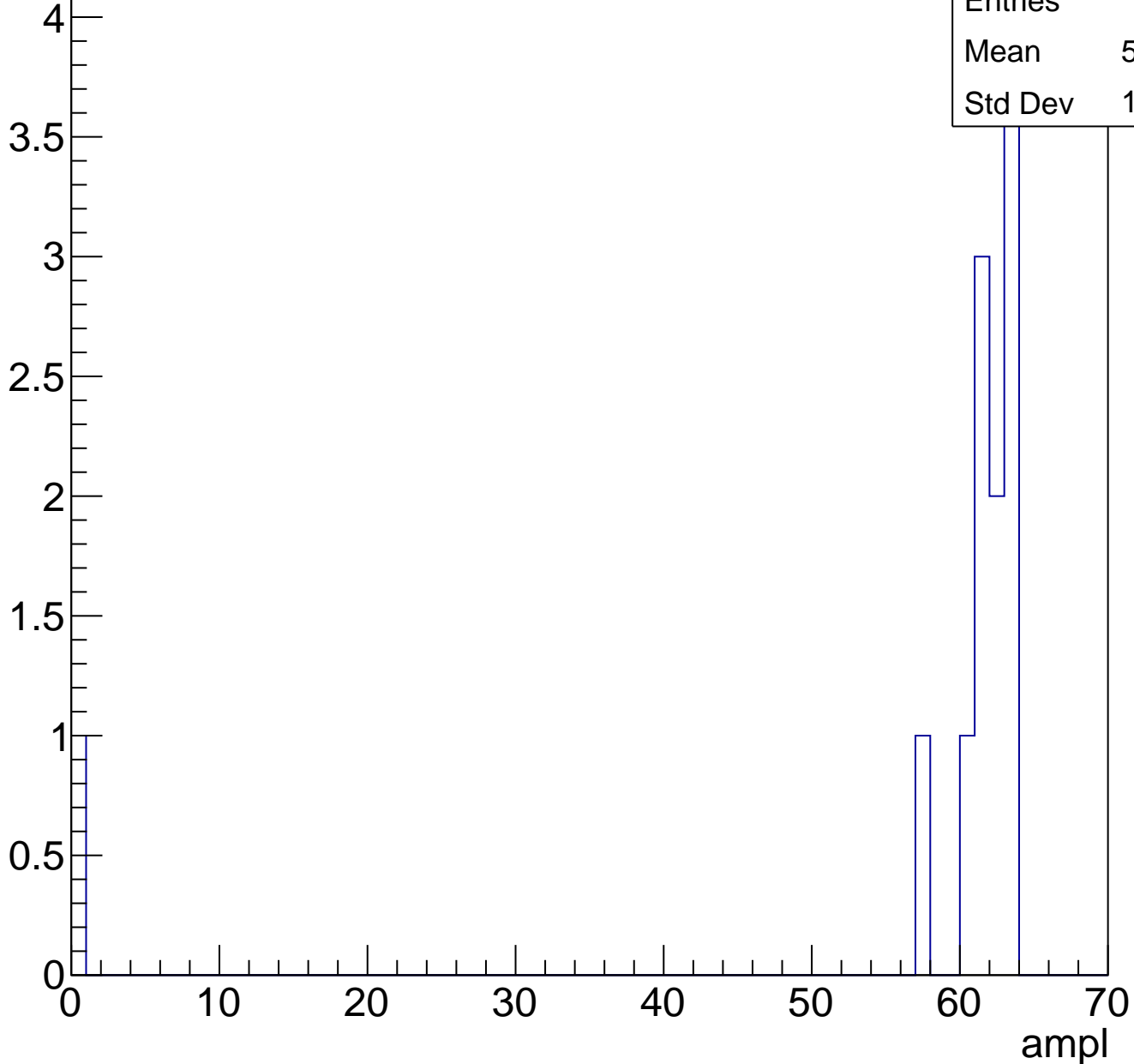
Entries	47
Mean	58.7
Std Dev	3.222



B1L103S, U24-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

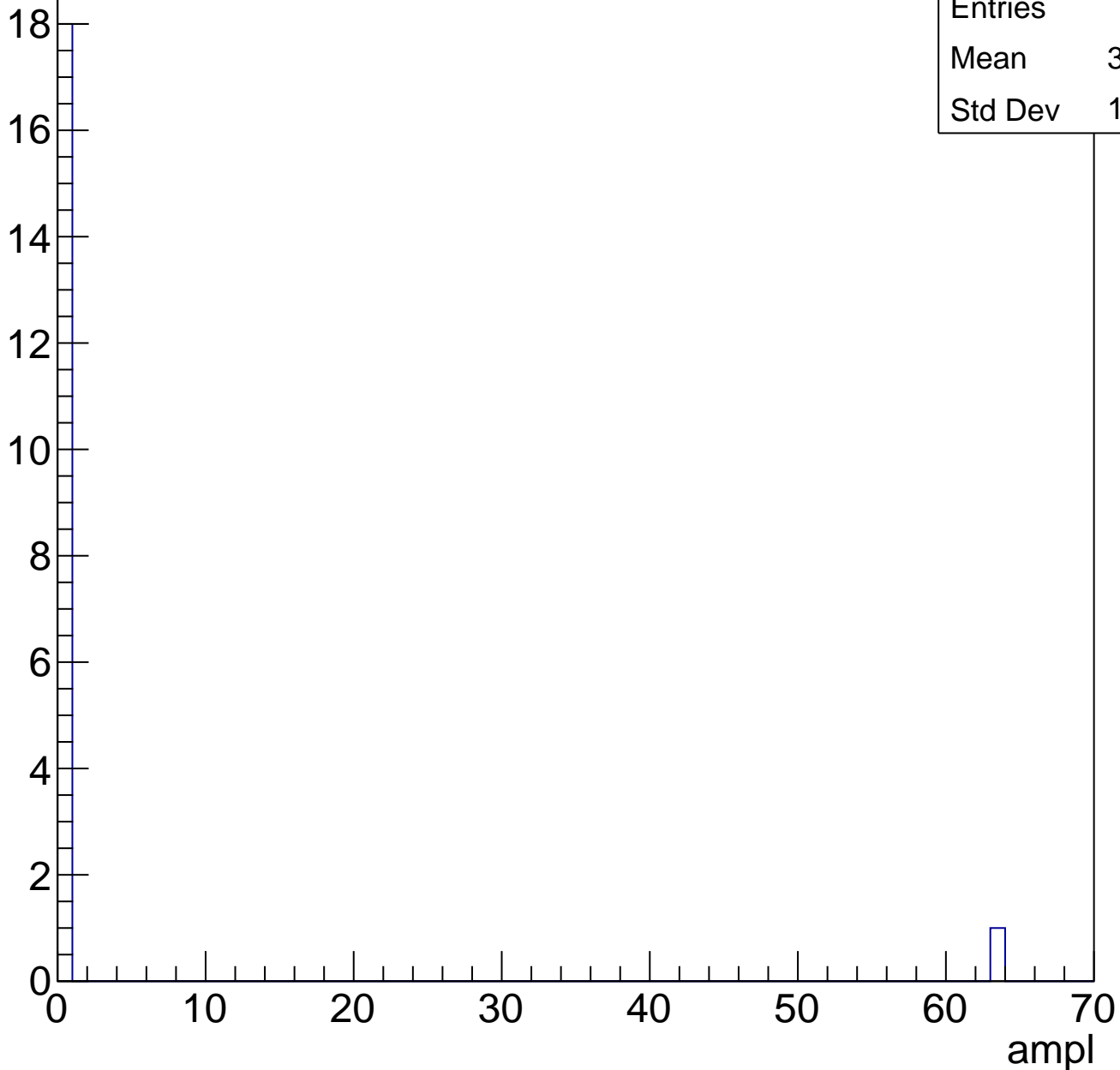


B1L103S, U24-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U24-ch125, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	23.2
Std Dev	11.28

Gaus mean : 28.6311

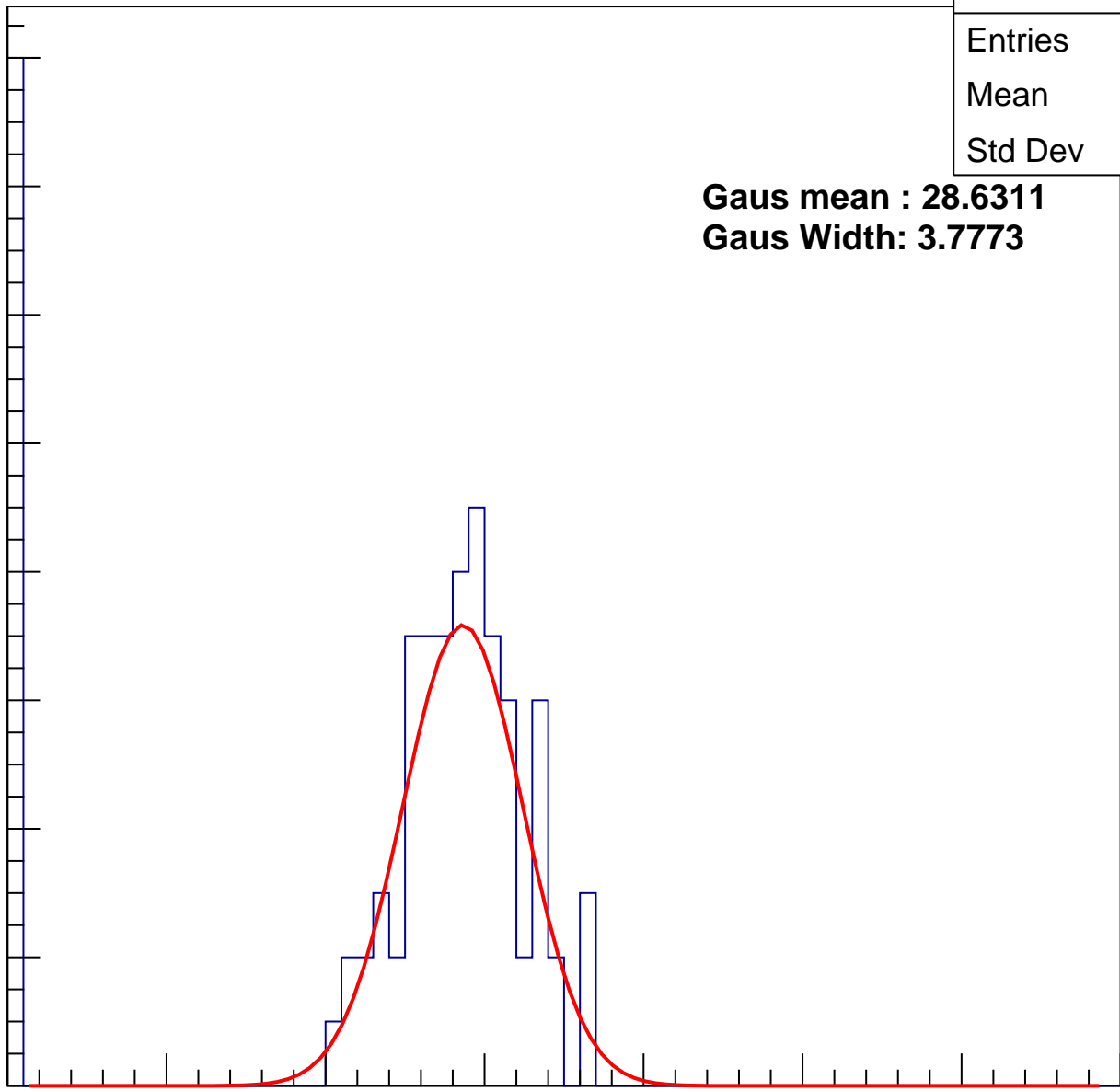
Gaus Width: 3.7773

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U24-ch125, adc1

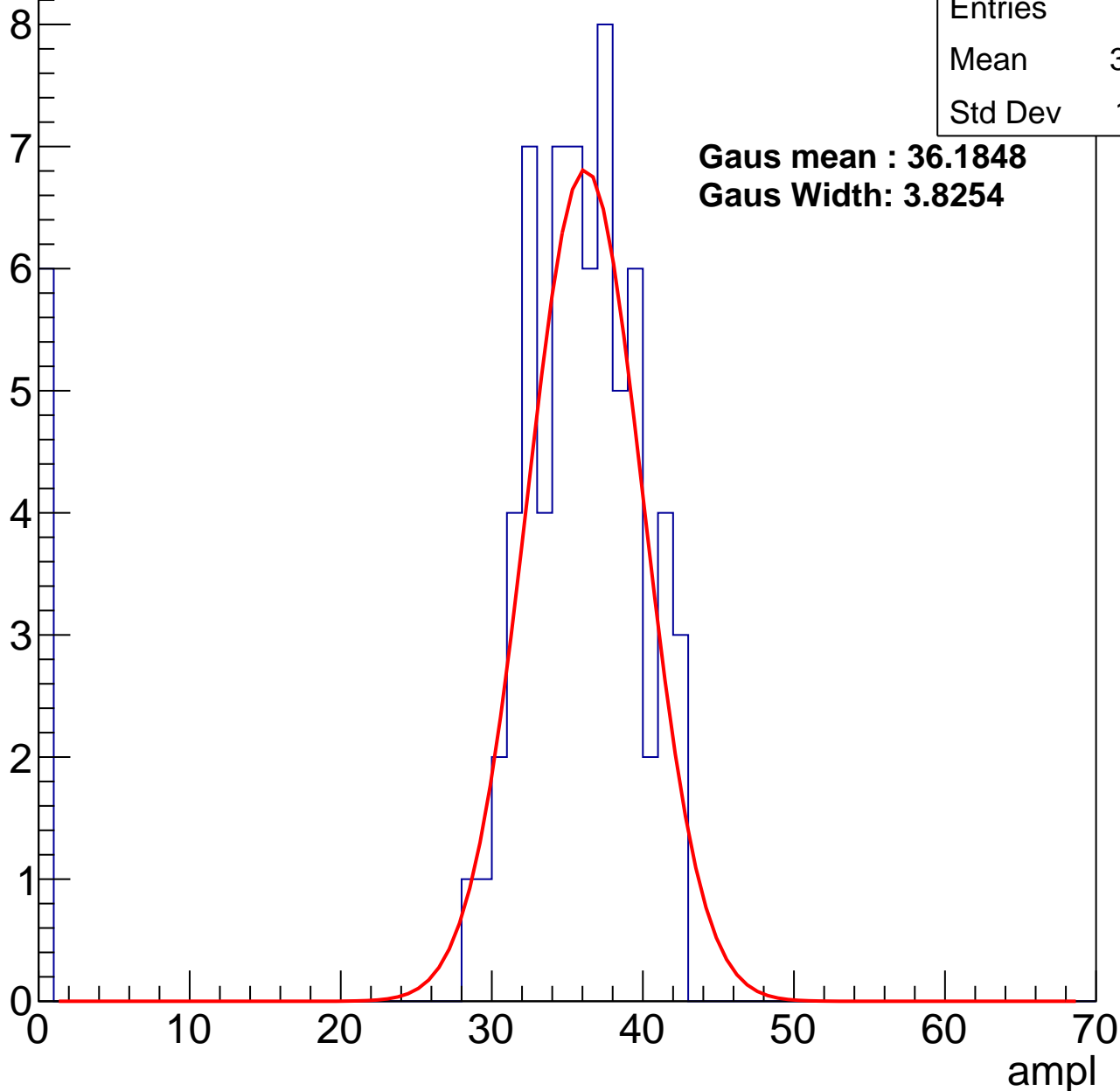
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.68
Std Dev	10.31

Gaus mean : 36.1848

Gaus Width: 3.8254



B1L103S, U24-ch125, adc2

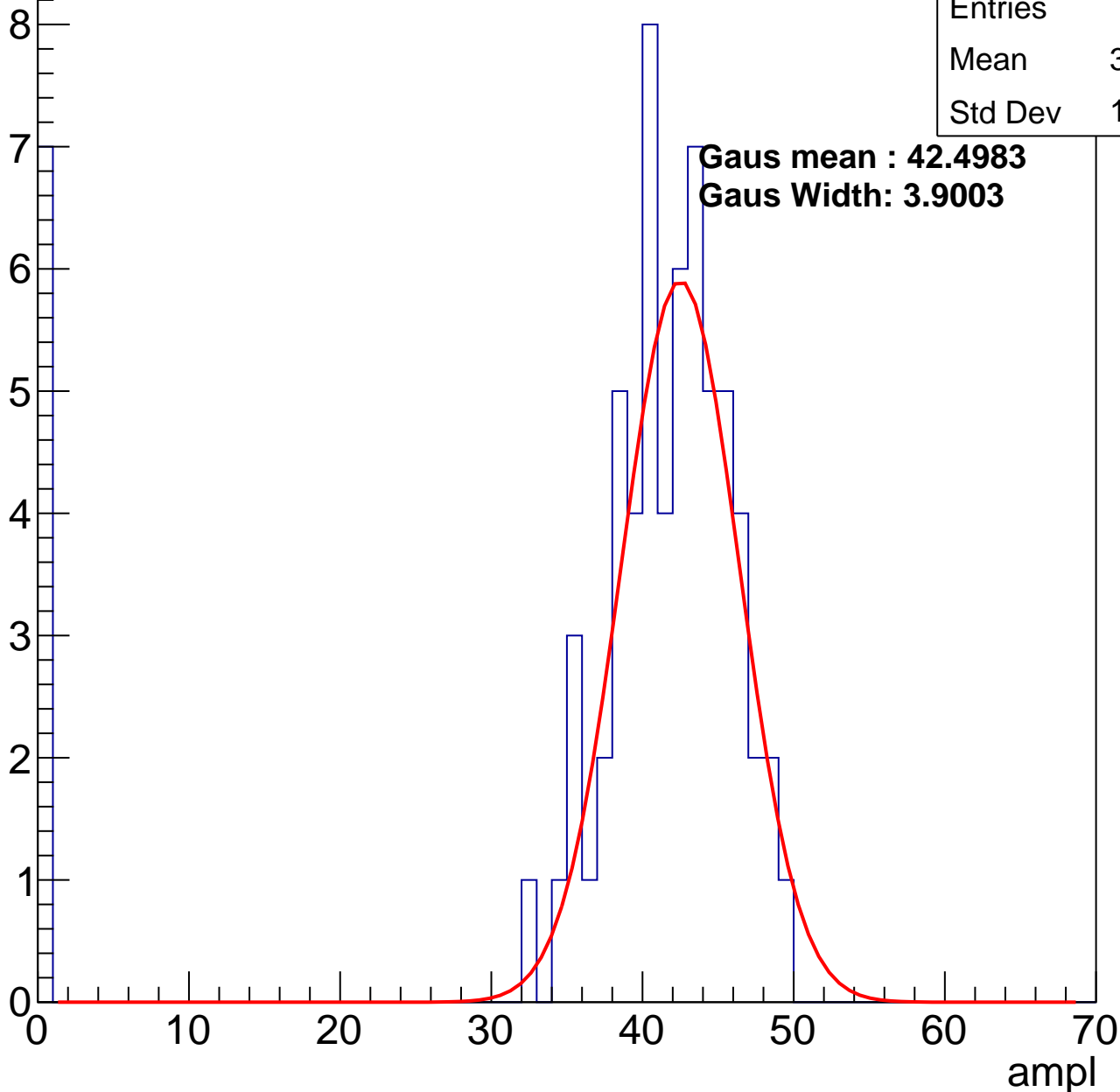
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	37.24
Std Dev	13.09

Gaus mean : 42.4983

Gaus Width: 3.9003

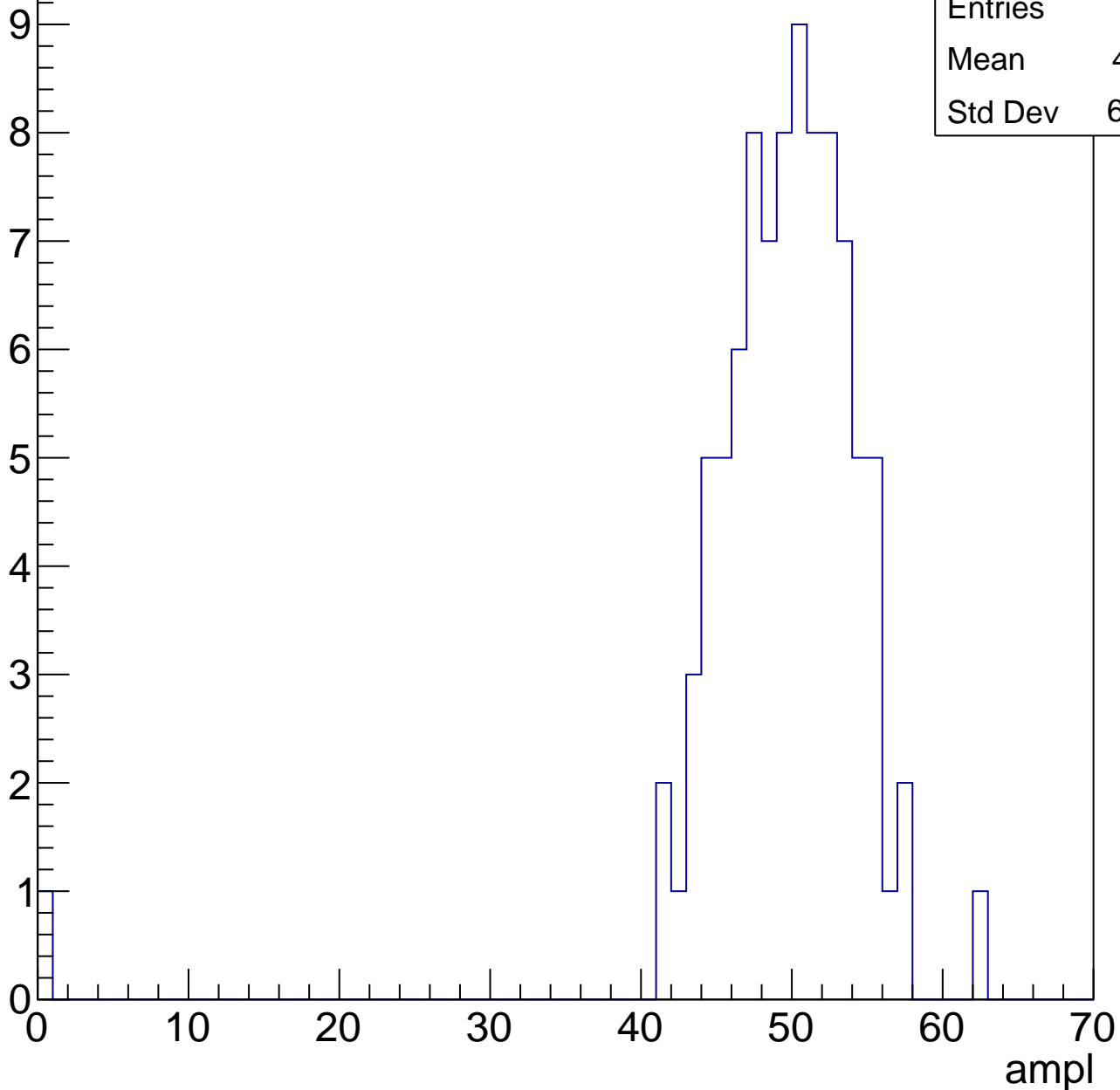


B1L103S, U24-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

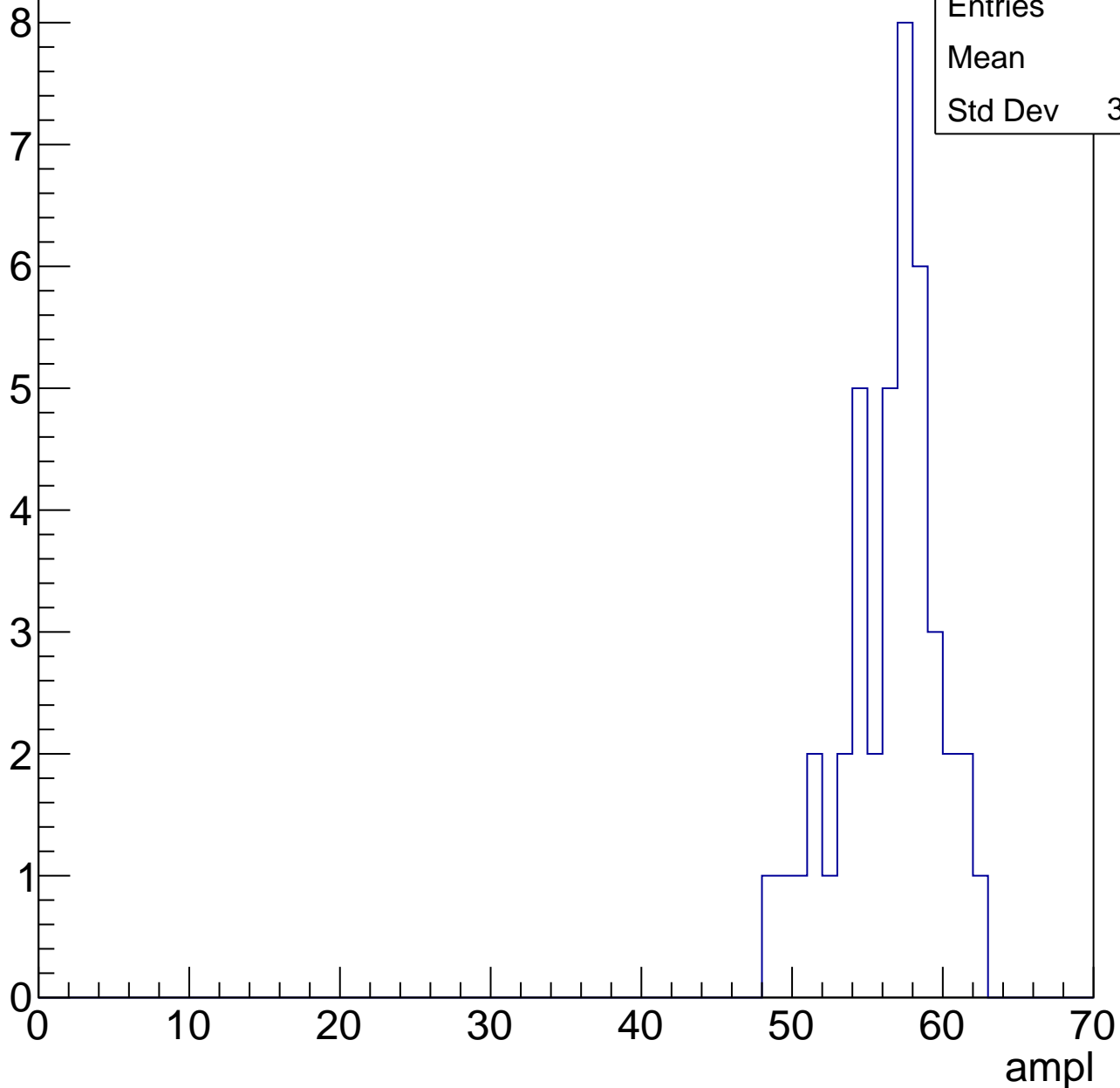
Entries	92
Mean	48.91
Std Dev	6.477



B1L103S, U24-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



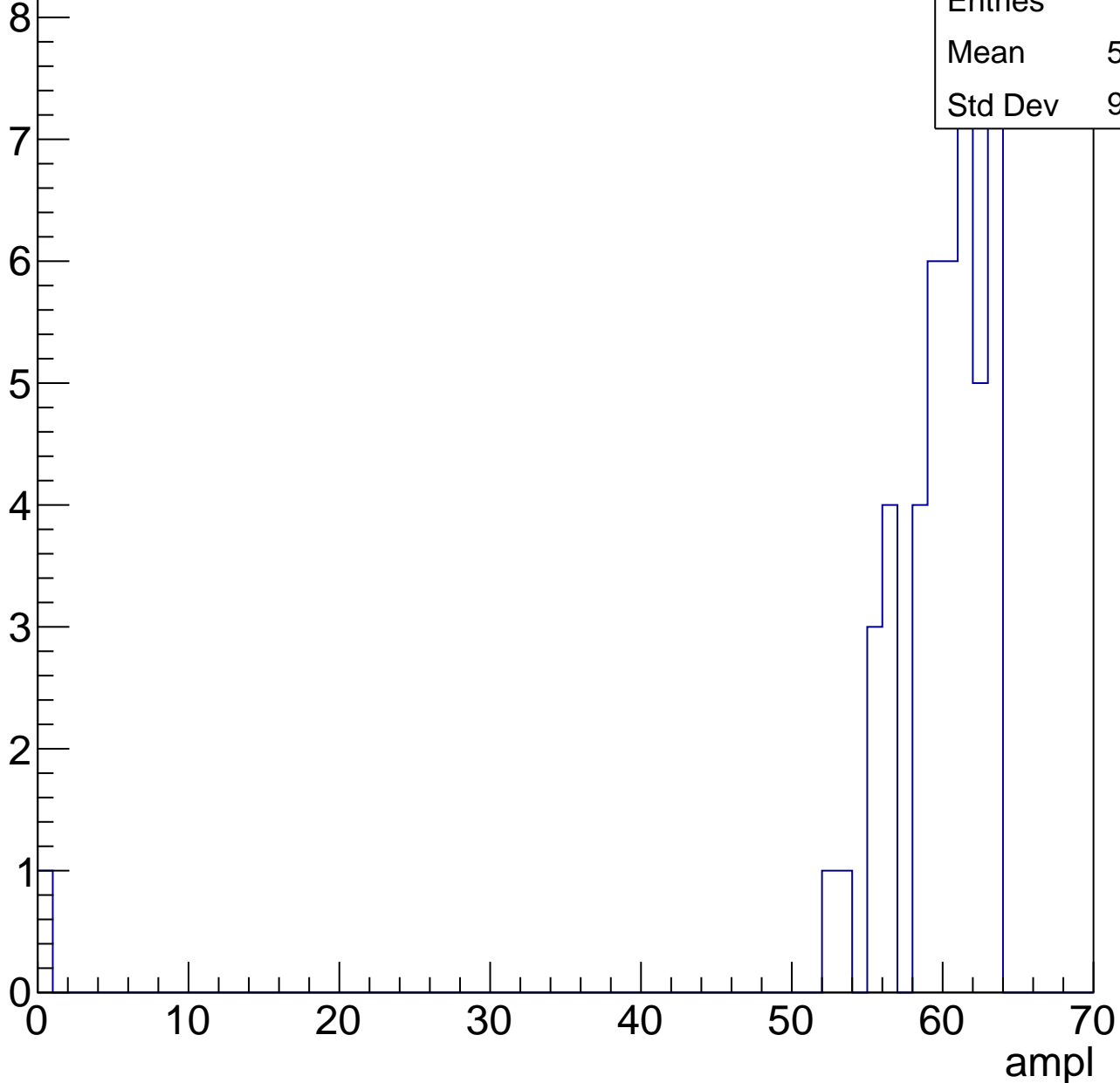
Entries	42
Mean	56
Std Dev	3.207

B1L103S, U24-ch125, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

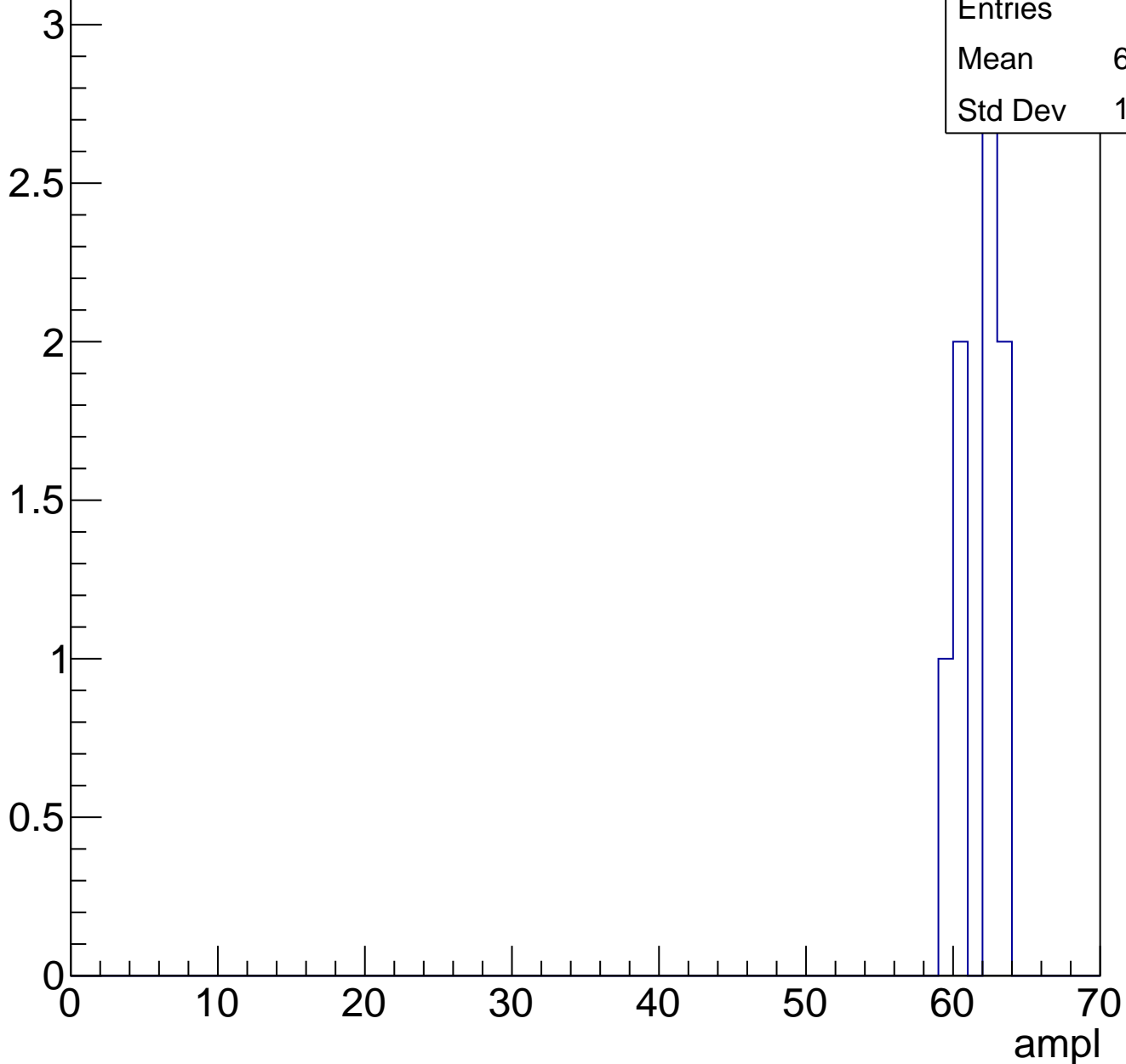
Entries	47
Mean	58.34
Std Dev	9.044



B1L103S, U24-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U24-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

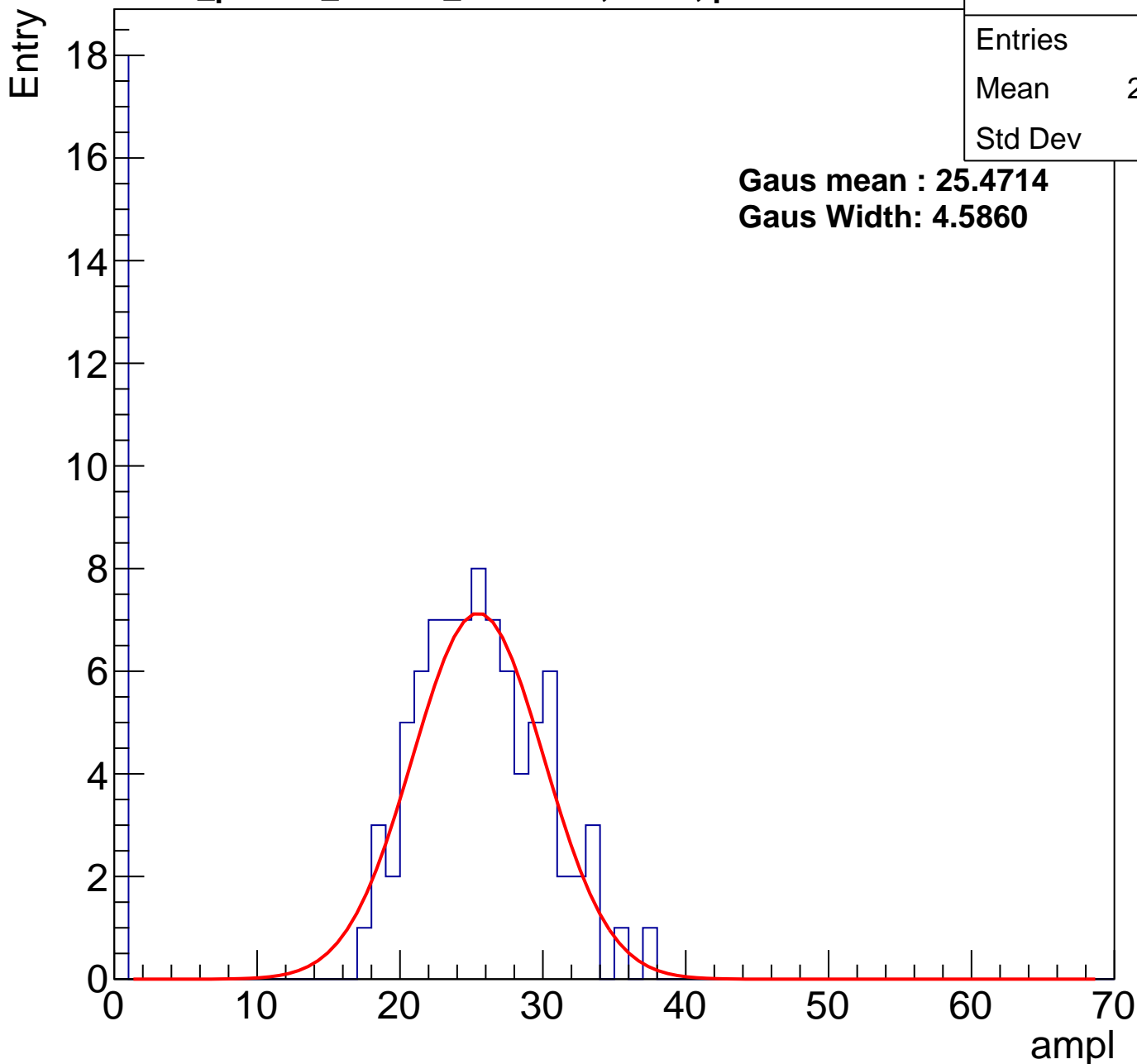
B1L103S, U24-ch126, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	20.75
Std Dev	10.4

Gaus mean : 25.4714

Gaus Width: 4.5860



B1L103S, U24-ch126, adc1

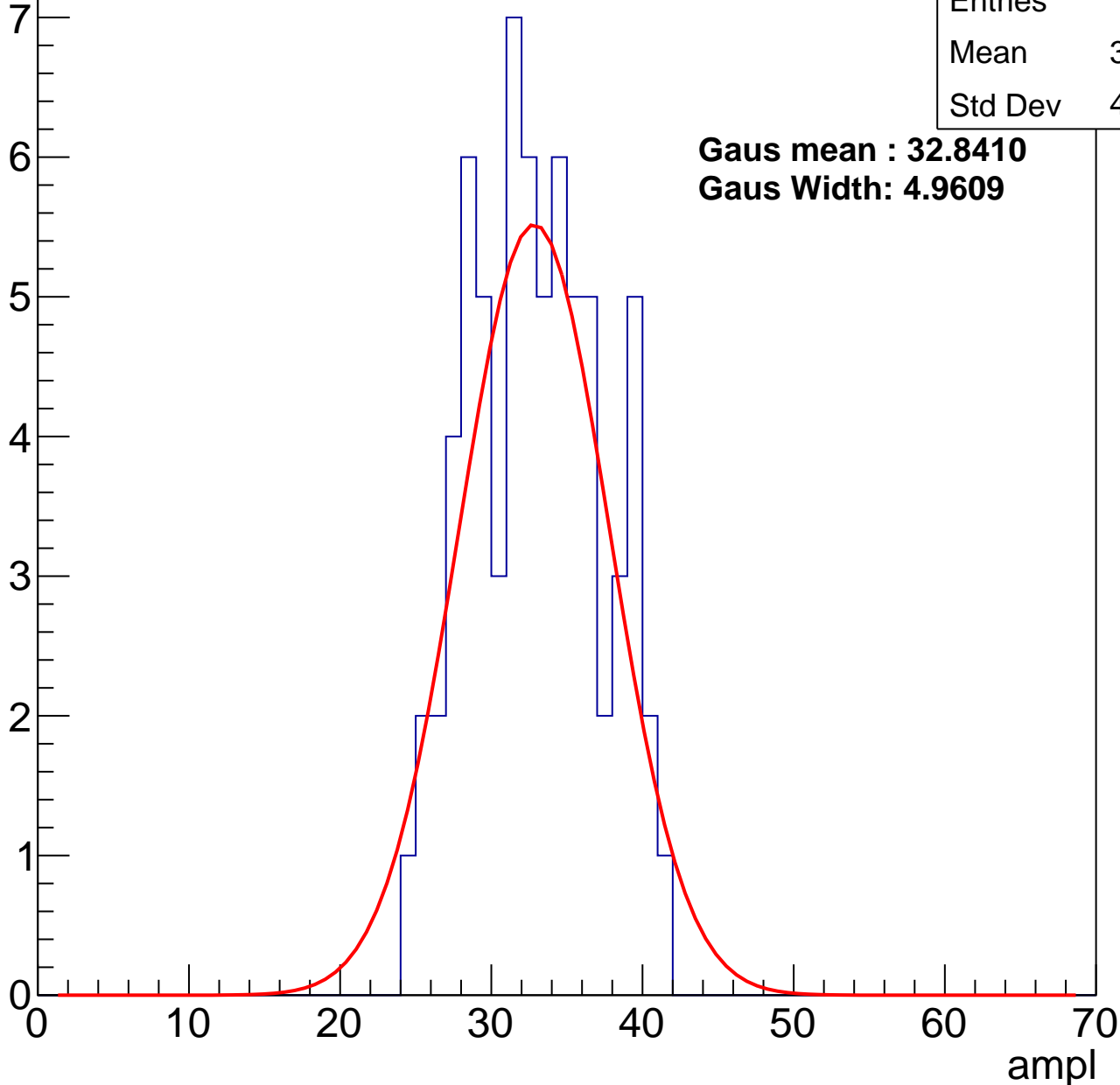
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	32.49
Std Dev	4.215

Gaus mean : 32.8410

Gaus Width: 4.9609



B1L103S, U24-ch126, adc2

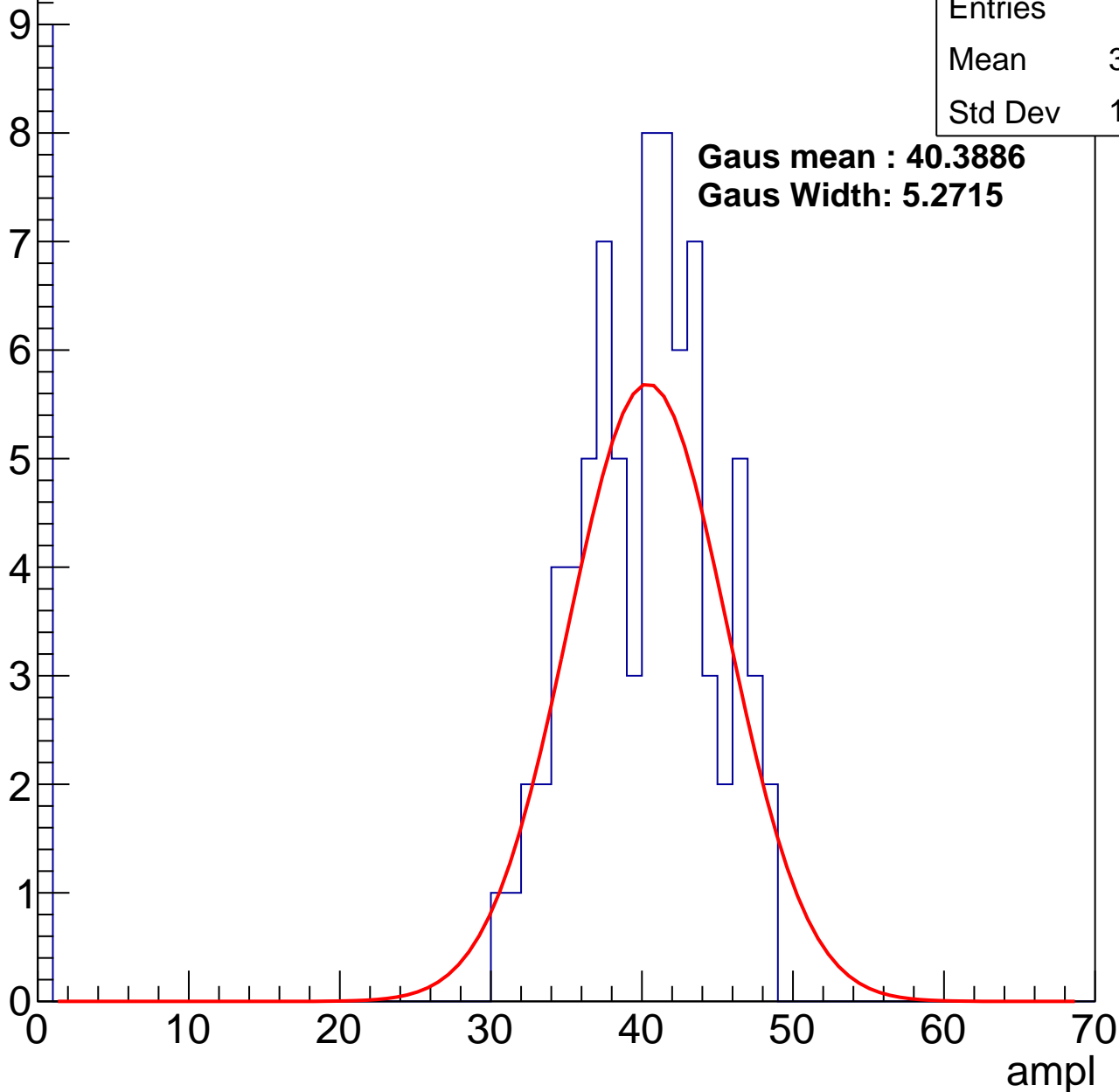
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	35.67
Std Dev	12.79

Gaus mean : 40.3886

Gaus Width: 5.2715

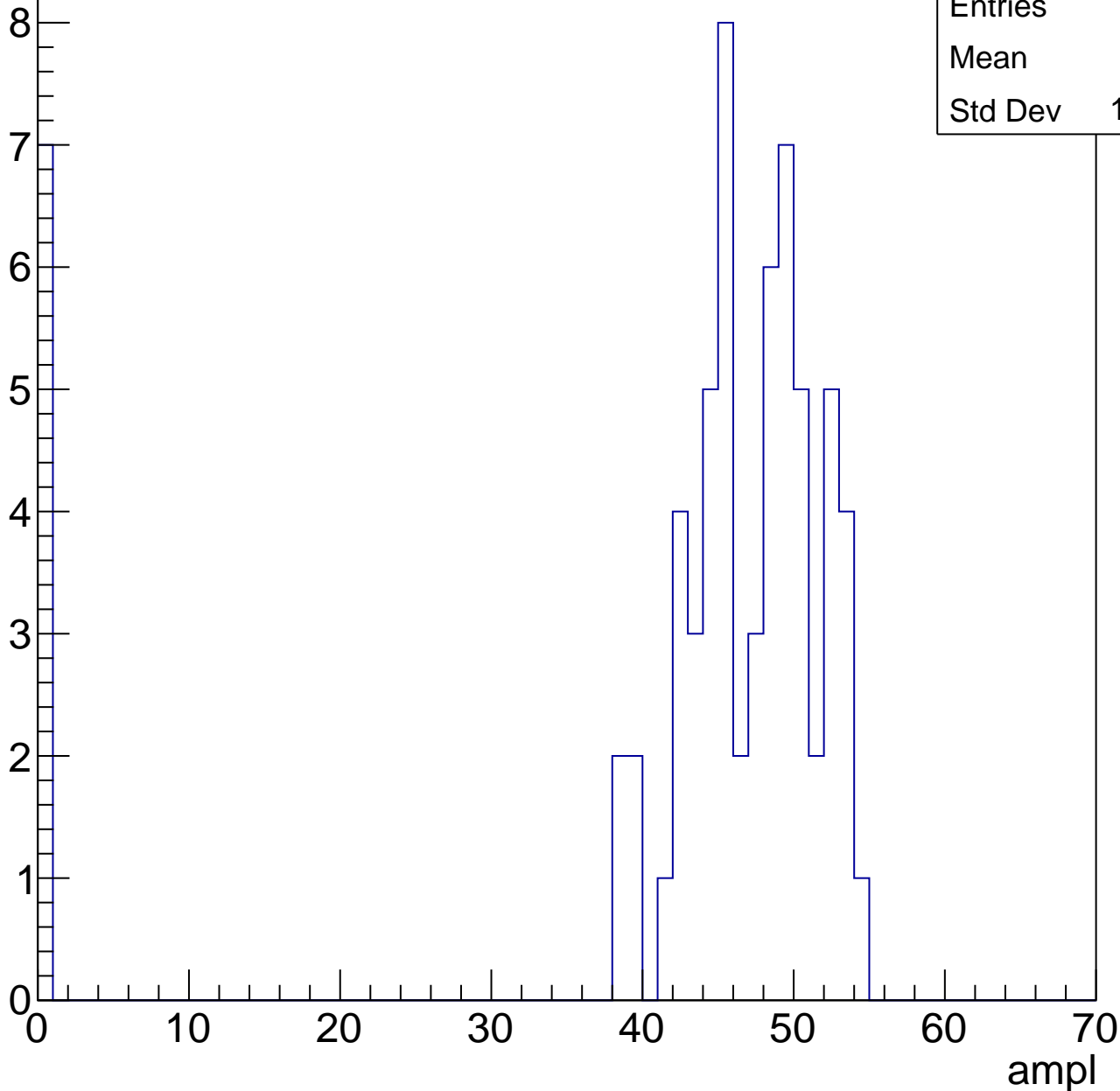


B1L103S, U24-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	42
Std Dev	14.85

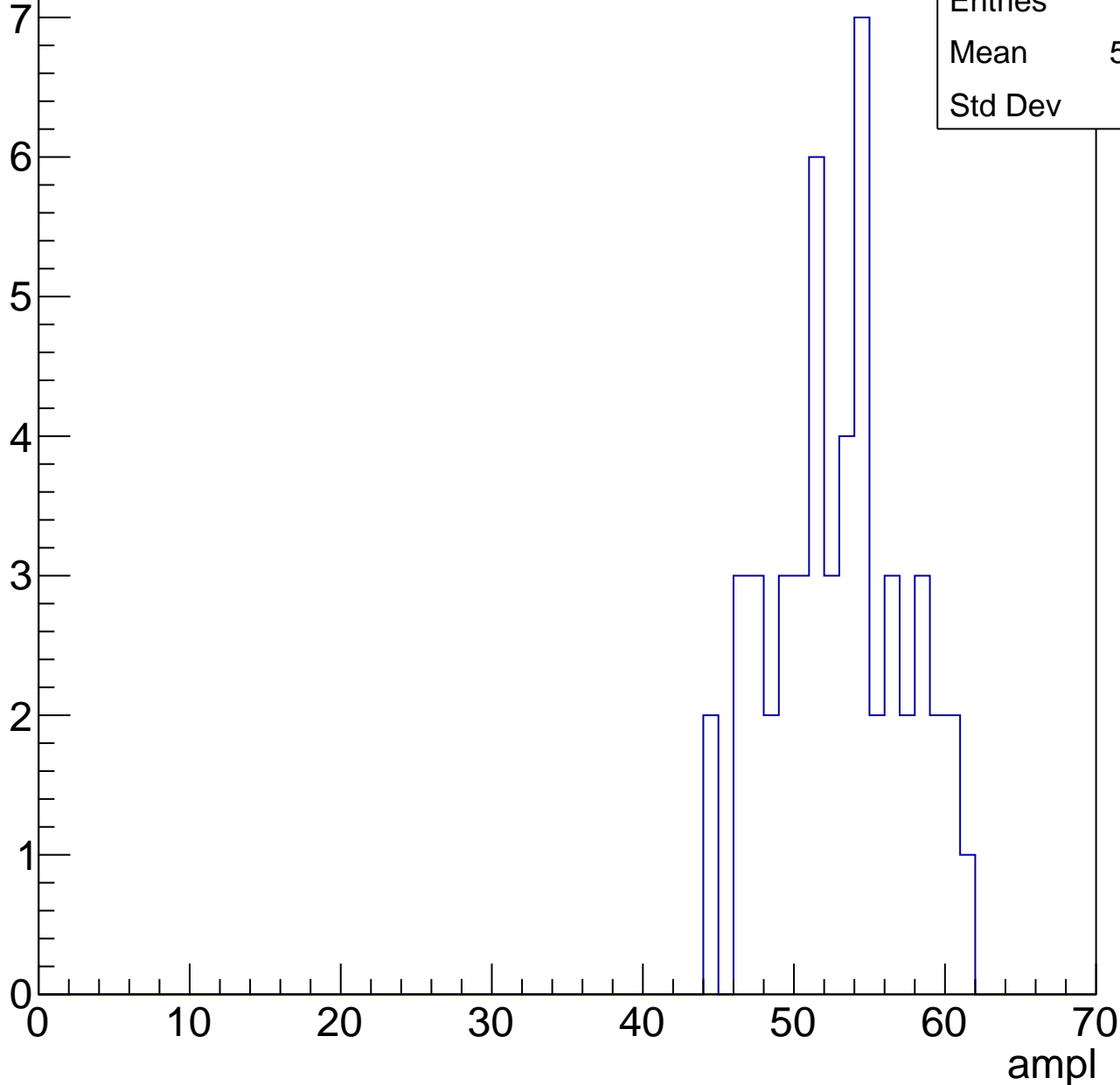


B1L103S, U24-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	52.49
Std Dev	4.29

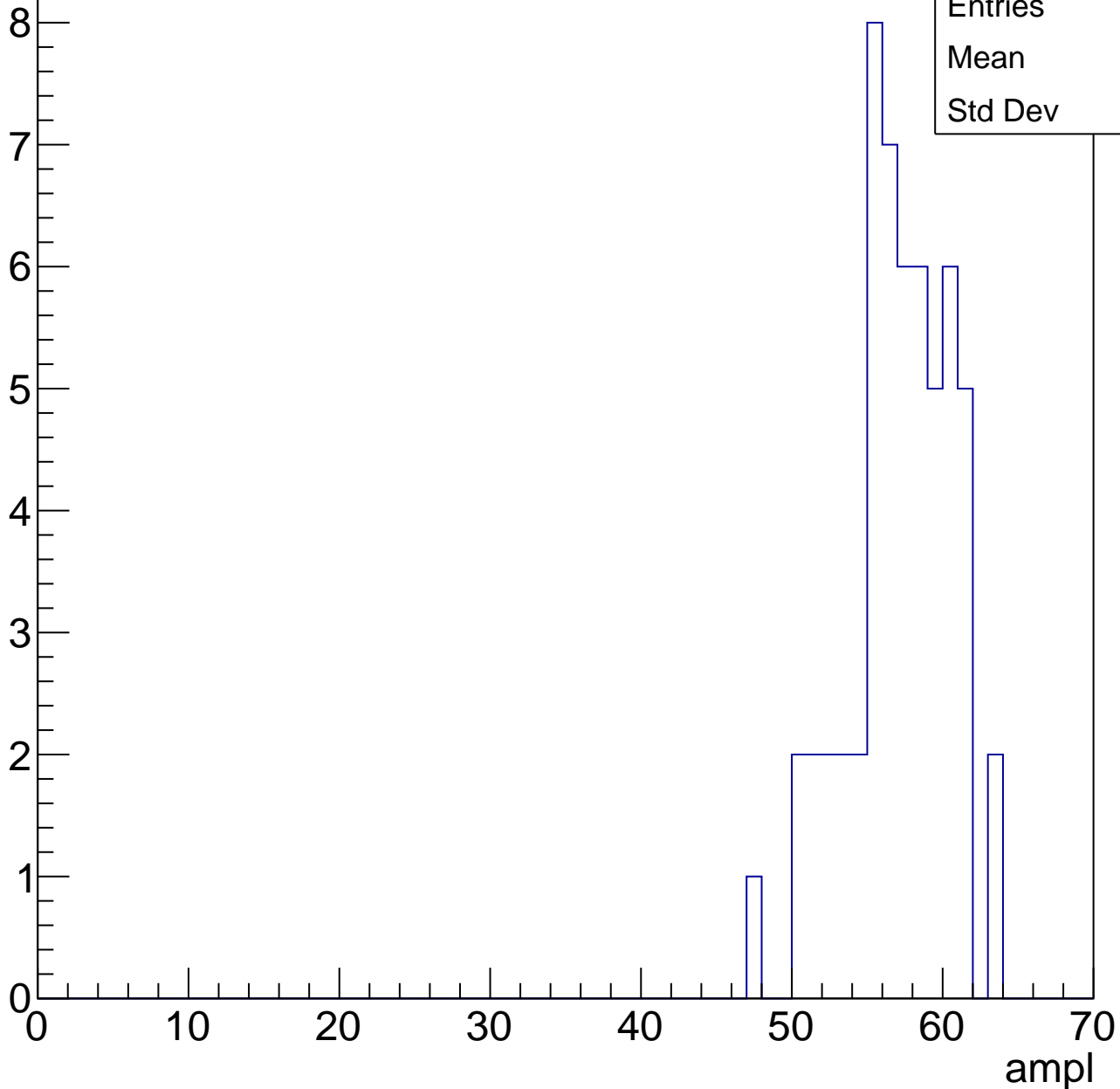


B1L103S, U24-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	56.7
Std Dev	3.37

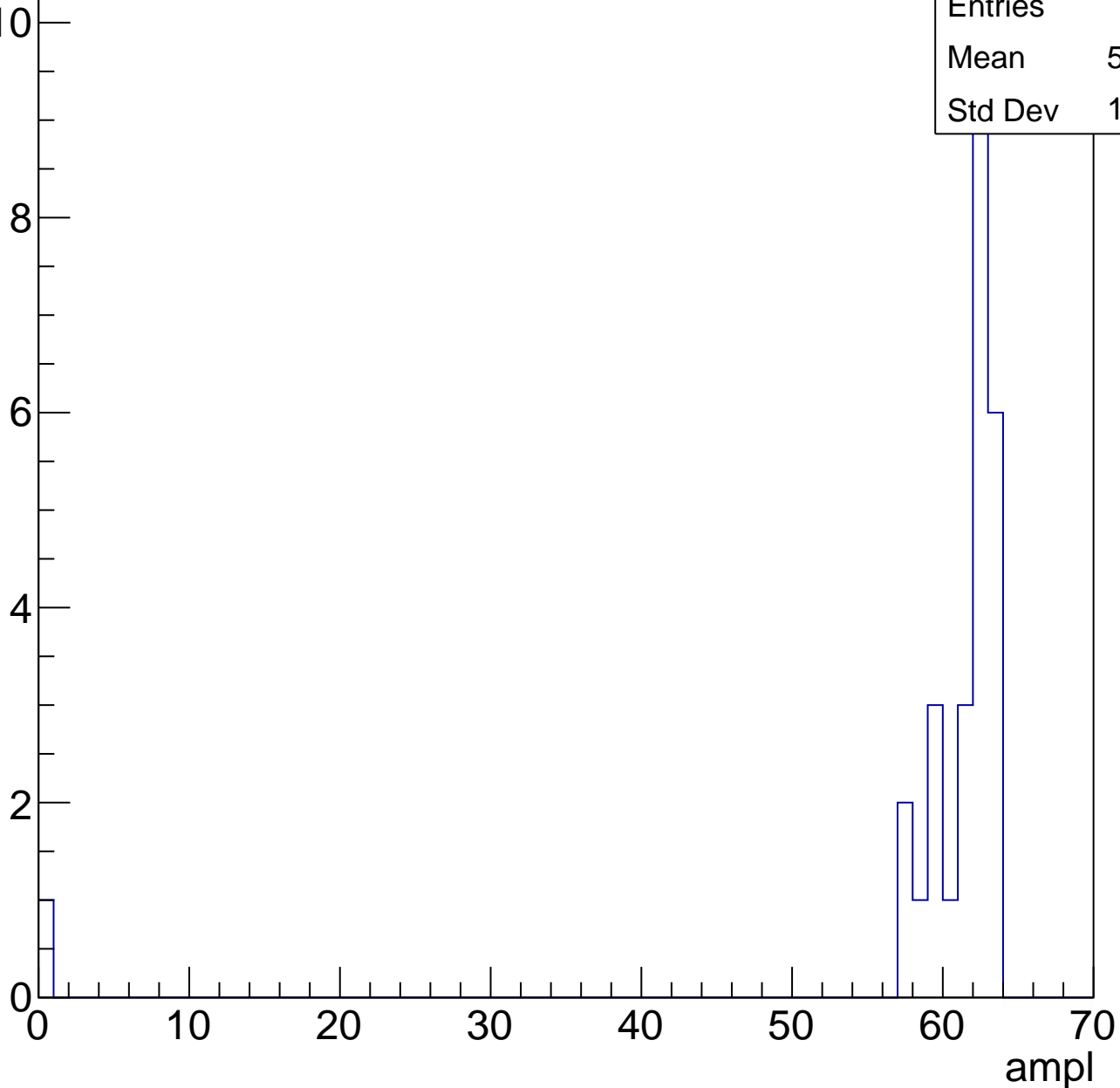


B1L103S, U24-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	58.89
Std Dev	11.69

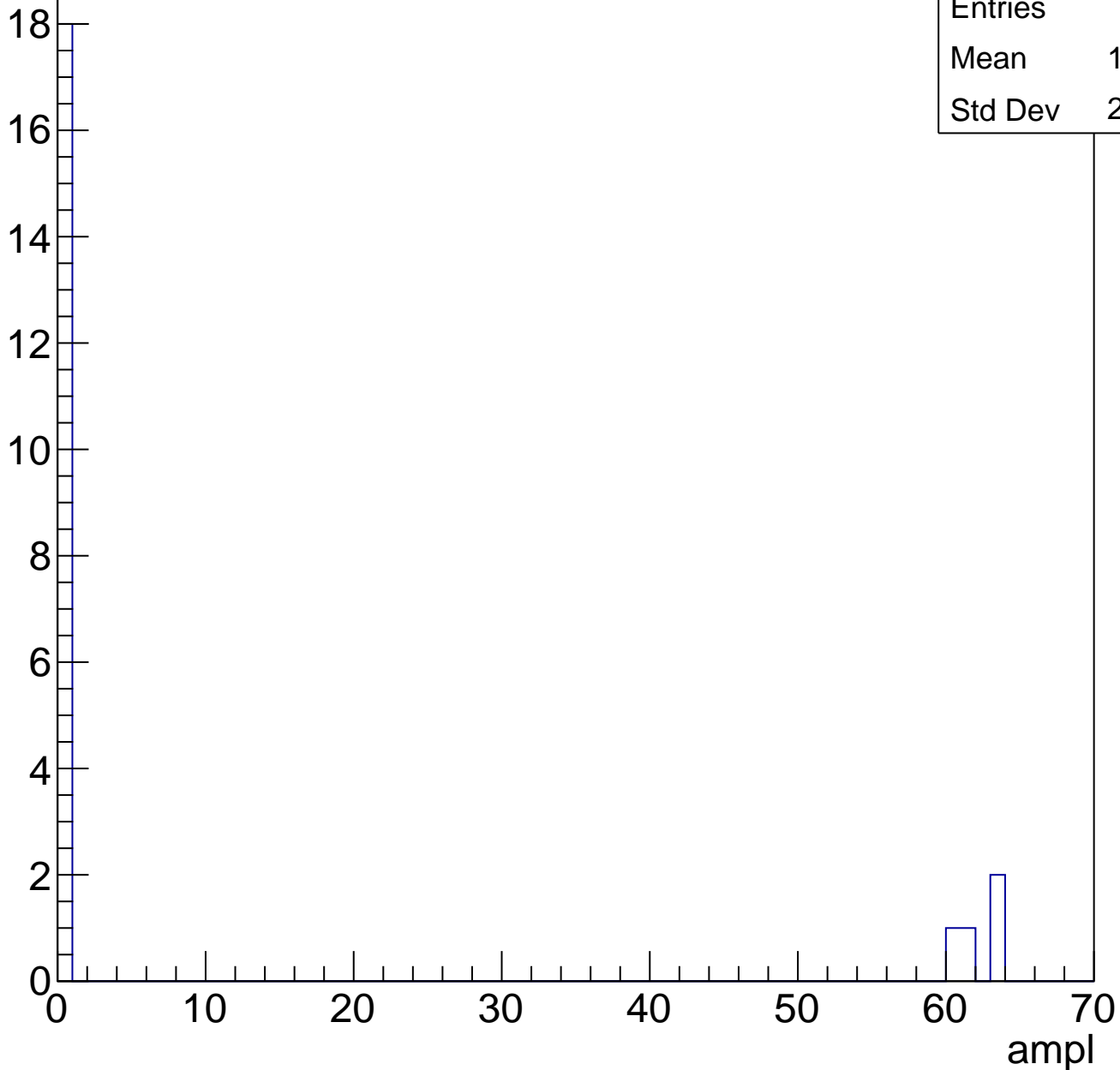


B1L103S, U24-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	11.23
Std Dev	23.82

Entry



B1L103S, U24-ch127, adc0

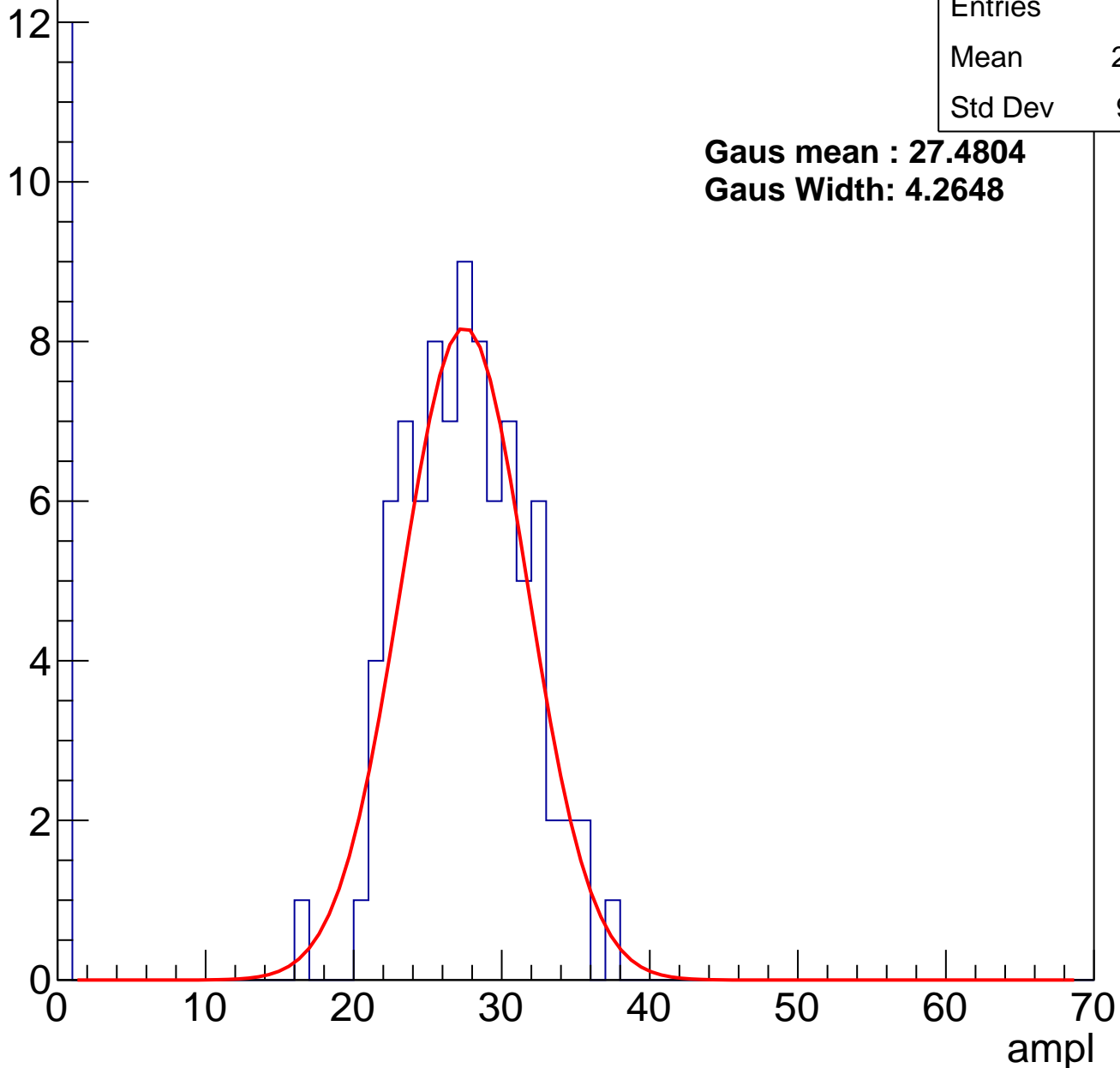
calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	23.78
Std Dev	9.541

Gaus mean : 27.4804

Gaus Width: 4.2648

Entry



B1L103S, U24-ch127, adc1

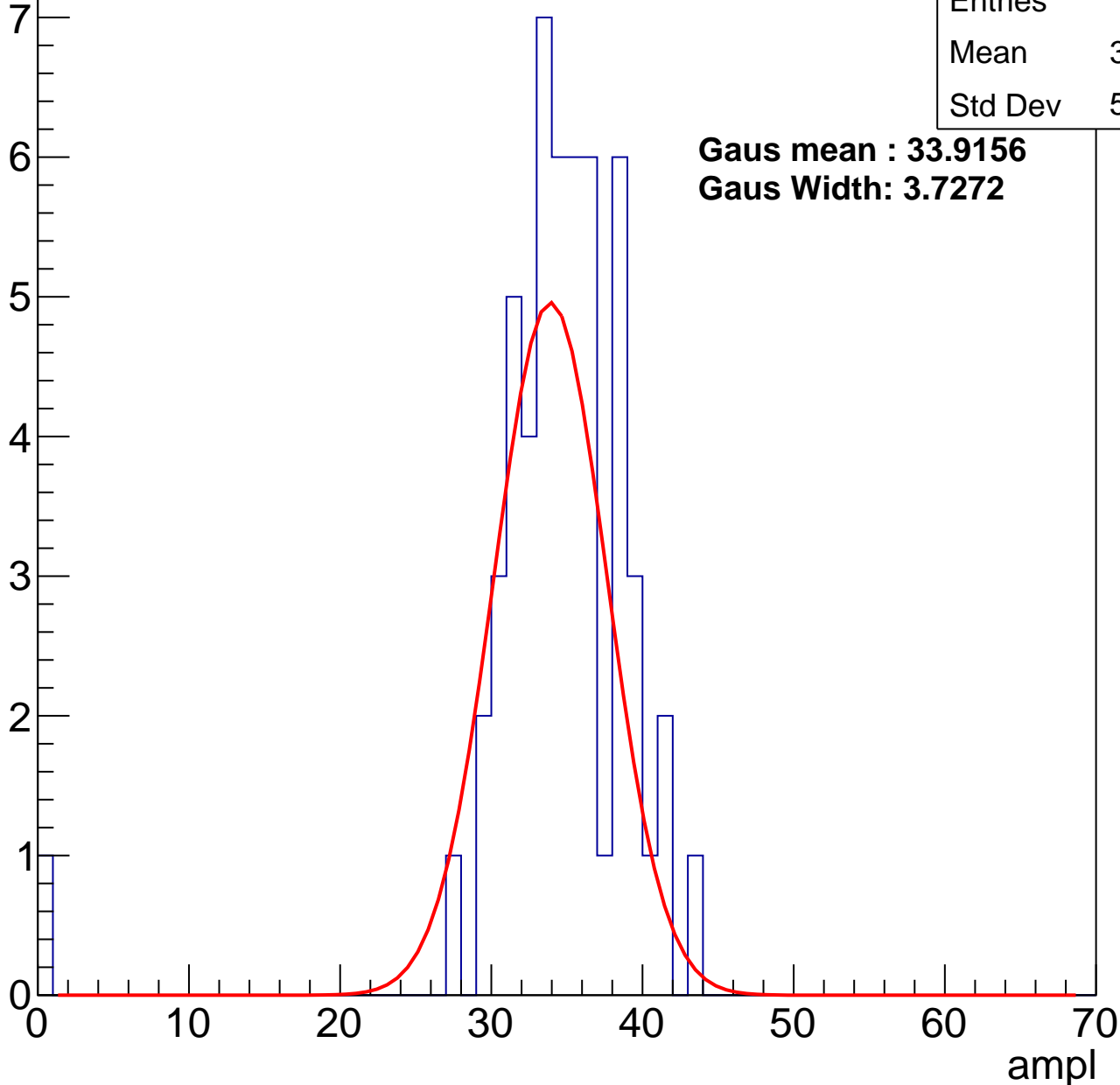
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	33.93
Std Dev	5.714

Gaus mean : 33.9156

Gaus Width: 3.7272



B1L103S, U24-ch127, adc2

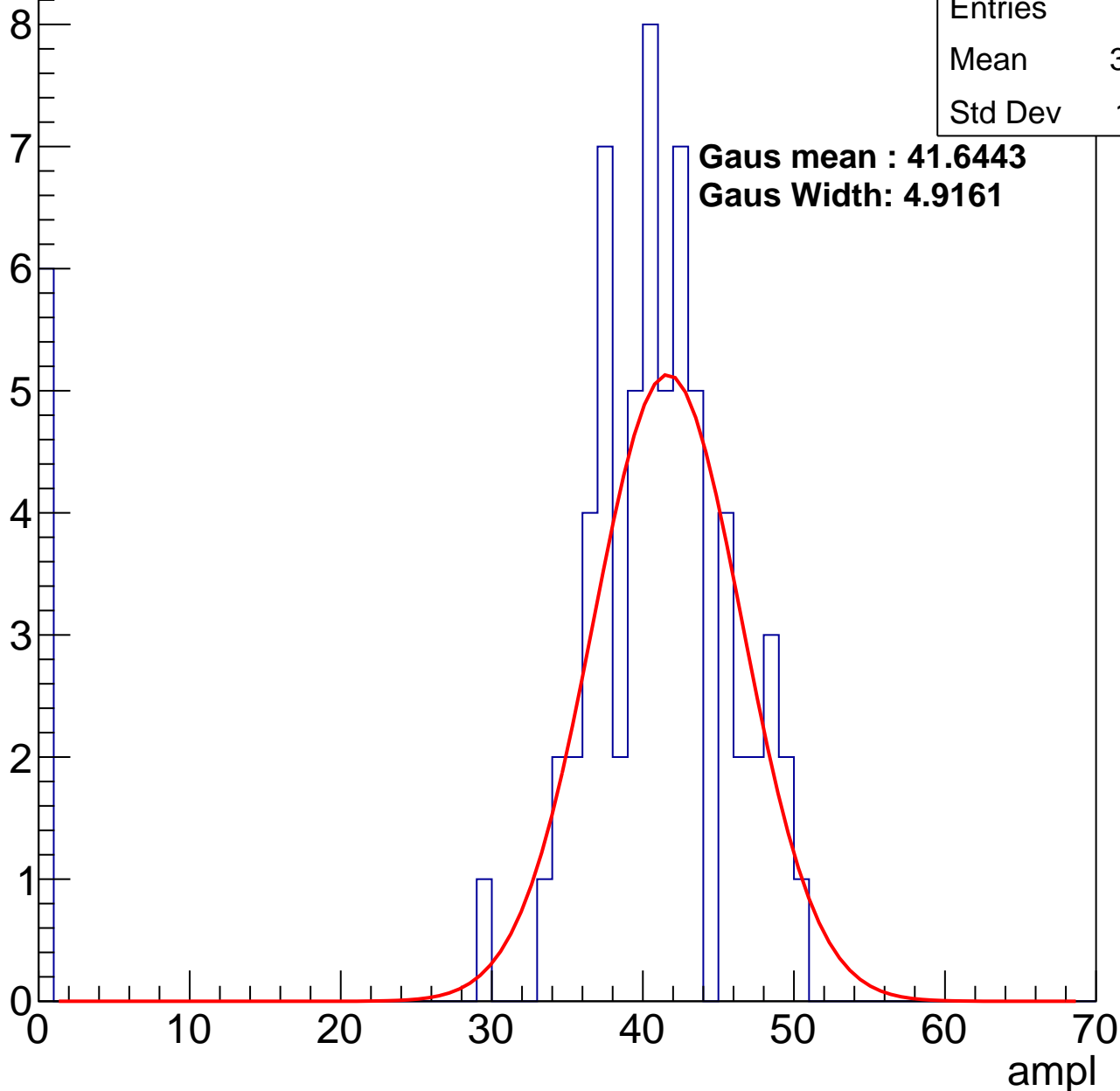
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	37.19
Std Dev	12.21

Gaus mean : 41.6443

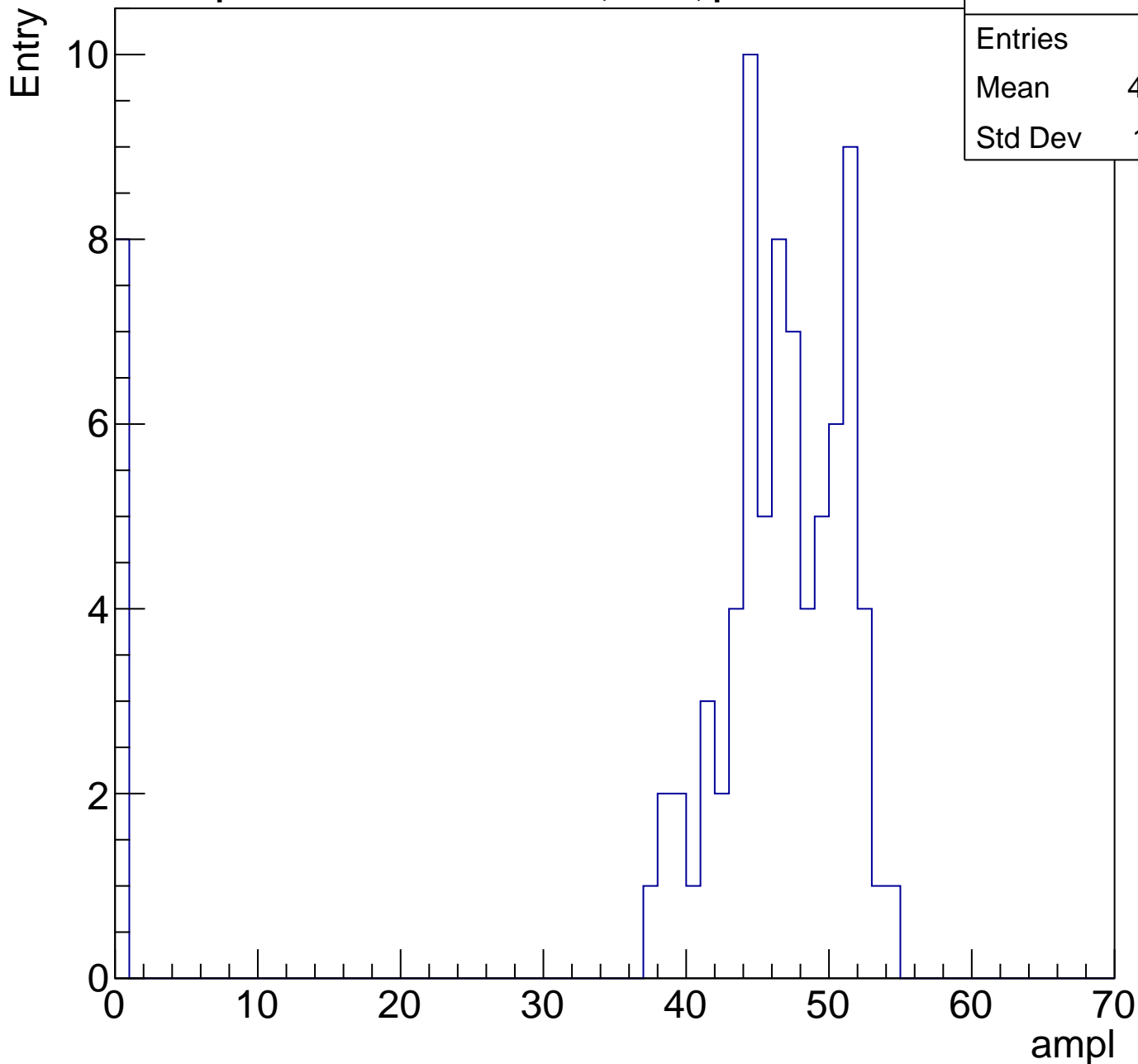
Gaus Width: 4.9161



B1L103S, U24-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	41.96
Std Dev	14.21

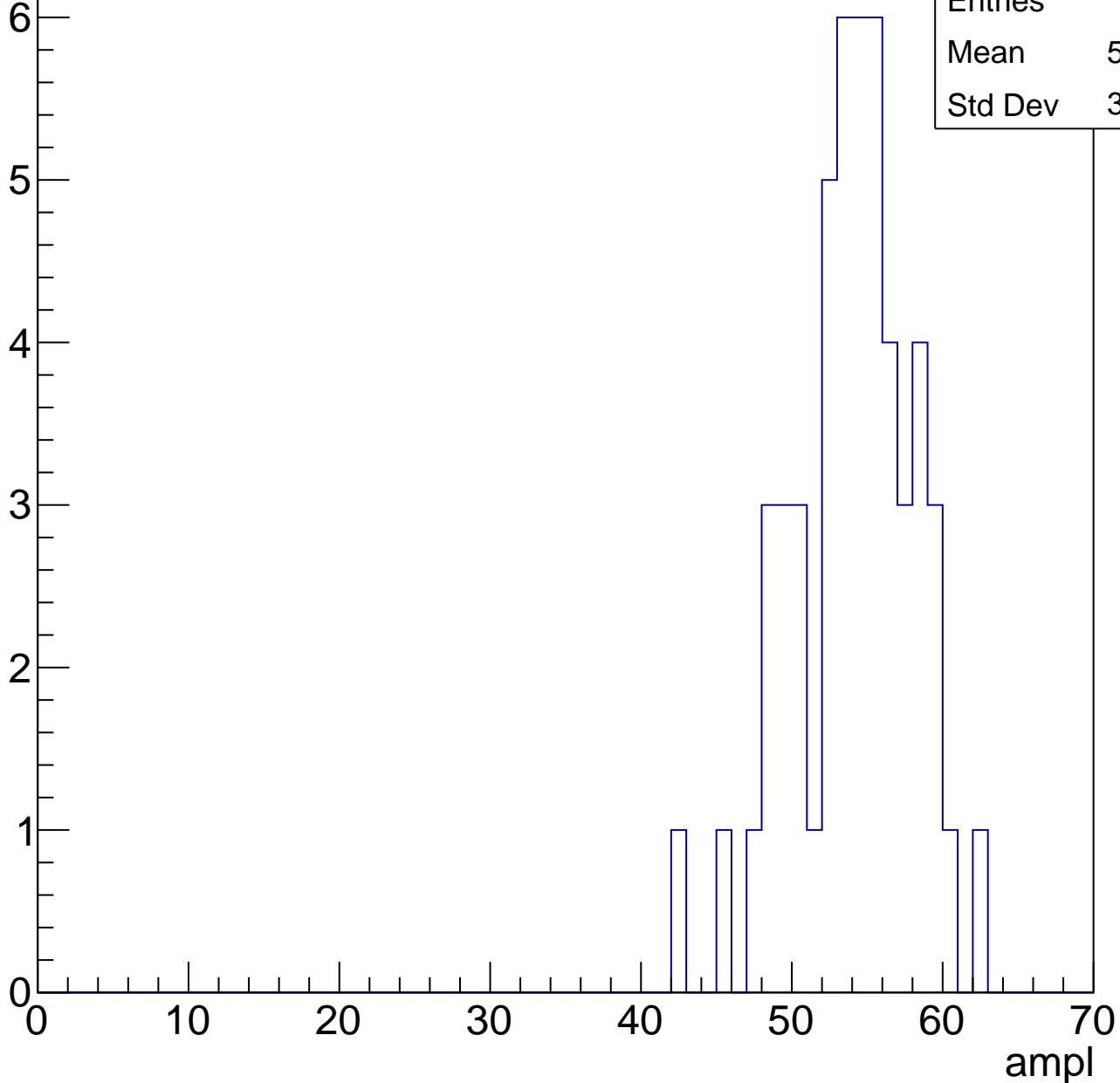


B1L103S, U24-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	53.54
Std Dev	3.964



B1L103S, U24-ch127, adc5

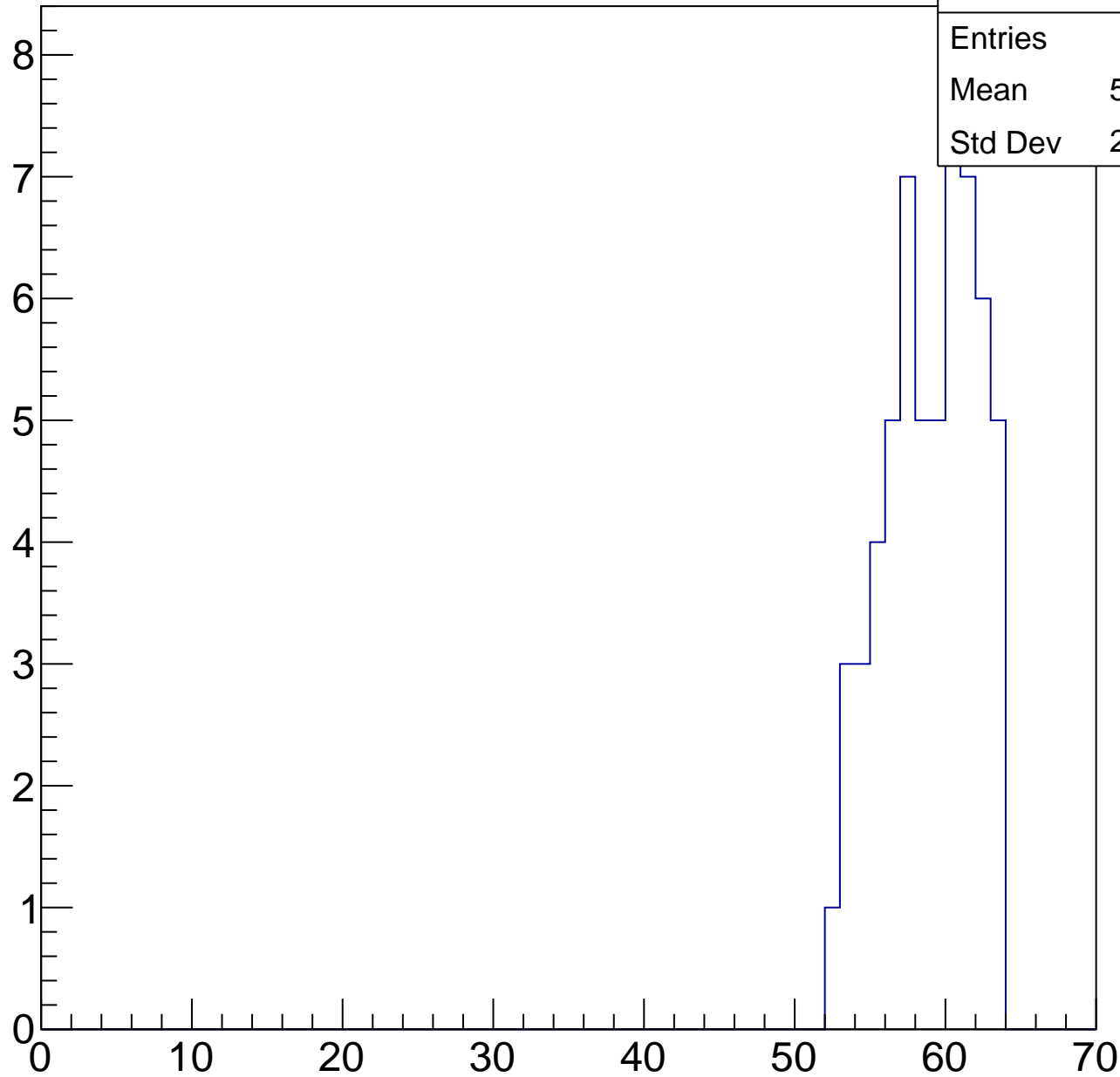
calib_packv5_041523_1651.root, FC#0, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	59
Mean	58.49
Std Dev	2.994

ampl



B1L103S, U24-ch127, adc6

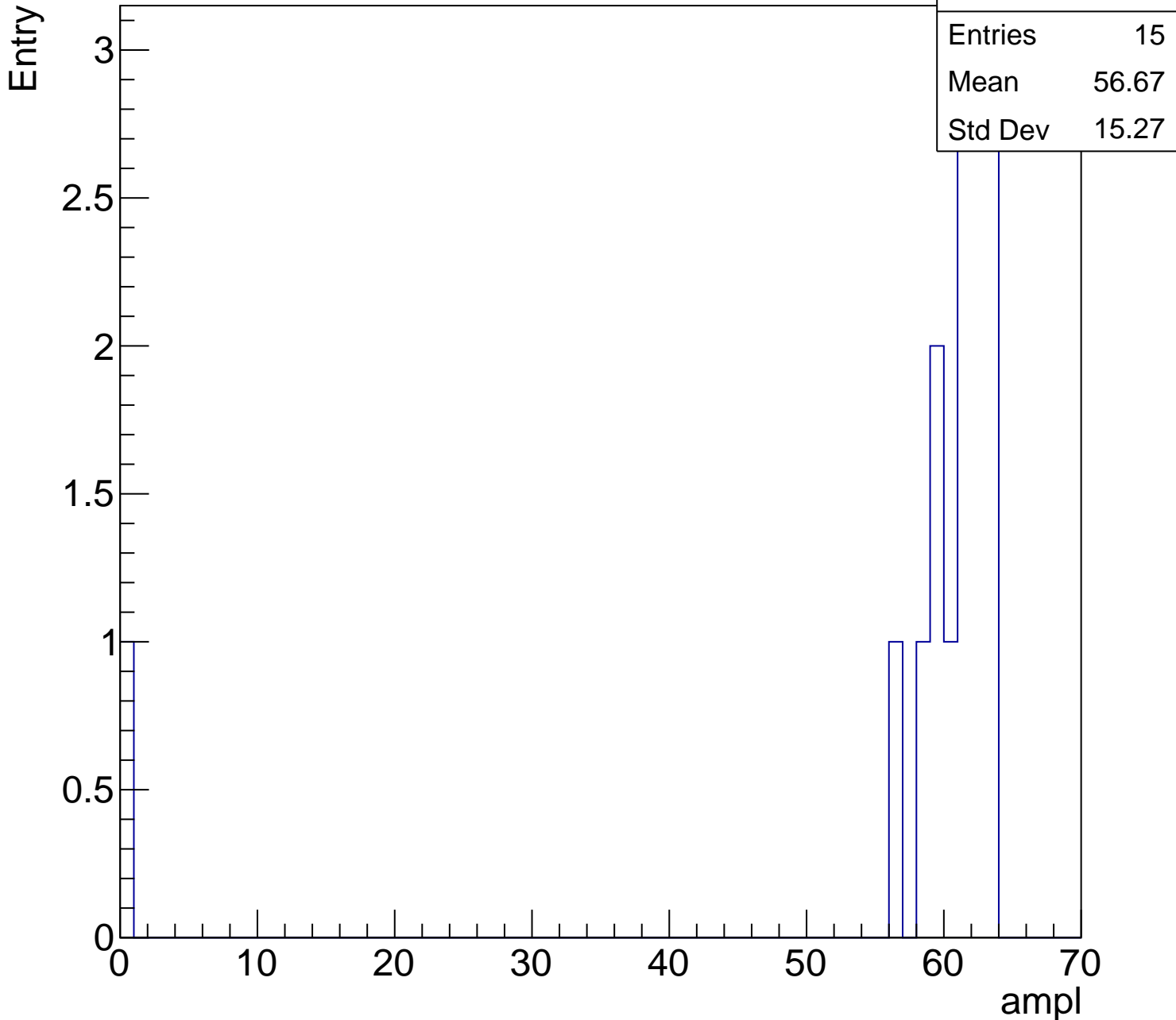
calib_packv5_041523_1651.root, FC#0, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	15
Mean	56.67
Std Dev	15.27

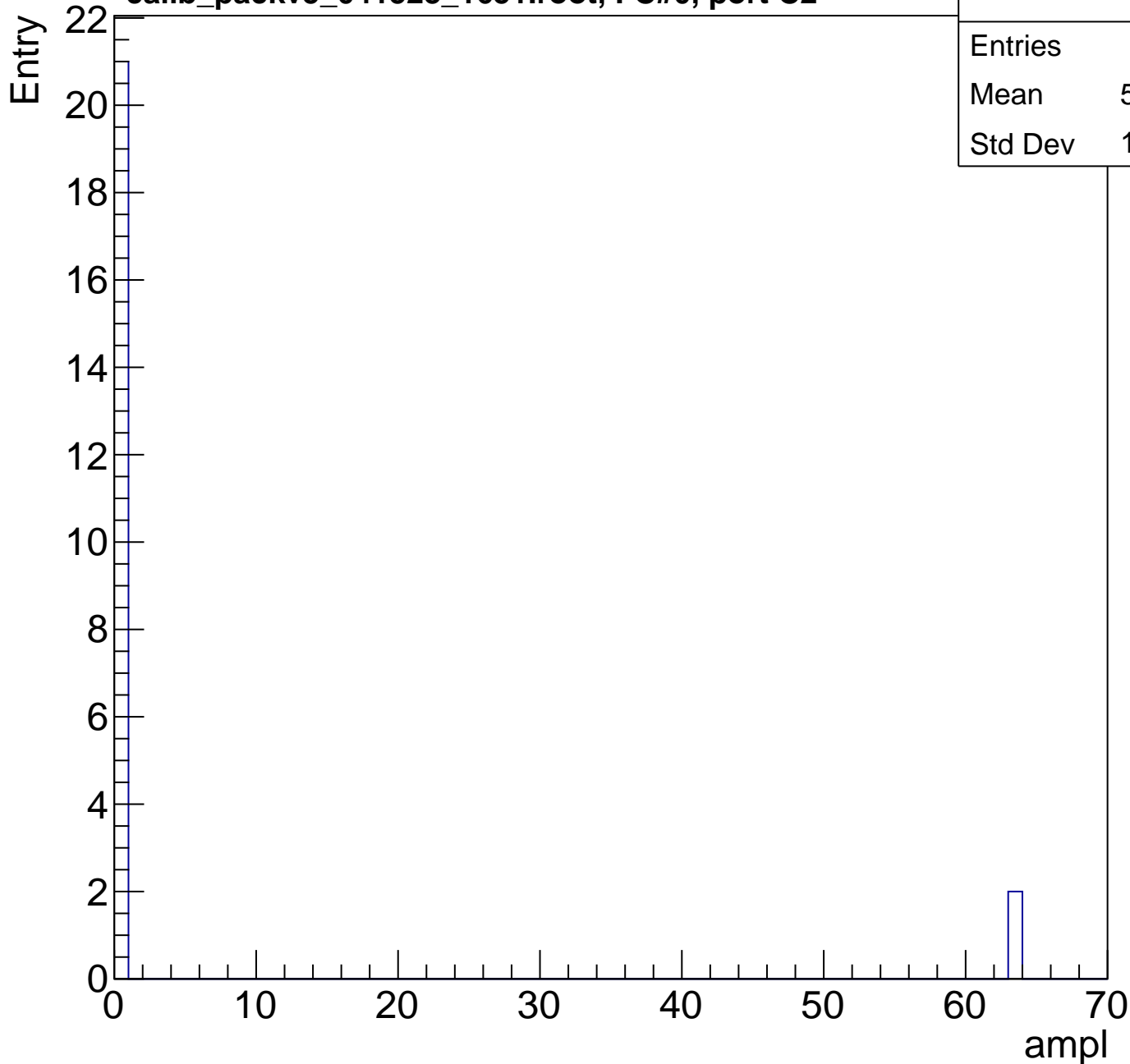
ampl



B1L103S, U24-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	5.478
Std Dev	17.75



B1L103S, U24-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	5.478
Std Dev	17.75

