

B1L104S, U19-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	35.38
Std Dev	27.86

Turn on : 51.9023

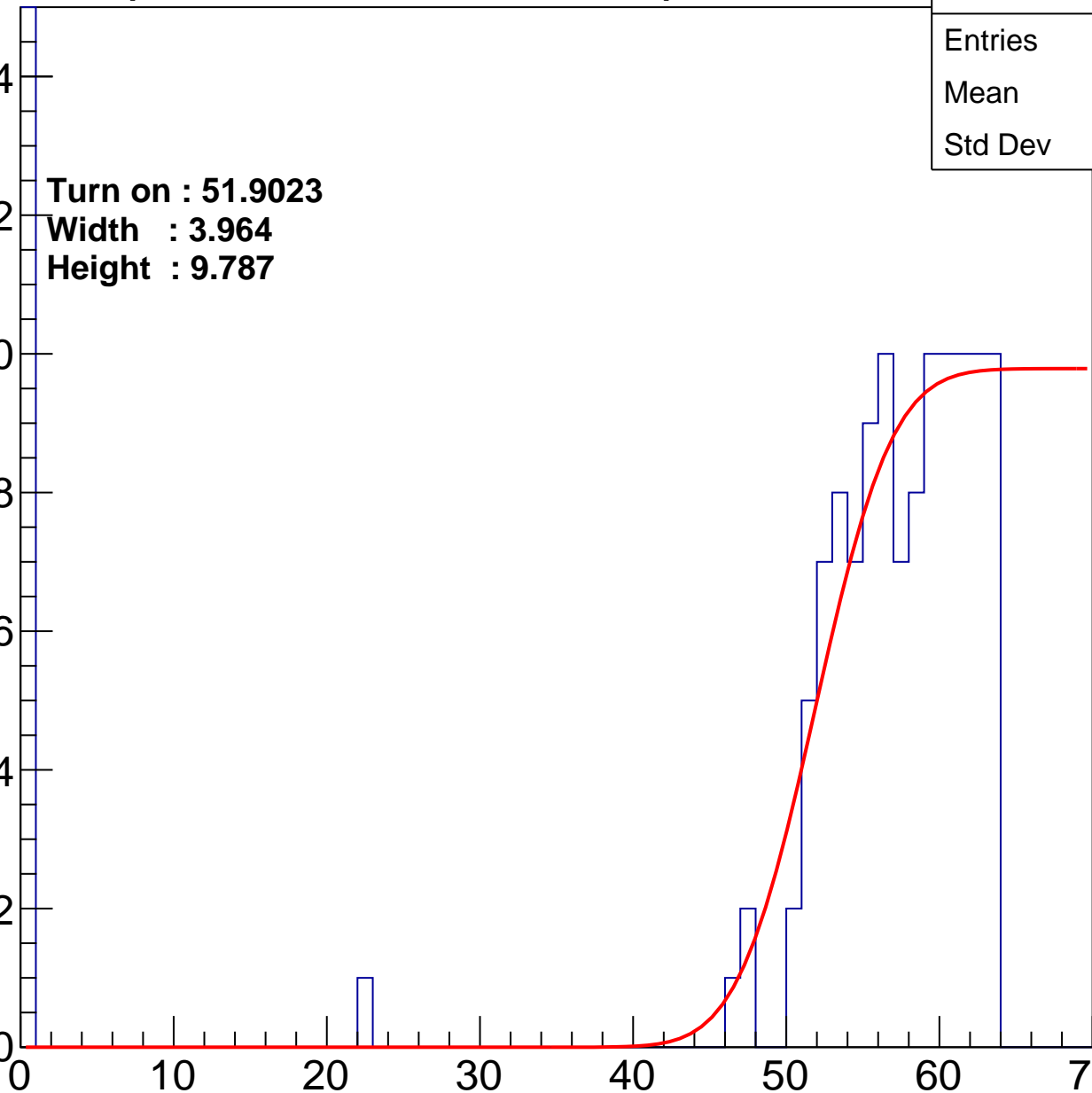
Width : 3.964

Height : 9.787

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	34.78
Std Dev	28.14

Turn on : 52.6372

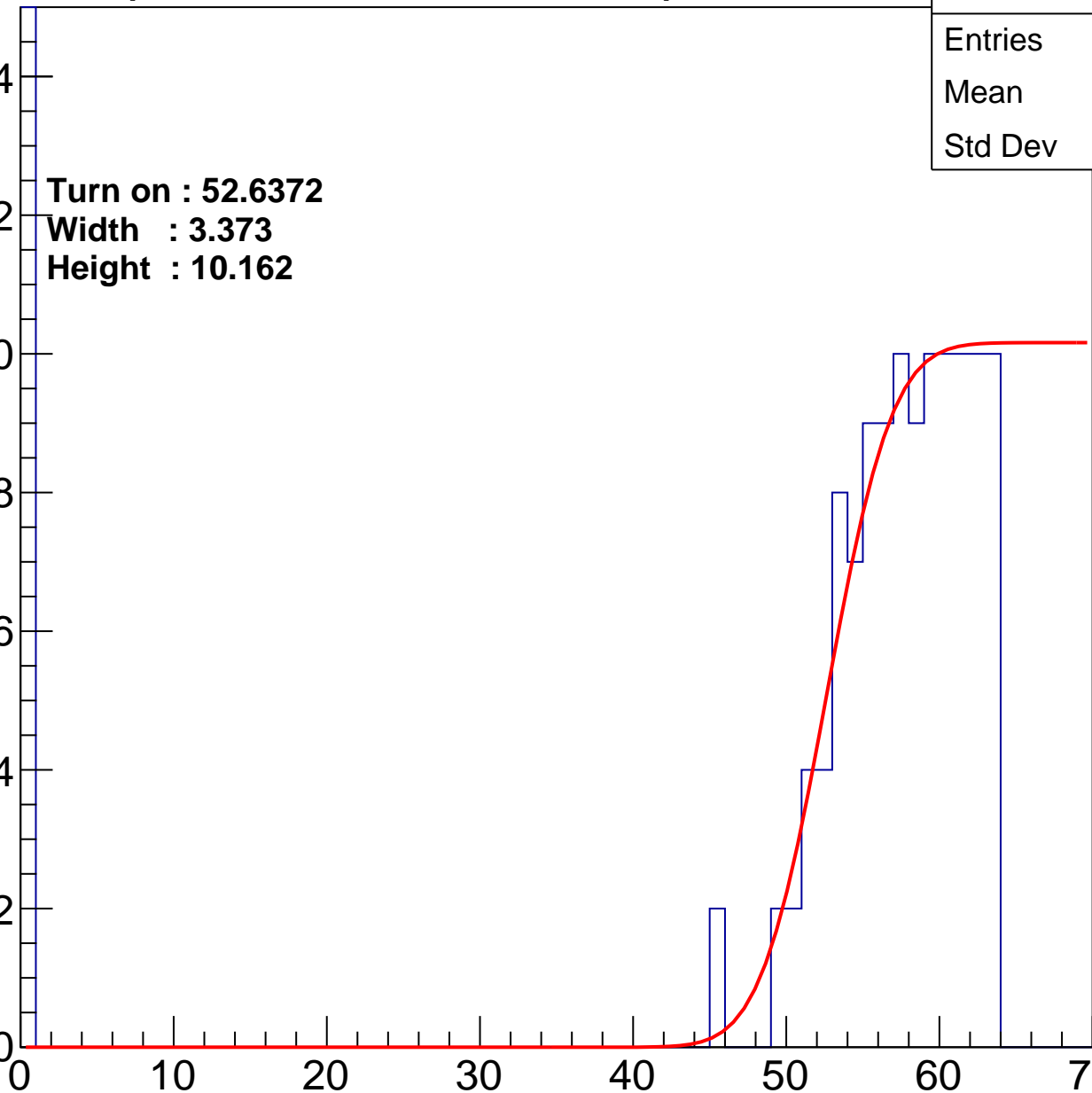
Width : 3.373

Height : 10.162

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	35.81
Std Dev	28.17

Turn on : 54.1321

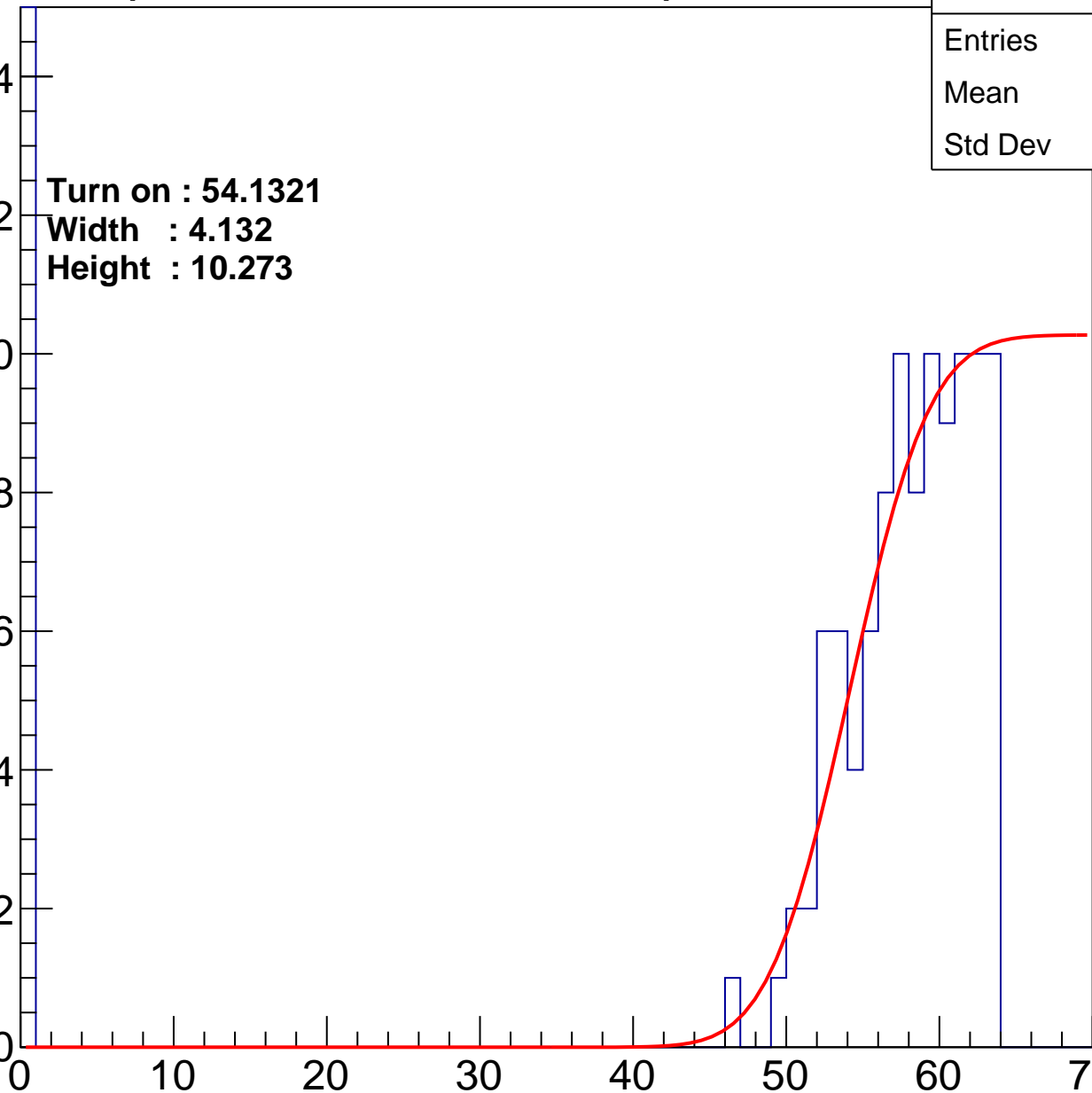
Width : 4.132

Height : 10.273

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	33.16
Std Dev	28.76

Turn on : 54.9276

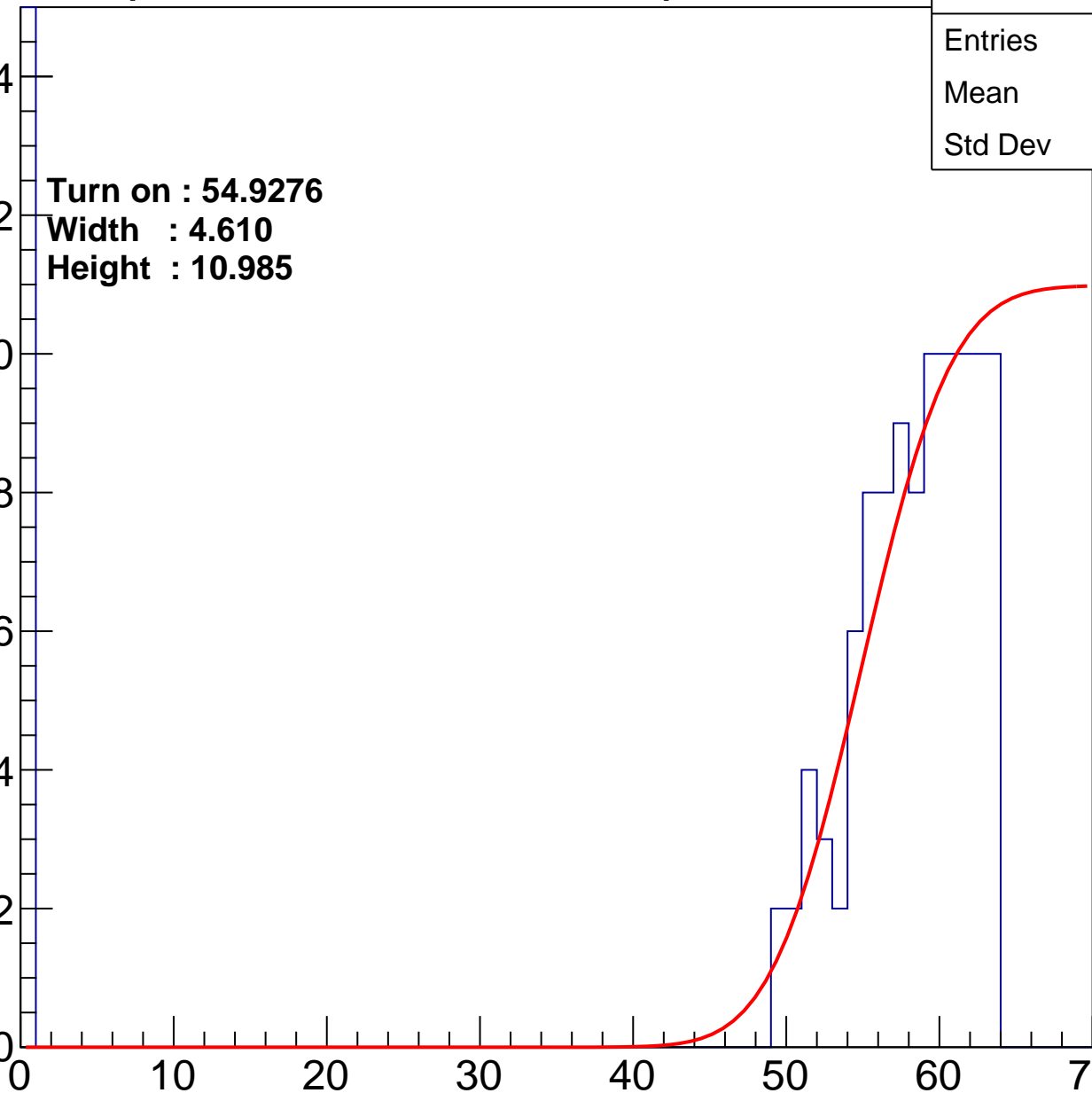
Width : 4.610

Height : 10.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	35.14
Std Dev	28.19

Turn on : 52.7235

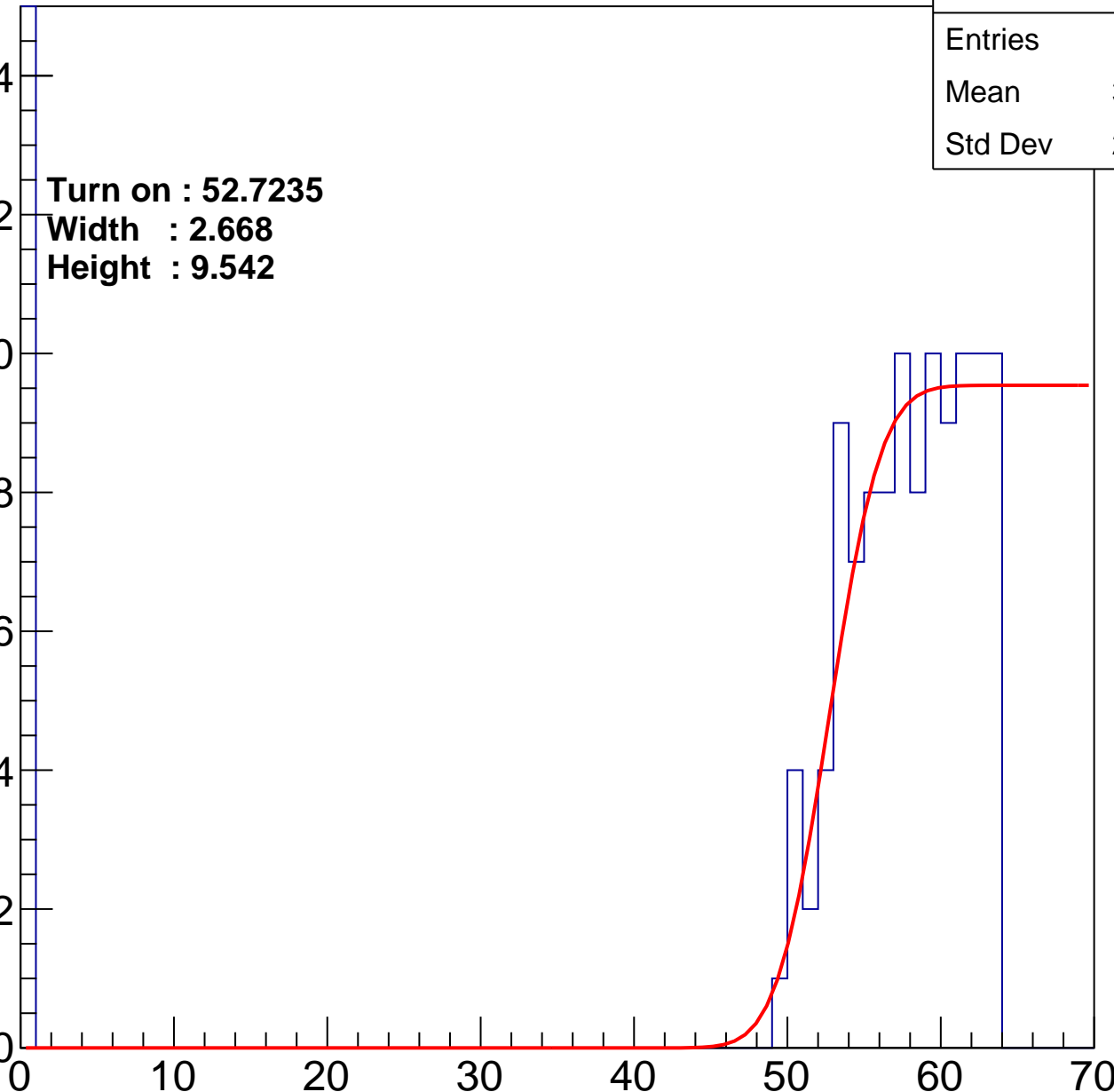
Width : 2.668

Height : 9.542

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	33.15
Std Dev	28.52

Turn on : 53.2159

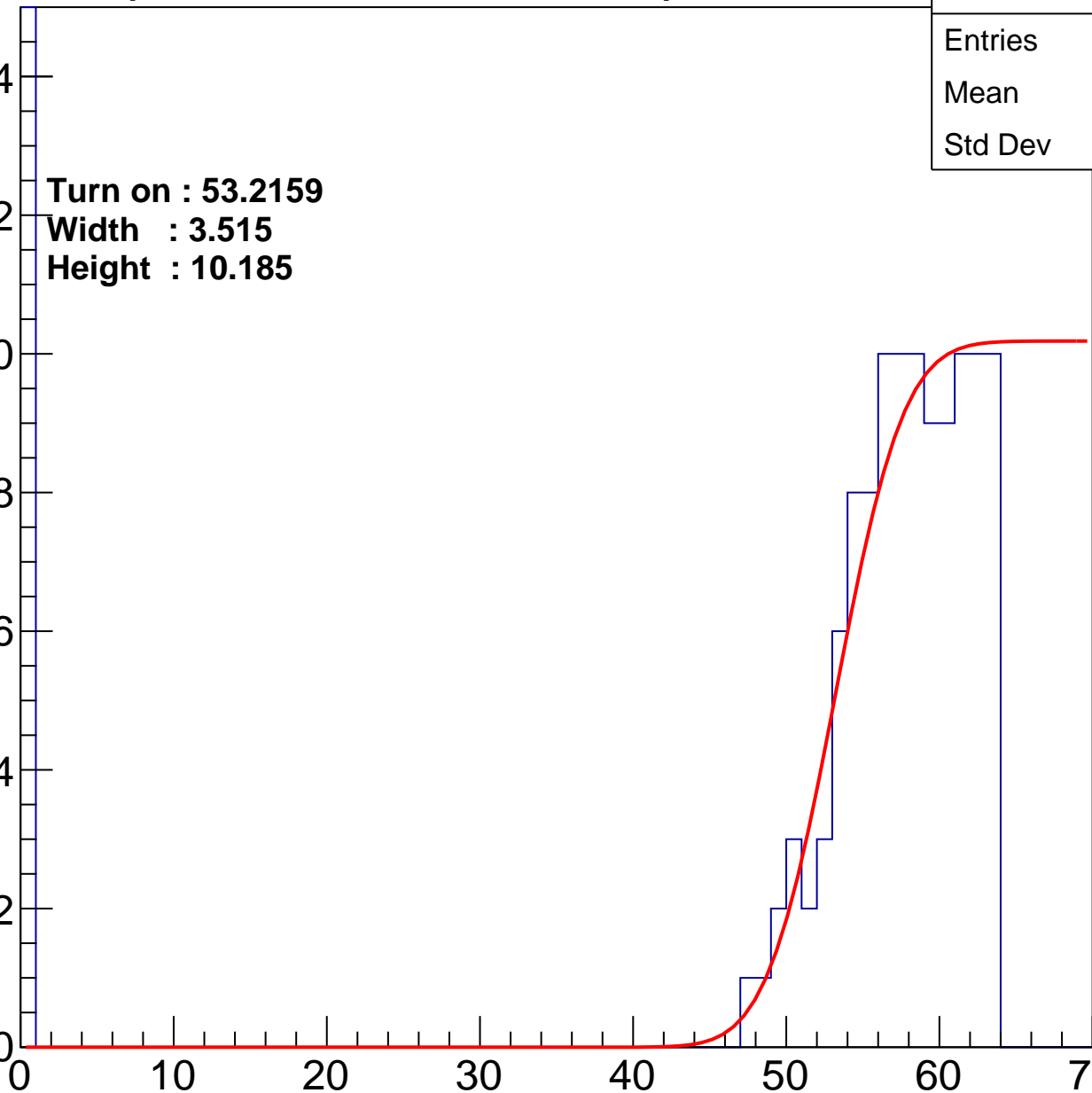
Width : 3.515

Height : 10.185

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	33.82
Std Dev	28.49

Turn on : 53.5984

Width : 4.139

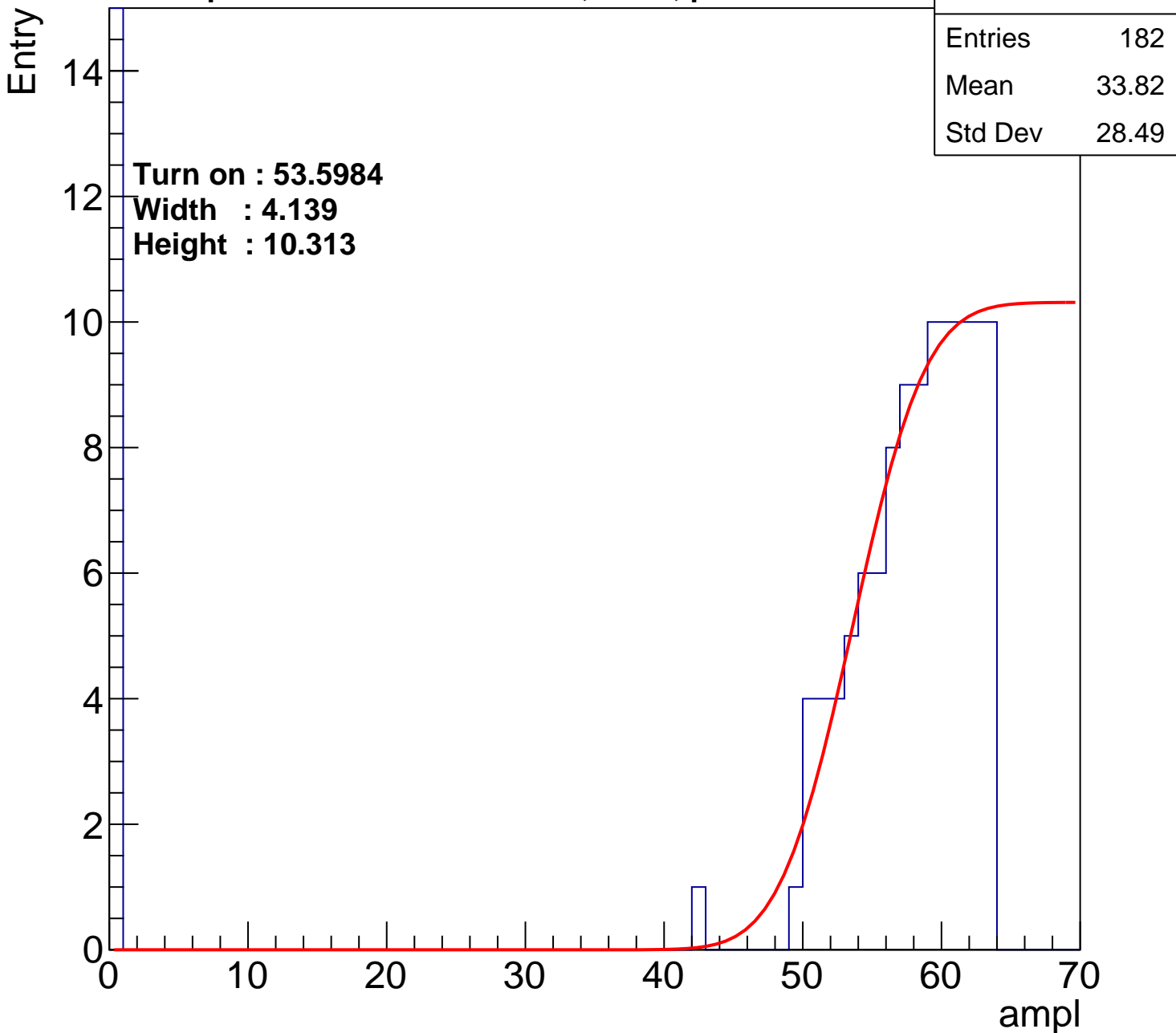
Height : 10.313

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U19-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	32.28
Std Dev	29.27

Turn on : 55.7970

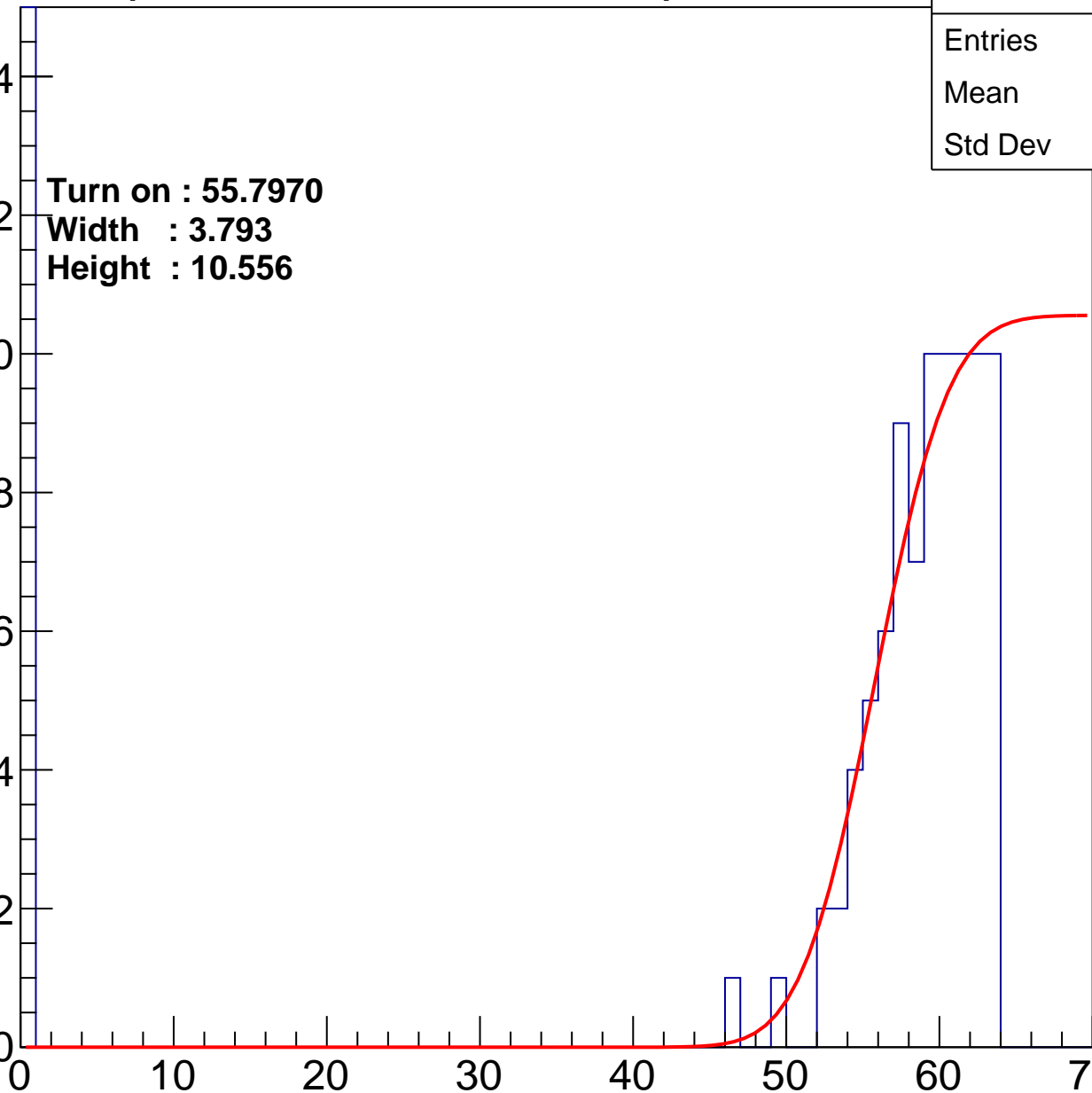
Width : 3.793

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch8

calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	31.99
Std Dev	29.24

Turn on : 56.1458

Width : 3.479

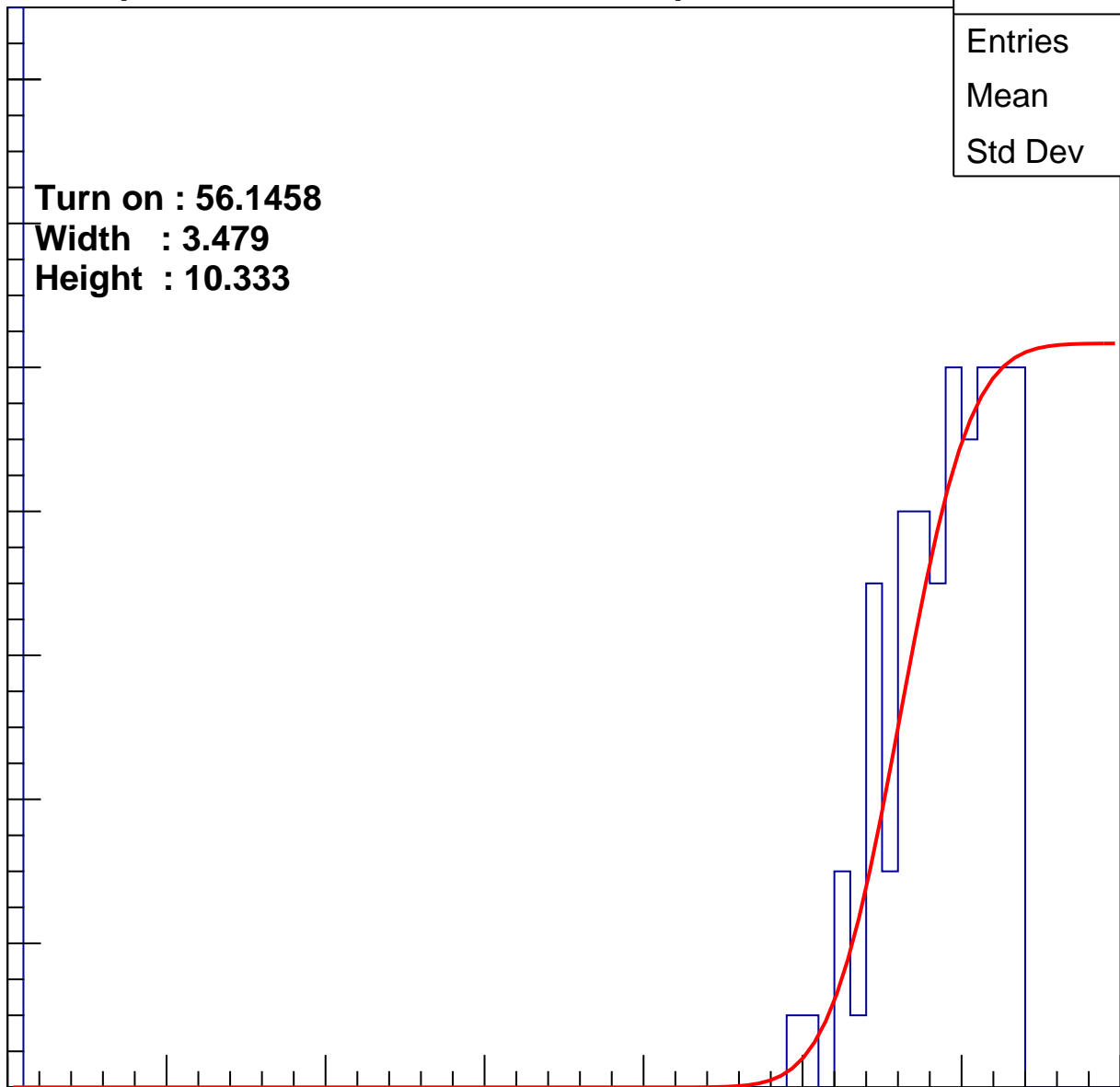
Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U19-ch9

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	27.02
Std Dev	29.17

Turn on : 54.8568

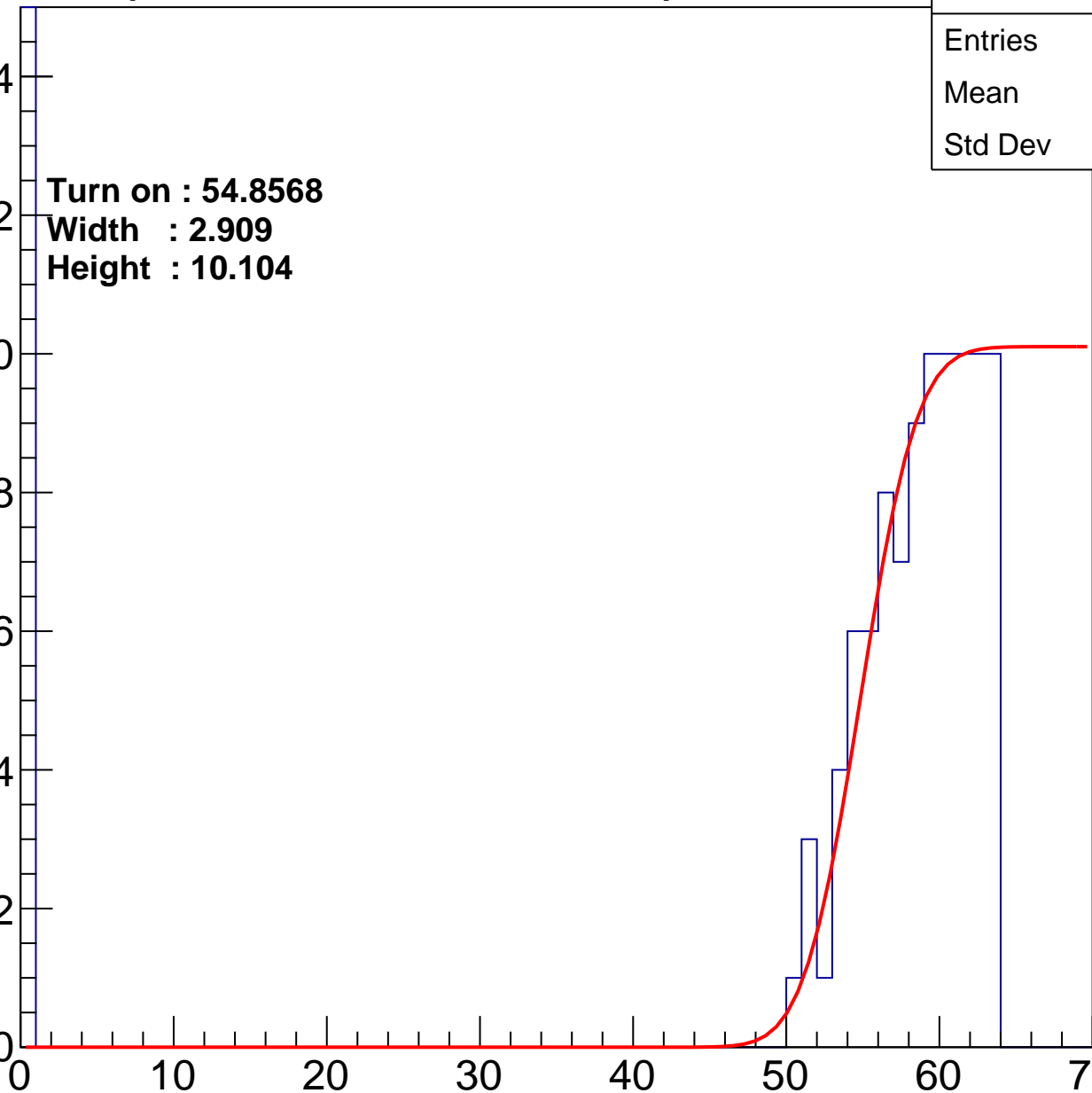
Width : 2.909

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	33.49
Std Dev	28.97

Turn on : 55.1947

Width : 3.177

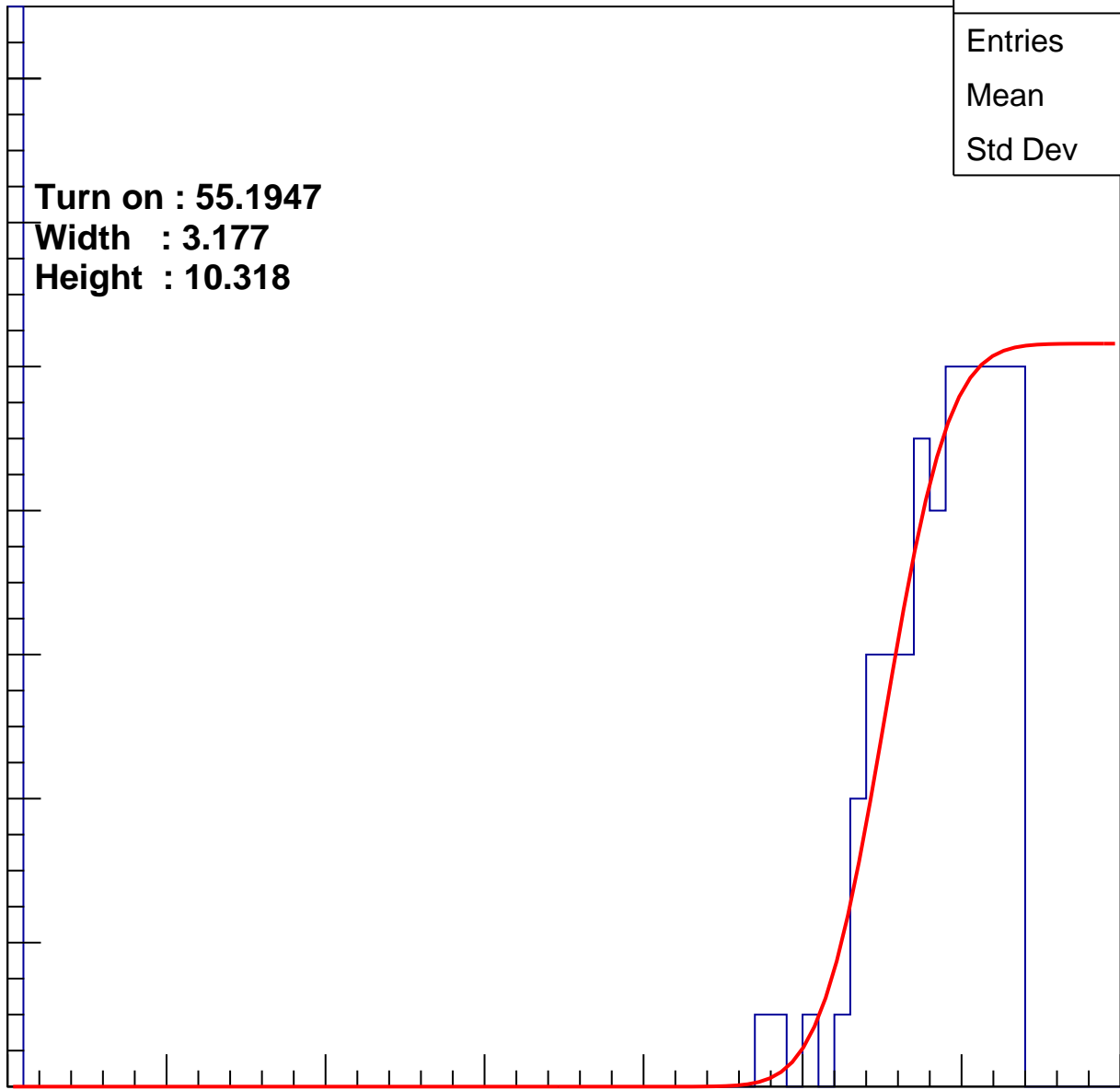
Height : 10.318

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U19-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	31.81
Std Dev	29.06

Turn on : 54.1540

Width : 2.606

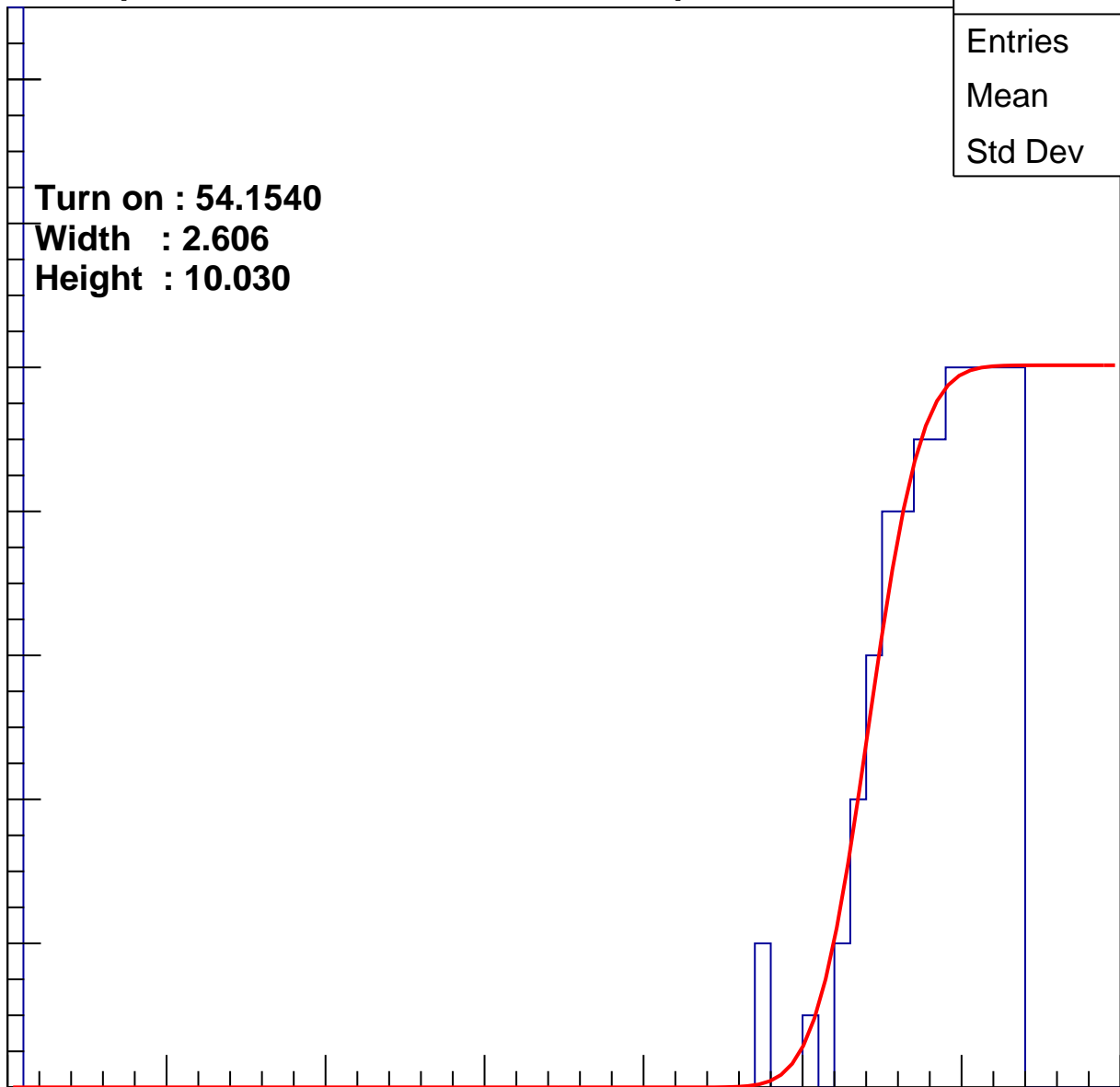
Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U19-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	34.22
Std Dev	28.61

Turn on : 54.1396

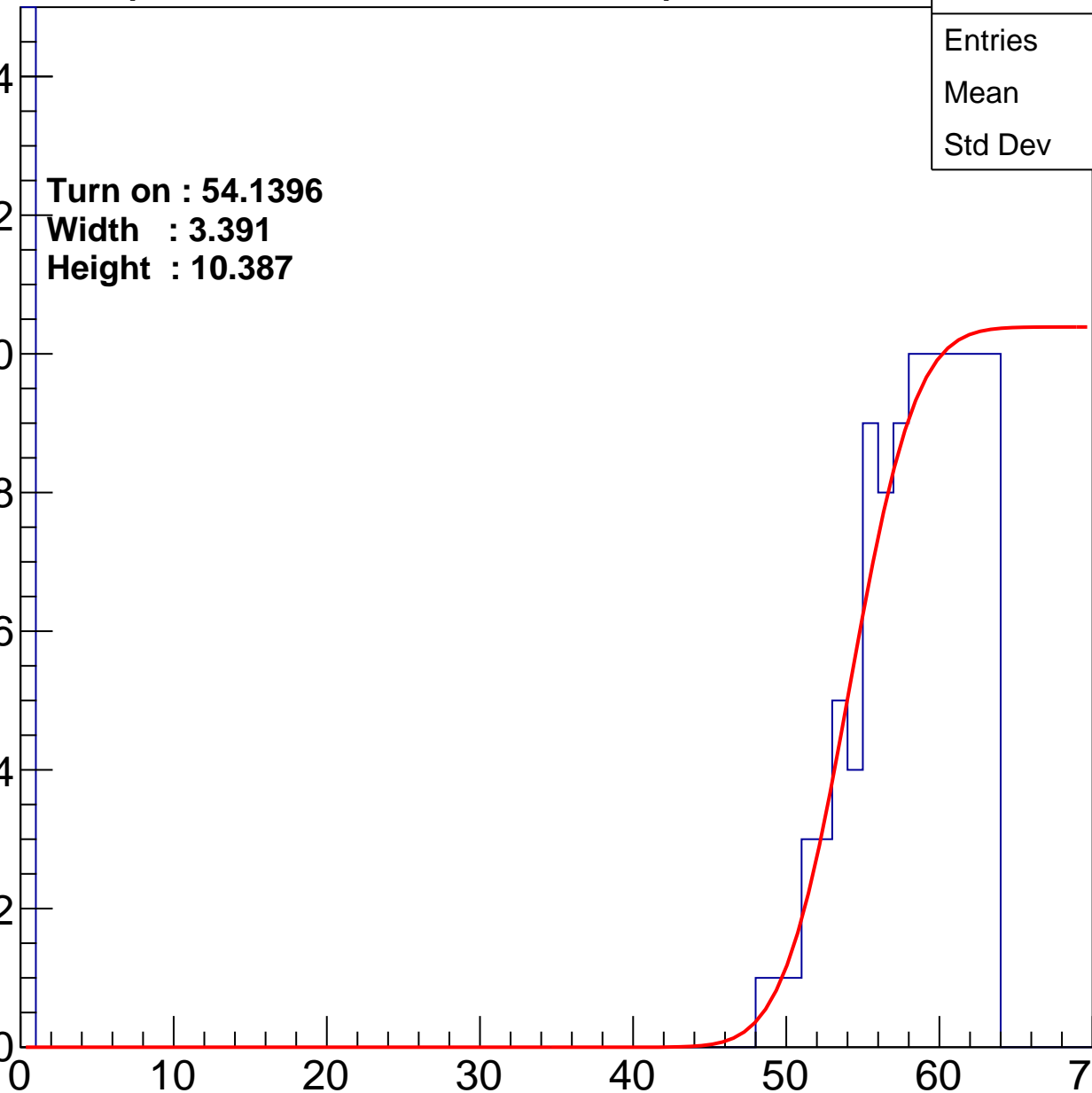
Width : 3.391

Height : 10.387

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	34.87
Std Dev	28.36

Turn on : 54.0935

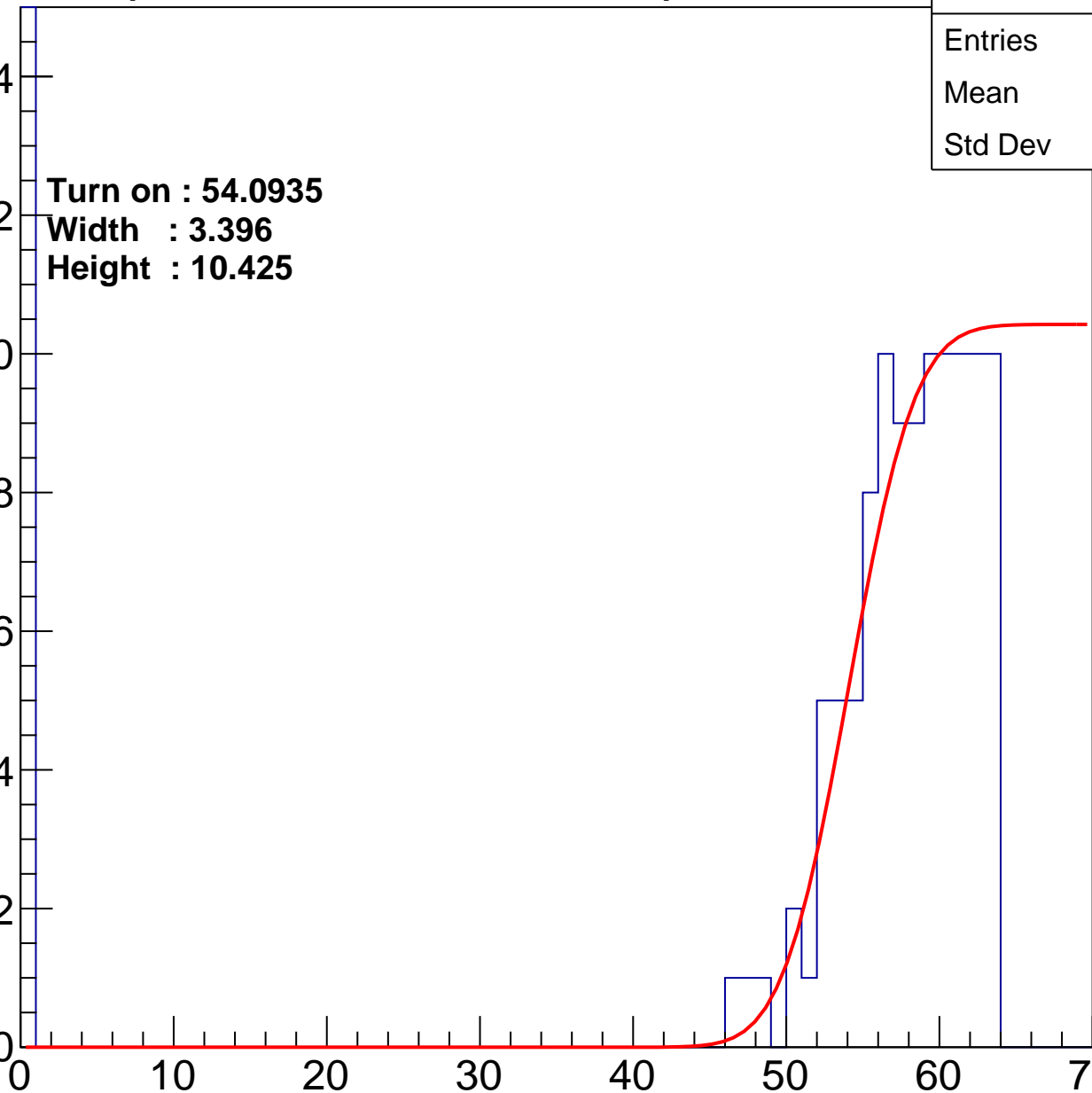
Width : 3.396

Height : 10.425

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch14

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.58
Std Dev	28.47

Turn on : 54.1914

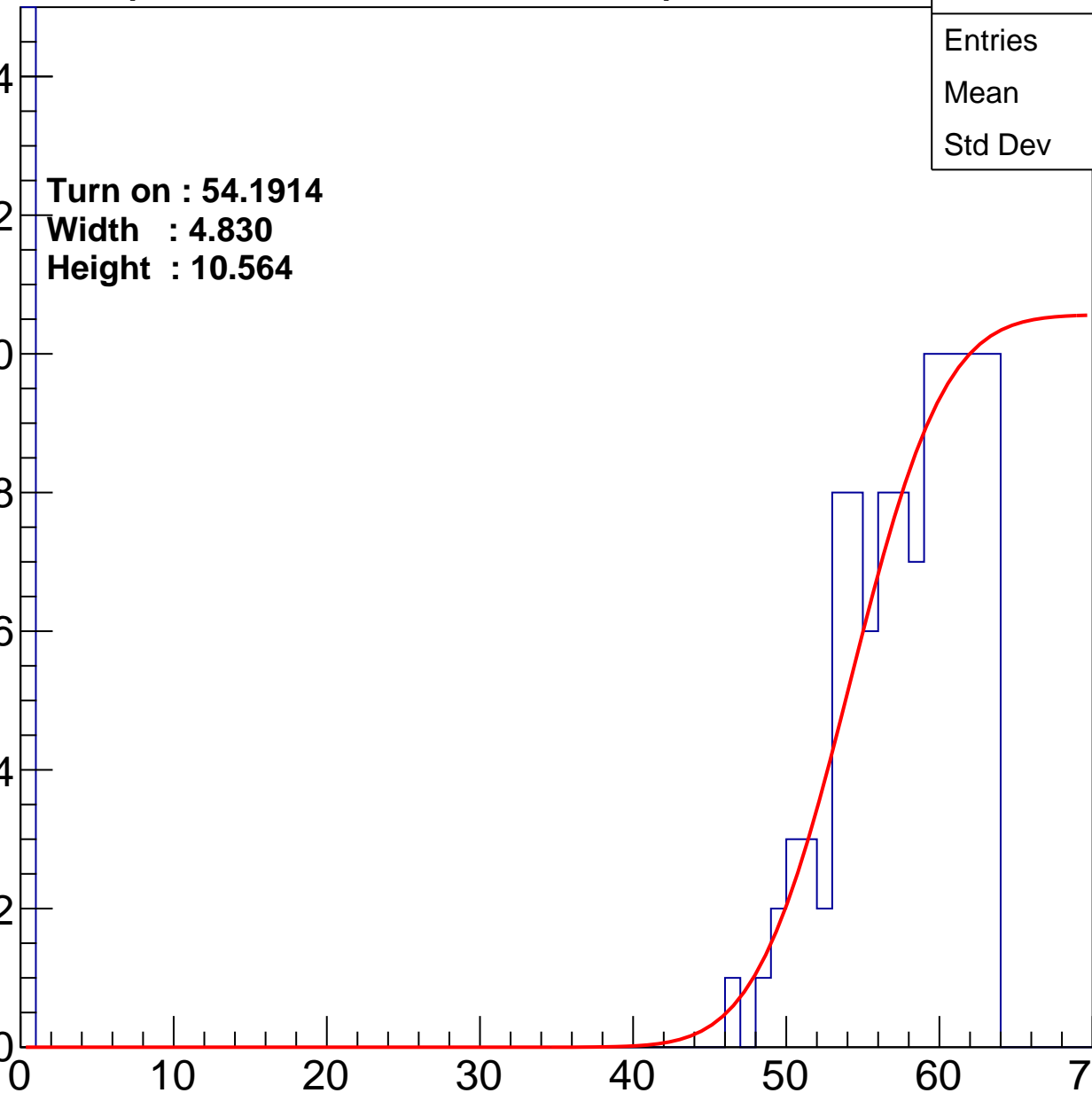
Width : 4.830

Height : 10.564

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	29.67
Std Dev	29.17

Turn on : 53.4606

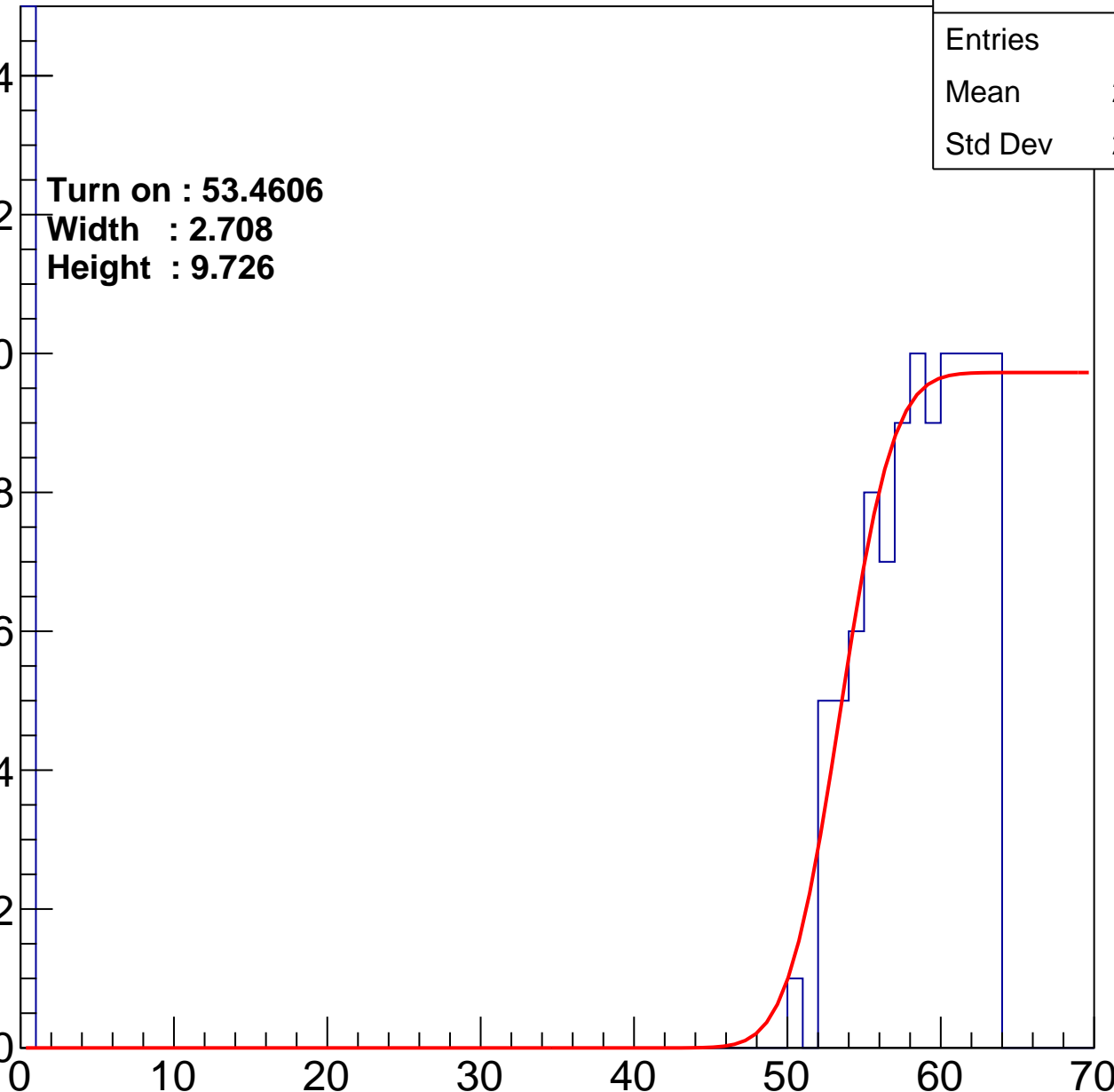
Width : 2.708

Height : 9.726

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	27.9
Std Dev	28.85

Turn on : 55.2781

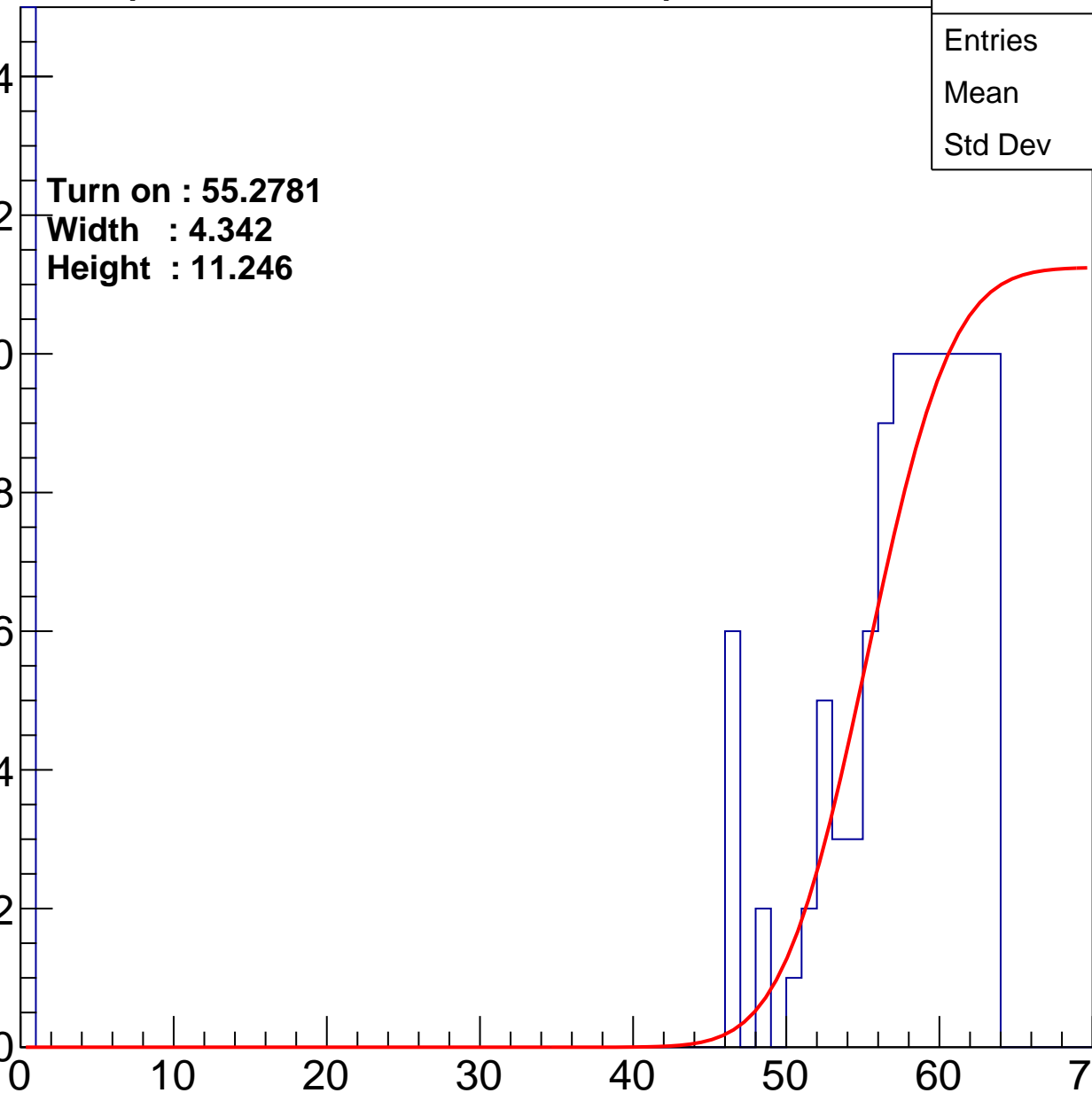
Width : 4.342

Height : 11.246

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch17

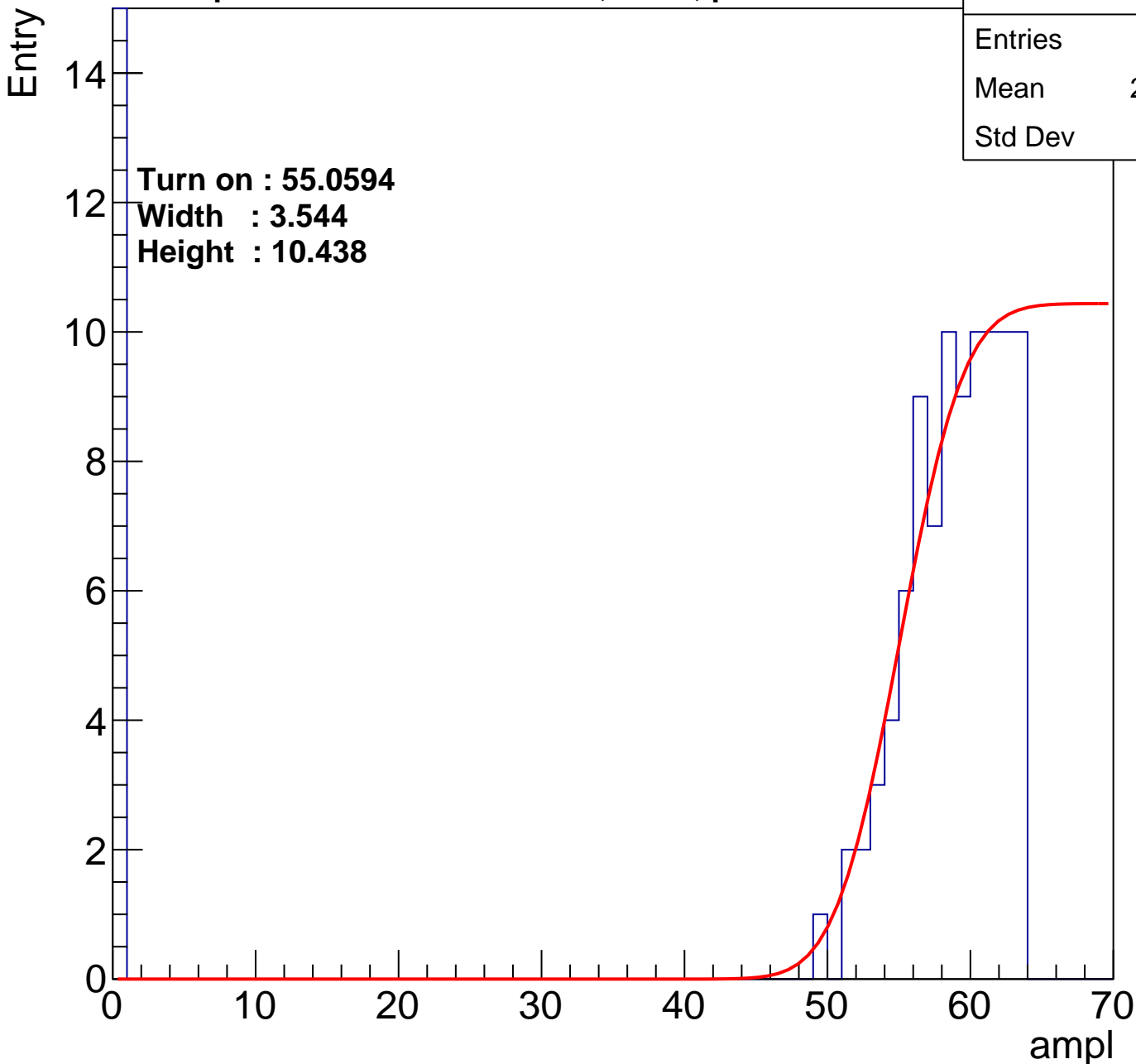
calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	29.86
Std Dev	29.3

Turn on : 55.0594

Width : 3.544

Height : 10.438



B1L104S, U19-ch18

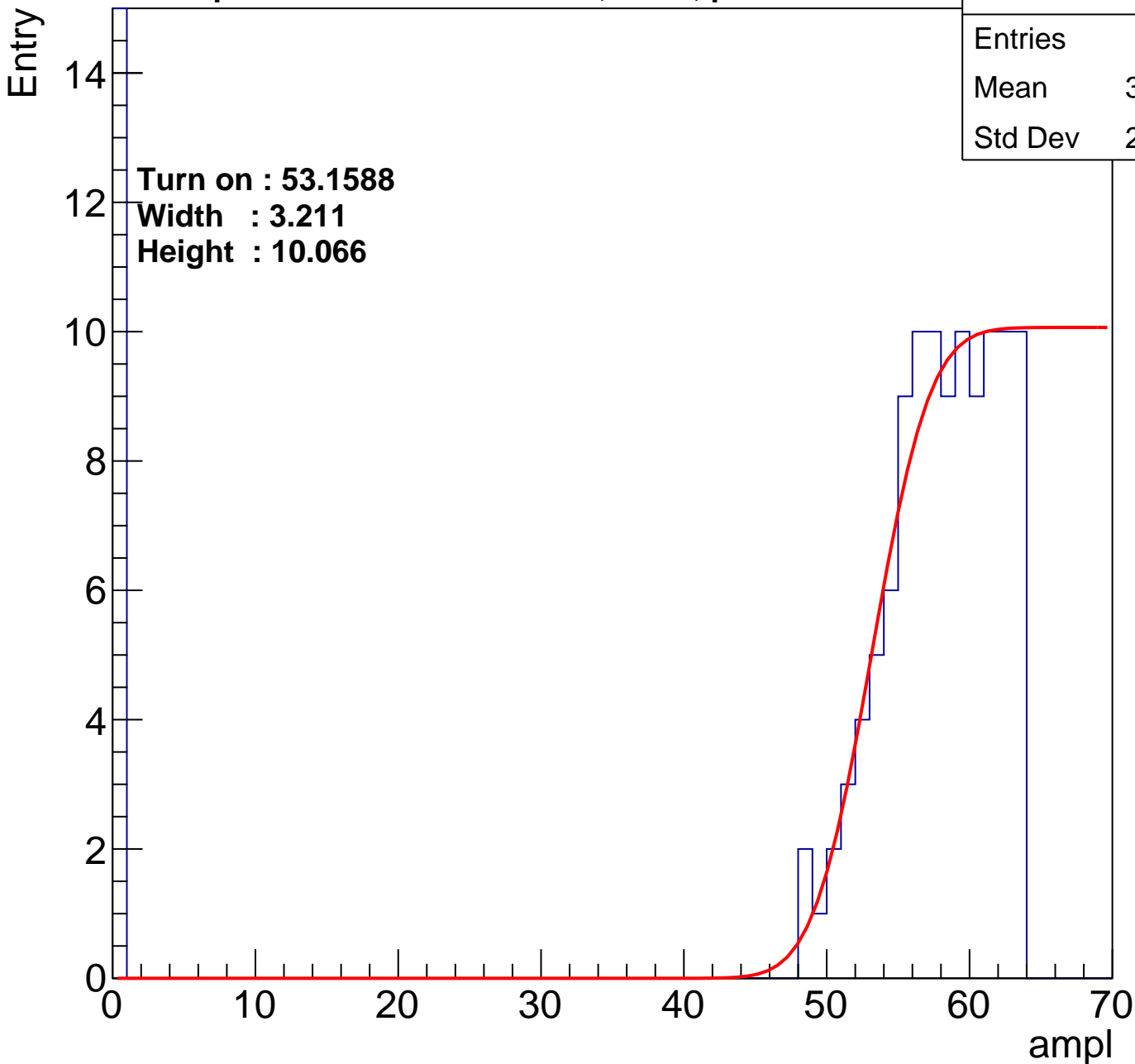
calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	37.03
Std Dev	27.74

Turn on : 53.1588

Width : 3.211

Height : 10.066



B1L104S, U19-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	29.83
Std Dev	29.28

Turn on : 54.9752

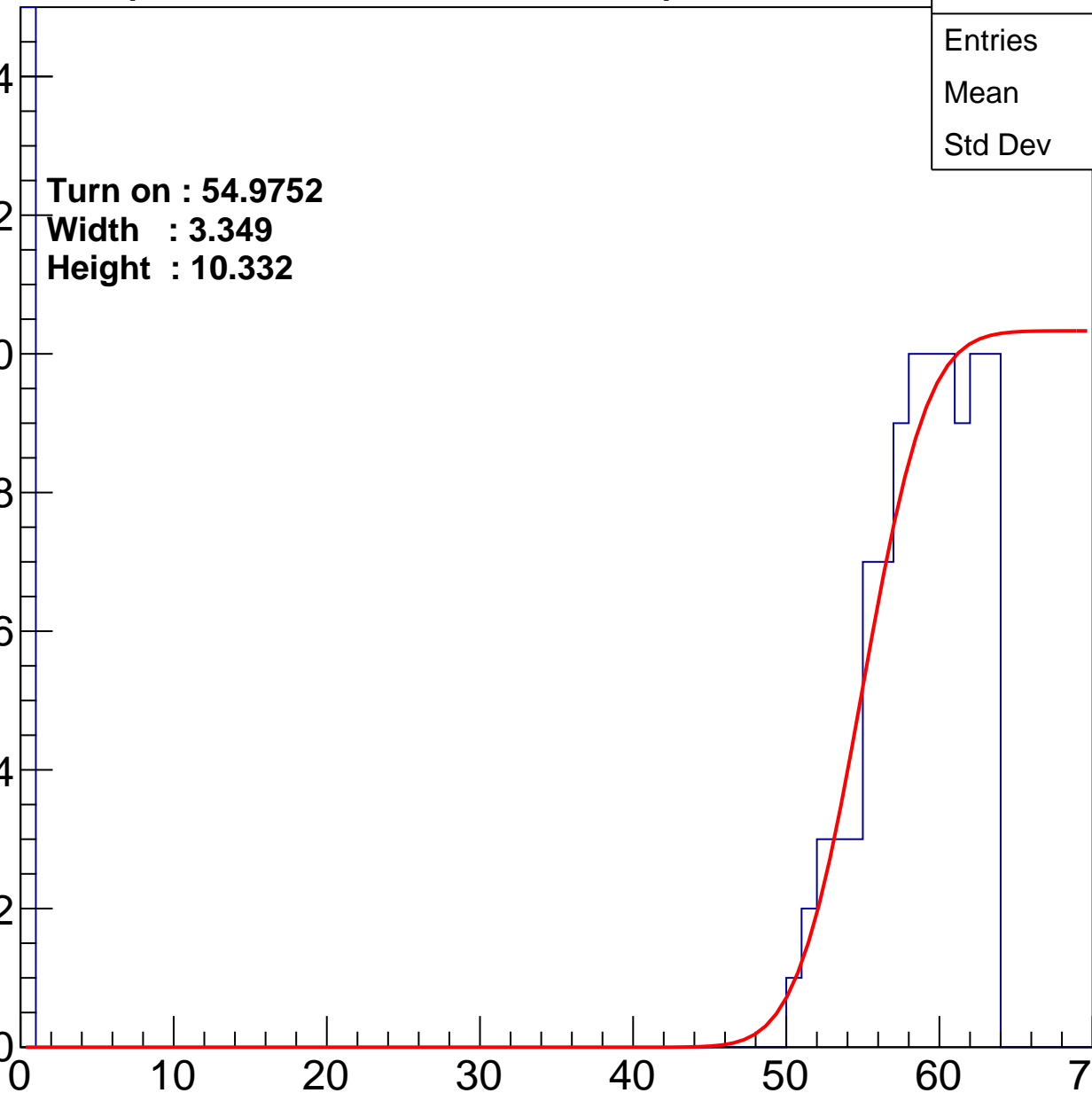
Width : 3.349

Height : 10.332

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	35.02
Std Dev	28.4

Turn on : 54.8205

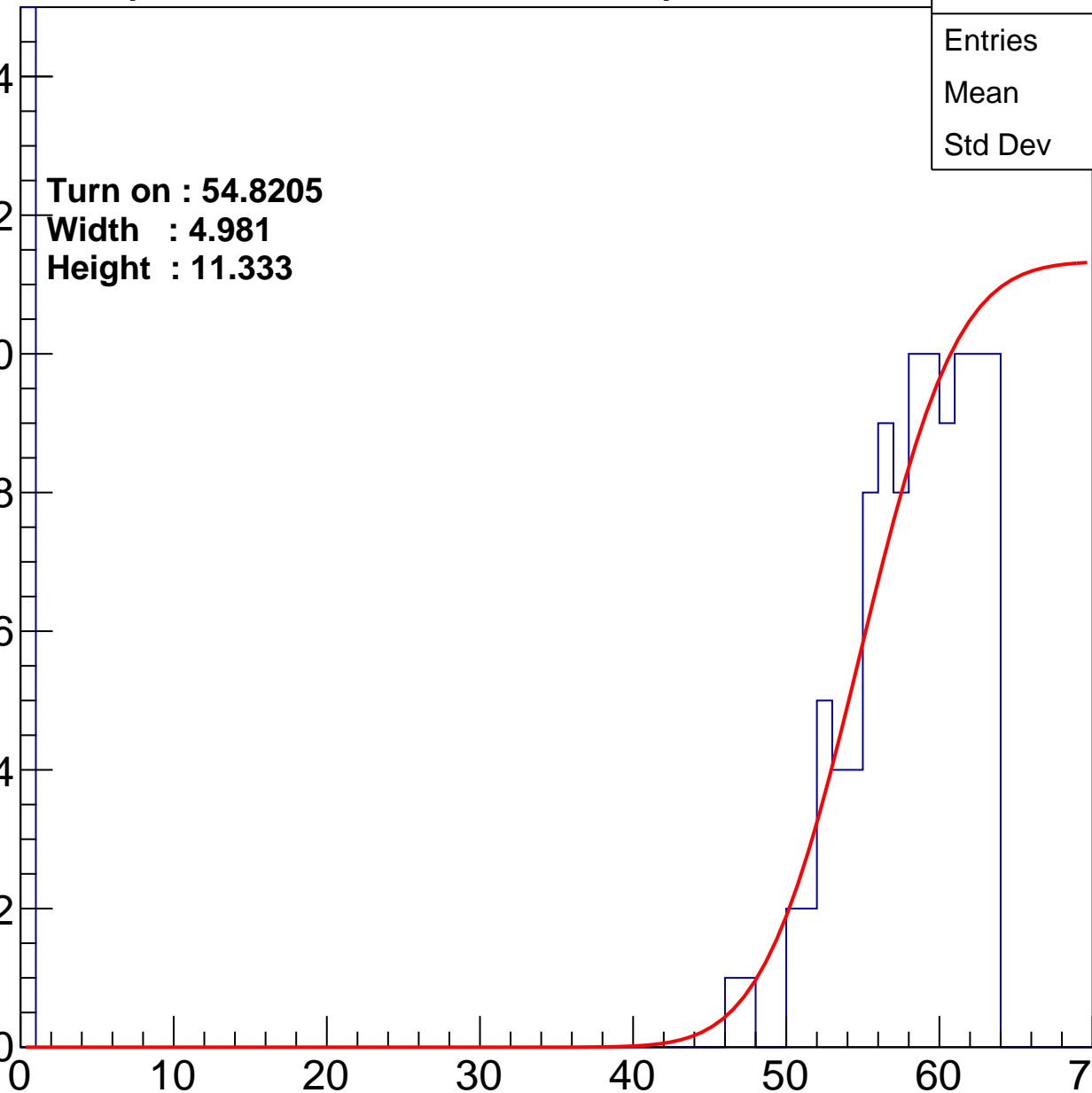
Width : 4.981

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	31.17
Std Dev	29.23

Turn on : 54.5011

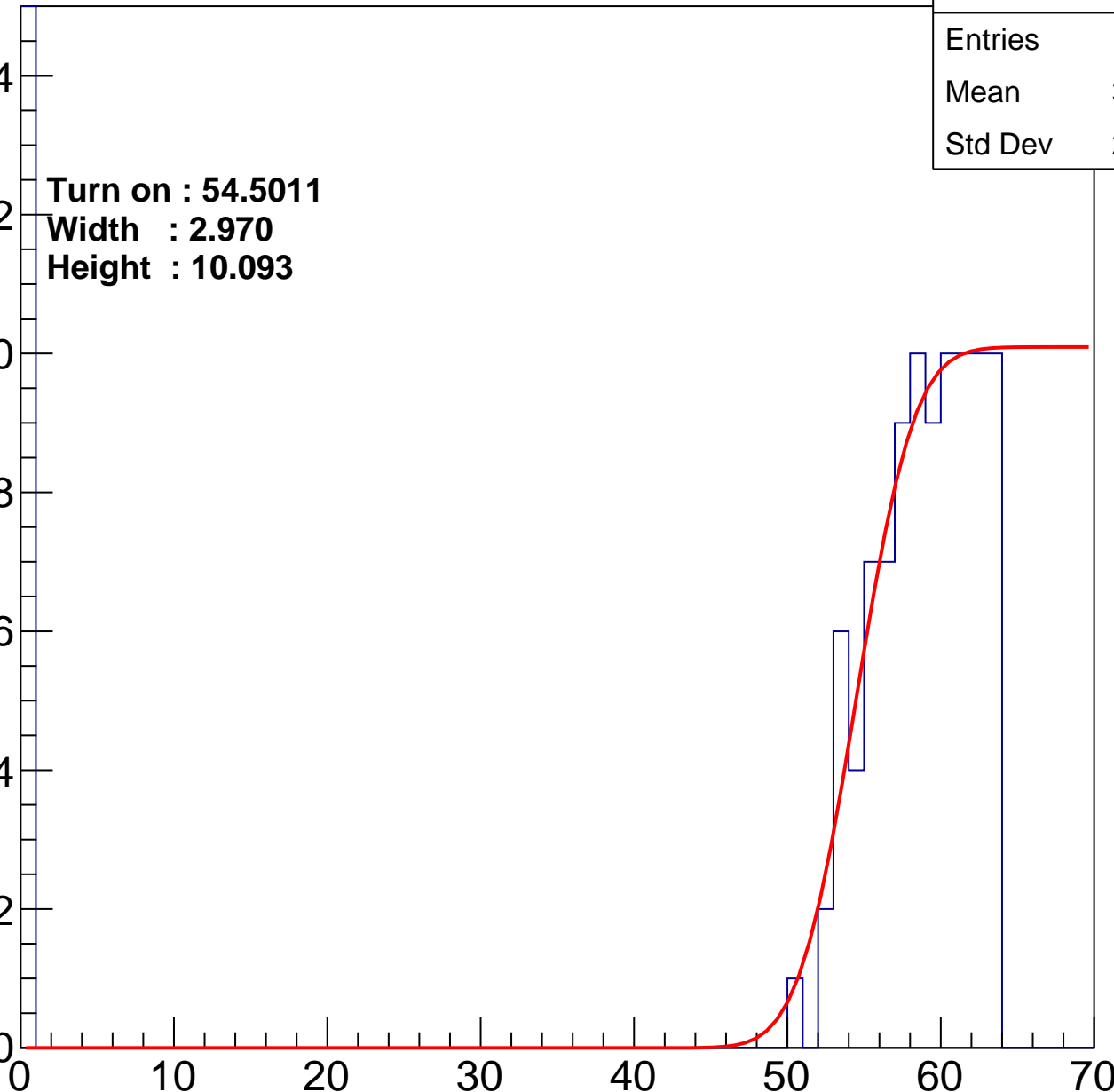
Width : 2.970

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.61
Std Dev	28.41

Turn on : 52.9768

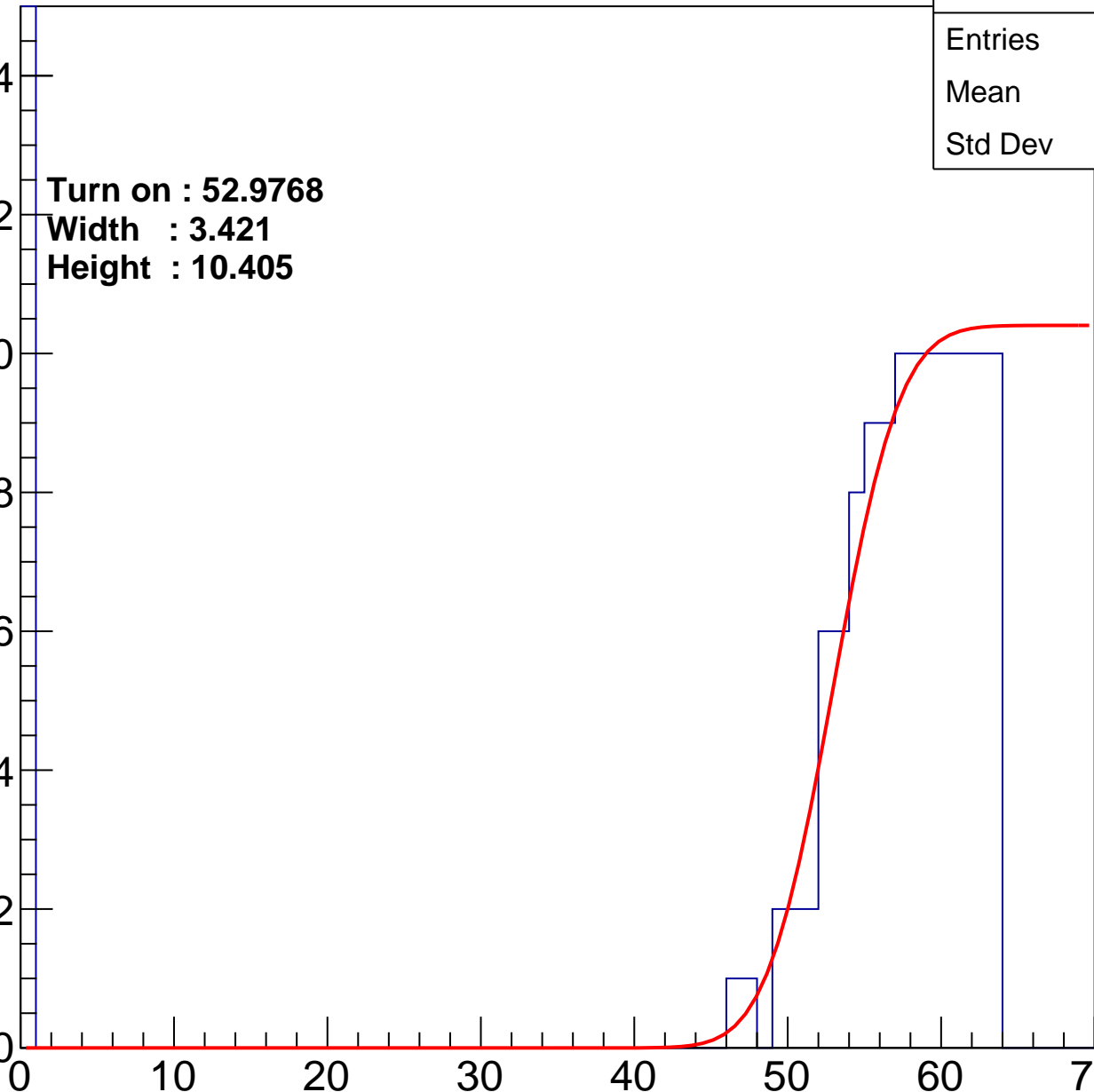
Width : 3.421

Height : 10.405

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	31.22
Std Dev	29.05

Turn on : 54.2058

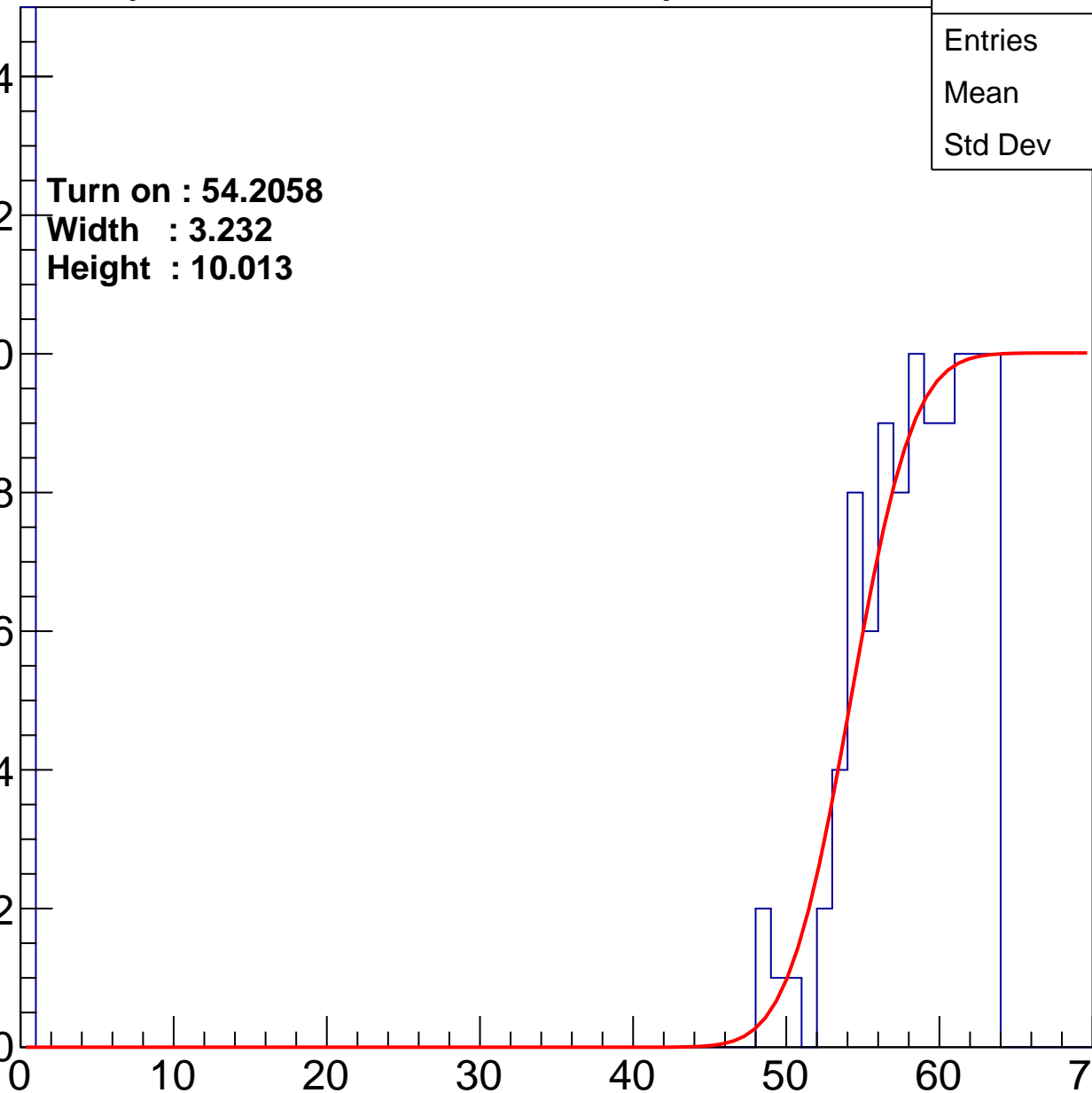
Width : 3.232

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	33.21
Std Dev	28.36

Turn on : 52.7922

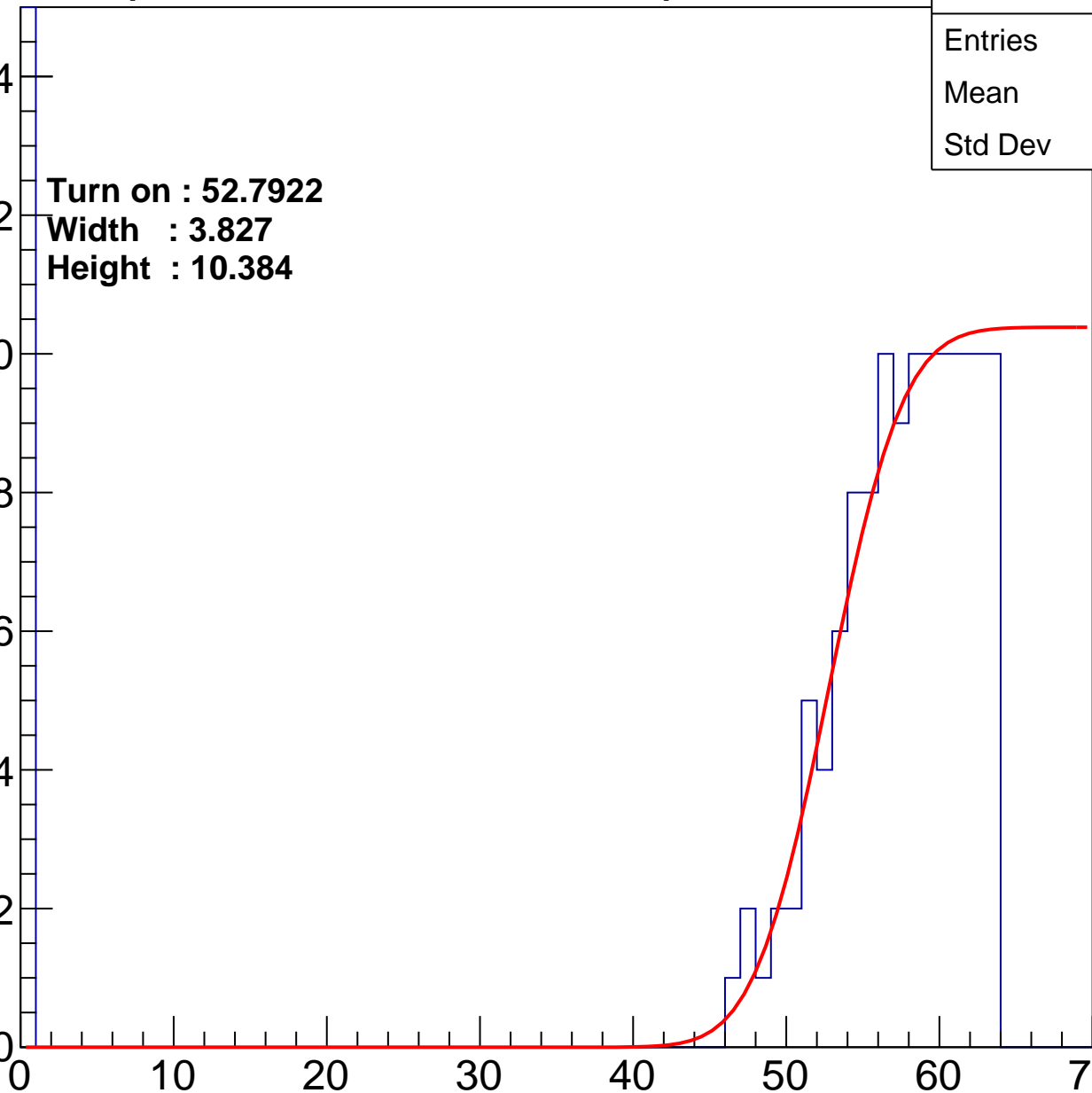
Width : 3.827

Height : 10.384

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch25

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	29.11
Std Dev	29.08

Turn on : 53.3869

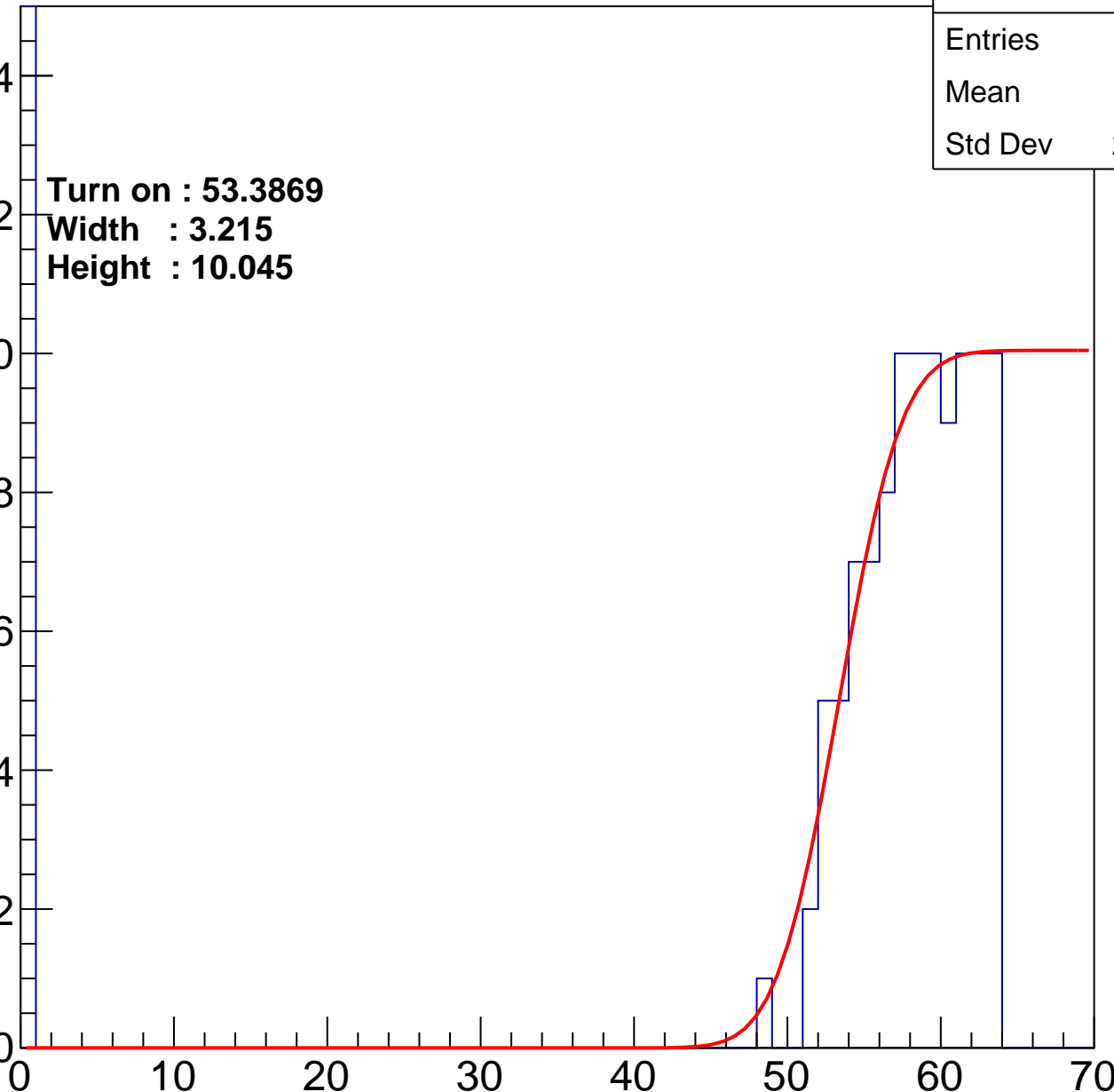
Width : 3.215

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	30
Std Dev	28.97

Turn on : 53.4936

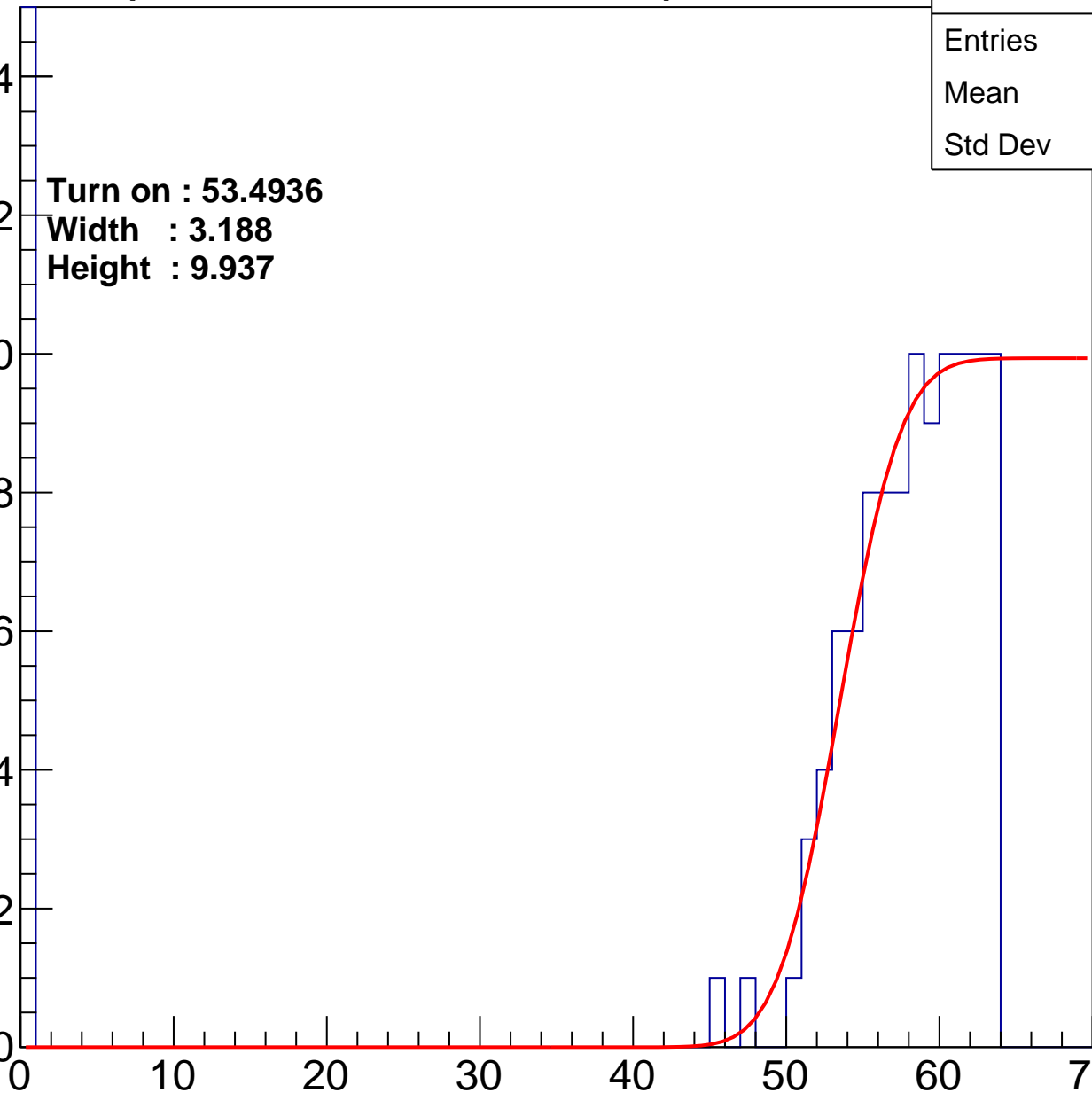
Width : 3.188

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	37.74
Std Dev	27.62

Turn on : 53.7893

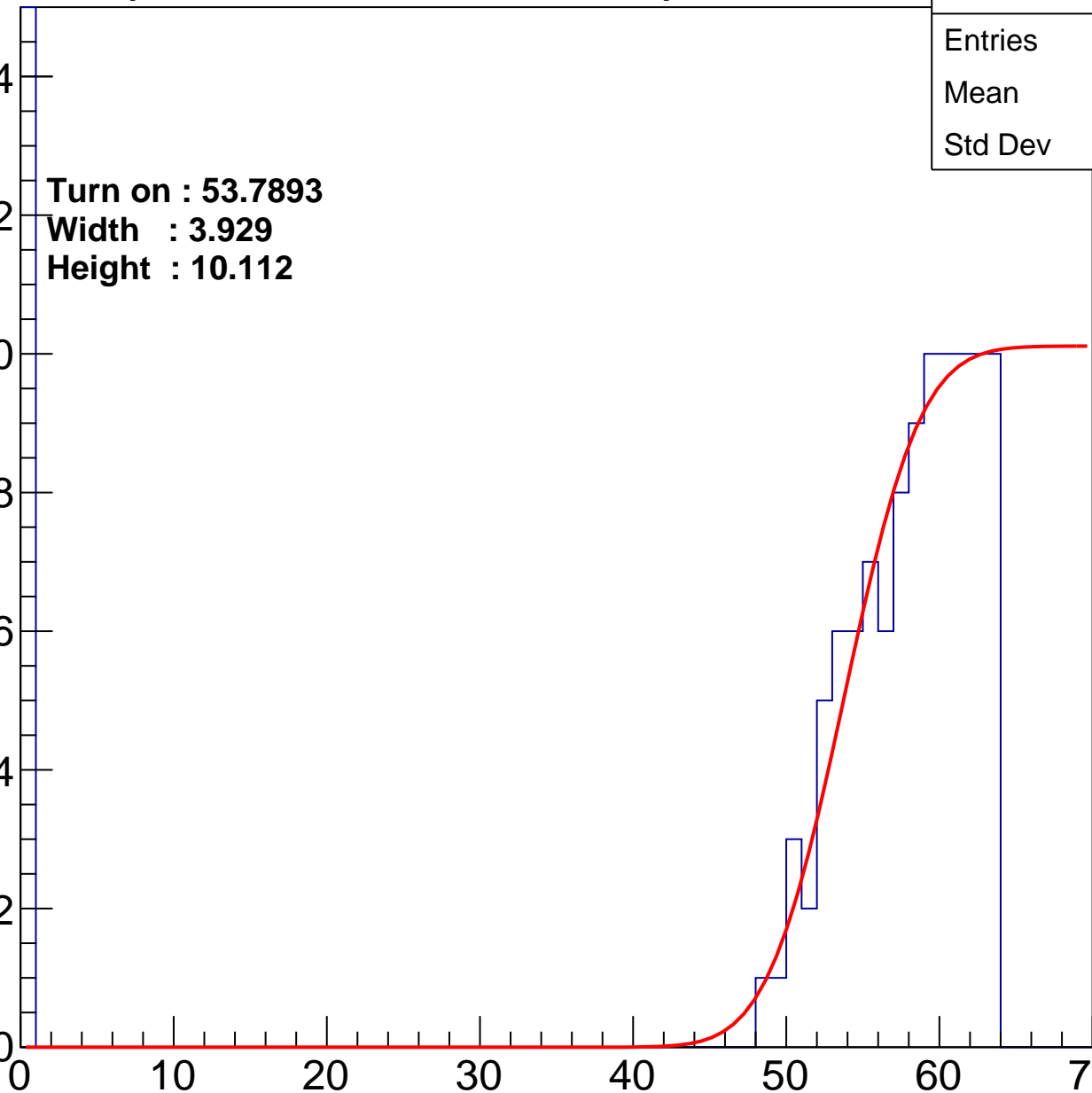
Width : 3.929

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	34.41
Std Dev	28.15

Turn on : 52.2733

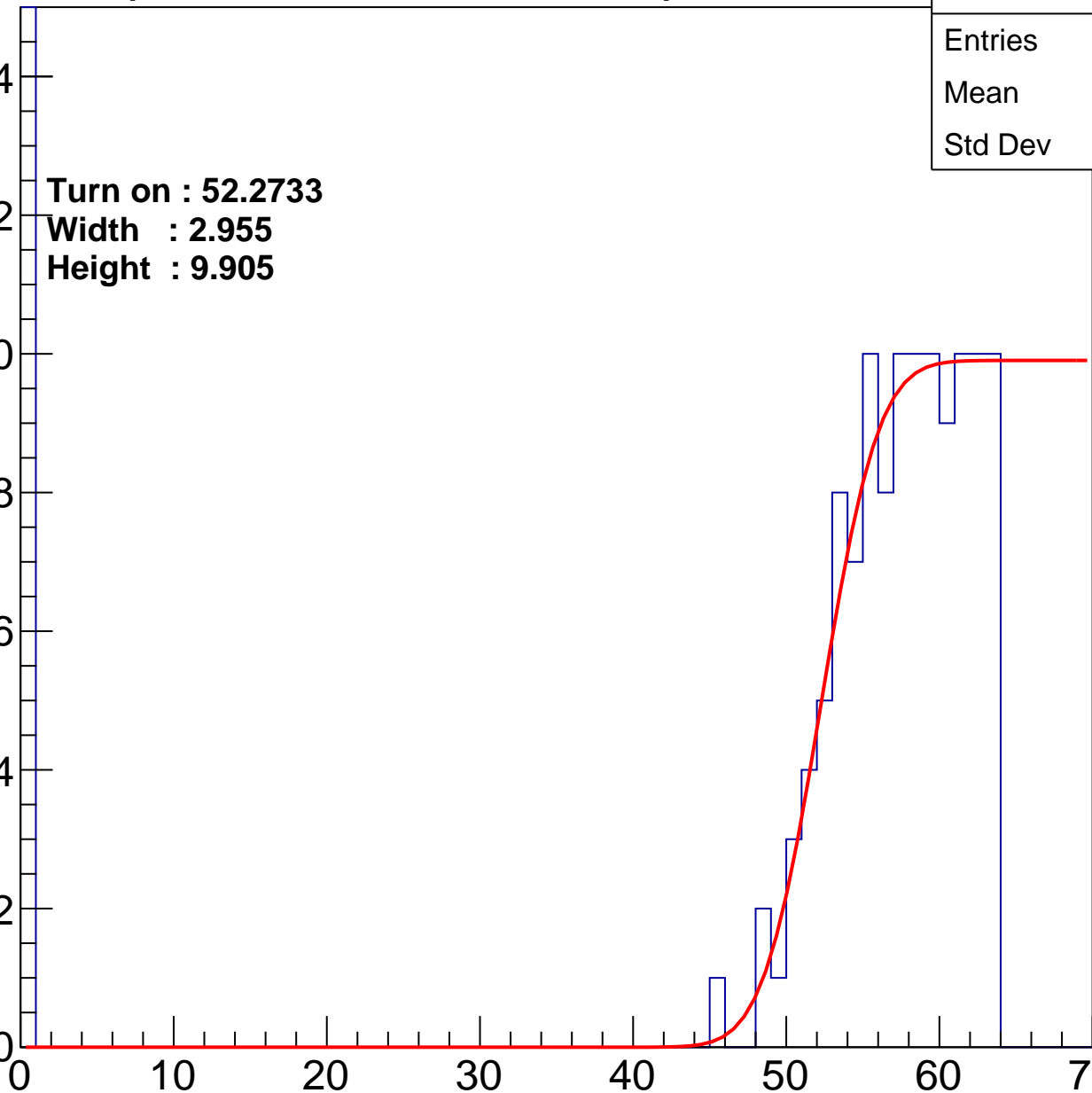
Width : 2.955

Height : 9.905

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	30.58
Std Dev	29.33

Turn on : 54.4148

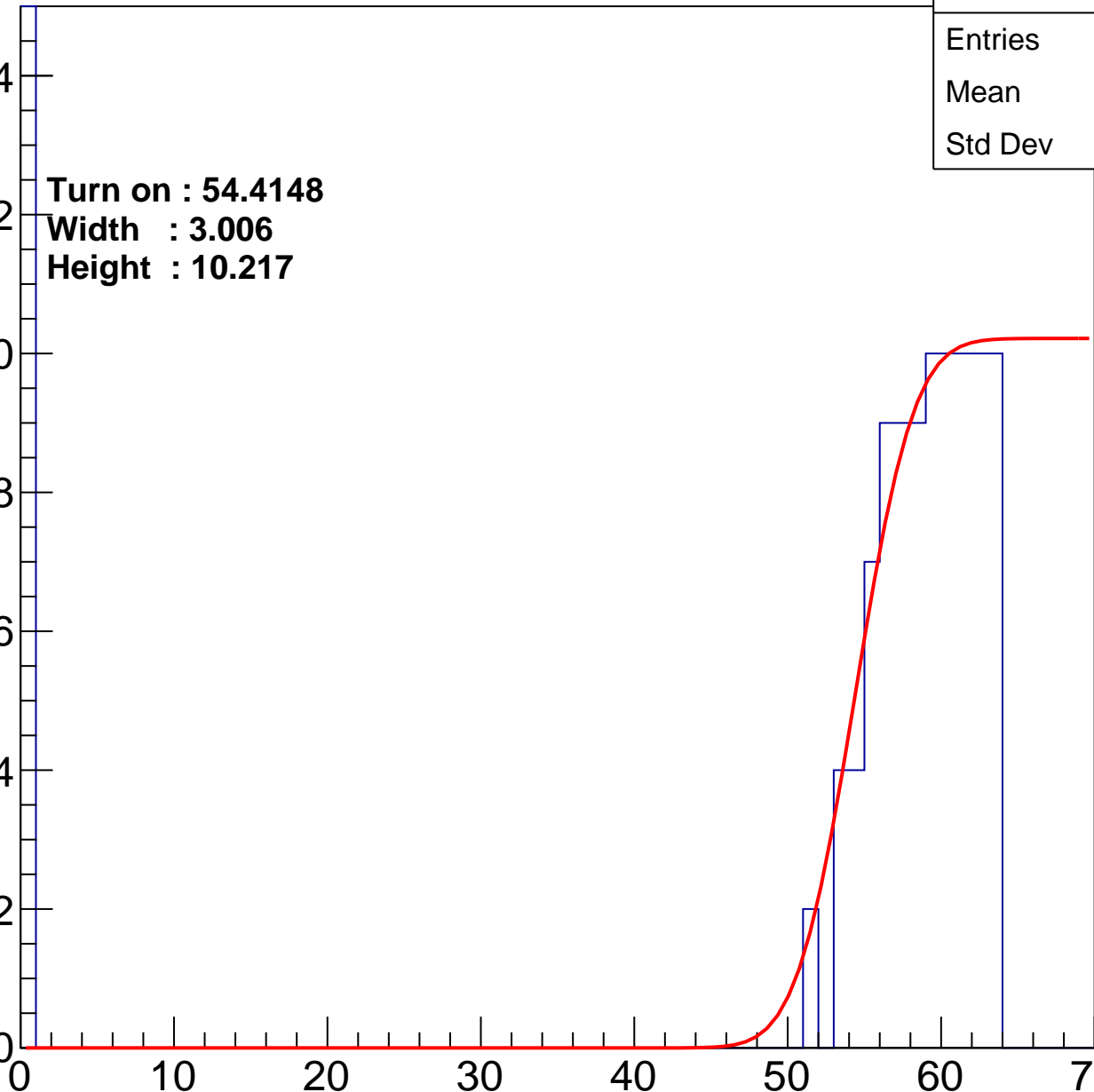
Width : 3.006

Height : 10.217

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch30

calib_packv5_033123_0516.root, FC#4, port A1

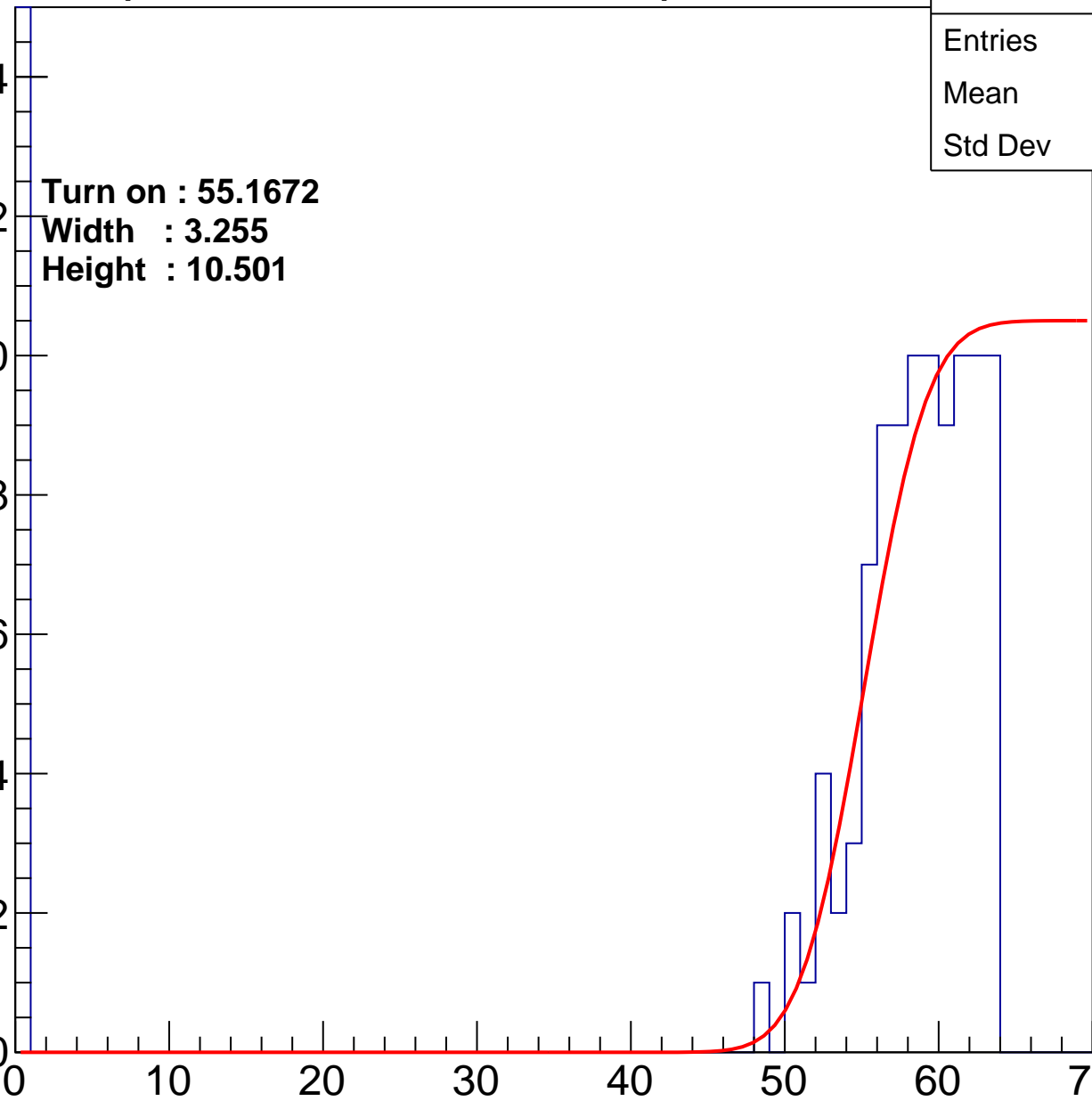
Entry

14
12
10
8
6
4
2
0

Turn on : 55.1672
Width : 3.255
Height : 10.501

Entries	166
Mean	34.02
Std Dev	28.81

ampl



B1L104S, U19-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.04
Std Dev	29.09

Turn on : 53.8436

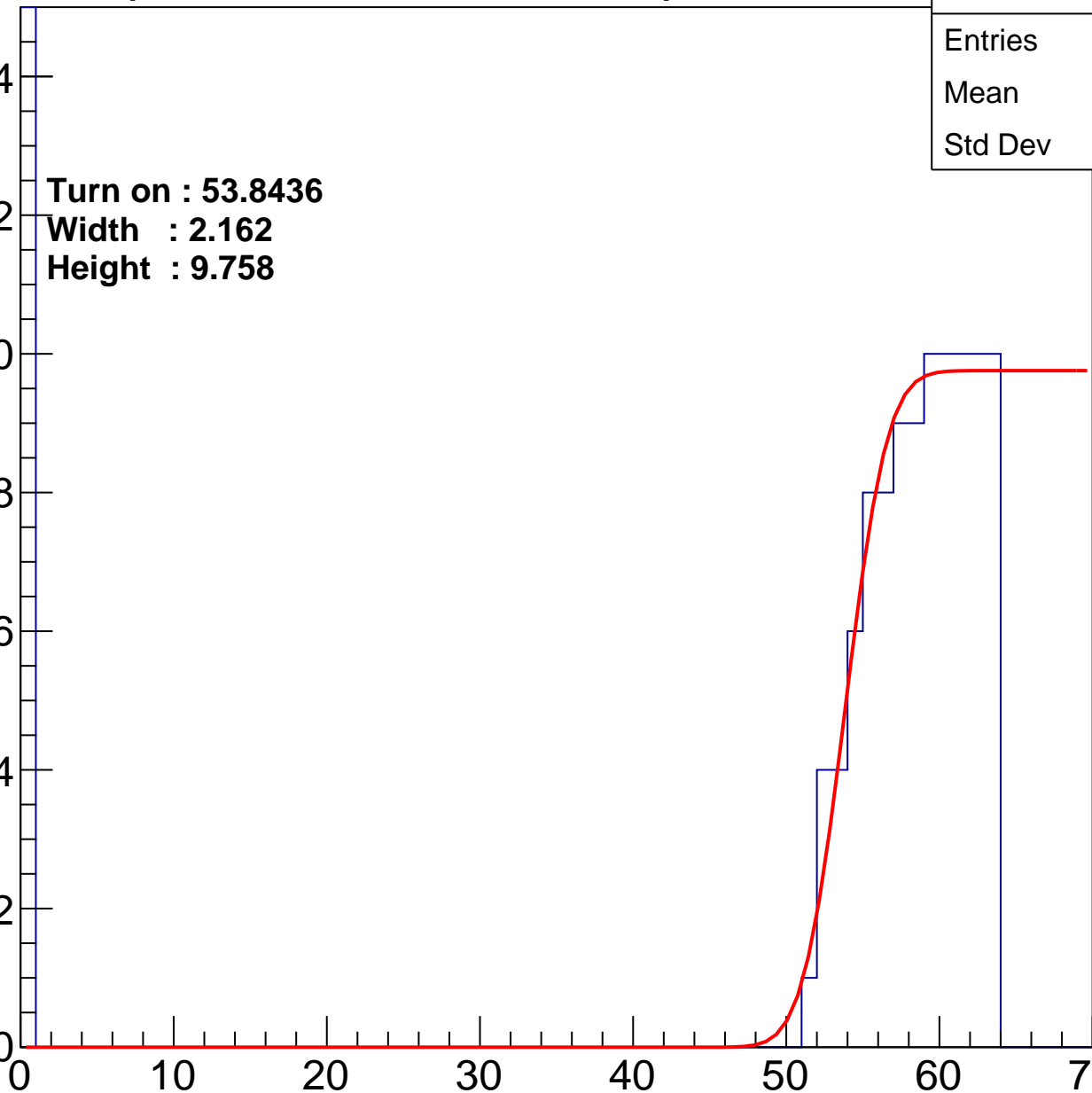
Width : 2.162

Height : 9.758

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch32

calib_packv5_033123_0516.root, FC#4, port A1

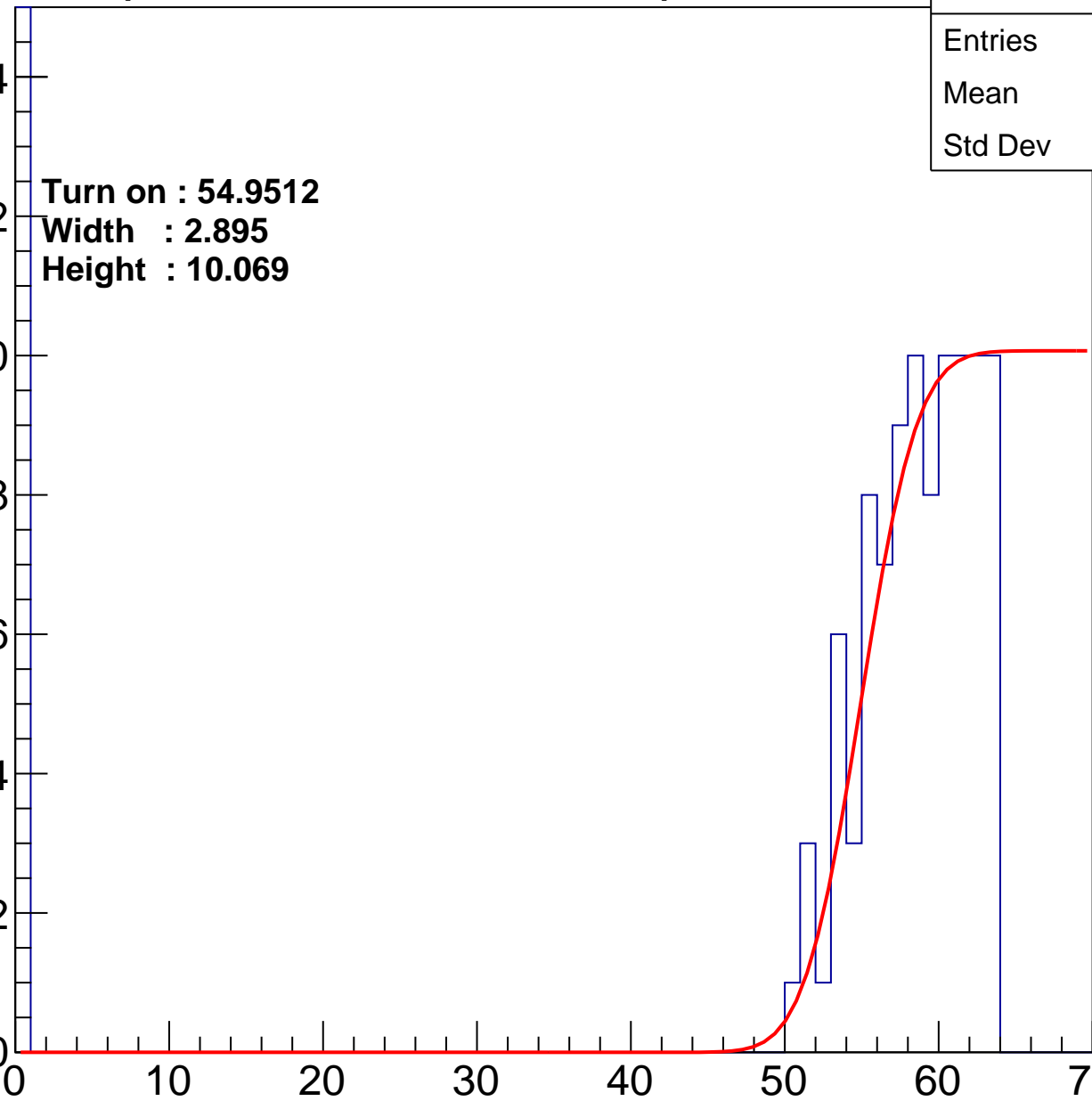
Entry

14
12
10
8
6
4
2
0

Turn on : 54.9512
Width : 2.895
Height : 10.069

Entries	172
Mean	32.51
Std Dev	29.04

ampl



B1L104S, U19-ch33

calib_packv5_033123_0516.root, FC#4, port A1

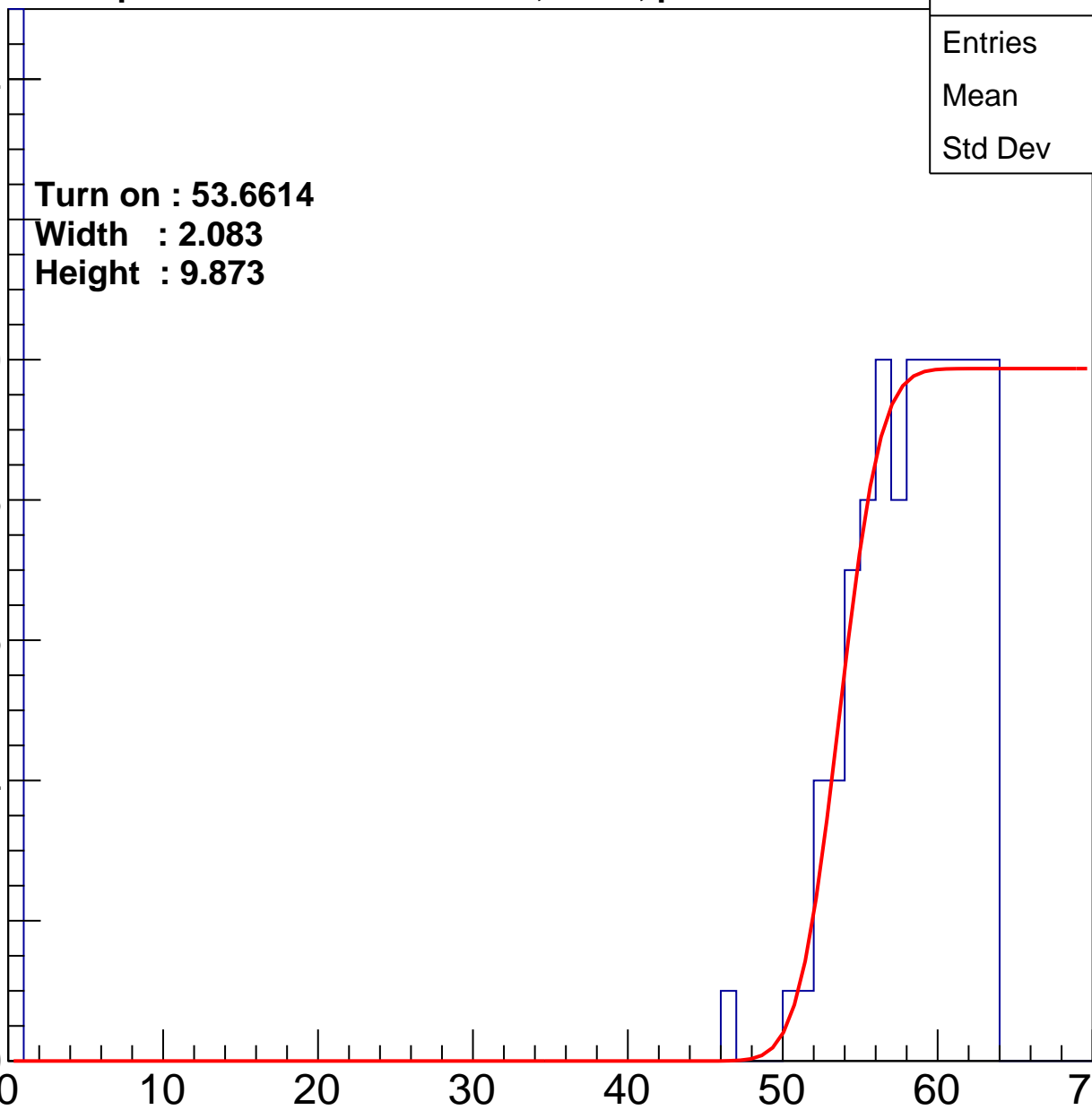
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6614
Width : 2.083
Height : 9.873

Entries	182
Mean	33.14
Std Dev	28.82

ampl



B1L104S, U19-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	34.4
Std Dev	28.25

Turn on : 52.3786

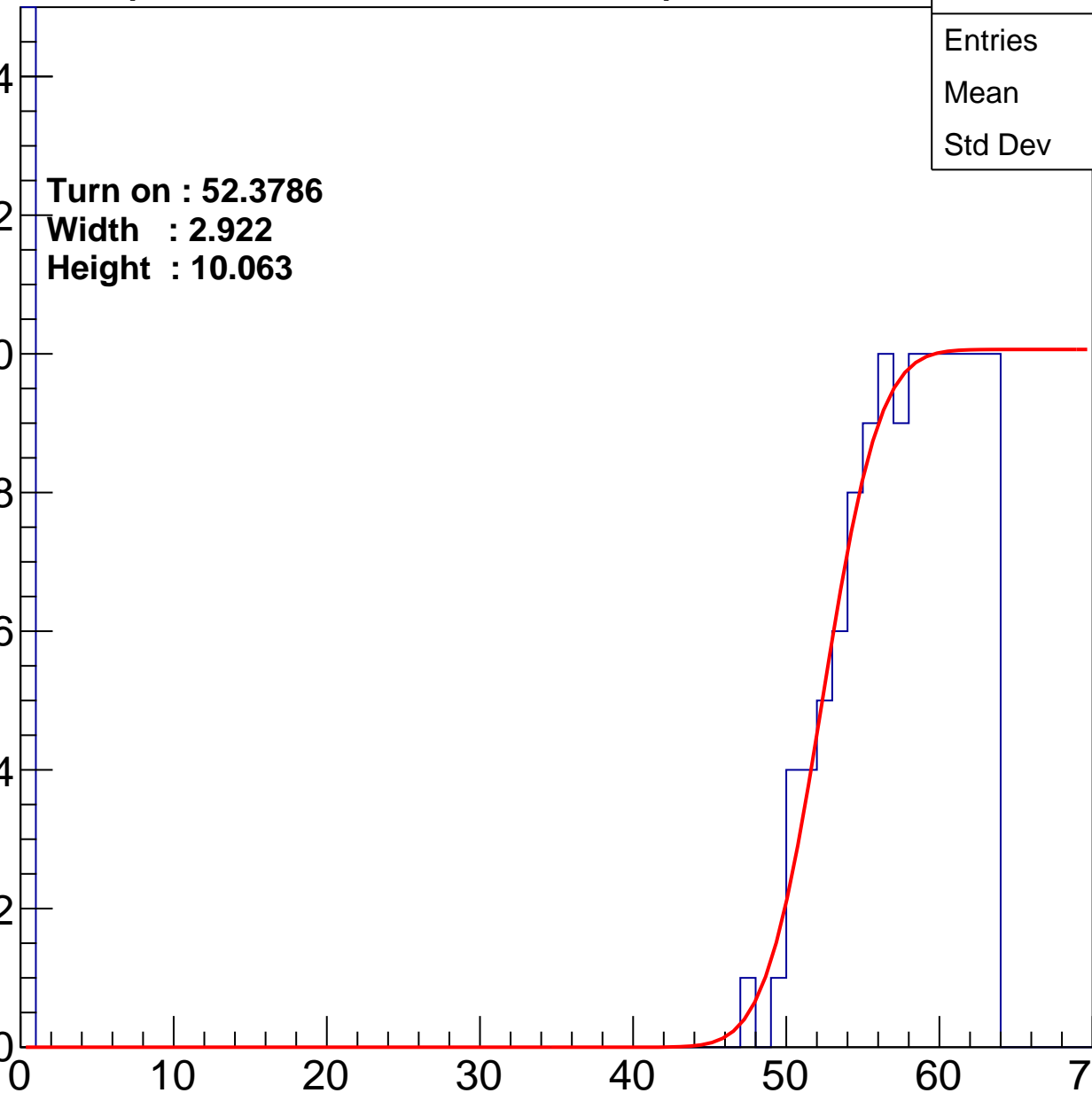
Width : 2.922

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	33.67
Std Dev	28.63

Turn on : 53.1157

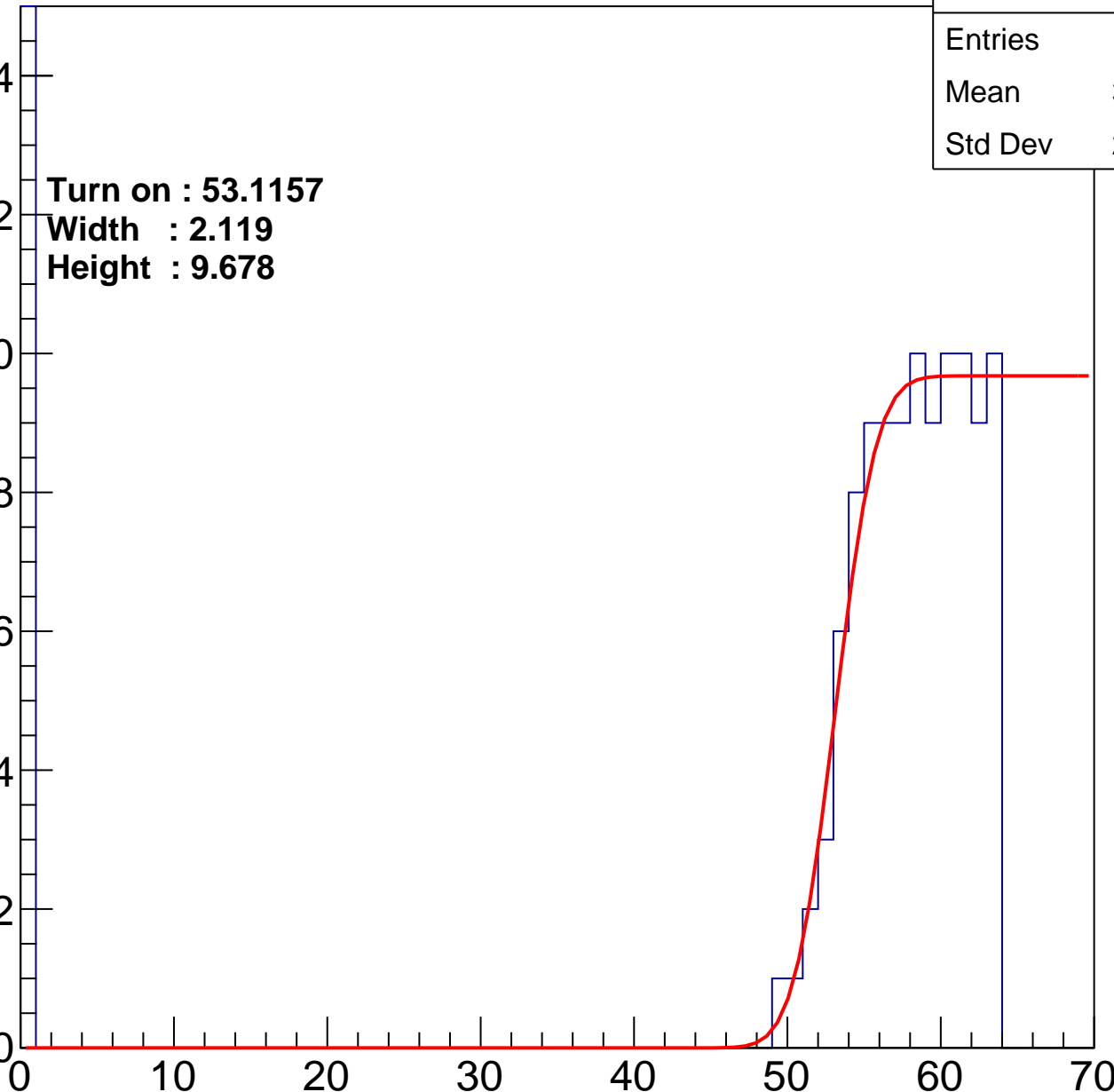
Width : 2.119

Height : 9.678

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	29.37
Std Dev	29.06

Turn on : 54.4033

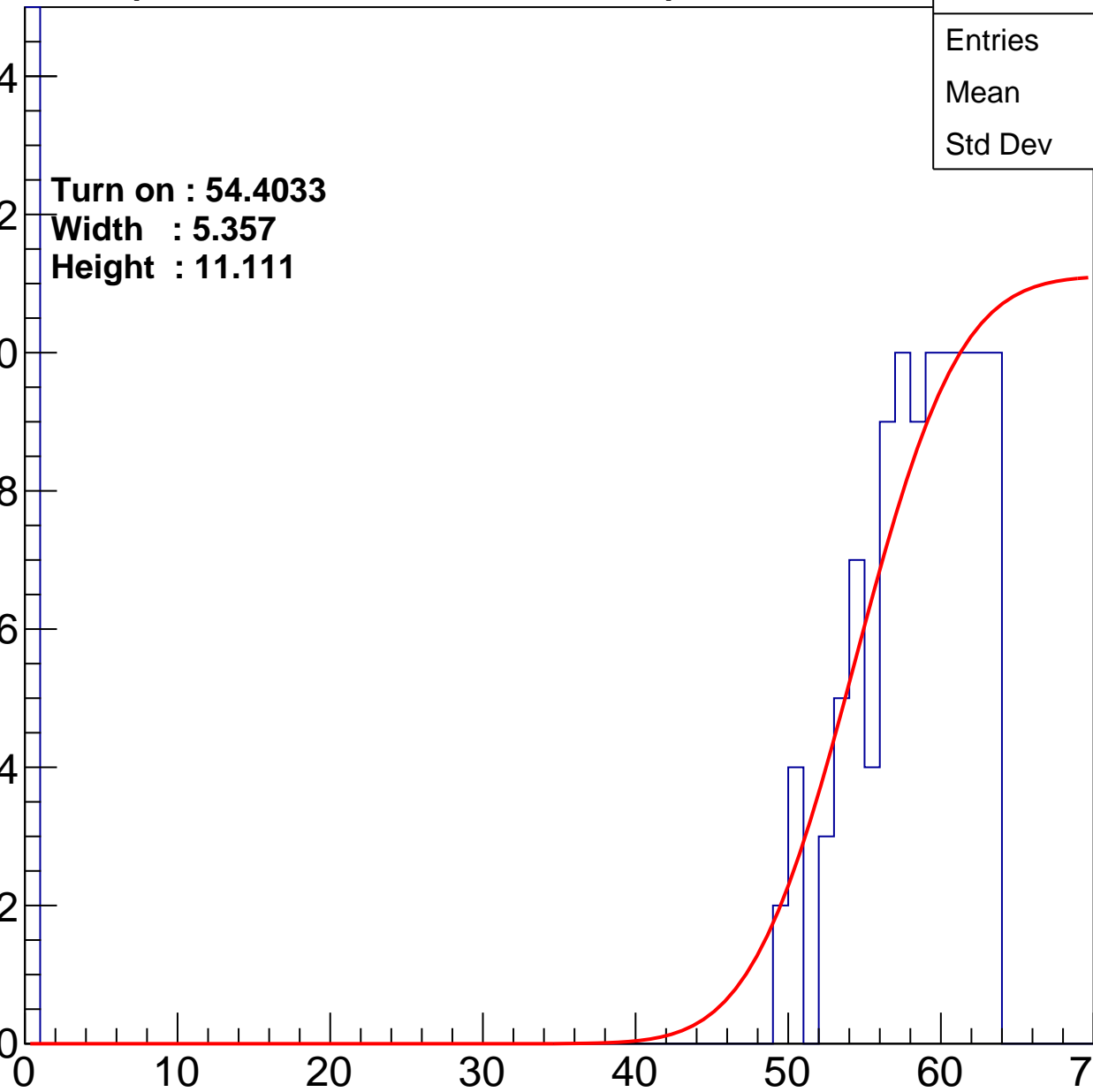
Width : 5.357

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch37

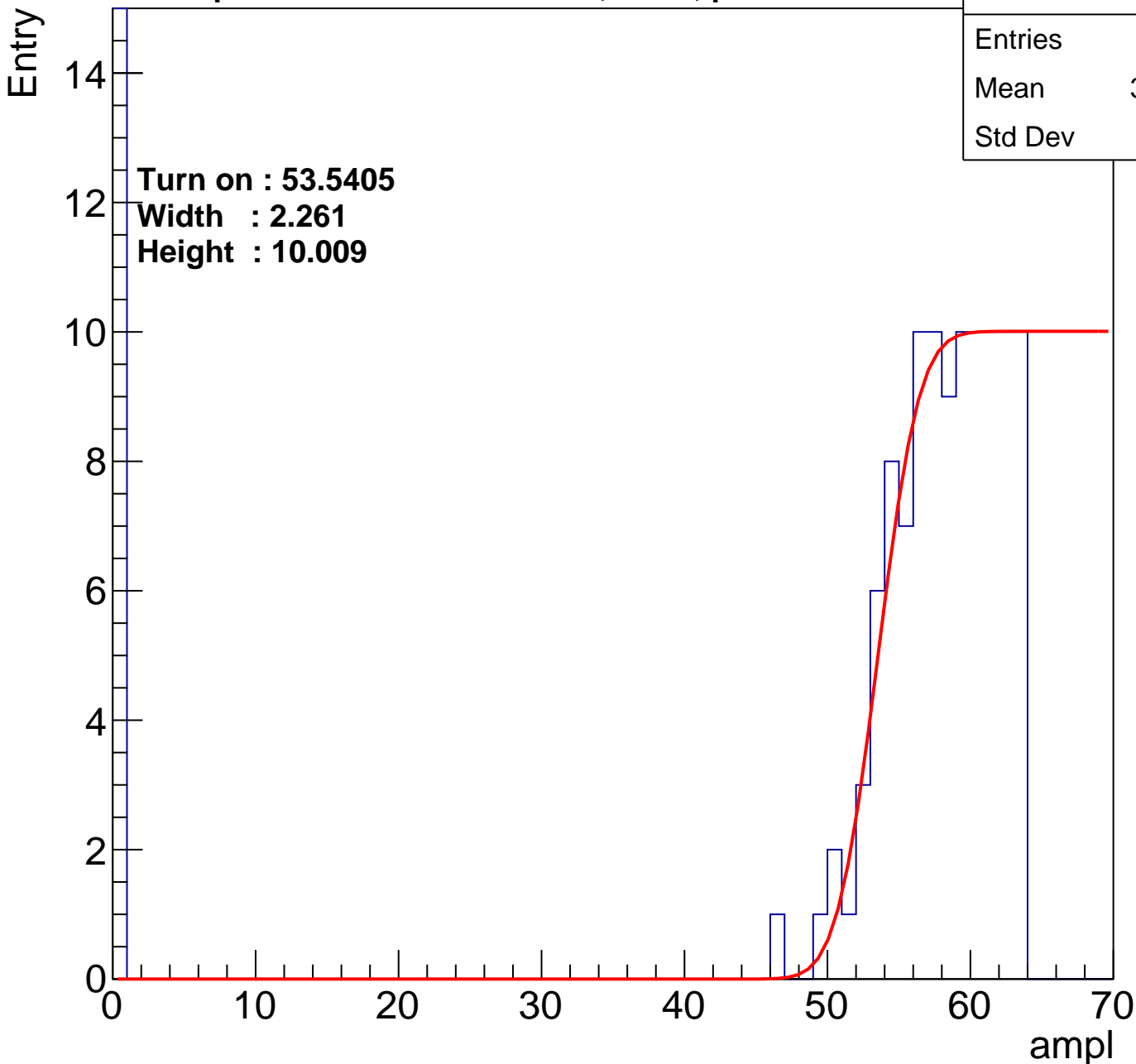
calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	31.35
Std Dev	28.9

Turn on : 53.5405

Width : 2.261

Height : 10.009



B1L104S, U19-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	33.34
Std Dev	28.73

Turn on : 54.3604

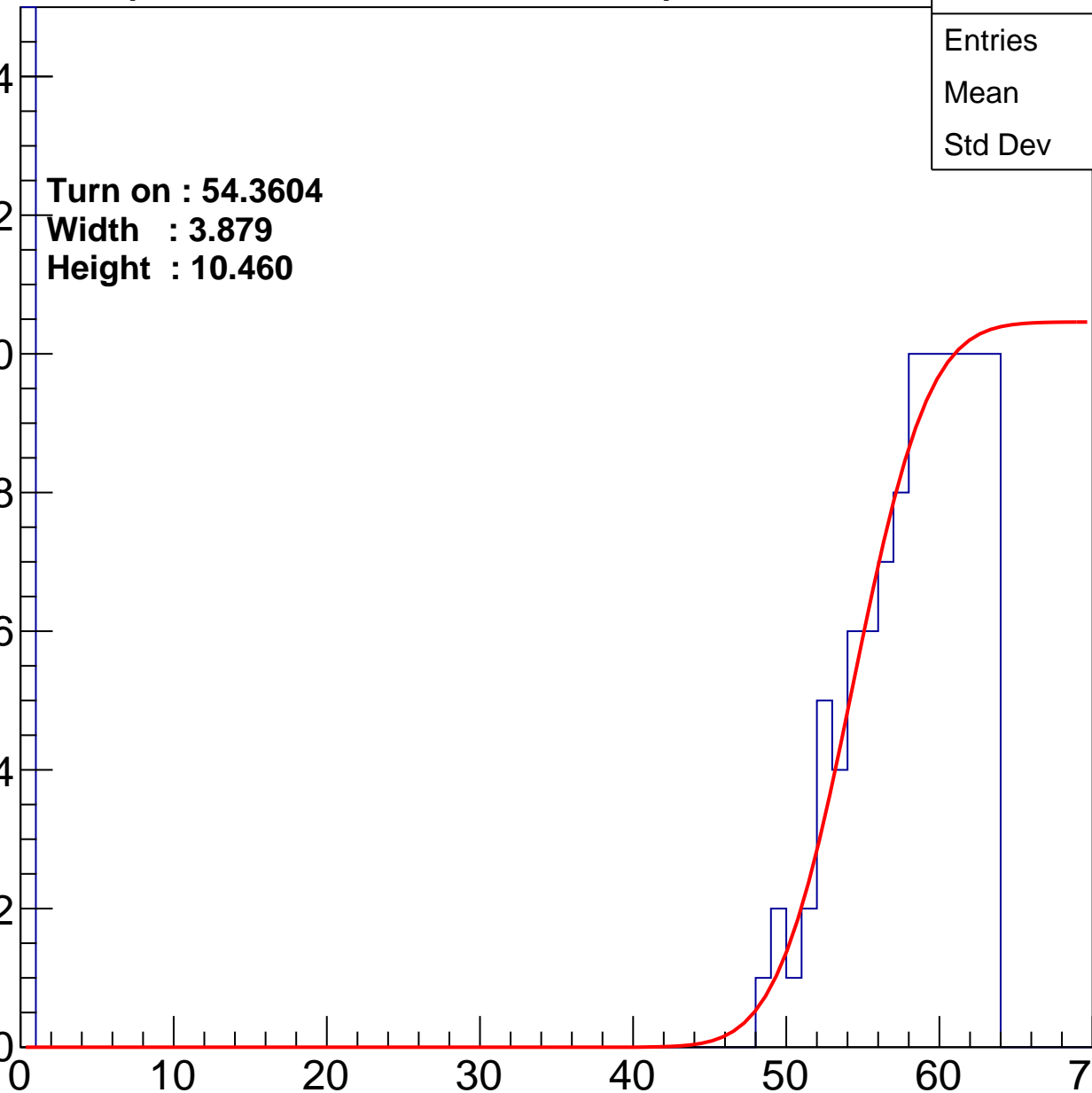
Width : 3.879

Height : 10.460

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	35.64
Std Dev	27.77

Turn on : 52.3232

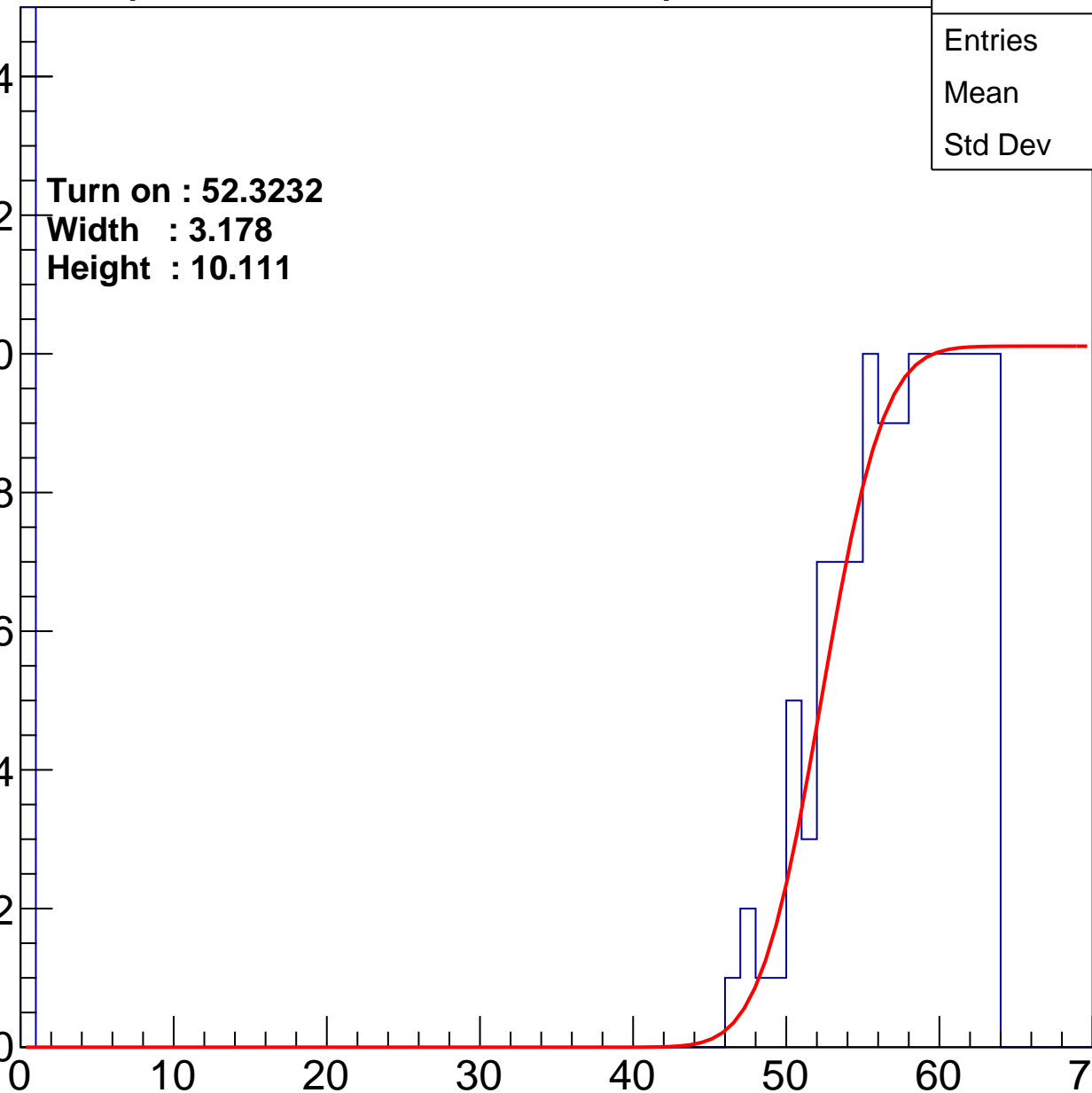
Width : 3.178

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	34.53
Std Dev	28.73

Turn on : 54.0371

Width : 2.073

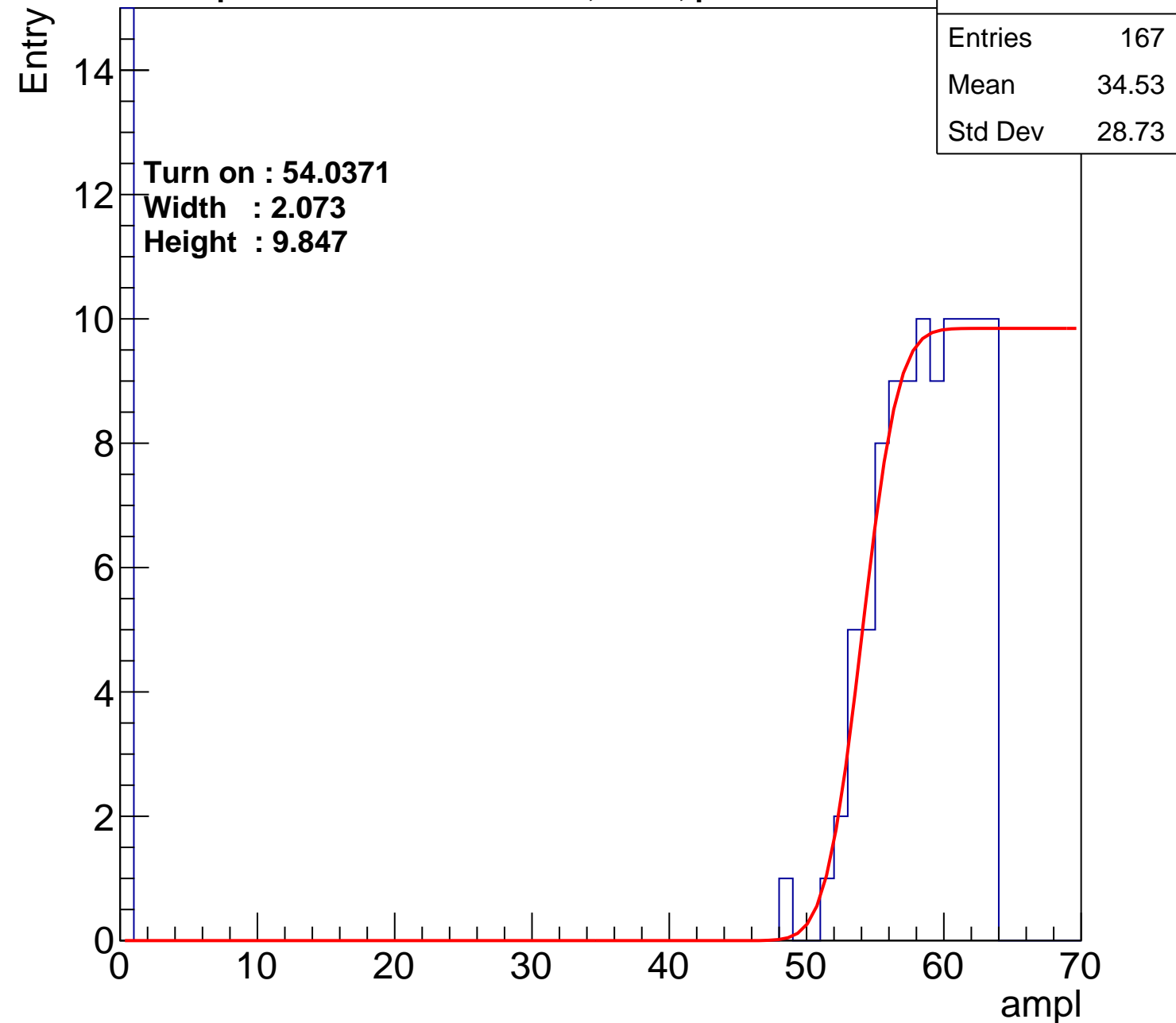
Height : 9.847

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U19-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	30.74
Std Dev	29.41

Turn on : 54.9101

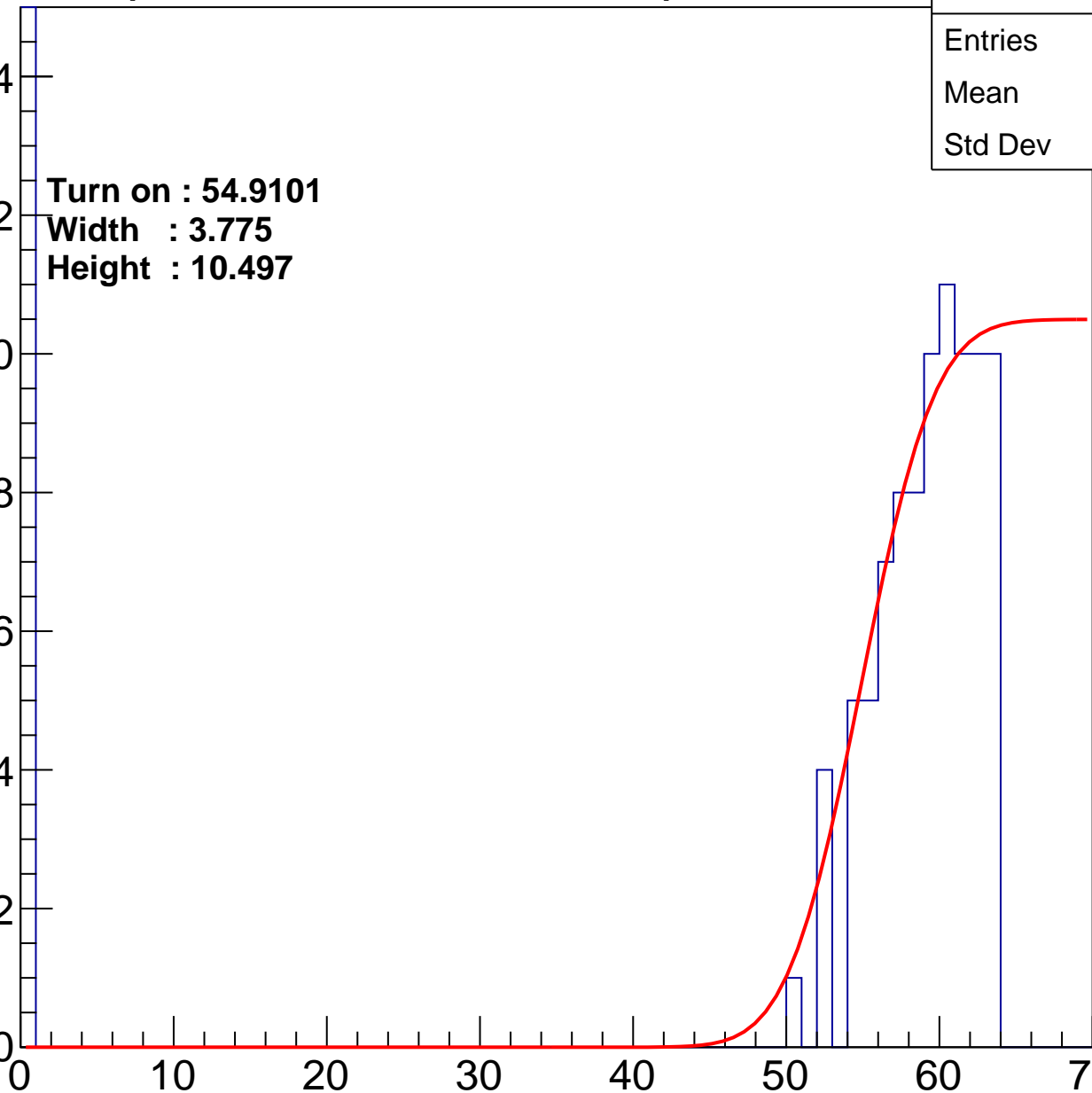
Width : 3.775

Height : 10.497

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	30.33
Std Dev	29.27

Turn on : 55.2985

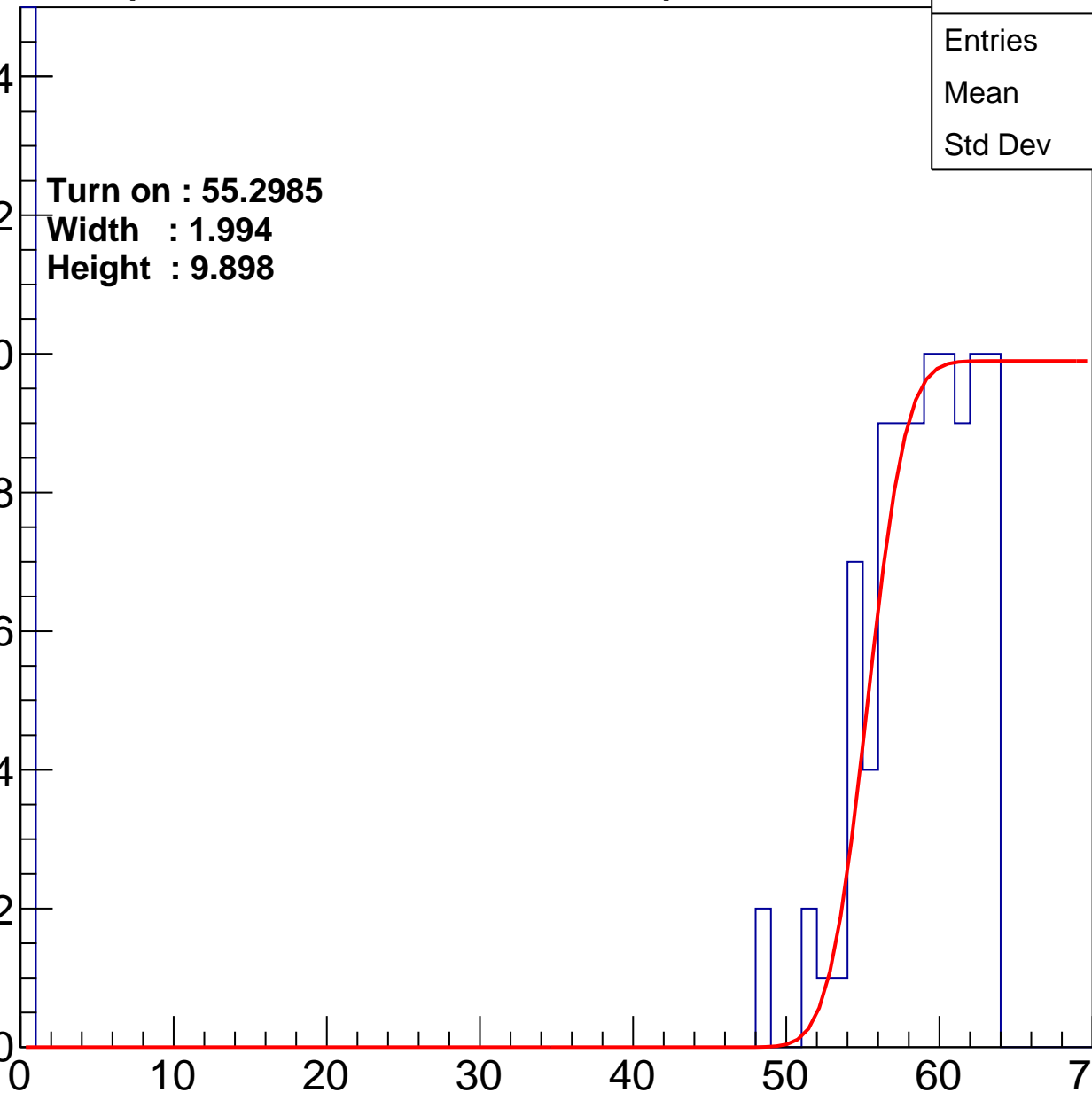
Width : 1.994

Height : 9.898

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	33.49
Std Dev	28.54

Turn on : 52.8824

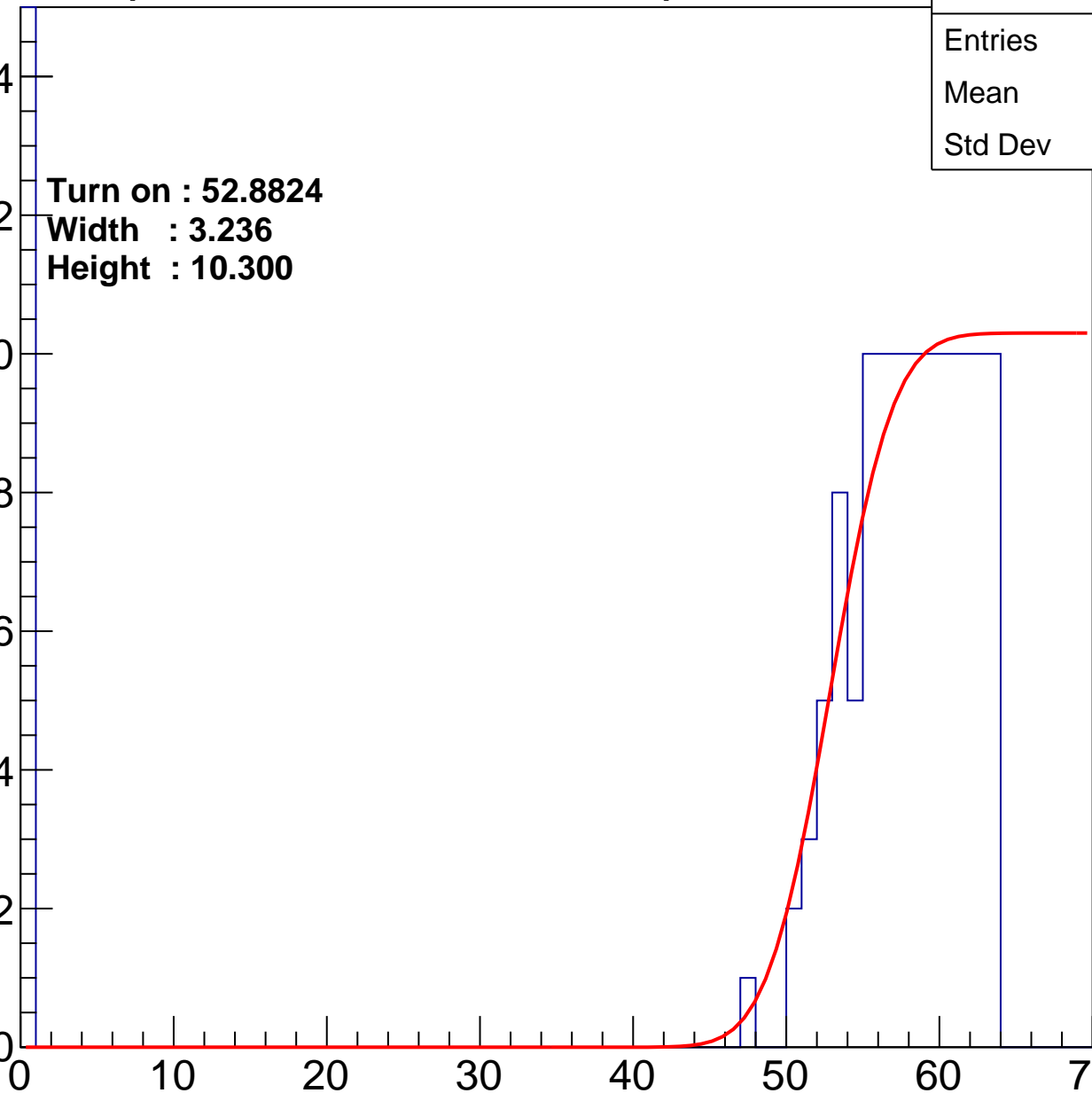
Width : 3.236

Height : 10.300

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	29.43
Std Dev	28.89

Turn on : 53.1591

Width : 3.320

Height : 9.953

Entry

14

12

10

8

6

4

2

0

0

10

20

30

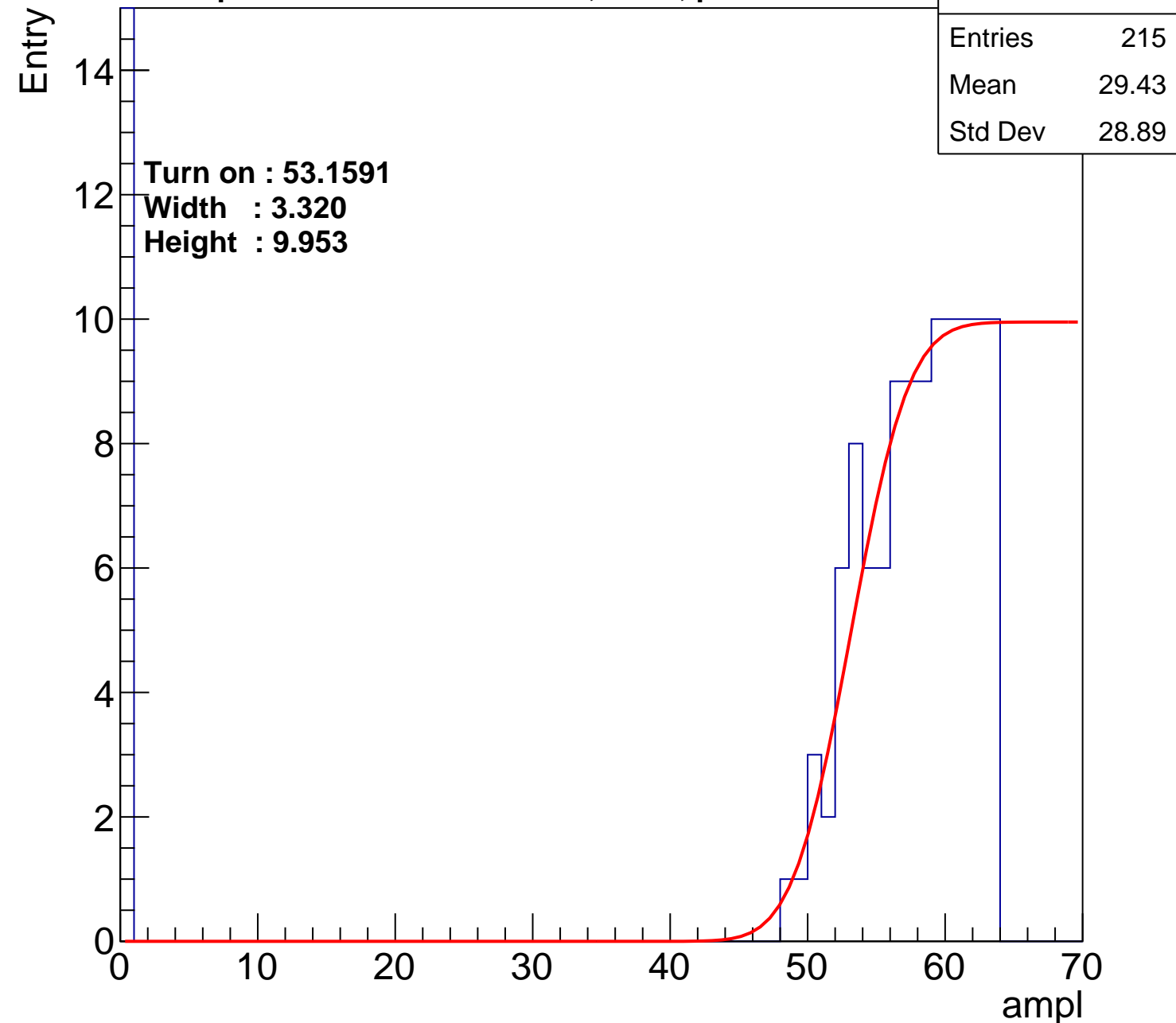
40

50

60

70

ampl



B1L104S, U19-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	30.84
Std Dev	29.15

Turn on : 54.2833

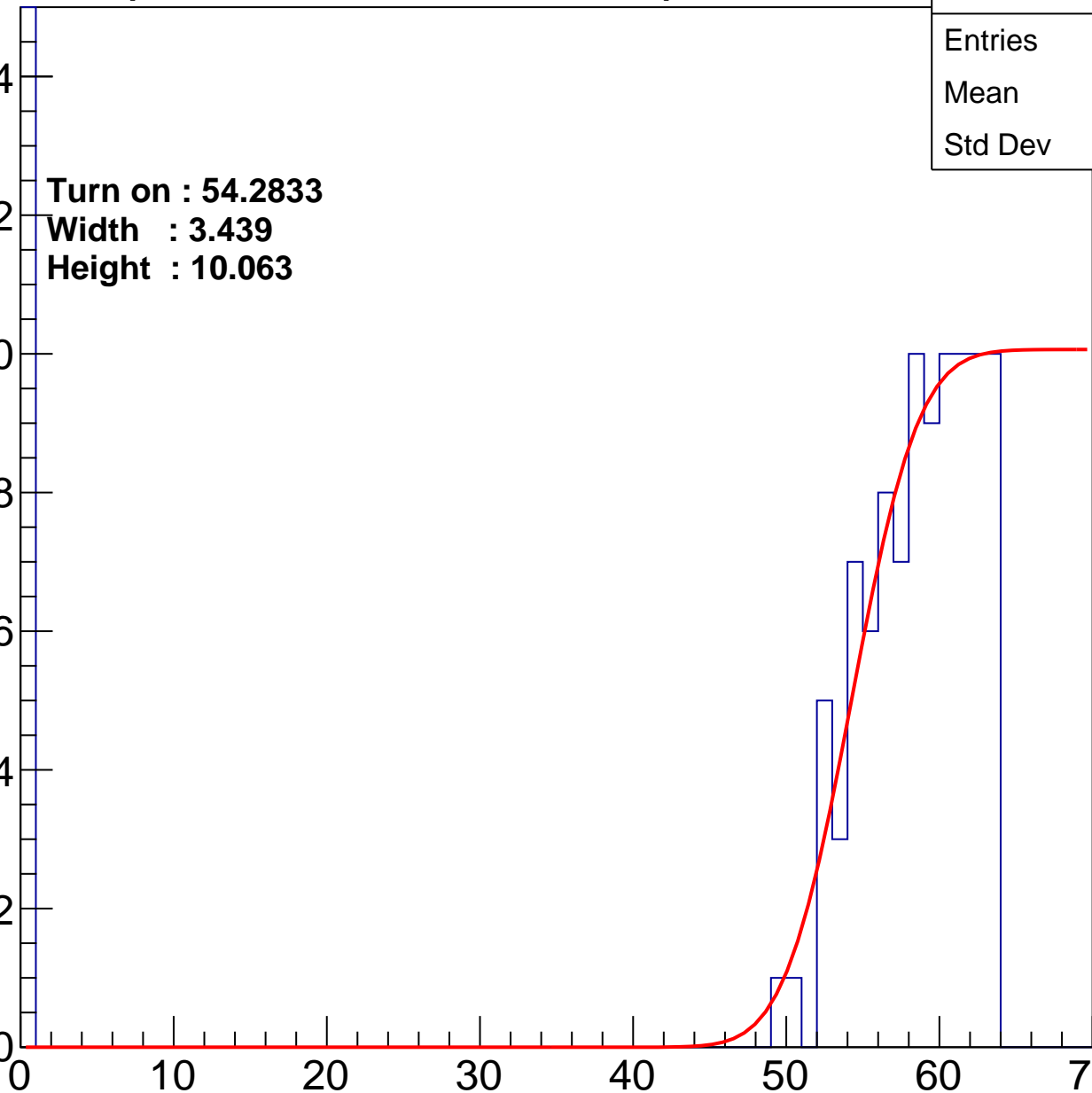
Width : 3.439

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	31.04
Std Dev	28.97

Turn on : 55.2692

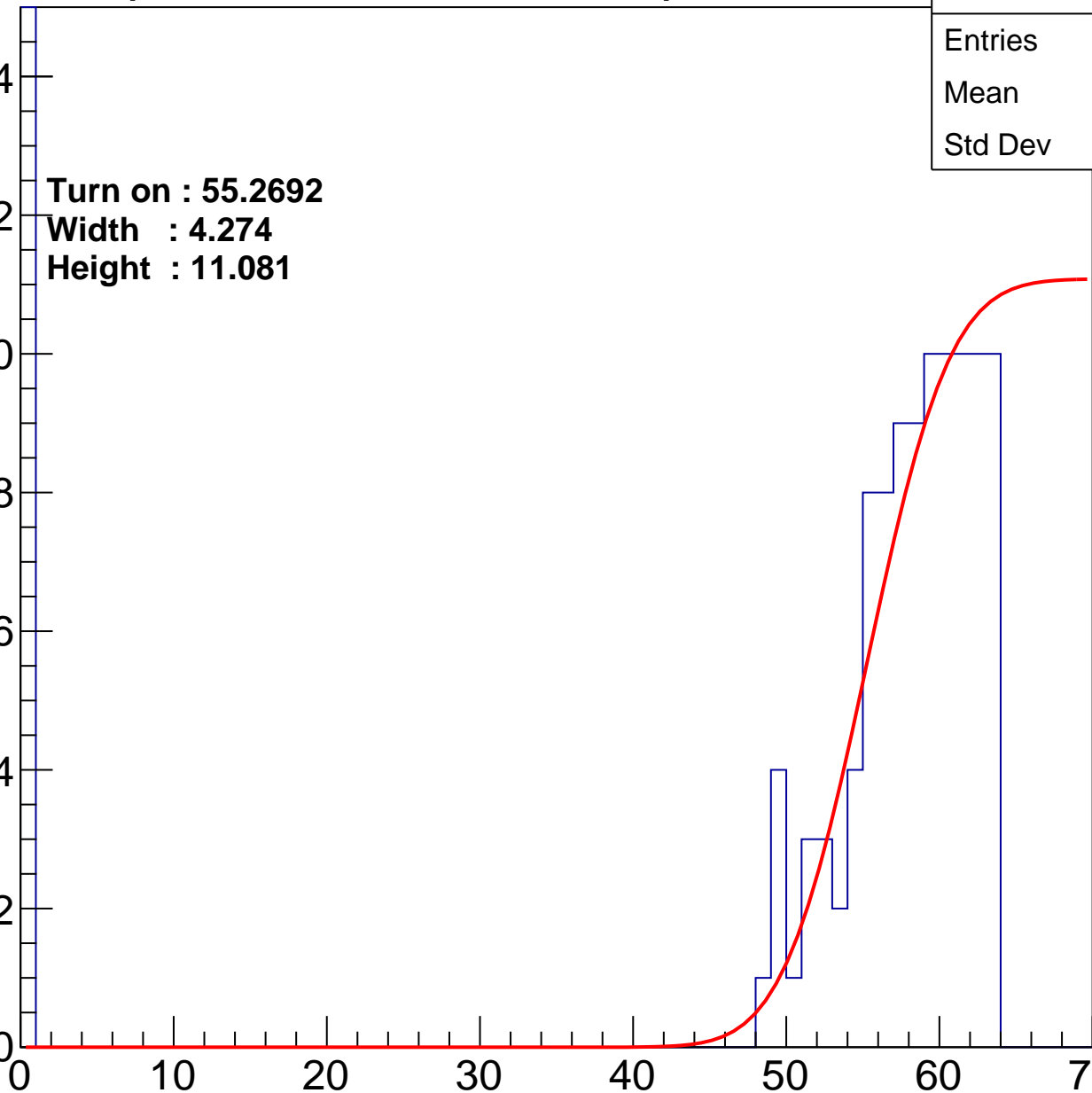
Width : 4.274

Height : 11.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch47

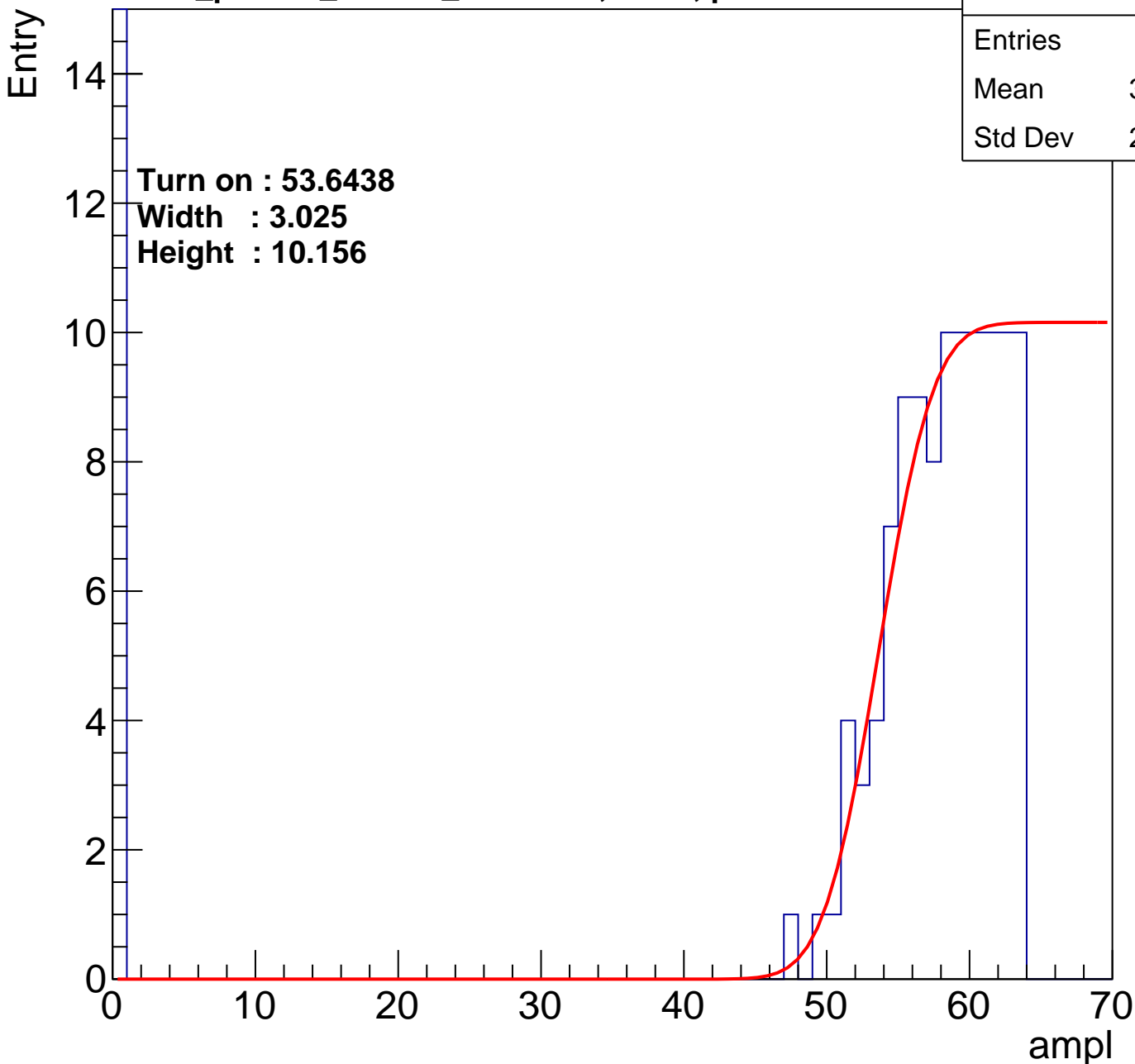
calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	32.88
Std Dev	28.74

Turn on : 53.6438

Width : 3.025

Height : 10.156



B1L104S, U19-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	33.24
Std Dev	28.82

Turn on : 54.2670

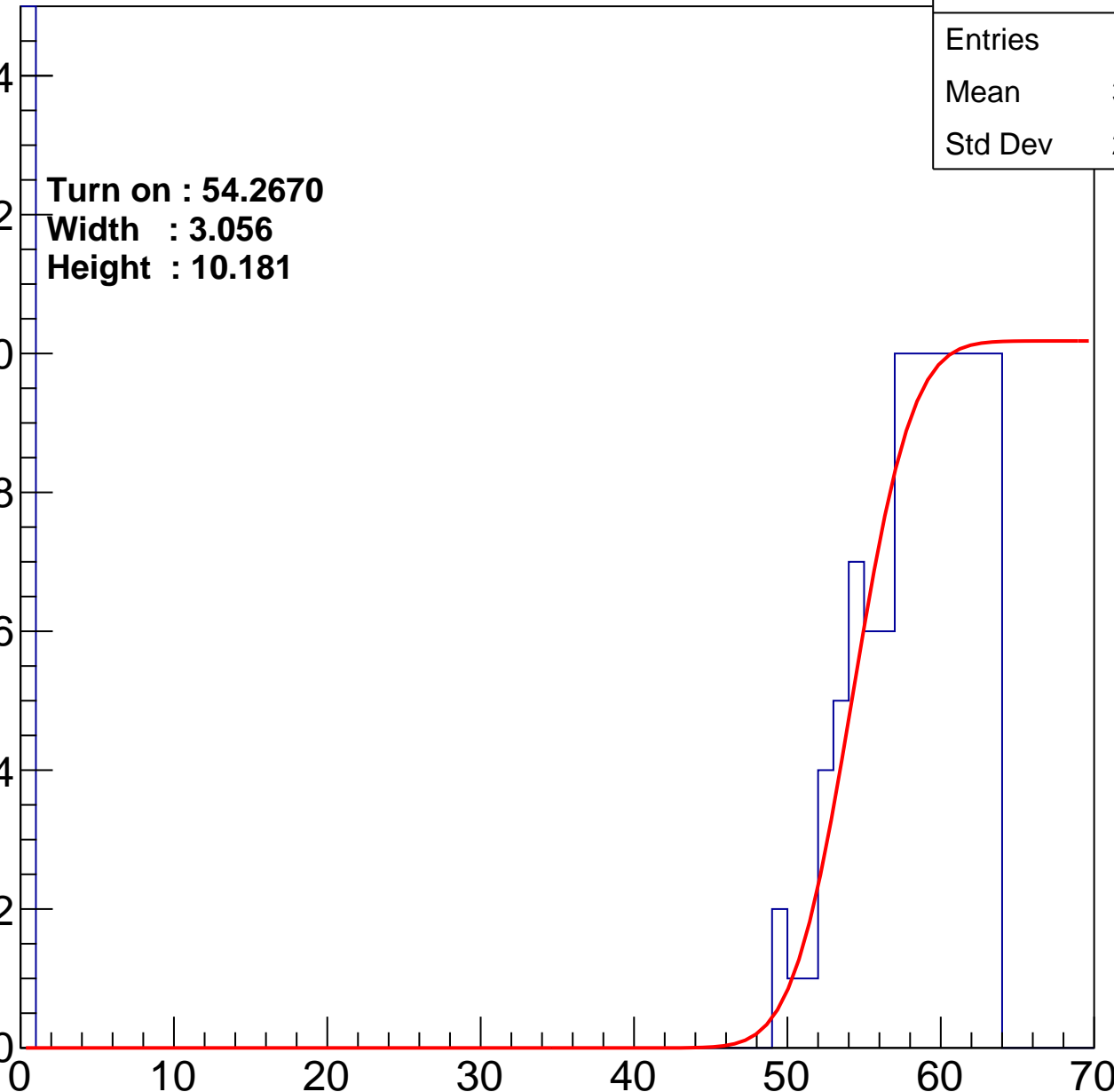
Width : 3.056

Height : 10.181

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch49

calib_packv5_033123_0516.root, FC#4, port A1

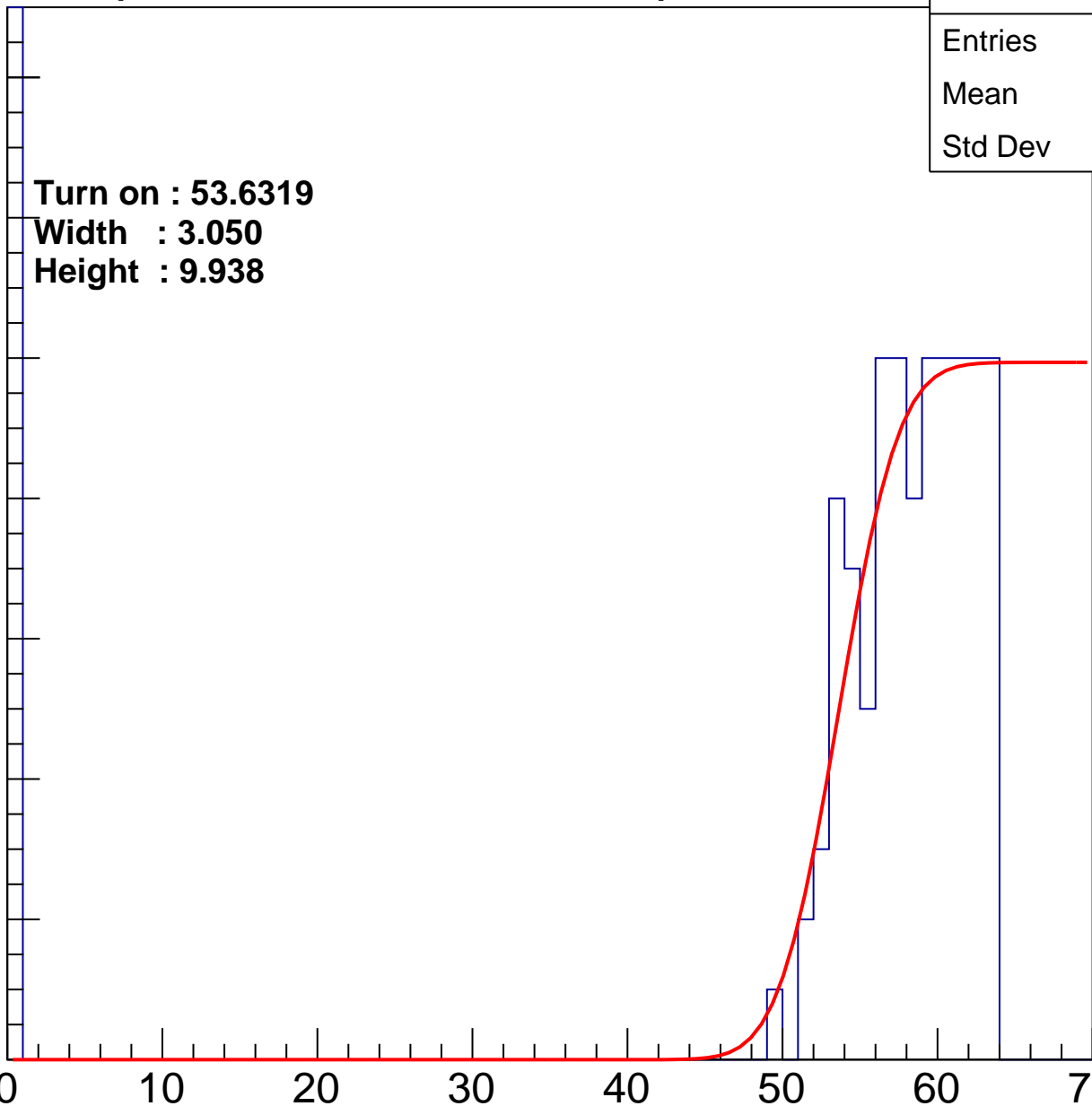
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6319
Width : 3.050
Height : 9.938

Entries	204
Mean	29.55
Std Dev	29.08

ampl



B1L104S, U19-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.45
Std Dev	28.65

Turn on : 53.5591

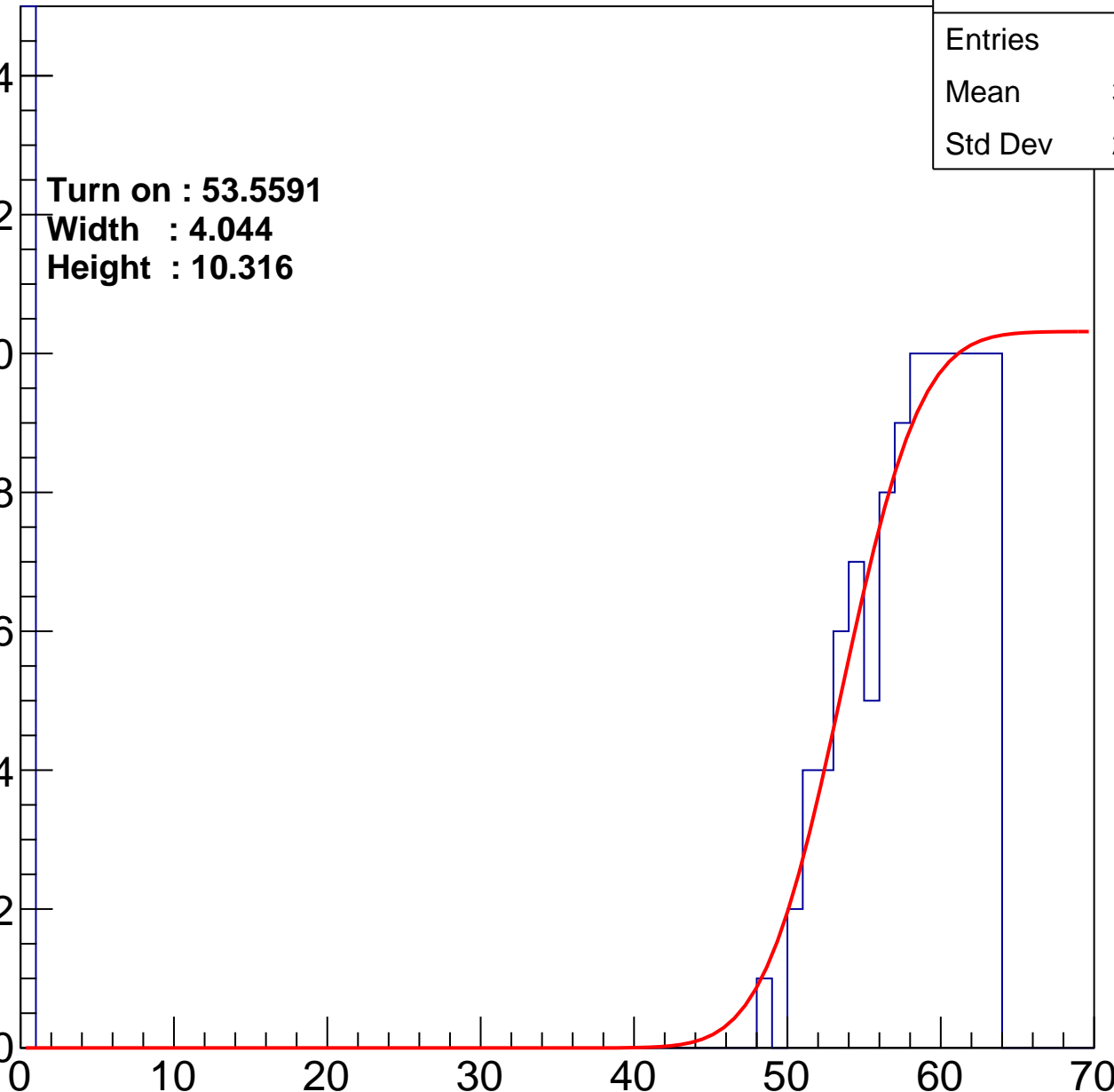
Width : 4.044

Height : 10.316

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch51

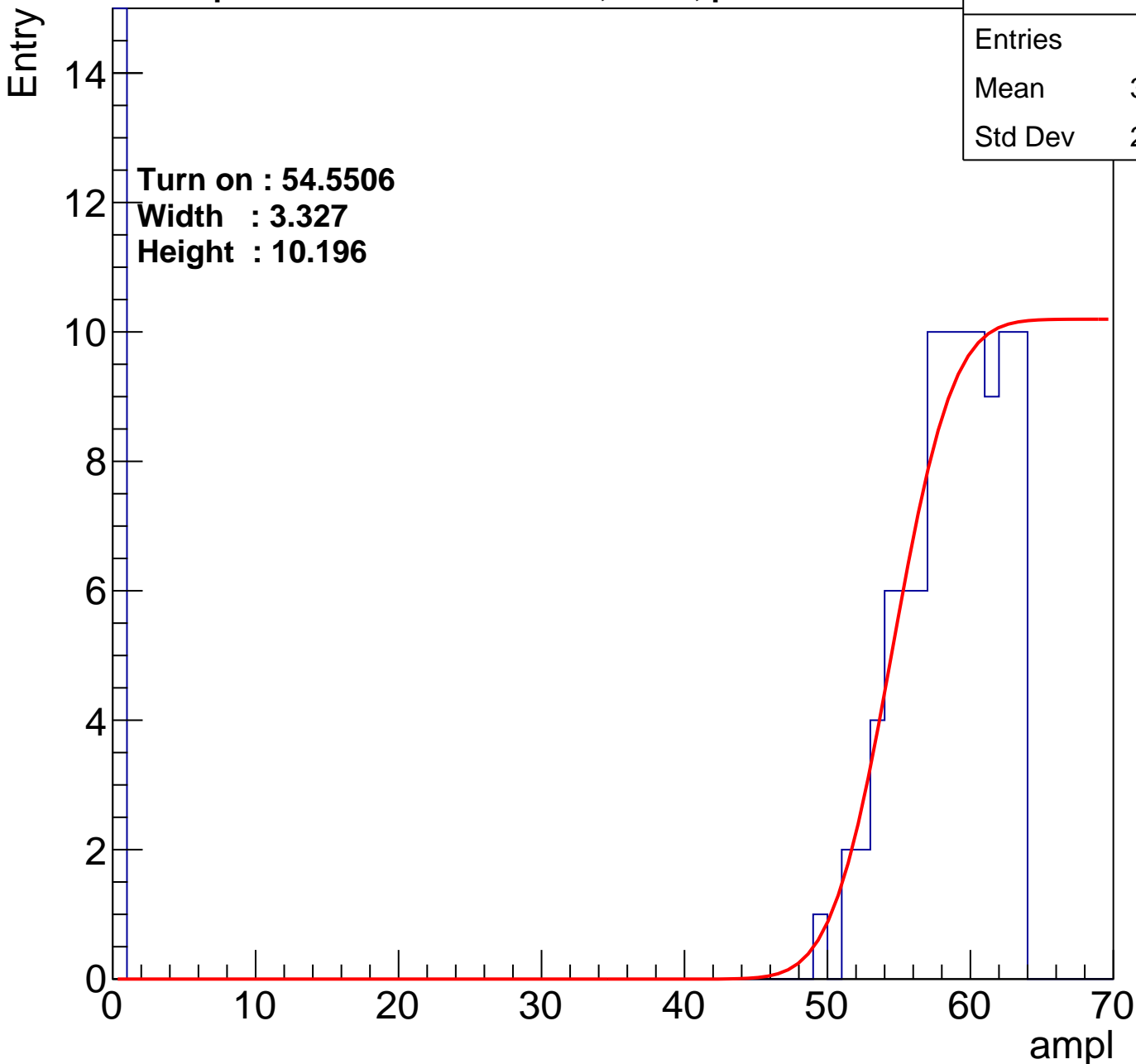
calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	31.09
Std Dev	29.18

Turn on : 54.5506

Width : 3.327

Height : 10.196



B1L104S, U19-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	31.73
Std Dev	28.72

Turn on : 53.3229

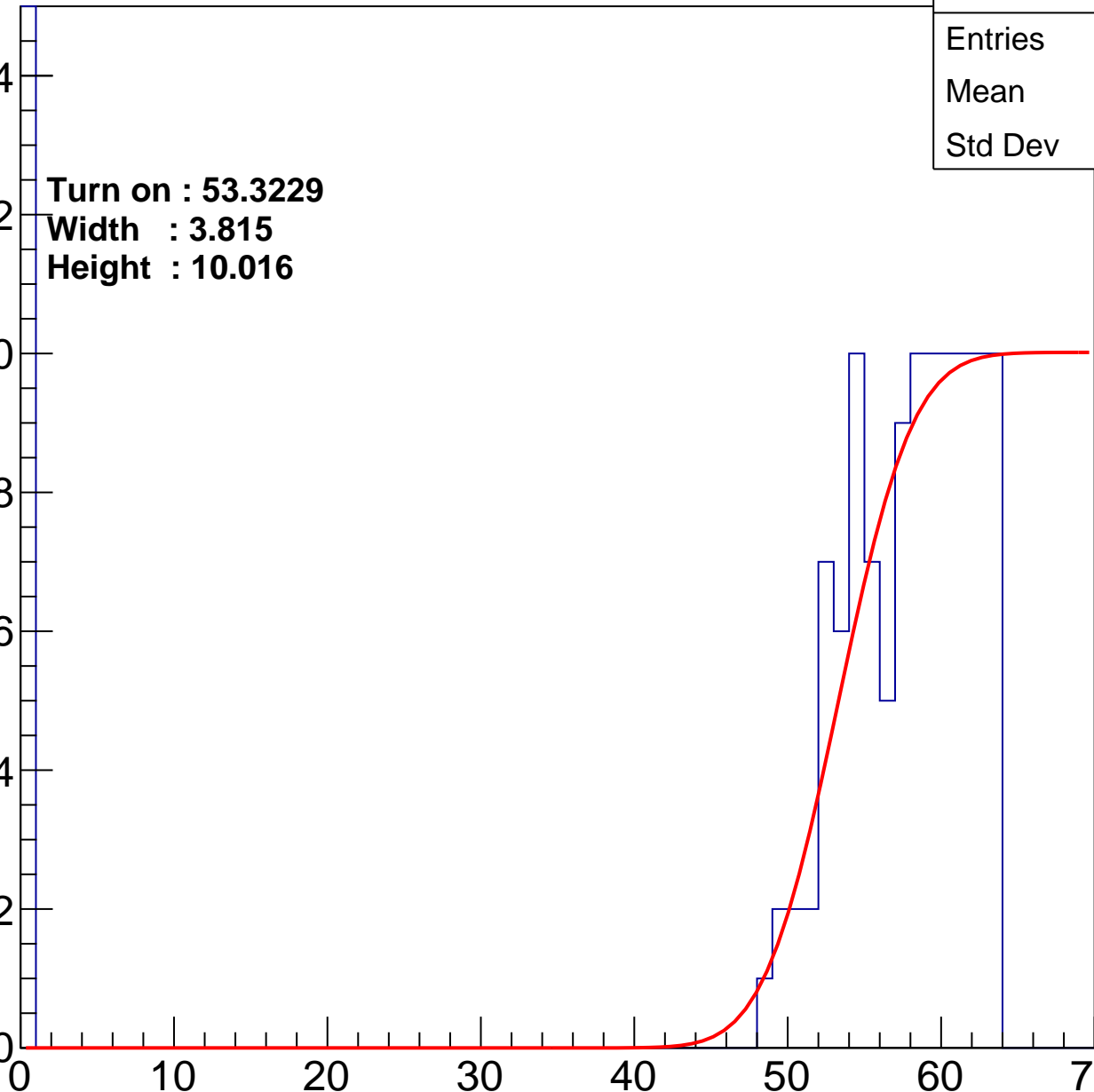
Width : 3.815

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	32.74
Std Dev	29.1

Turn on : 53.3472

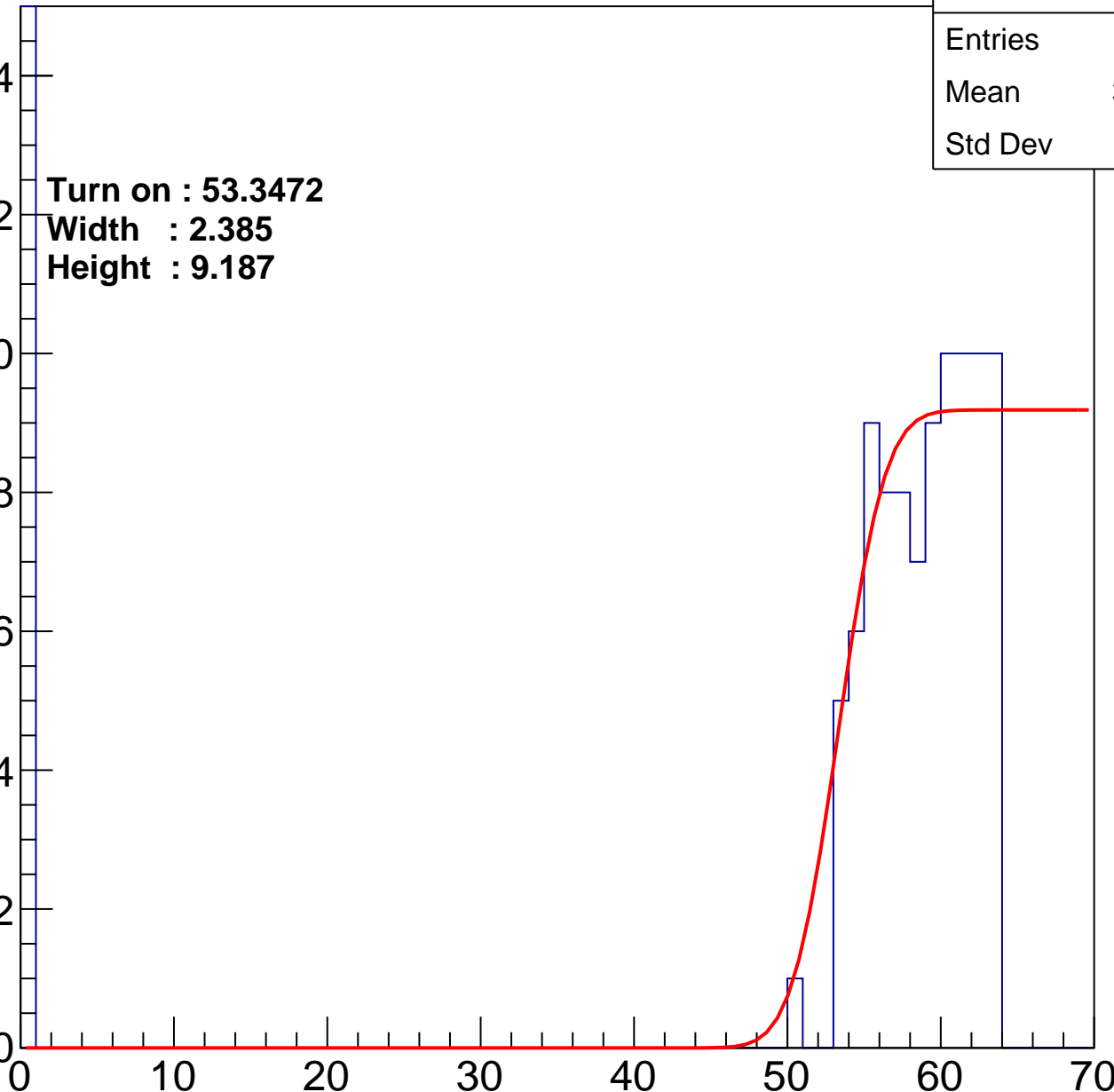
Width : 2.385

Height : 9.187

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch54

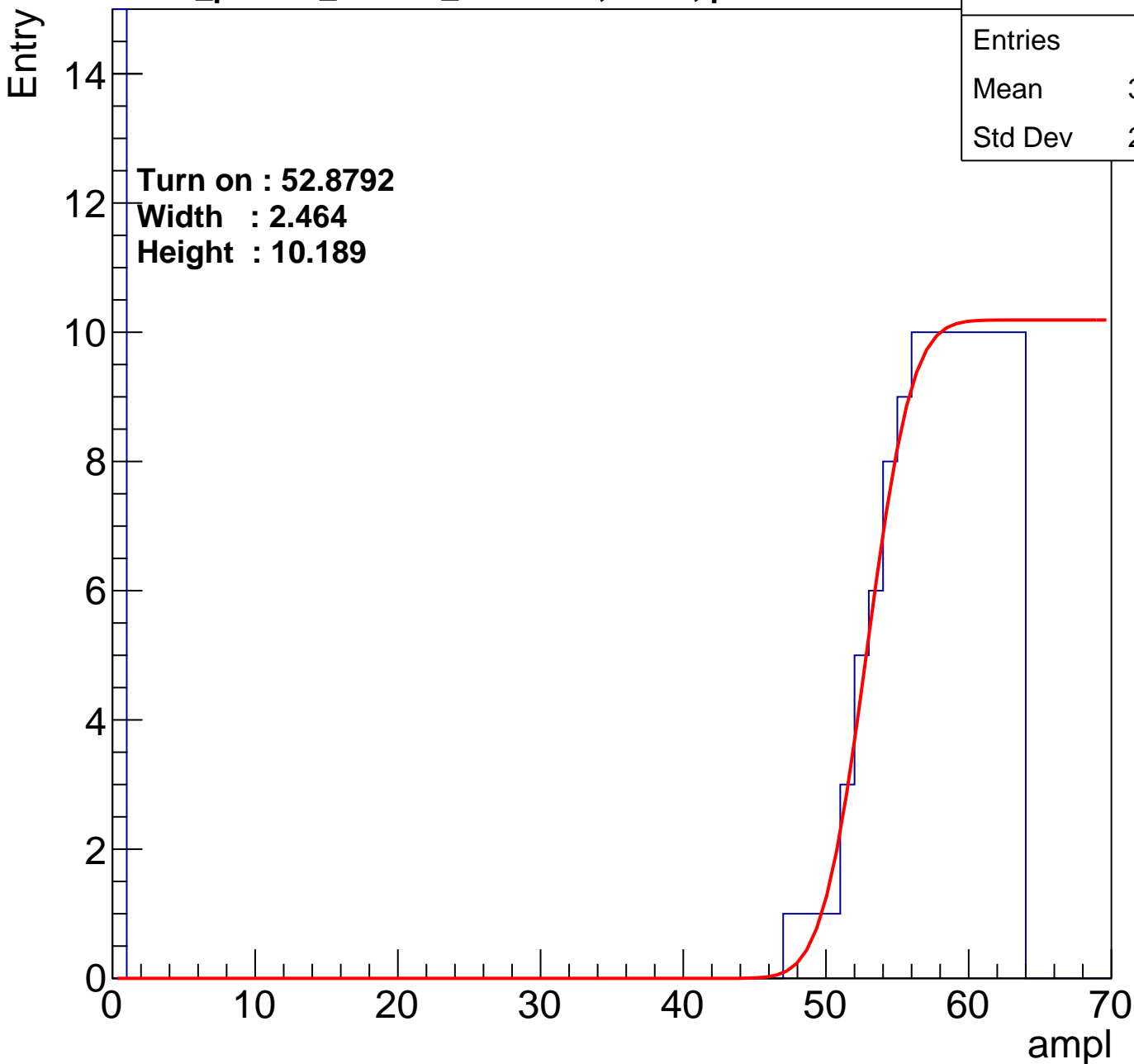
calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	36.33
Std Dev	27.89

Turn on : 52.8792

Width : 2.464

Height : 10.189



B1L104S, U19-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	35.06
Std Dev	28.26

Turn on : 52.9355

Width : 2.965

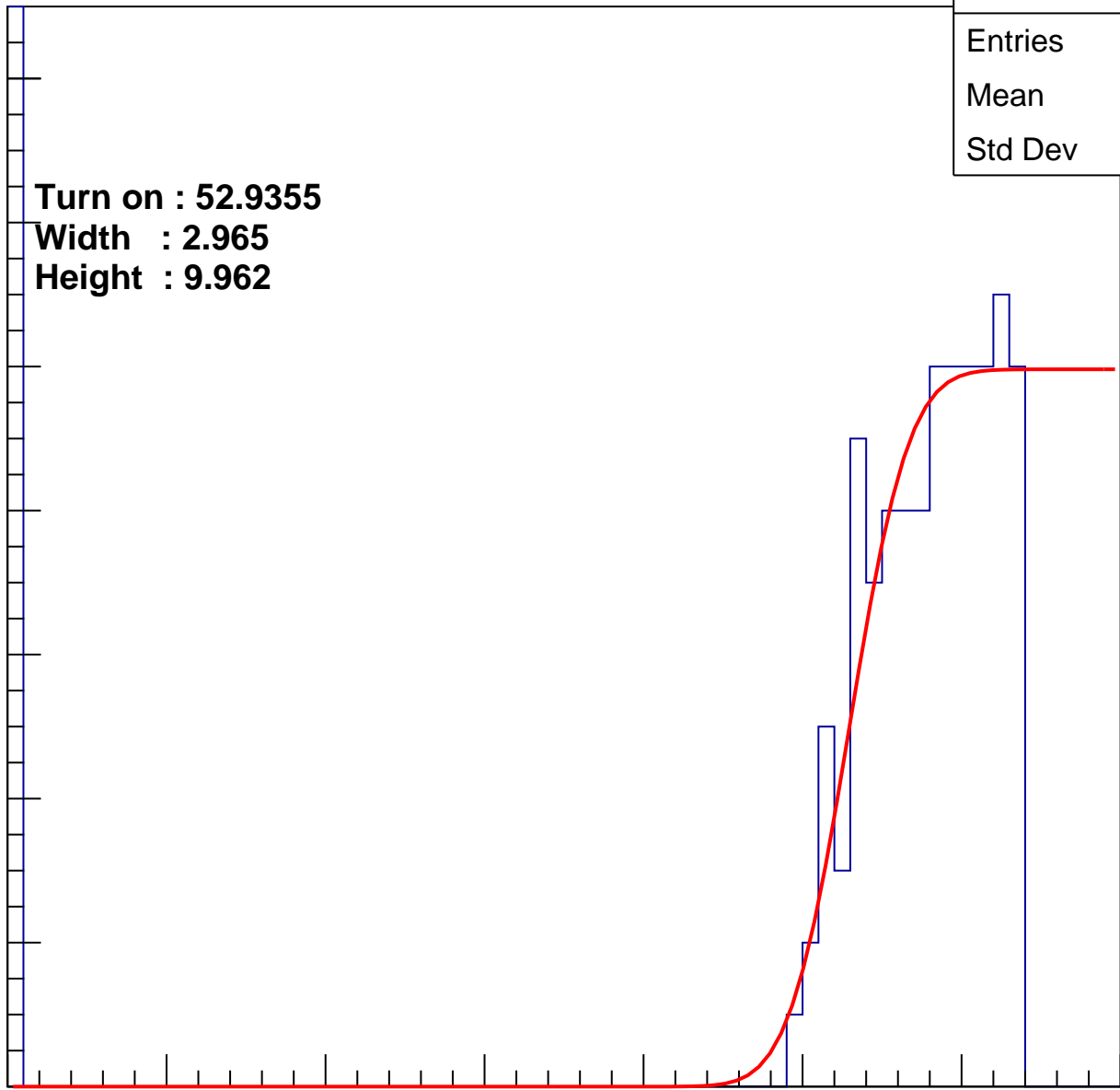
Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U19-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	225
Mean	34.52
Std Dev	27.63

Turn on : 51.3021

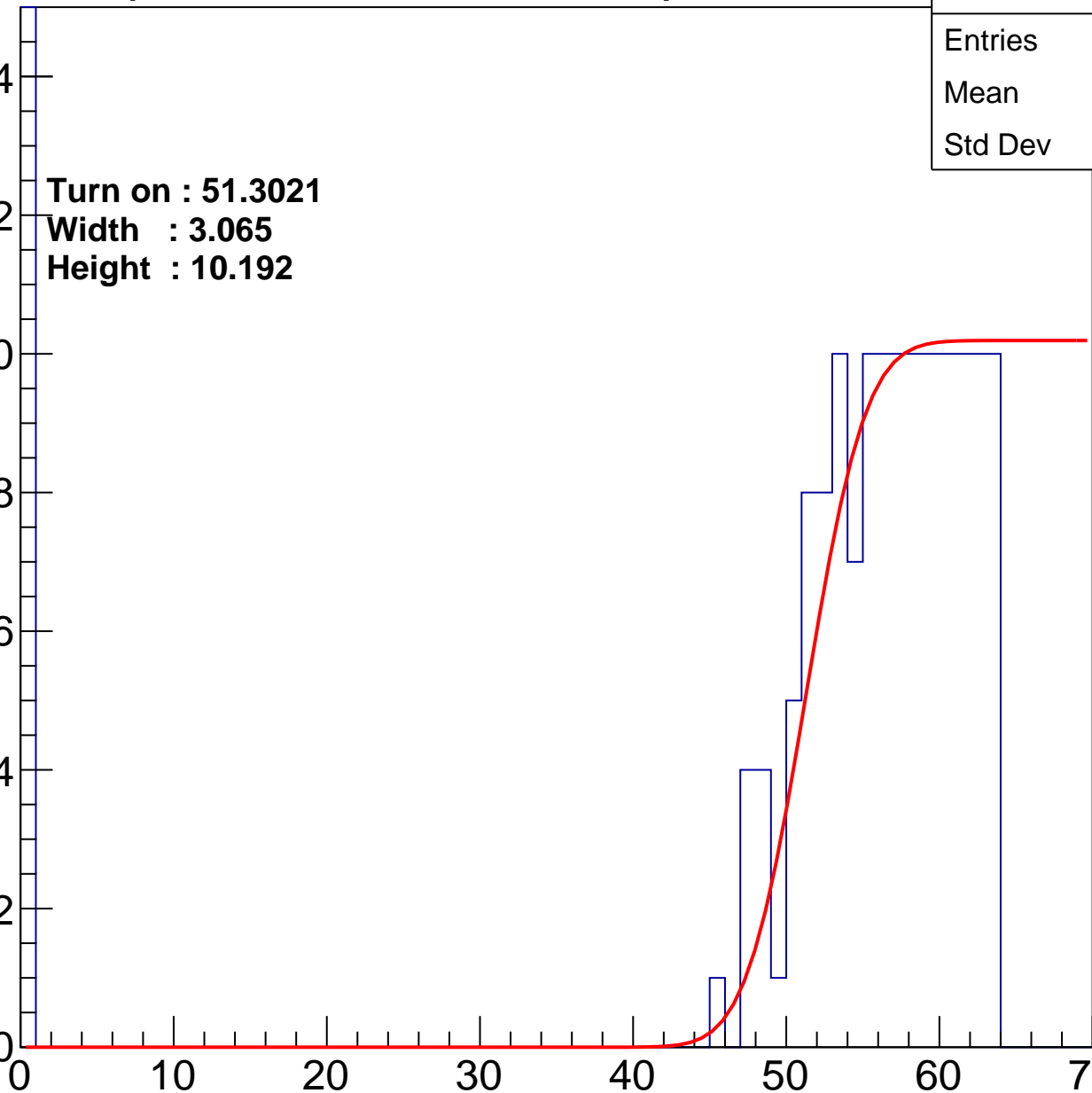
Width : 3.065

Height : 10.192

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch57

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	31.58
Std Dev	29.08

Turn on : 54.2766

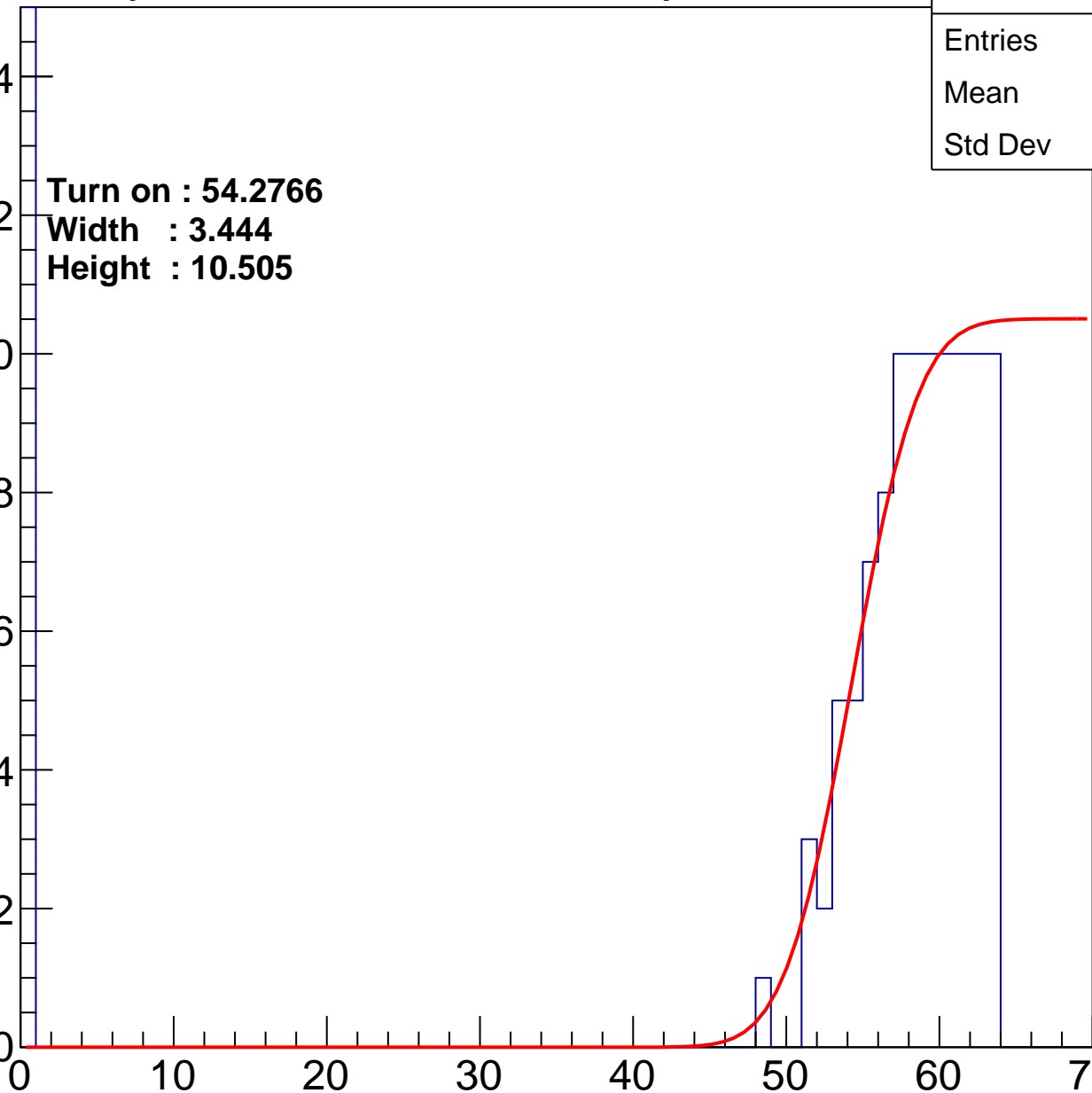
Width : 3.444

Height : 10.505

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch58

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.03
Std Dev	28.55

Turn on : 53.1911

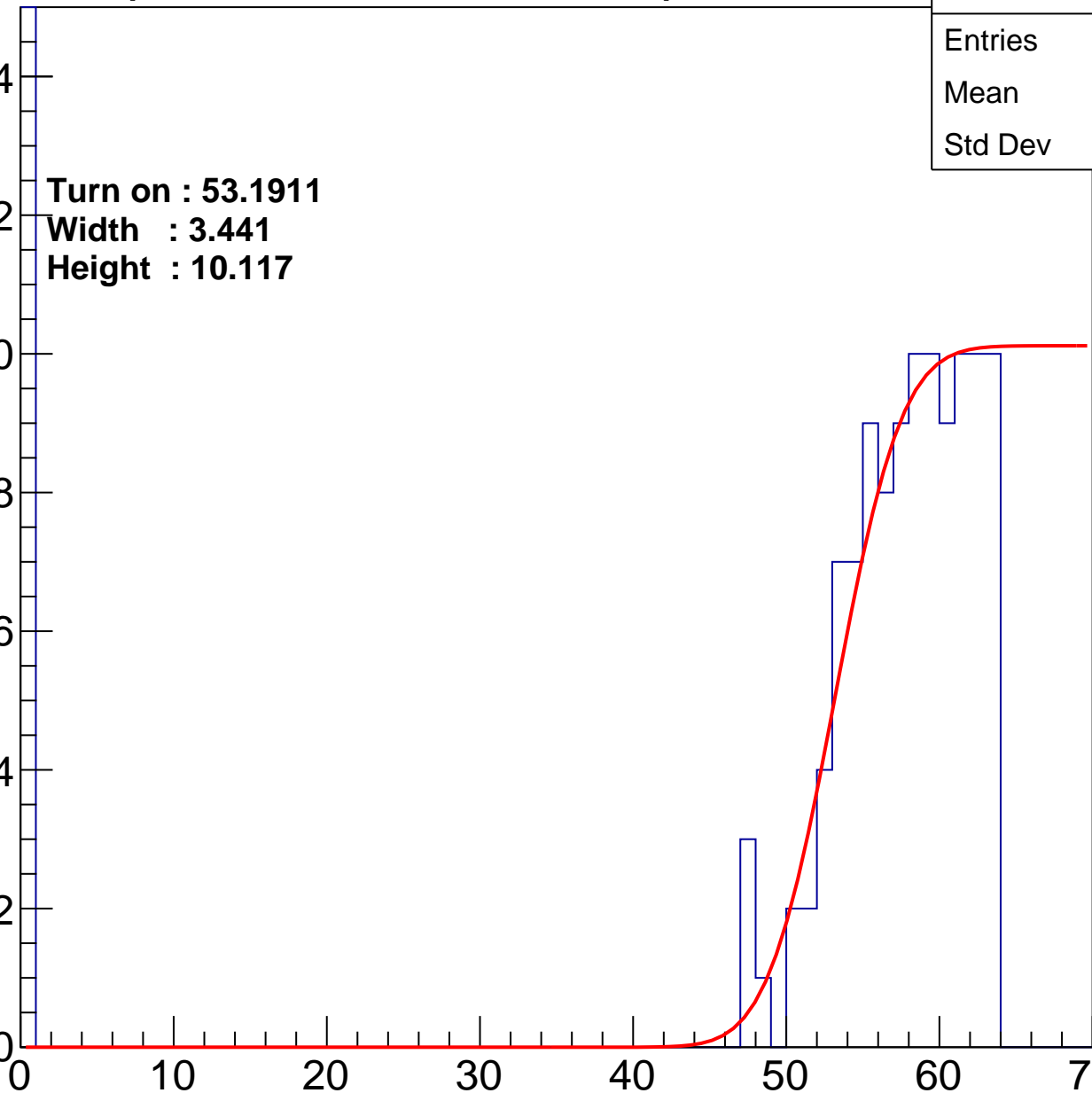
Width : 3.441

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	31.74
Std Dev	29.05

Turn on : 53.5945

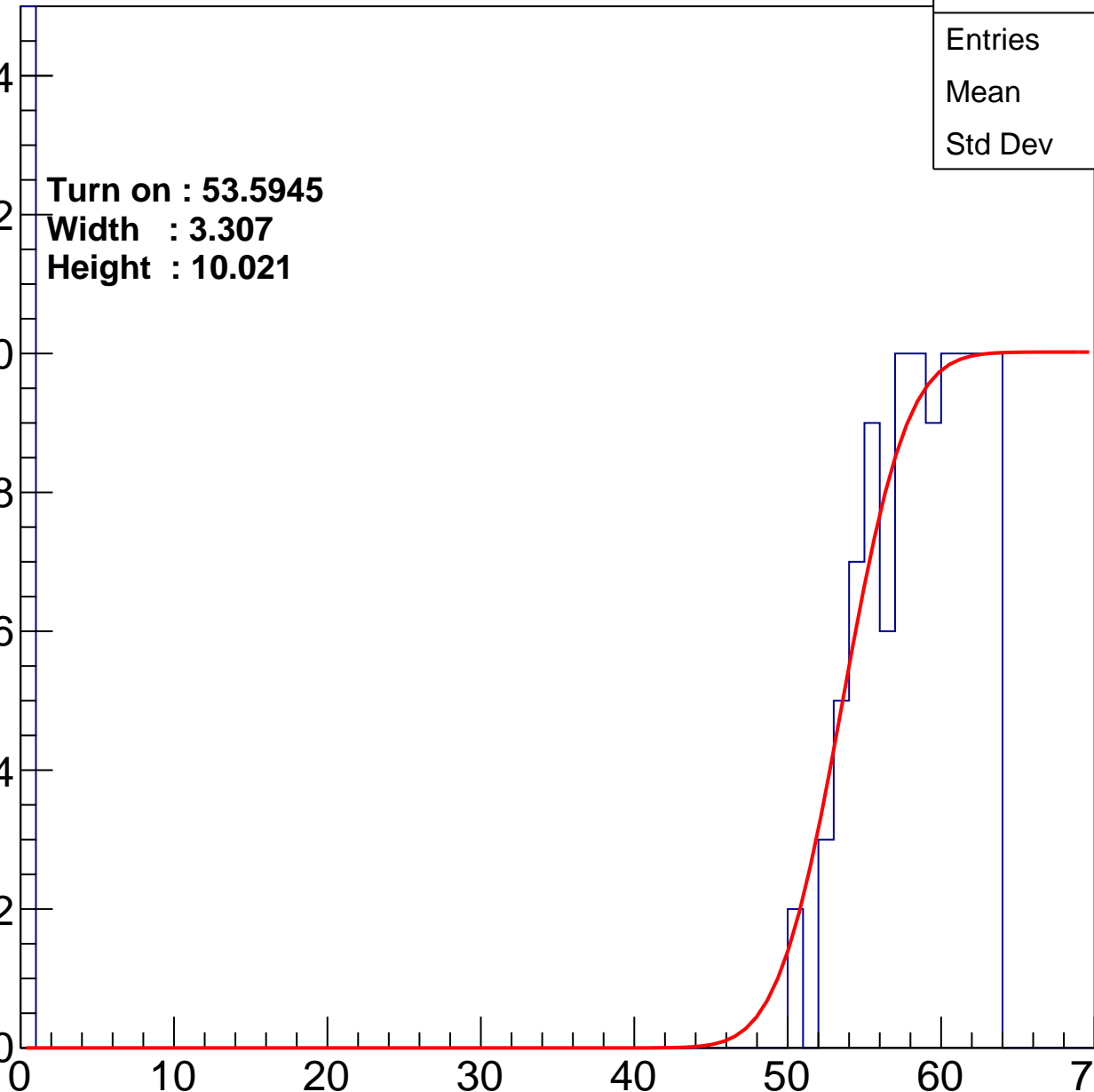
Width : 3.307

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	35.27
Std Dev	28.28

Turn on : 54.0452

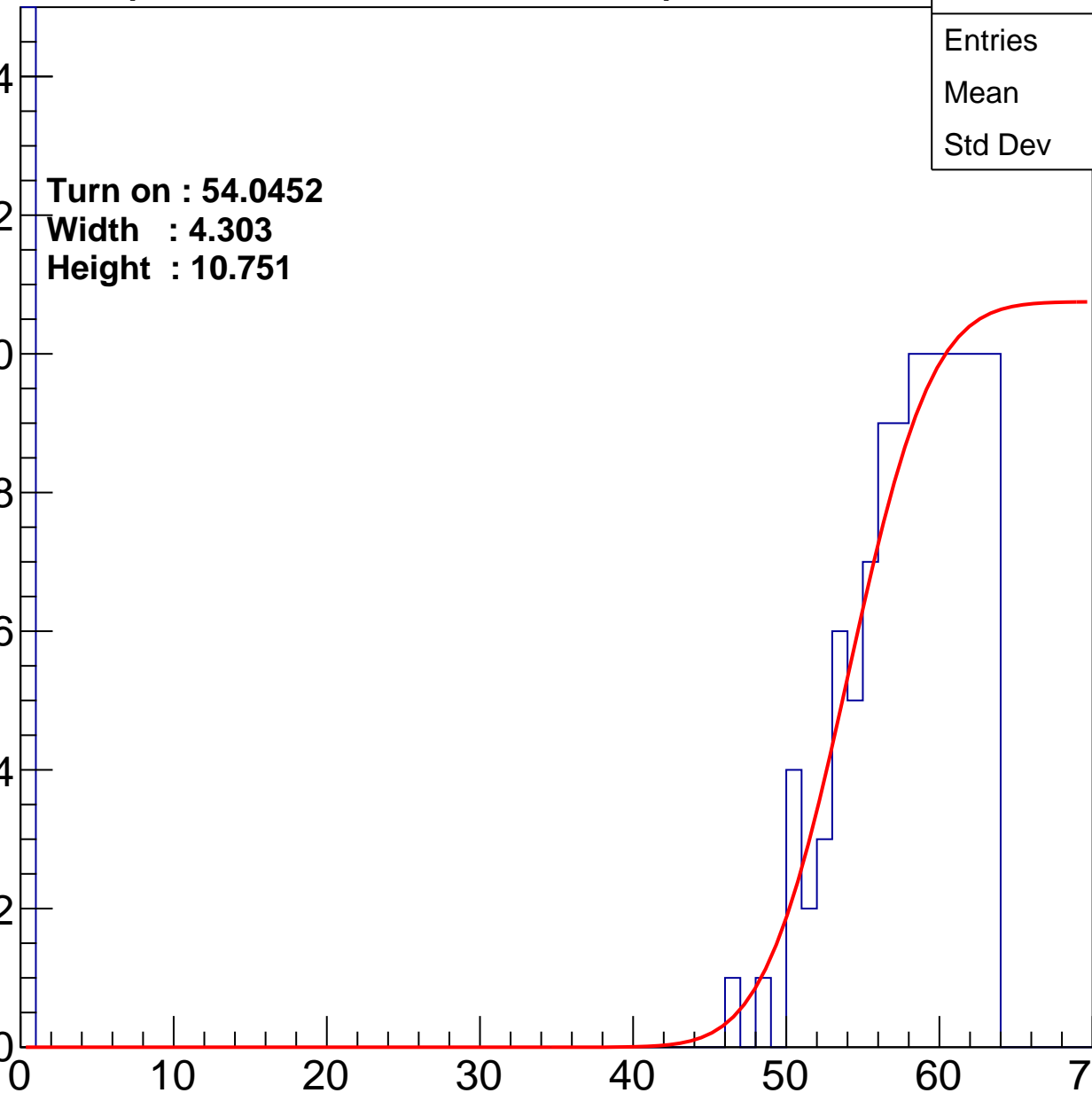
Width : 4.303

Height : 10.751

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	34.06
Std Dev	28.76

Turn on : 53.9070

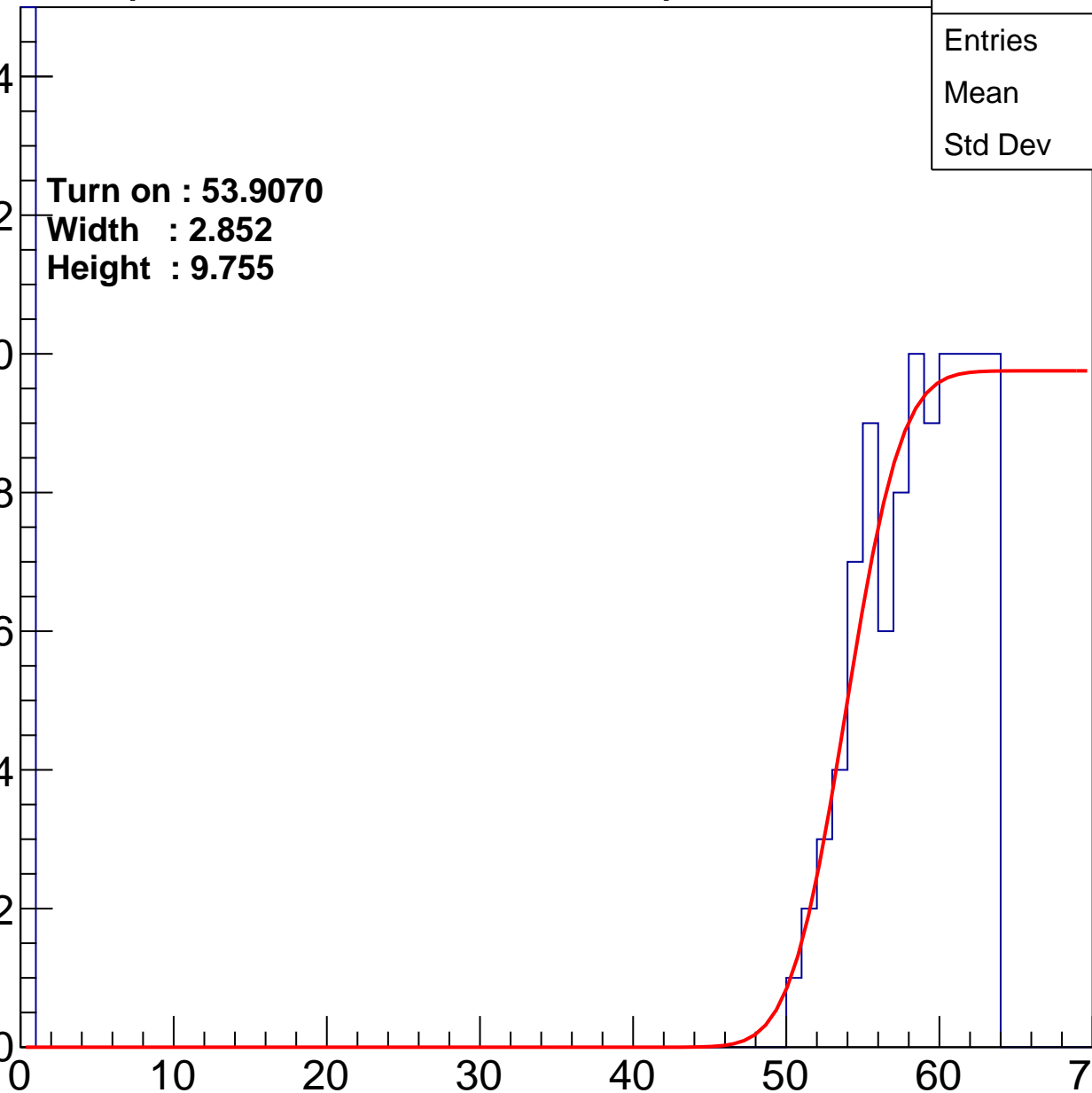
Width : 2.852

Height : 9.755

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch62

calib_packv5_033123_0516.root, FC#4, port A1

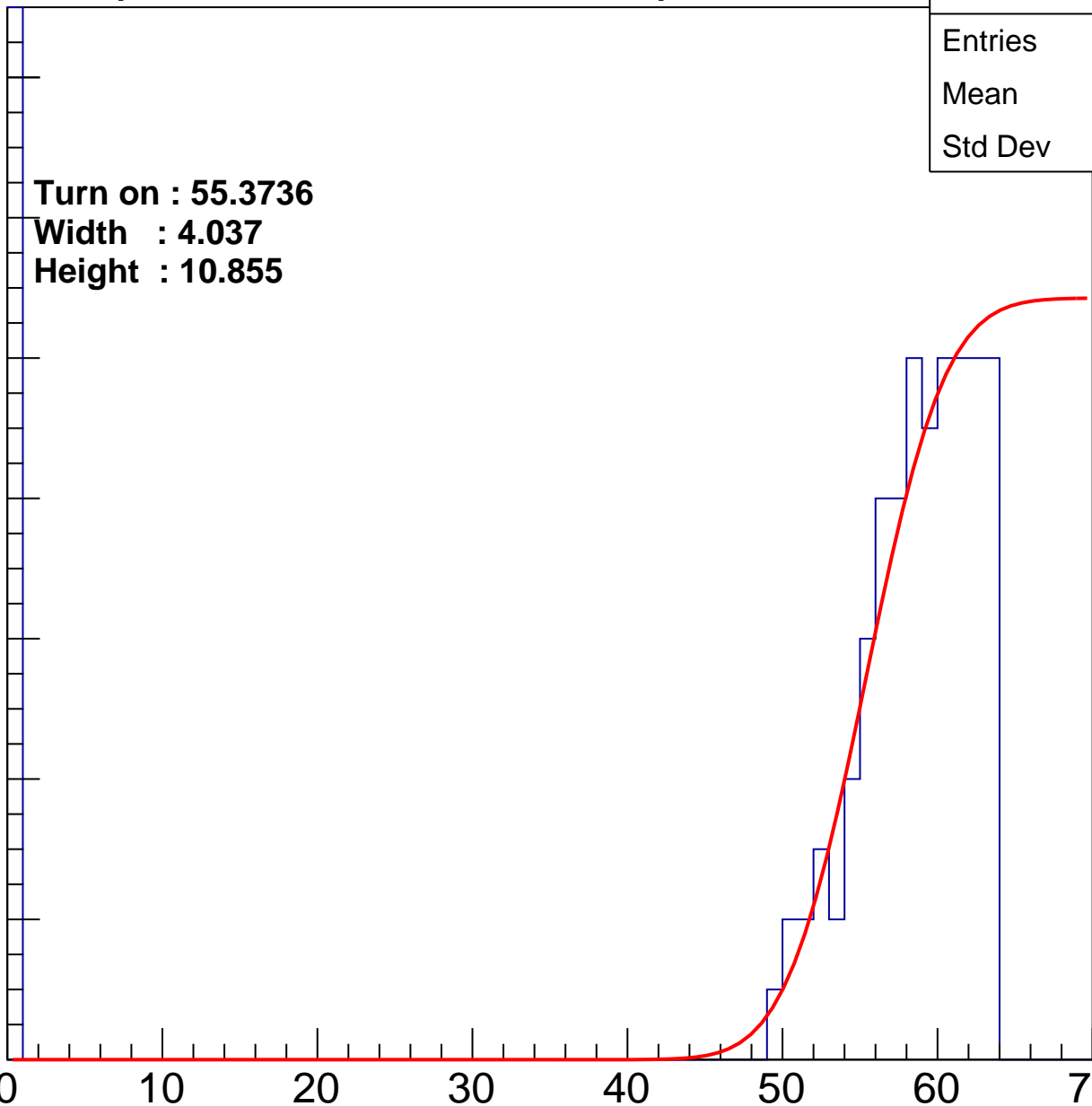
Entry

14
12
10
8
6
4
2
0

Turn on : 55.3736
Width : 4.037
Height : 10.855

Entries	167
Mean	33.14
Std Dev	28.97

ampl



B1L104S, U19-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	30.88
Std Dev	28.75

Turn on : 52.1915

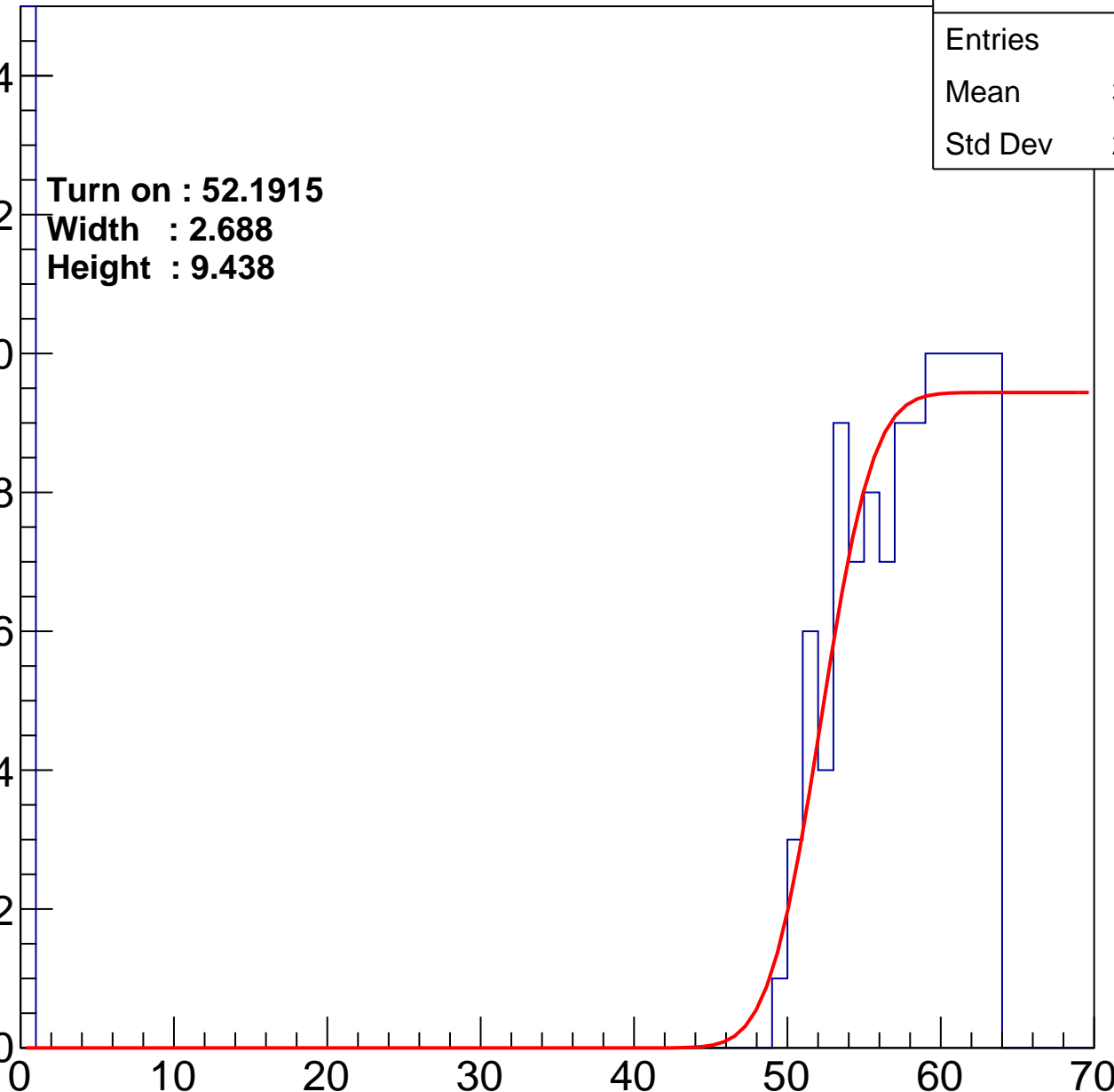
Width : 2.688

Height : 9.438

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	35.97
Std Dev	27.77

Turn on : 51.7080

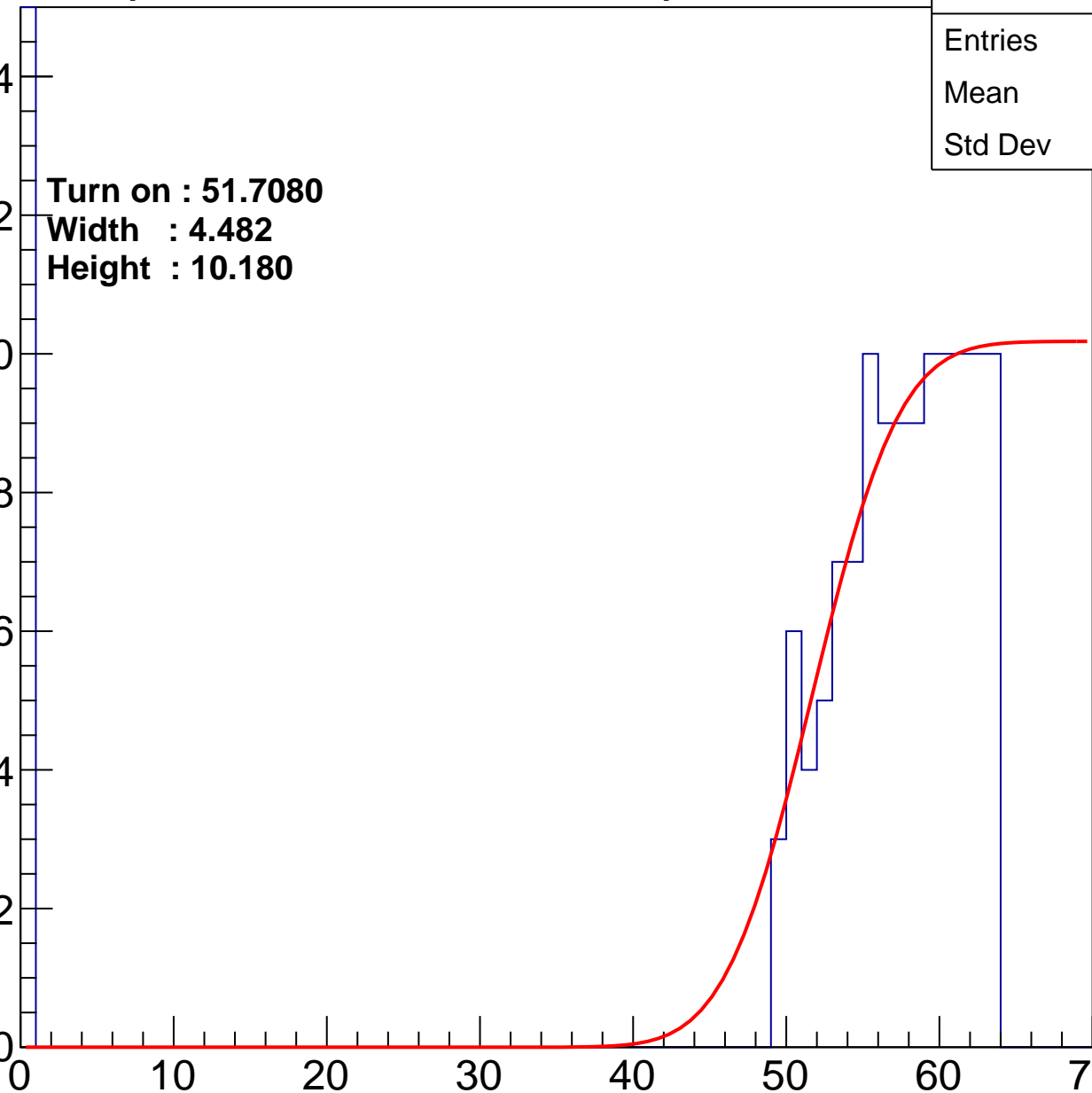
Width : 4.482

Height : 10.180

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch65

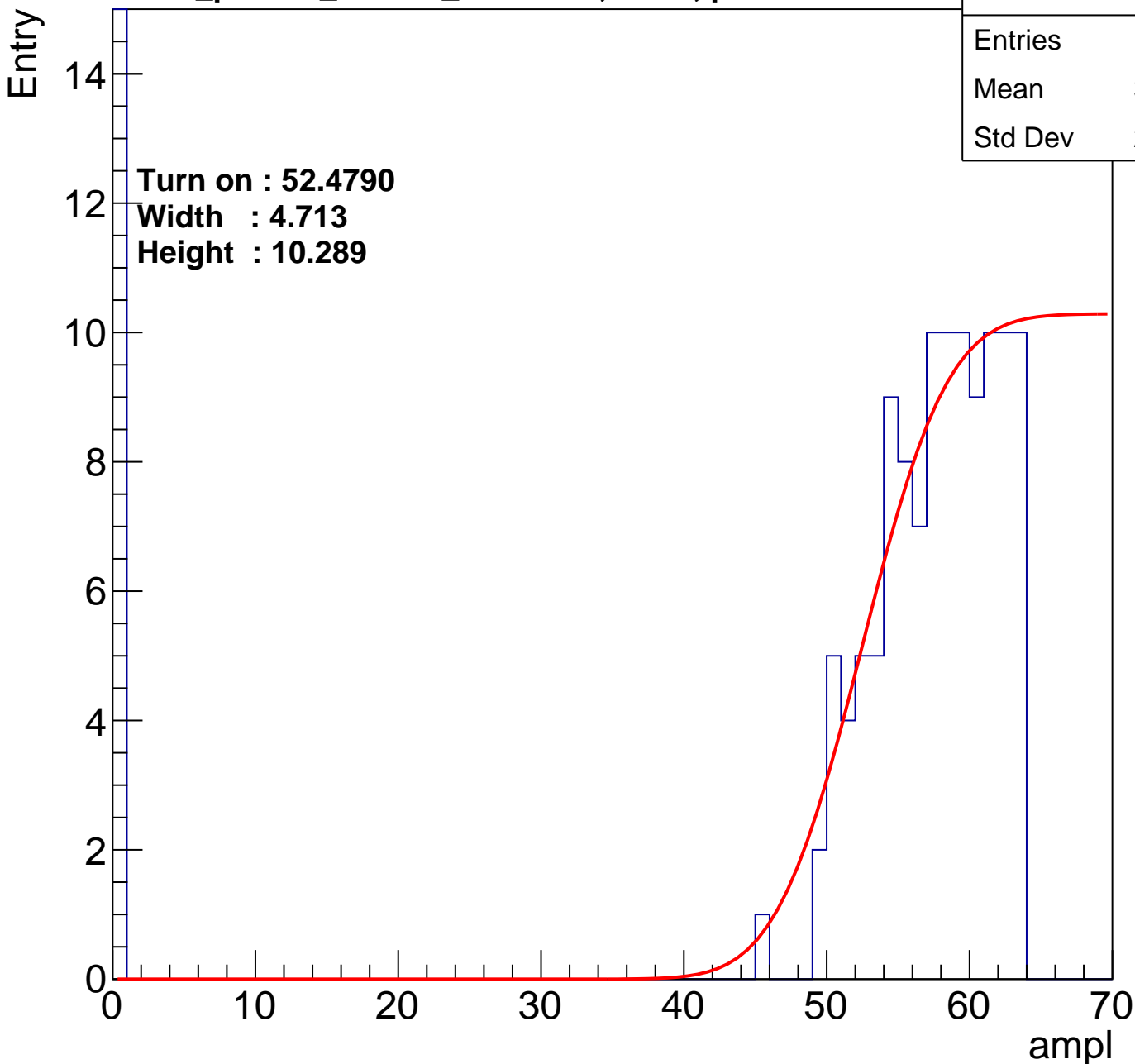
calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	34.81
Std Dev	28.11

Turn on : 52.4790

Width : 4.713

Height : 10.289



B1L104S, U19-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.83
Std Dev	28.34

Turn on : 55.0162

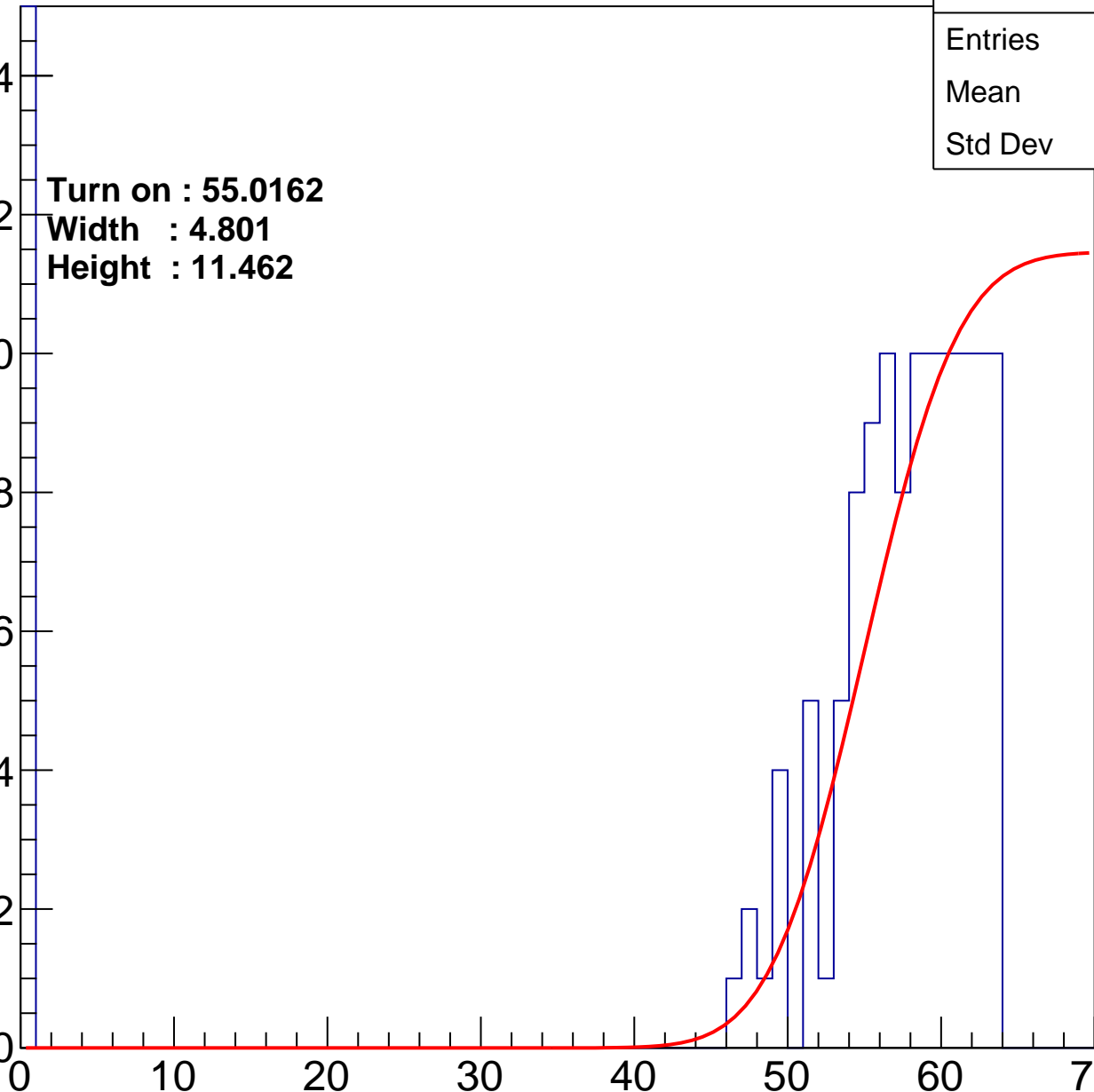
Width : 4.801

Height : 11.462

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	36.87
Std Dev	27.71

Turn on : 53.2183

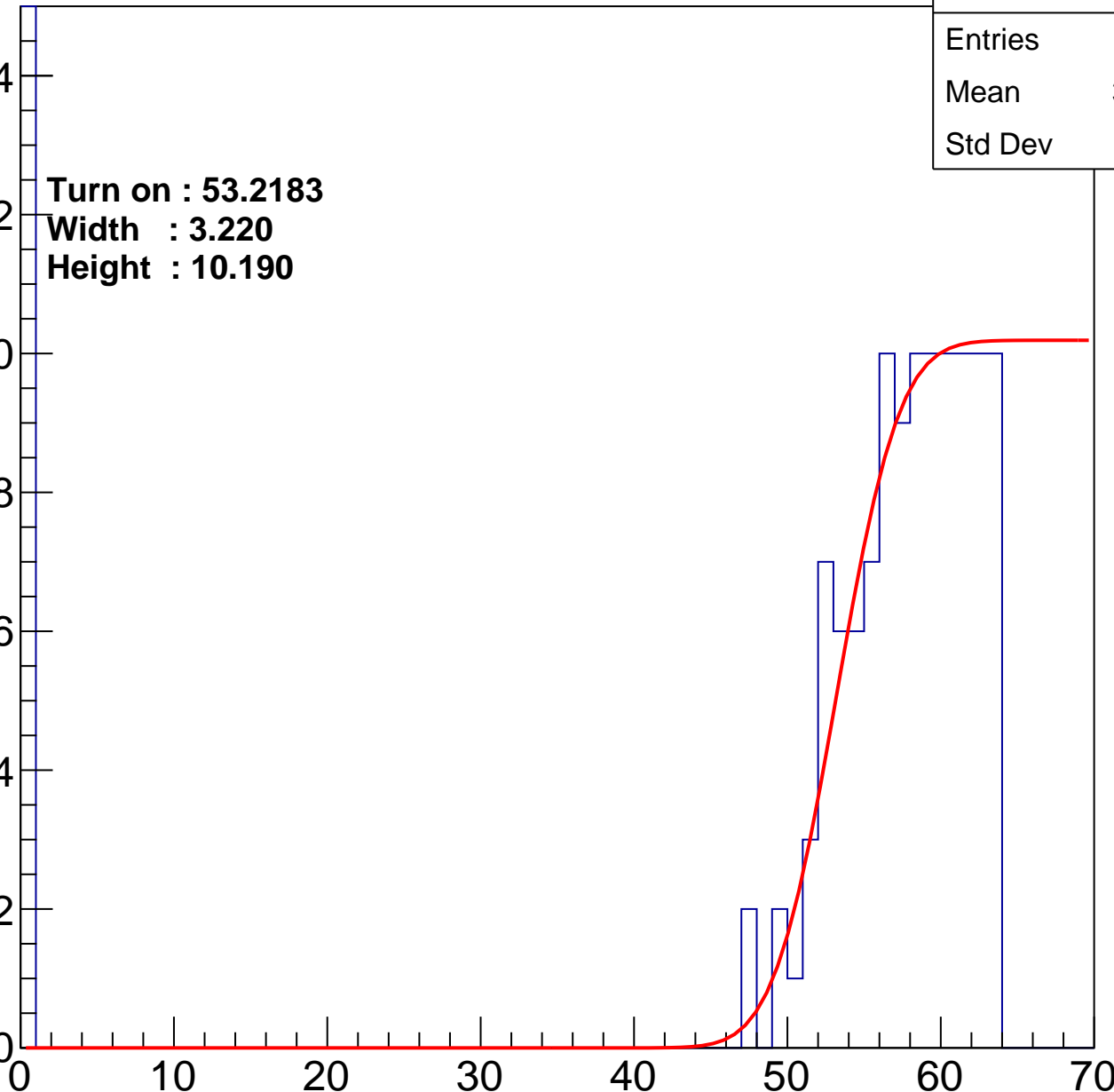
Width : 3.220

Height : 10.190

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	33.29
Std Dev	28.44

Turn on : 52.8232

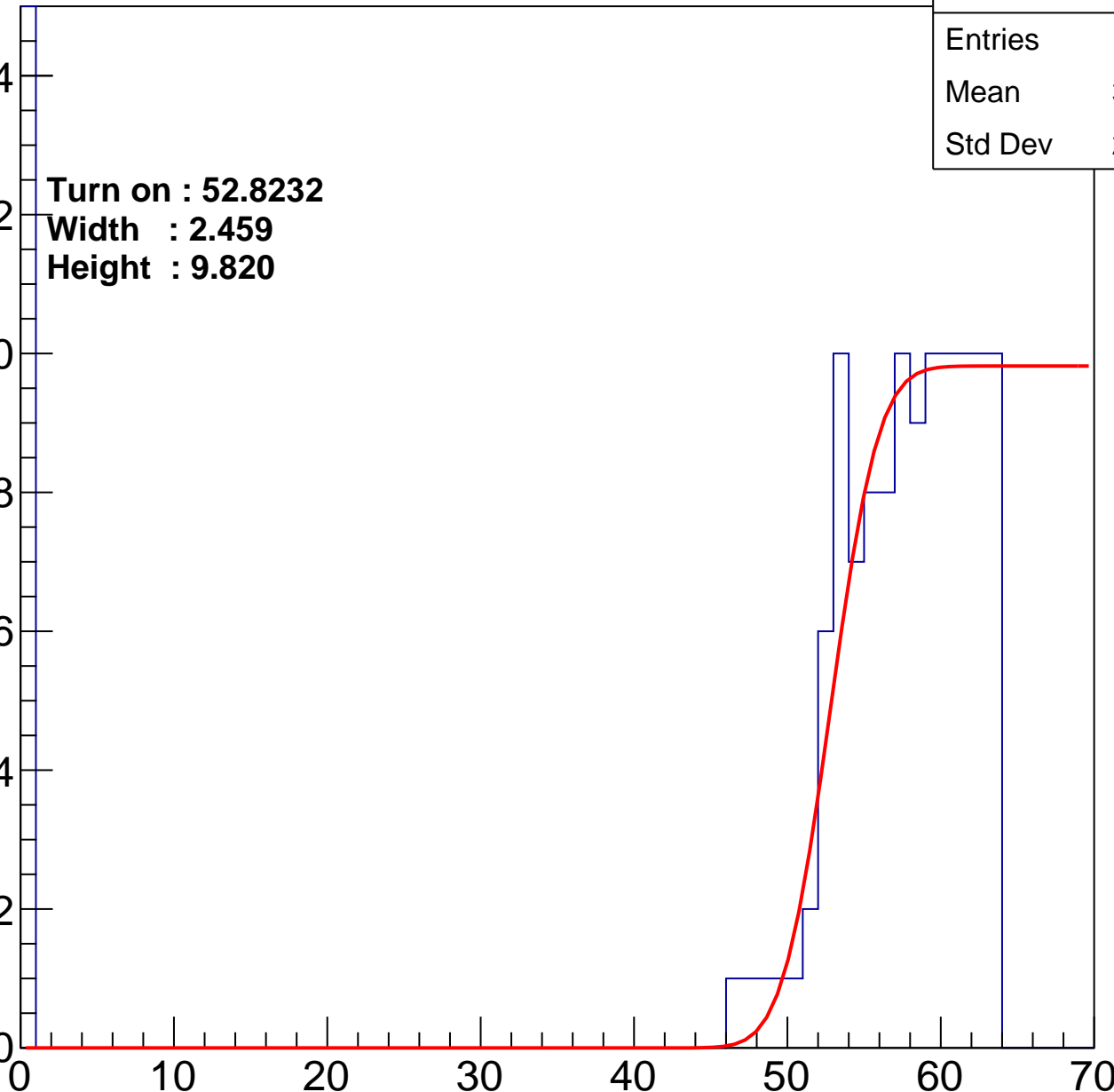
Width : 2.459

Height : 9.820

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch69

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	27.48
Std Dev	29.53

Turn on : 55.5171

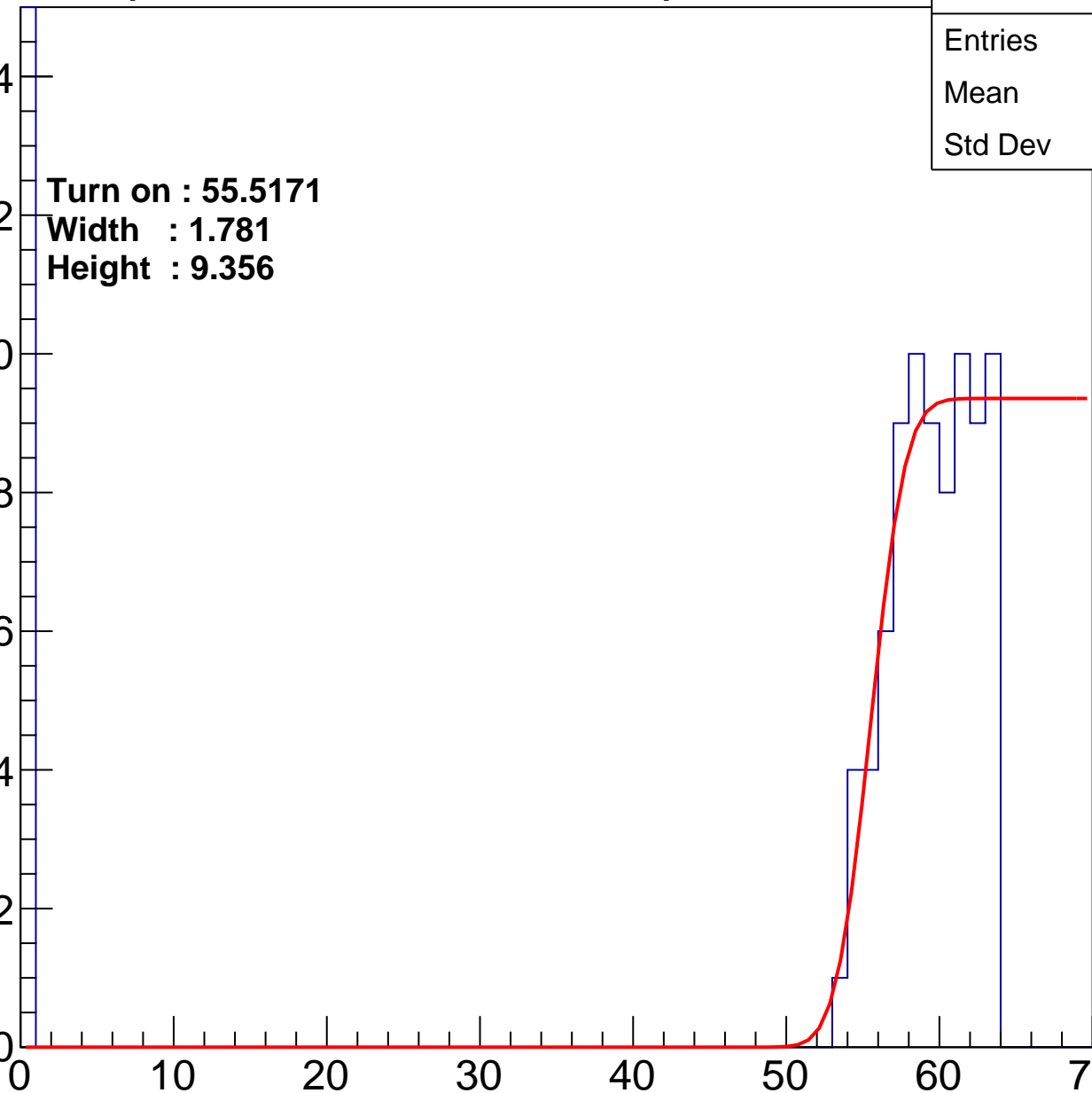
Width : 1.781

Height : 9.356

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	31.66
Std Dev	29.14

Turn on : 55.4609

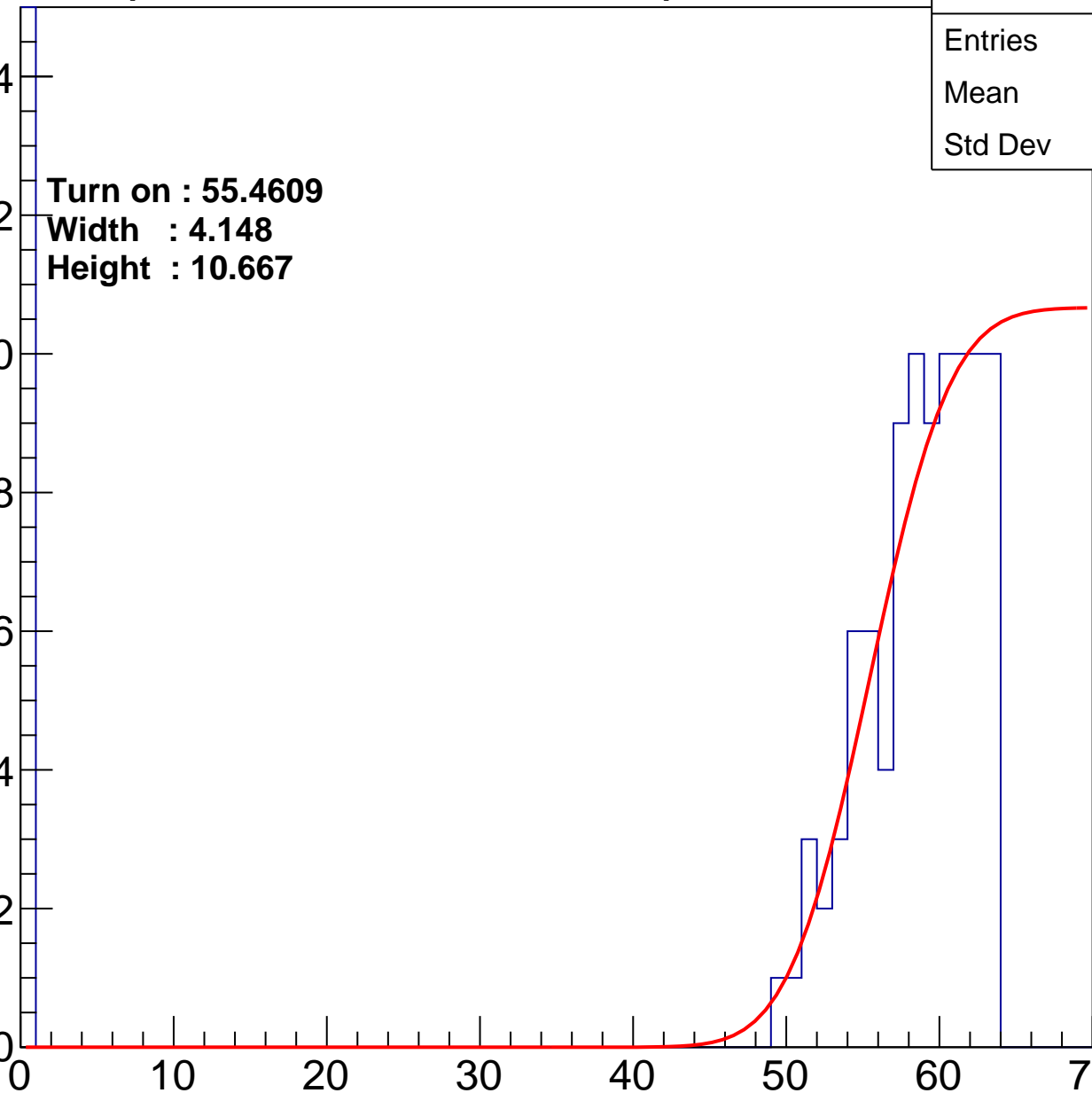
Width : 4.148

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	31.22
Std Dev	28.77

Turn on : 53.2660

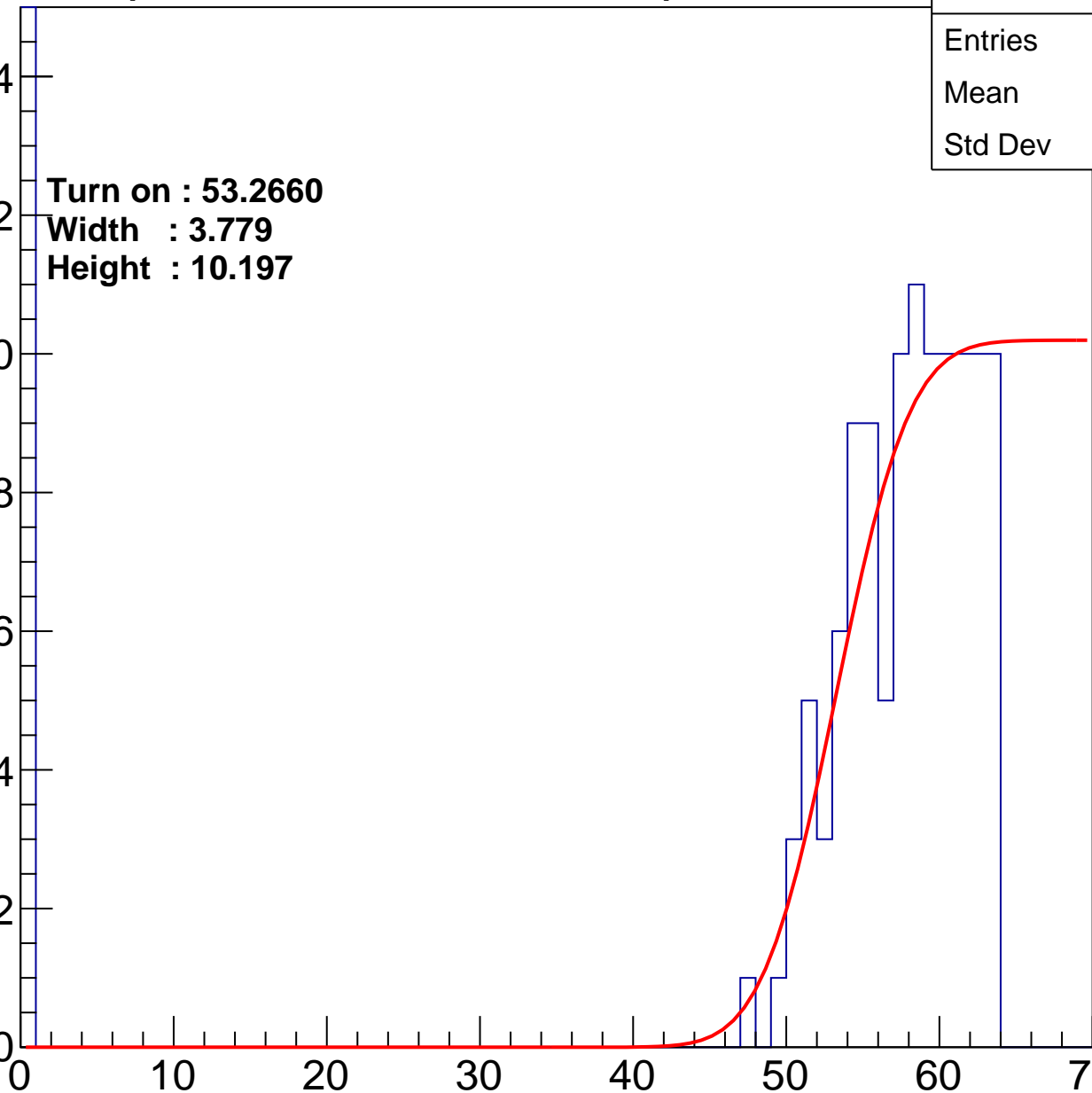
Width : 3.779

Height : 10.197

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	38.65
Std Dev	26.6

Turn on : 51.0988

Width : 2.819

Height : 10.001

Entry

14

12

10

8

6

4

2

0

0

10

20

30

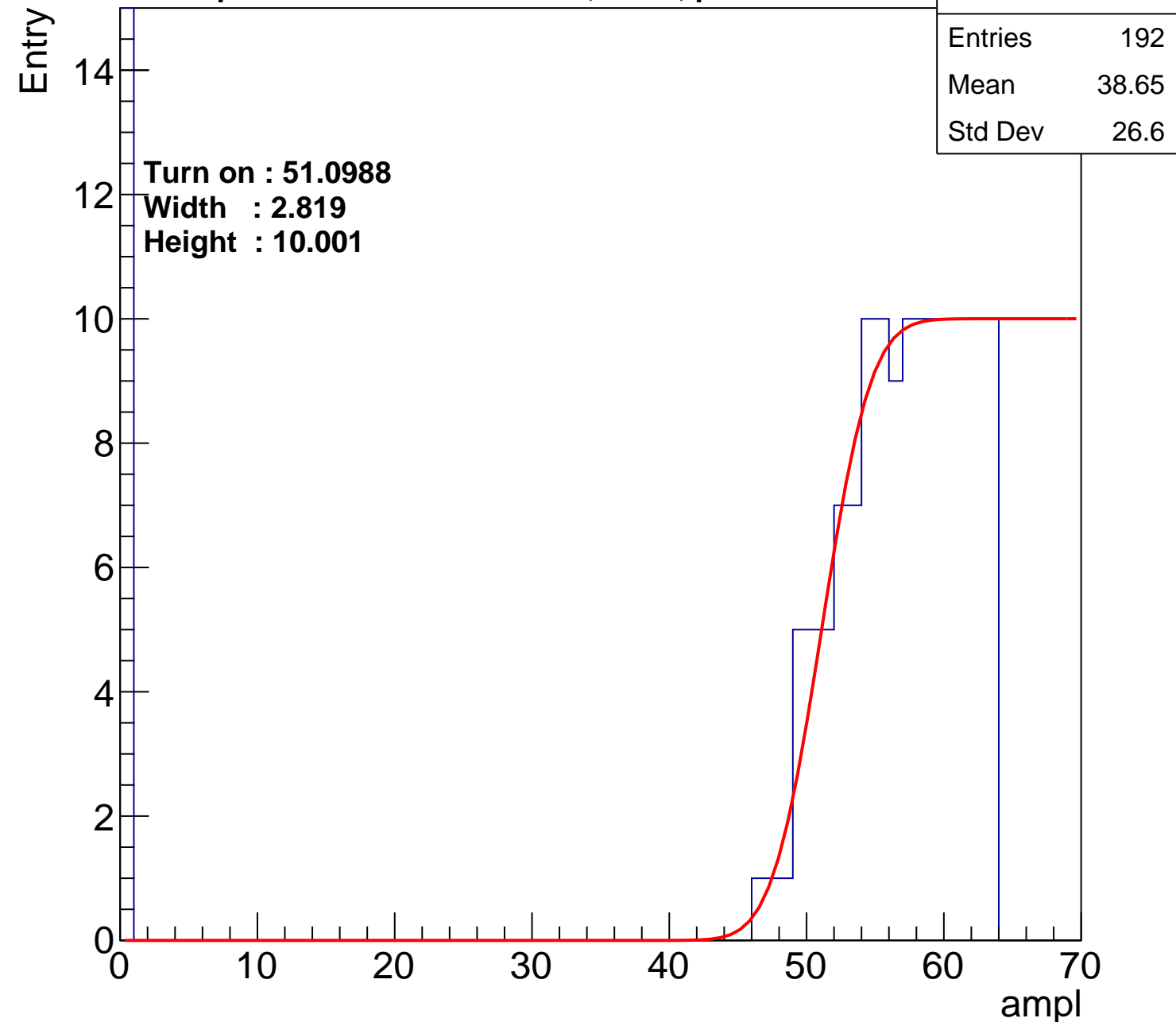
40

50

60

70

ampl



B1L104S, U19-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	38.59
Std Dev	27.22

Turn on : 53.4700

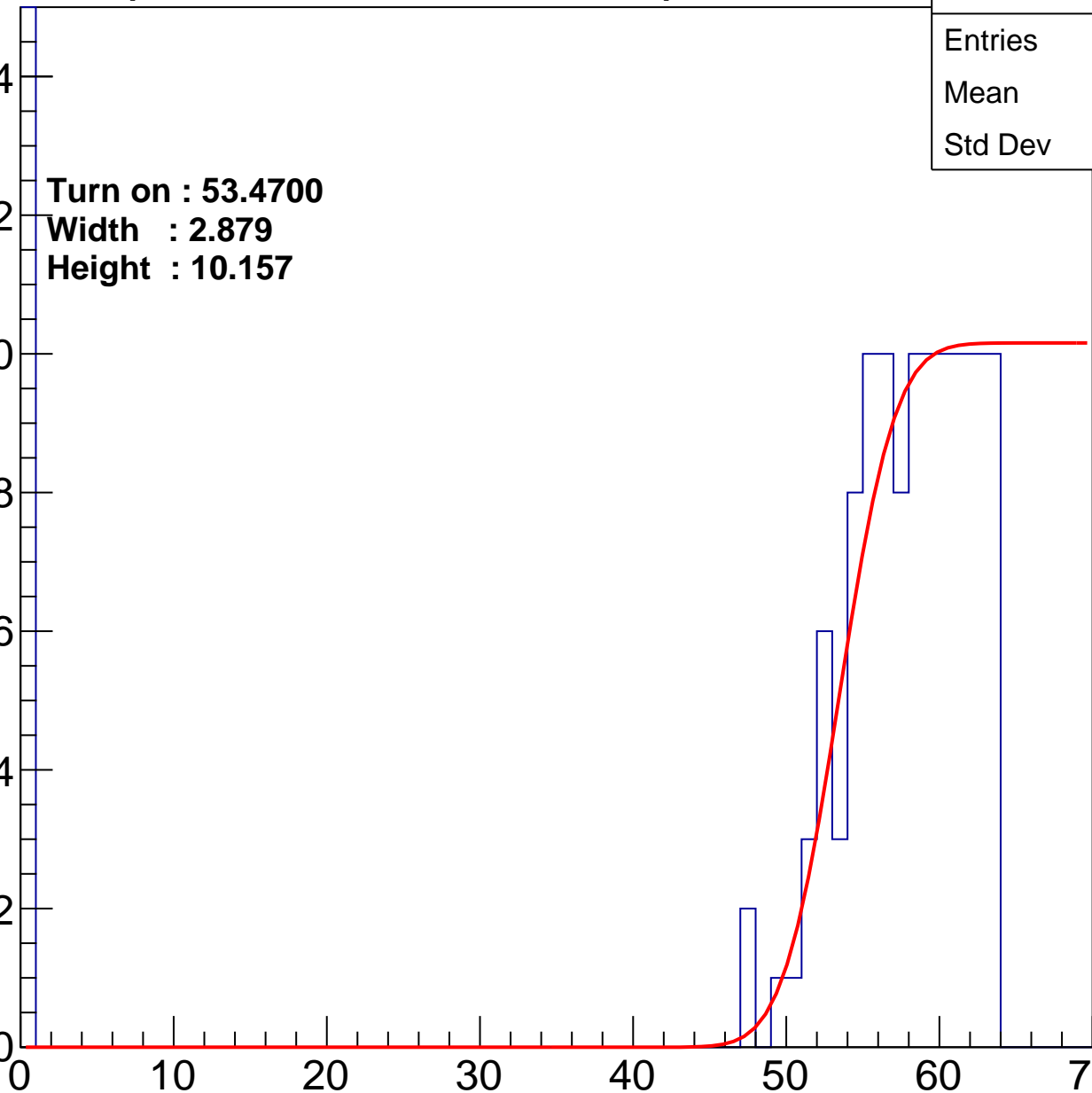
Width : 2.879

Height : 10.157

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	31.66
Std Dev	28.92

Turn on : 54.1416

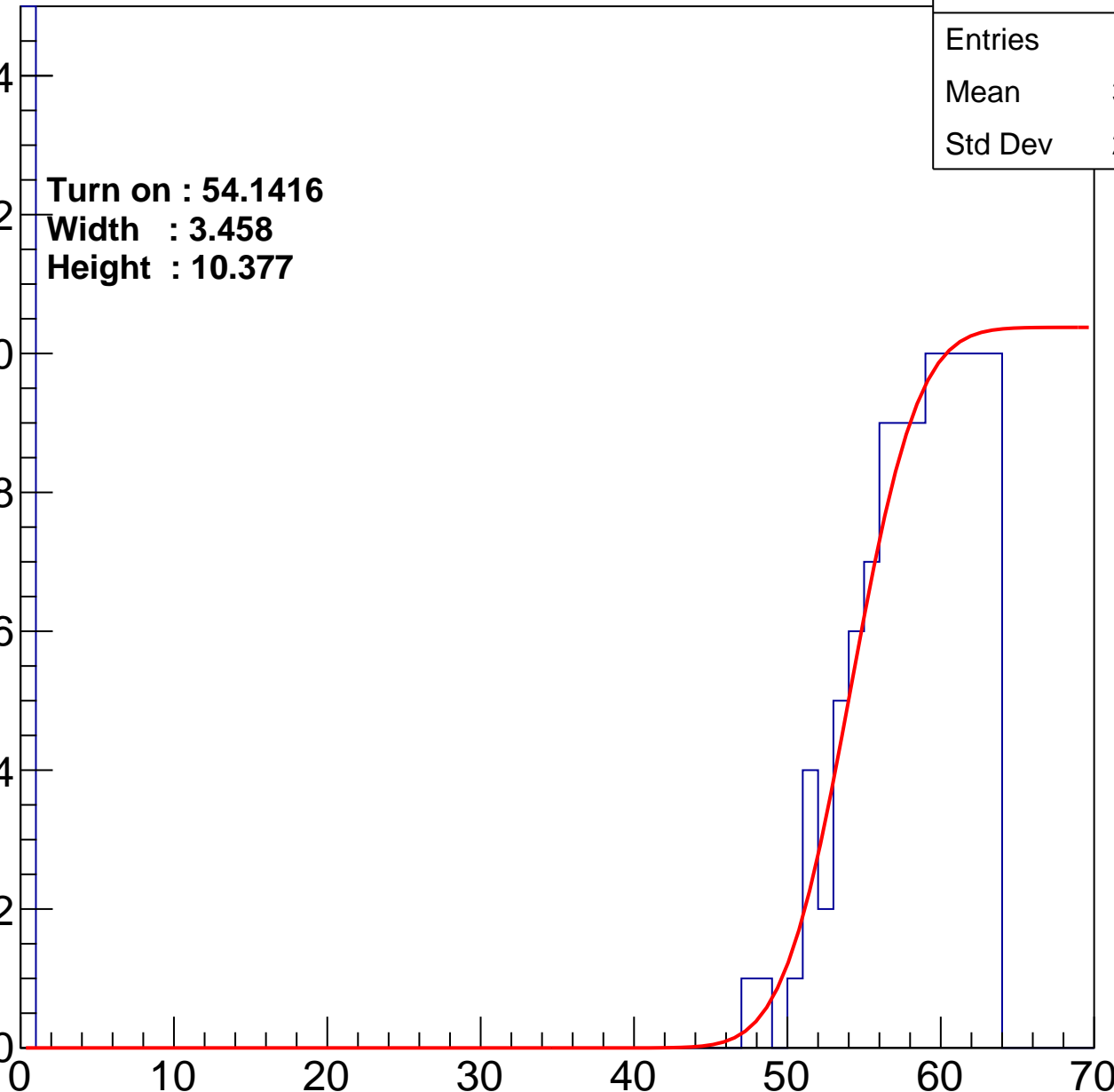
Width : 3.458

Height : 10.377

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch75

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	33.87
Std Dev	28.97

Turn on : 54.8876

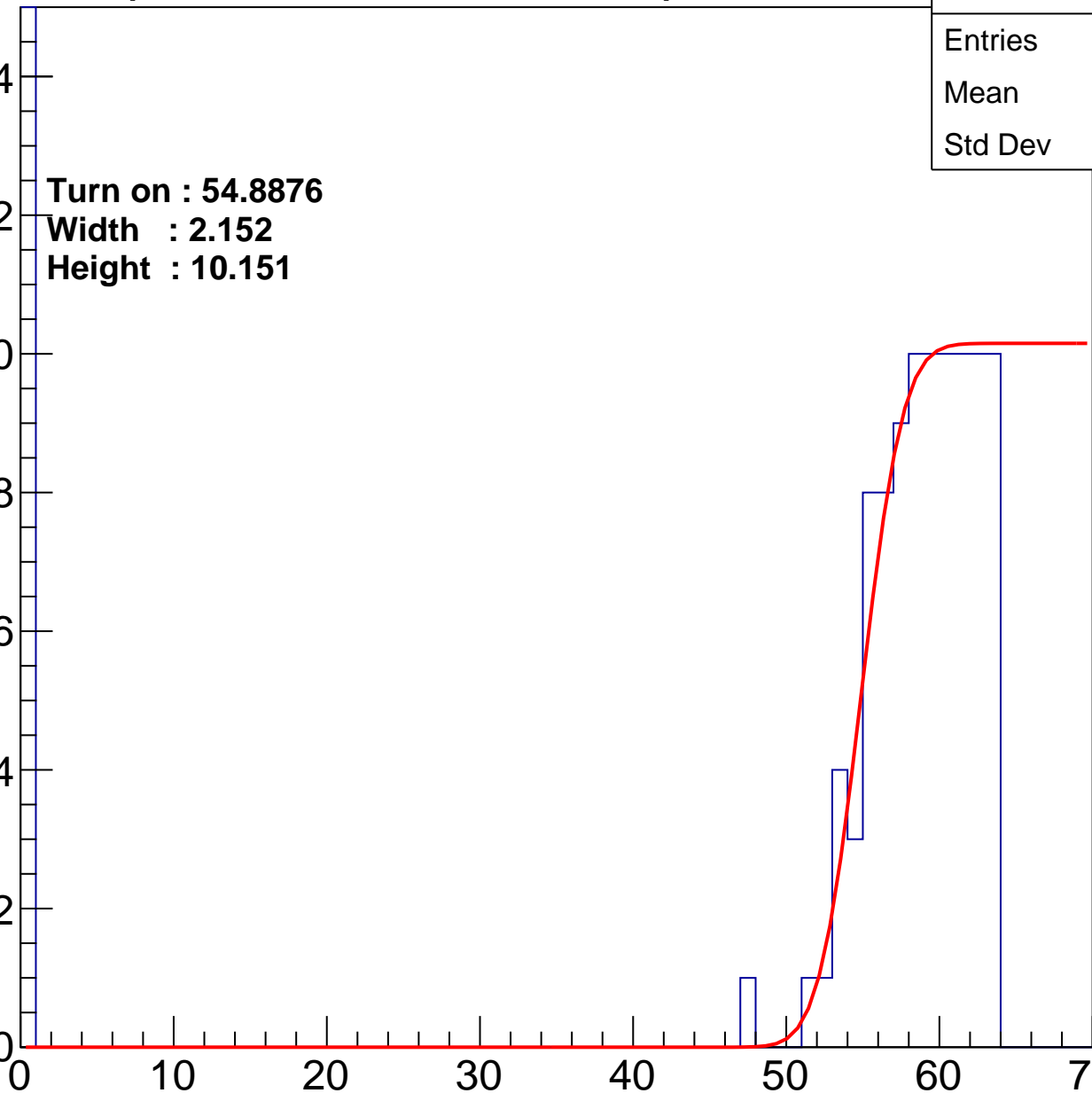
Width : 2.152

Height : 10.151

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch76

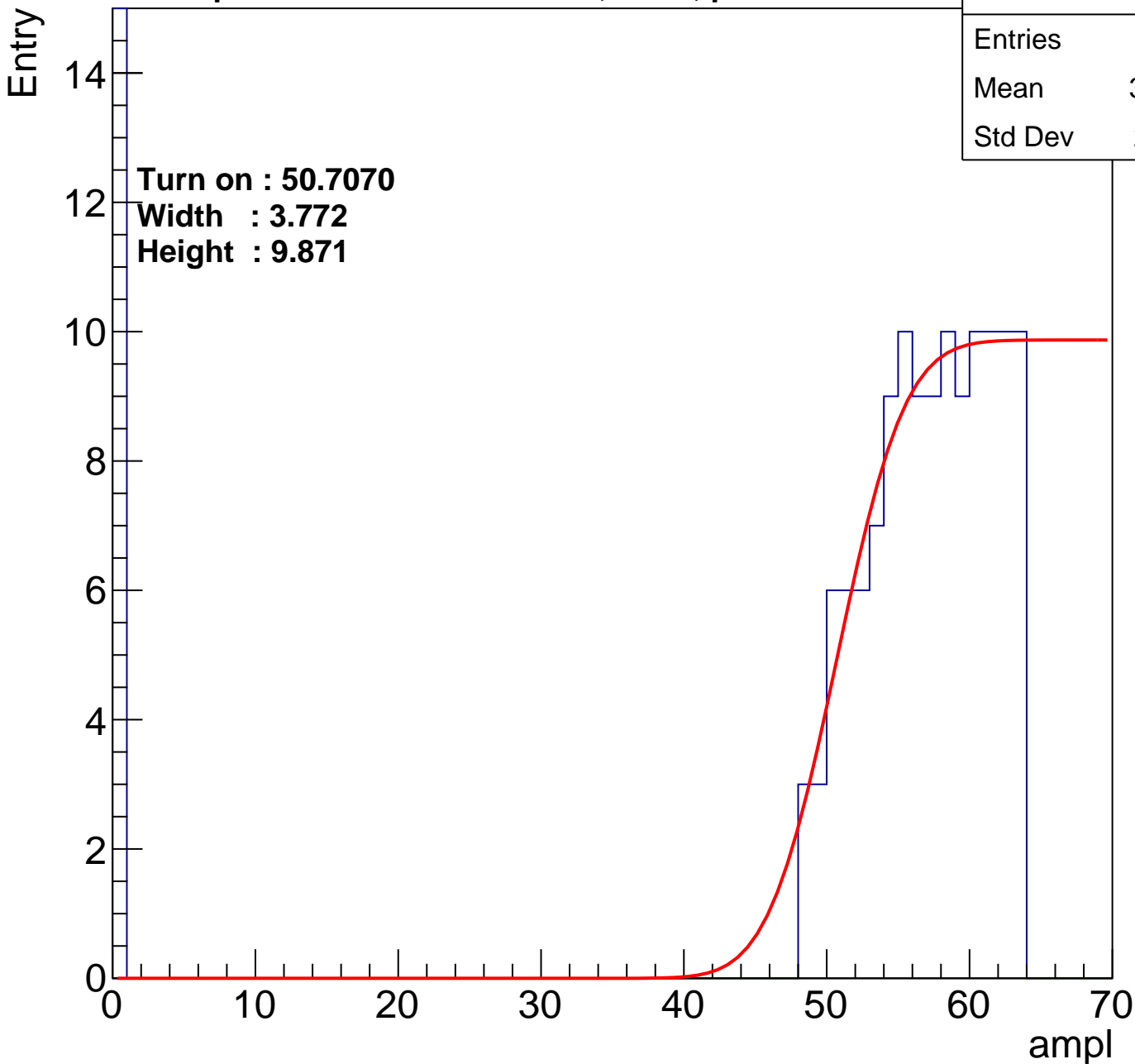
calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	36.38
Std Dev	27.41

Turn on : 50.7070

Width : 3.772

Height : 9.871



B1L104S, U19-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	35.25
Std Dev	27.71

Turn on : 53.2533

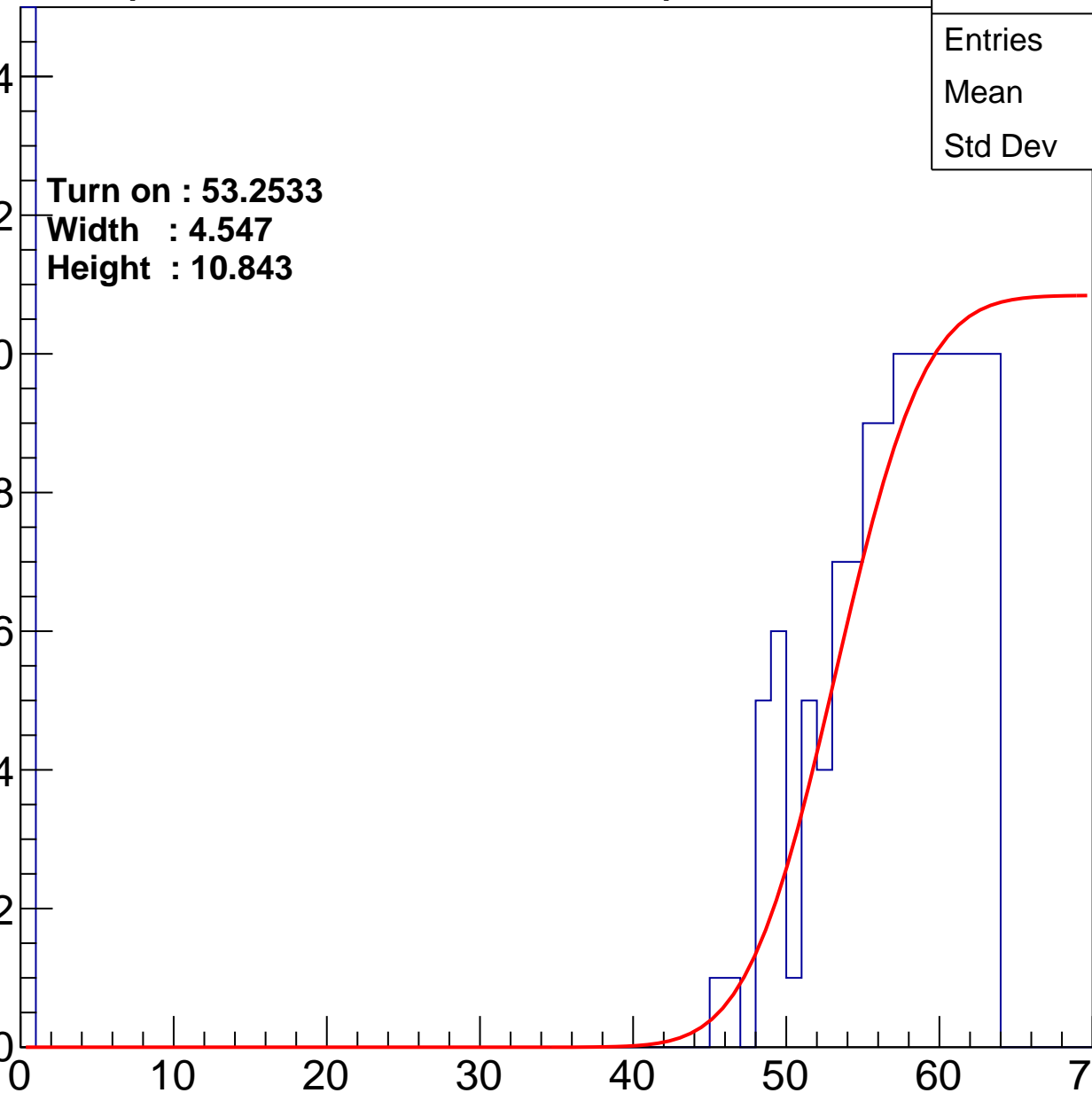
Width : 4.547

Height : 10.843

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch78

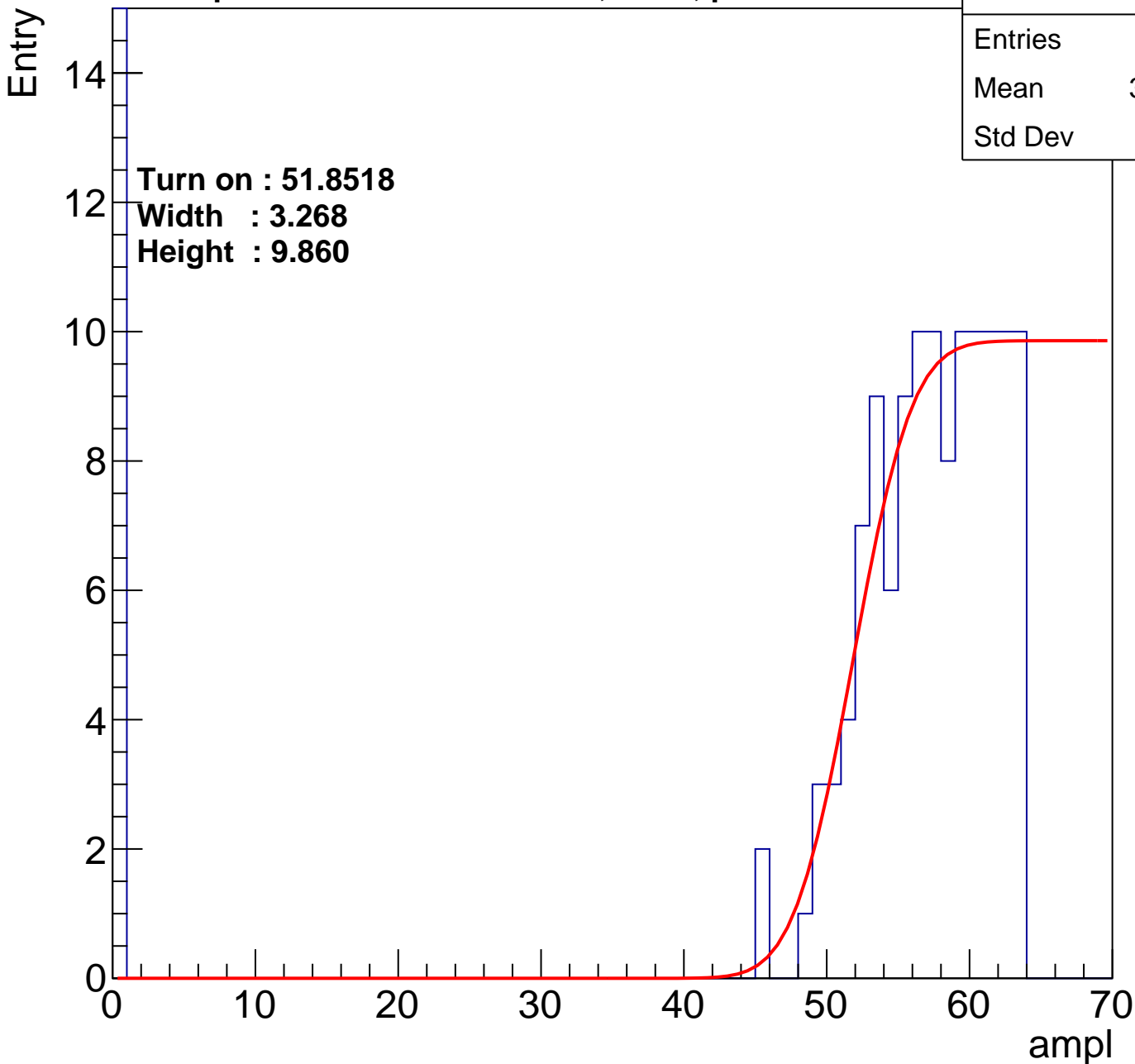
calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	36.16
Std Dev	27.6

Turn on : 51.8518

Width : 3.268

Height : 9.860



B1L104S, U19-ch79

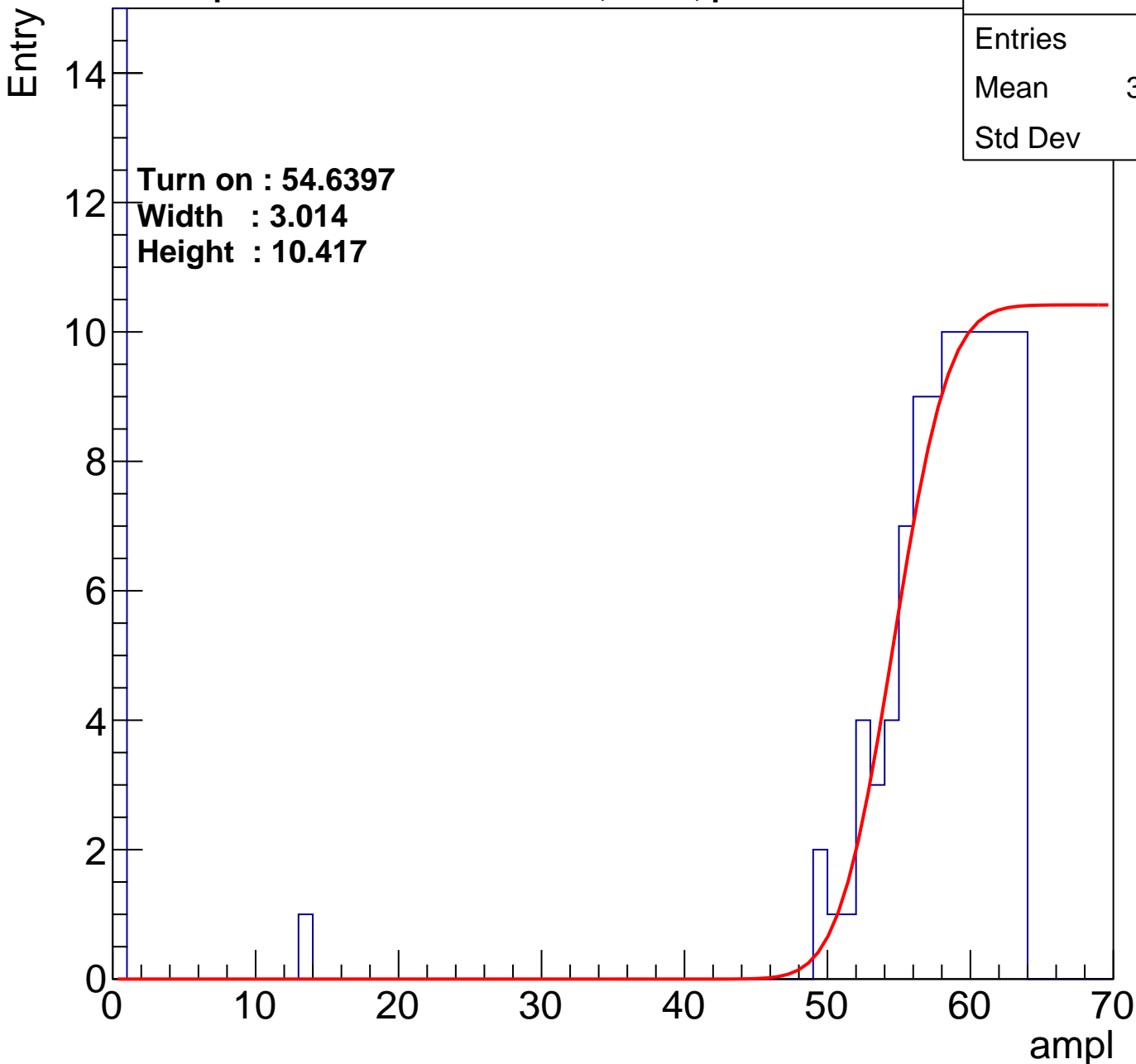
calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	34.08
Std Dev	28.7

Turn on : 54.6397

Width : 3.014

Height : 10.417



B1L104S, U19-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	32.87
Std Dev	29.04

Turn on : 55.4729

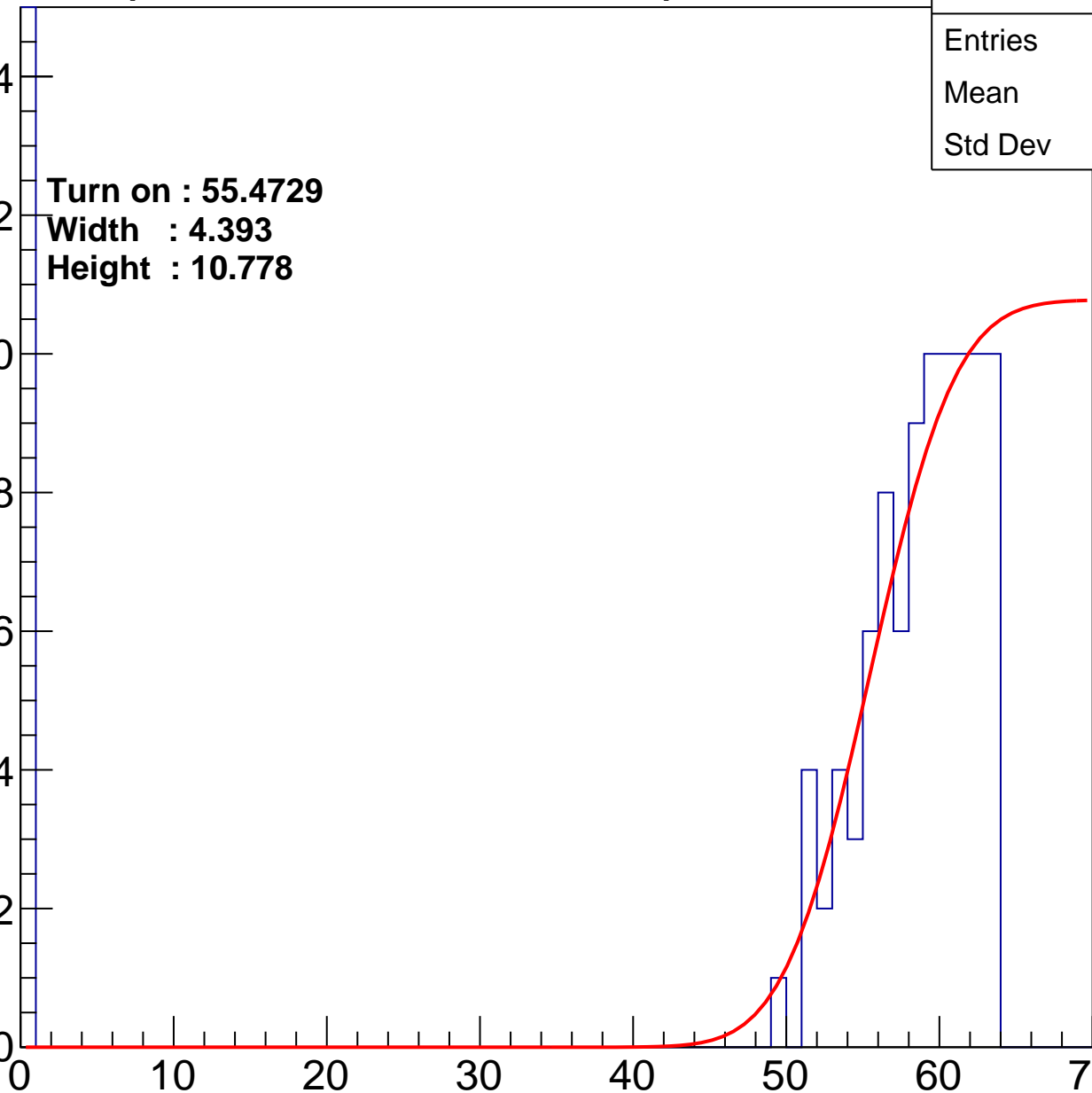
Width : 4.393

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	35.32
Std Dev	28.2

Turn on : 52.9919

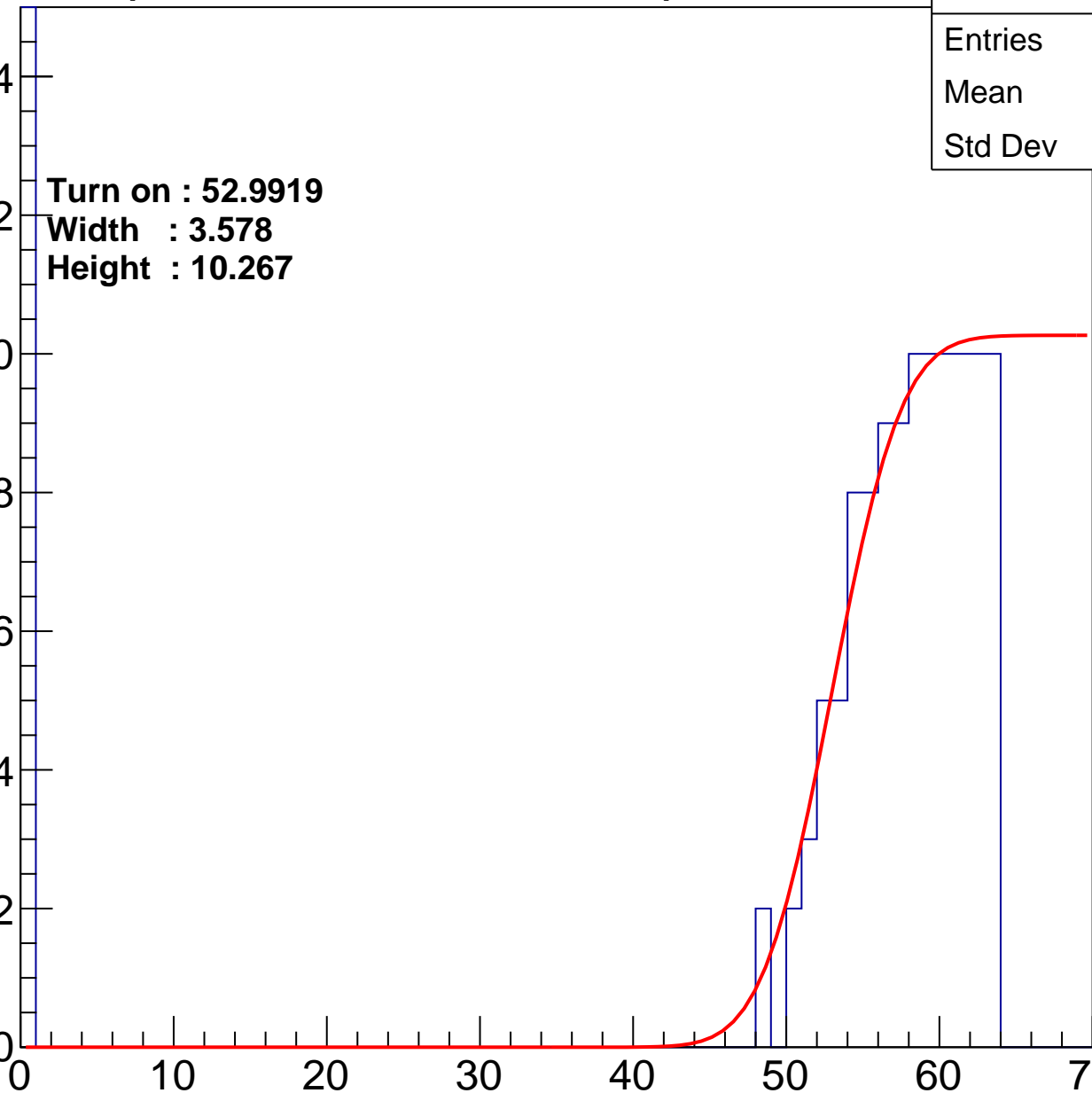
Width : 3.578

Height : 10.267

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	35.08
Std Dev	27.84

Turn on : 51.7996

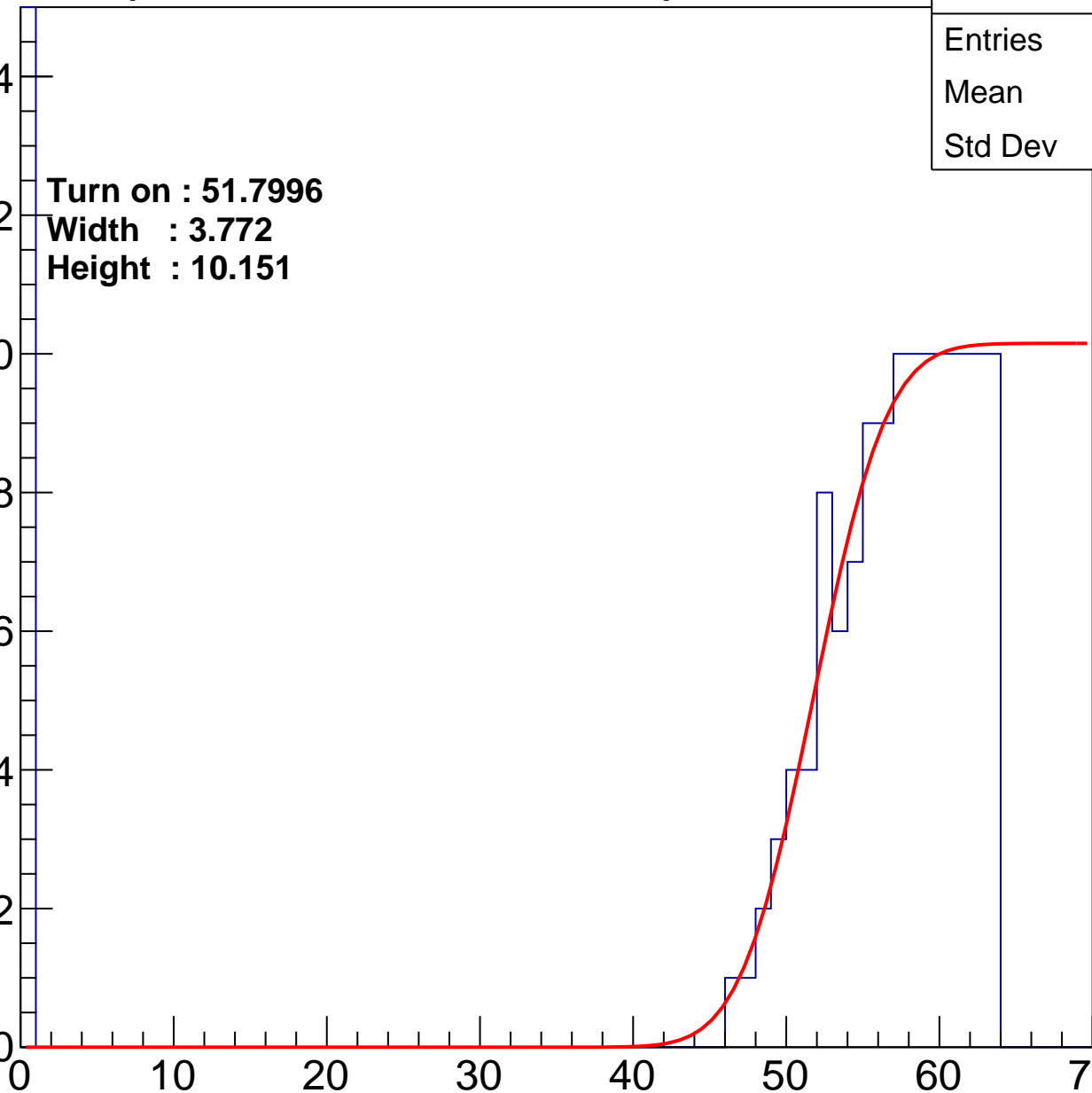
Width : 3.772

Height : 10.151

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	30.13
Std Dev	28.96

Turn on : 54.4201

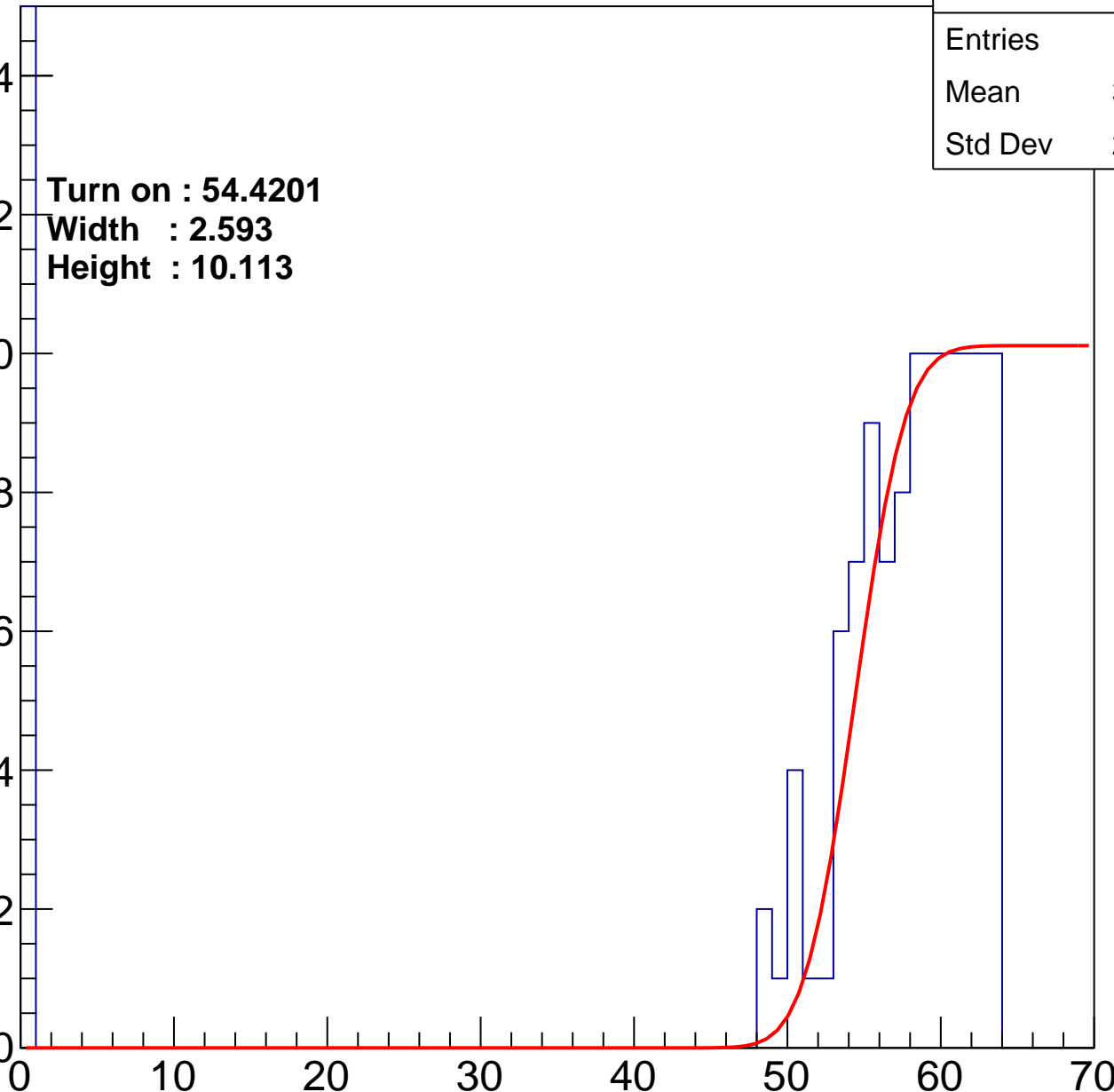
Width : 2.593

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	35.62
Std Dev	28.36

Turn on : 54.2033

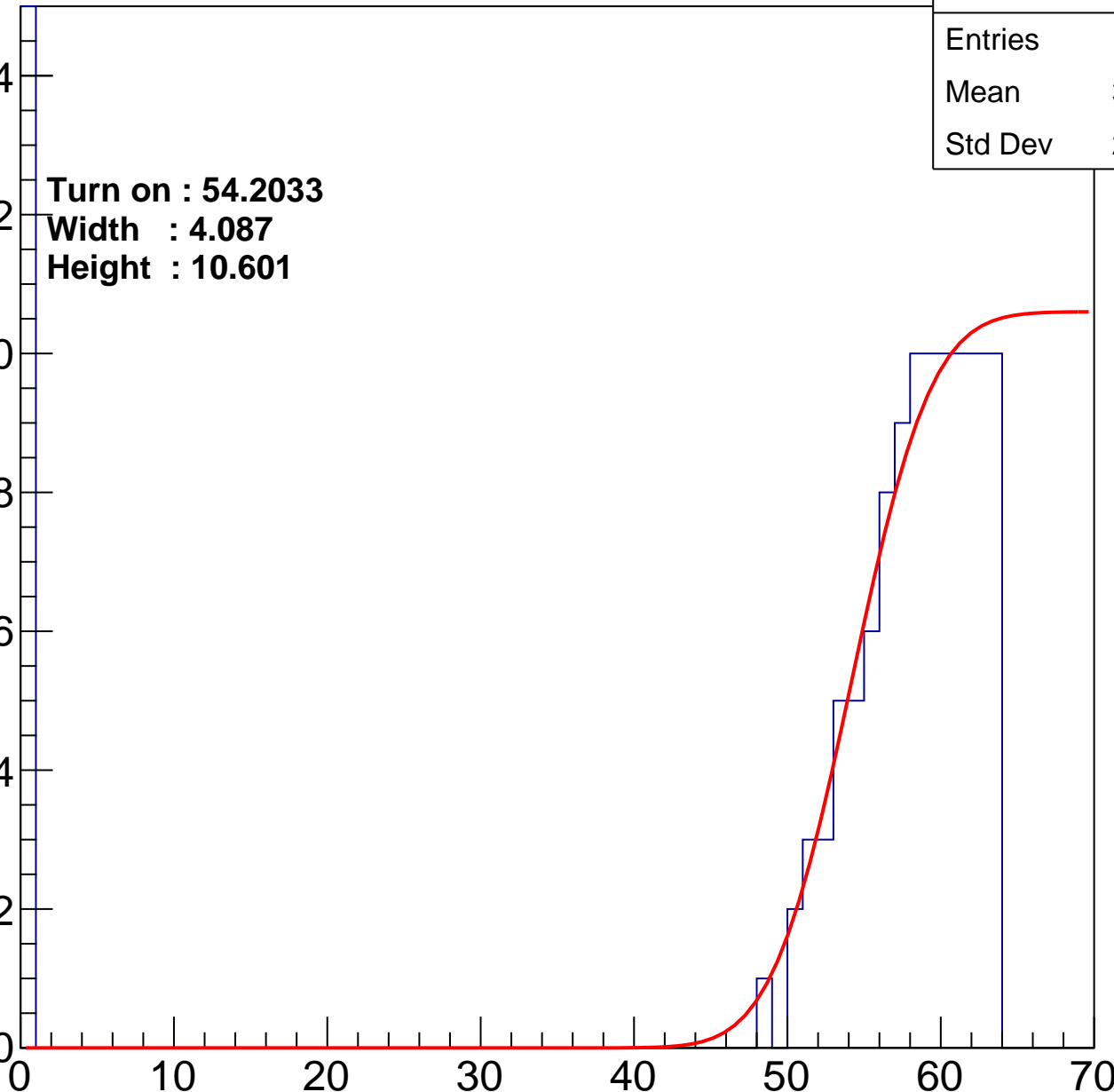
Width : 4.087

Height : 10.601

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	34.52
Std Dev	28.28

Turn on : 52.7629

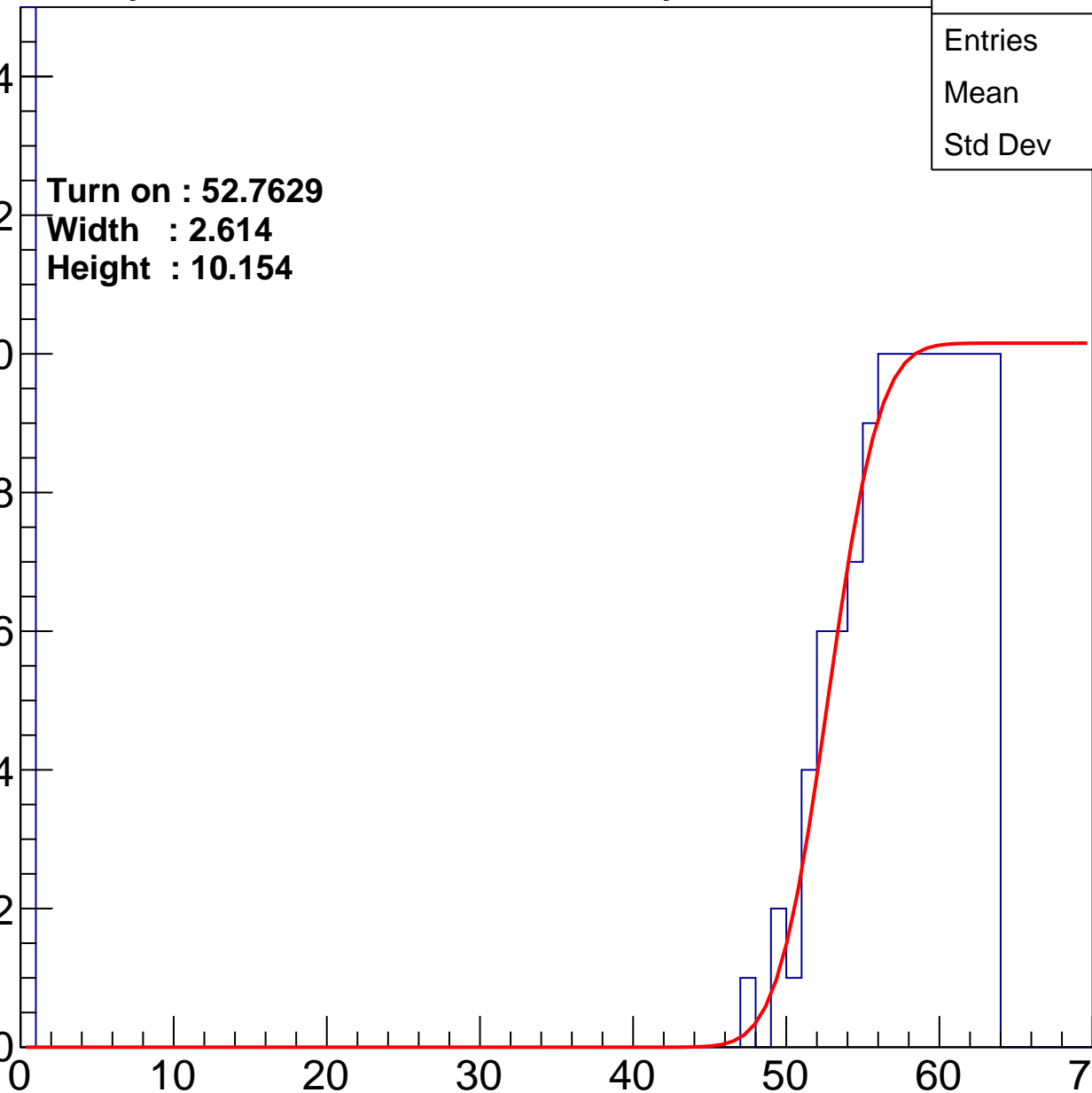
Width : 2.614

Height : 10.154

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	32.09
Std Dev	28.8

Turn on : 54.6073

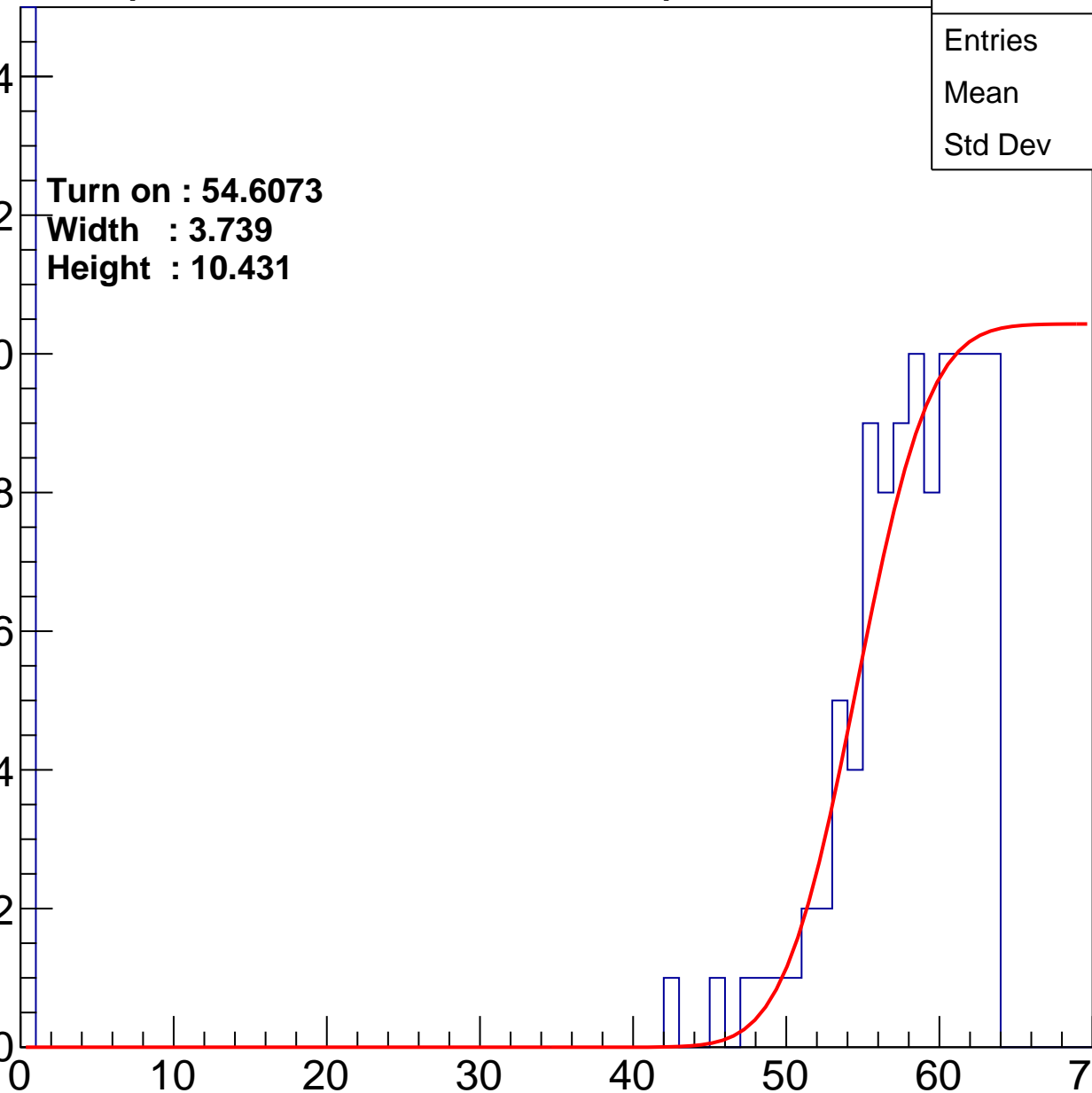
Width : 3.739

Height : 10.431

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch87

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	31.17
Std Dev	28.82

Turn on : 53.1343

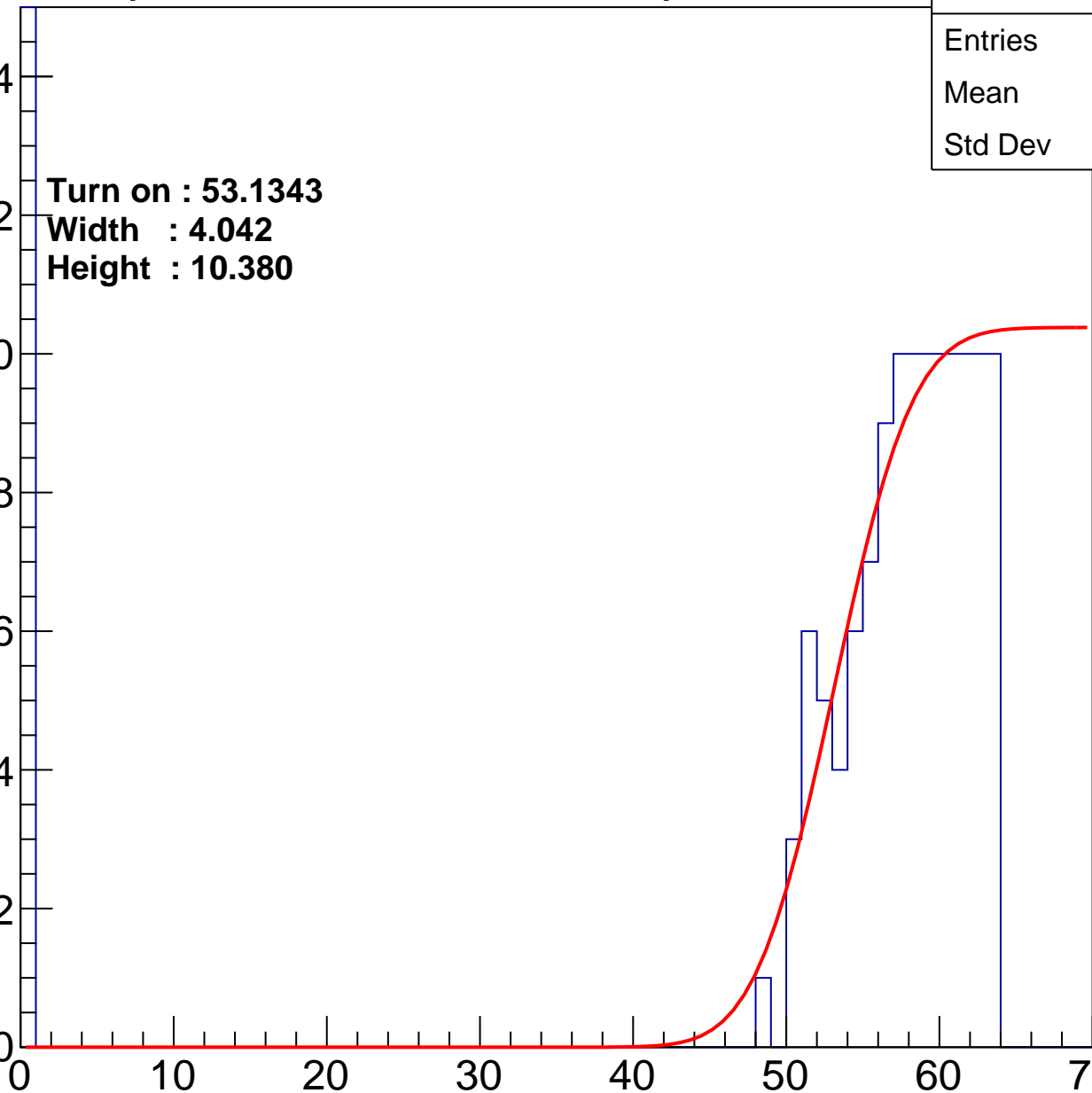
Width : 4.042

Height : 10.380

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch88

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	28.31
Std Dev	29.3

Turn on : 55.1888

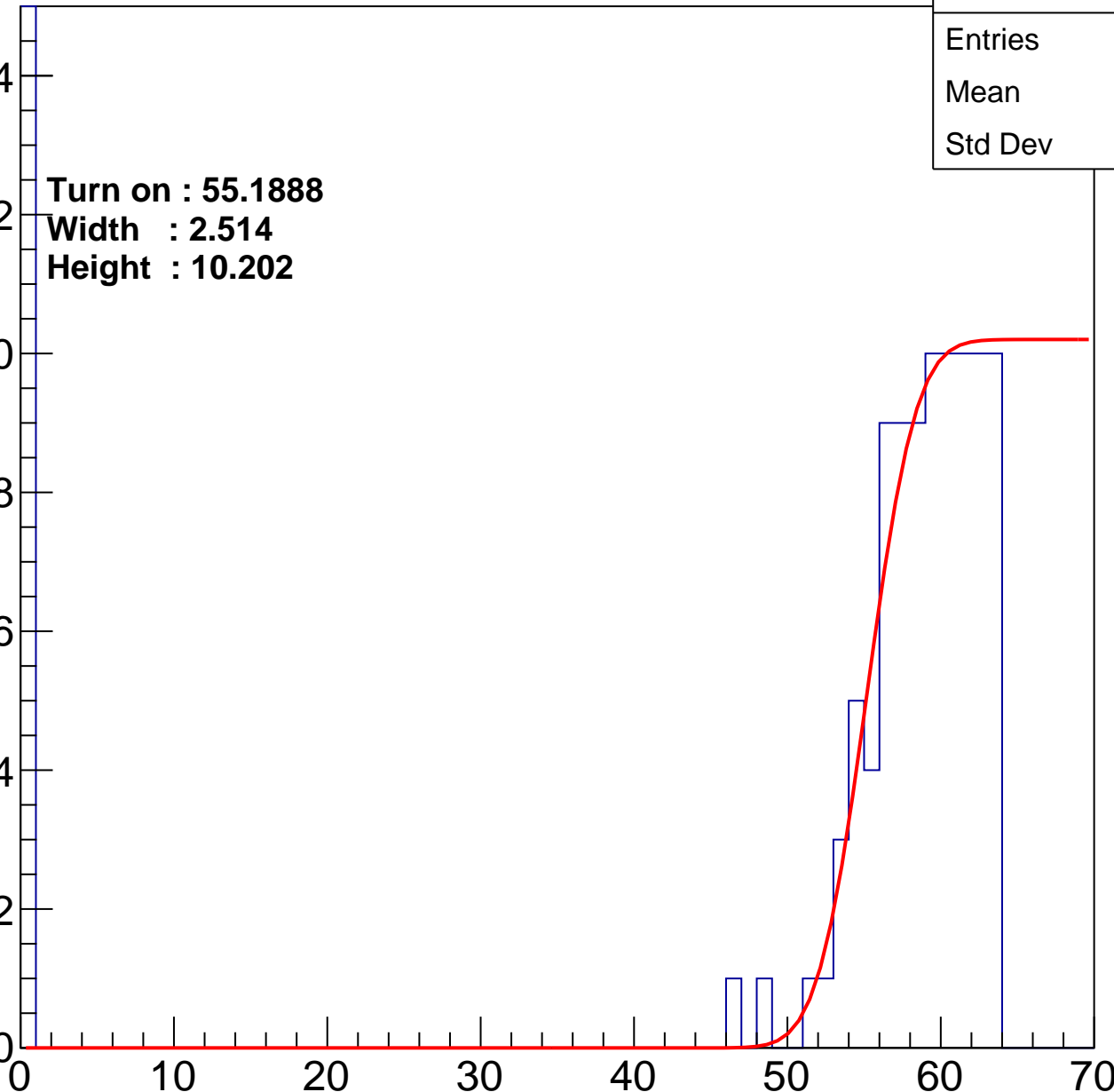
Width : 2.514

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	32.02
Std Dev	29.09

Turn on : 55.8846

Width : 4.045

Height : 9.889

Entry

70

60

50

40

30

20

10

0

0

10

20

30

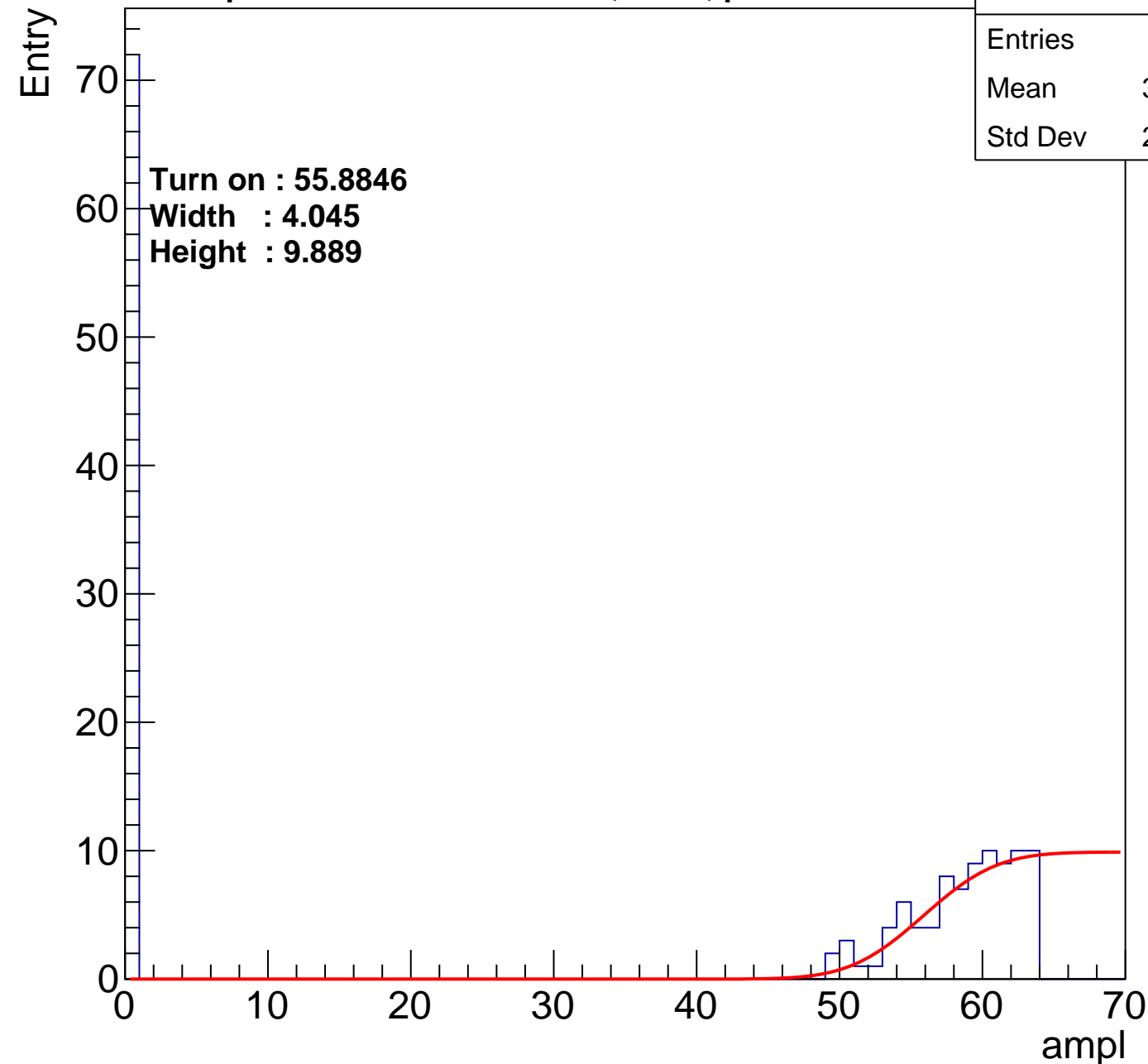
40

50

60

70

ampl



B1L104S, U19-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	229
Mean	30.4
Std Dev	28.62

Turn on : 52.5834

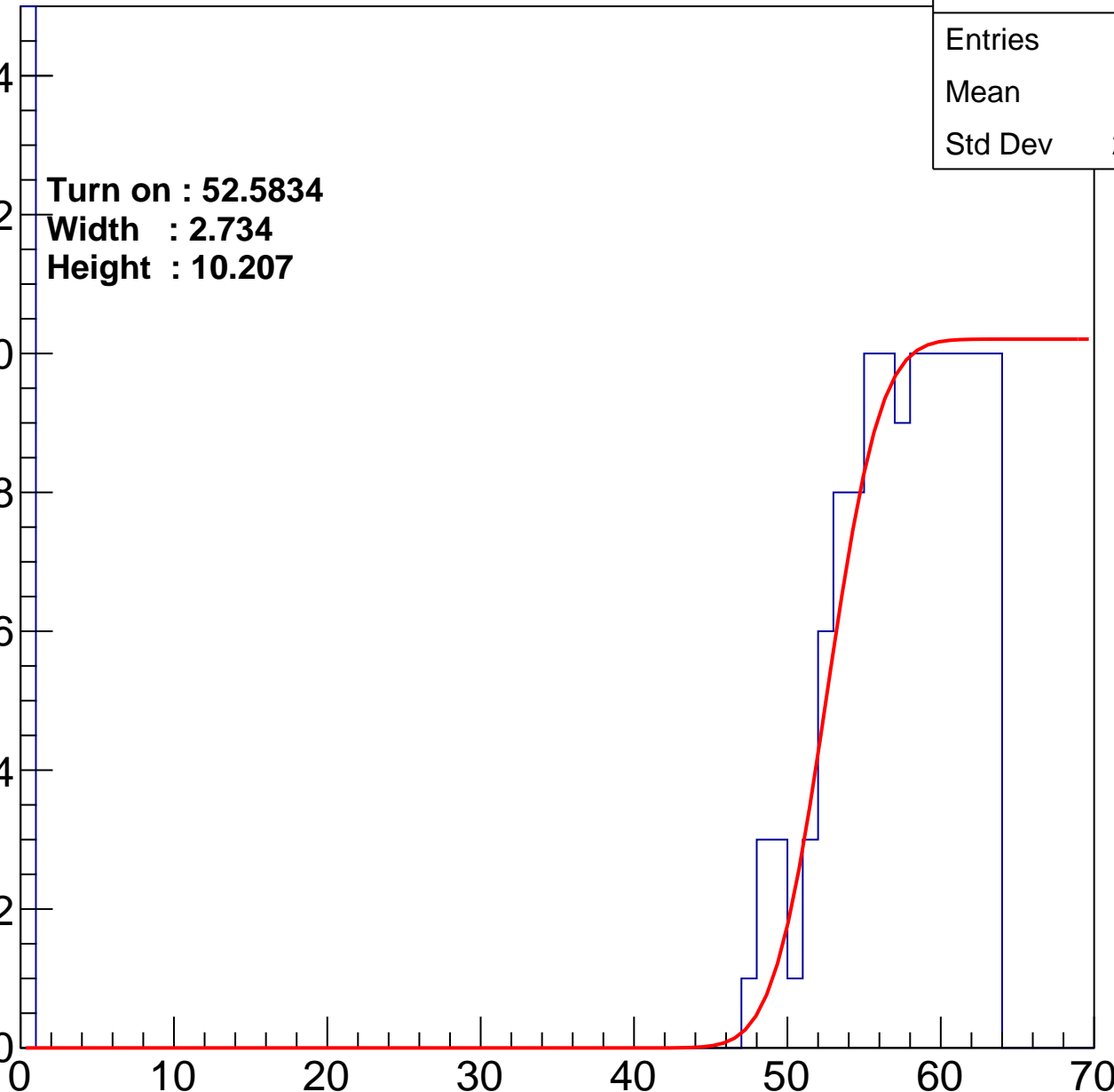
Width : 2.734

Height : 10.207

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	30.11
Std Dev	29.14

Turn on : 55.2489

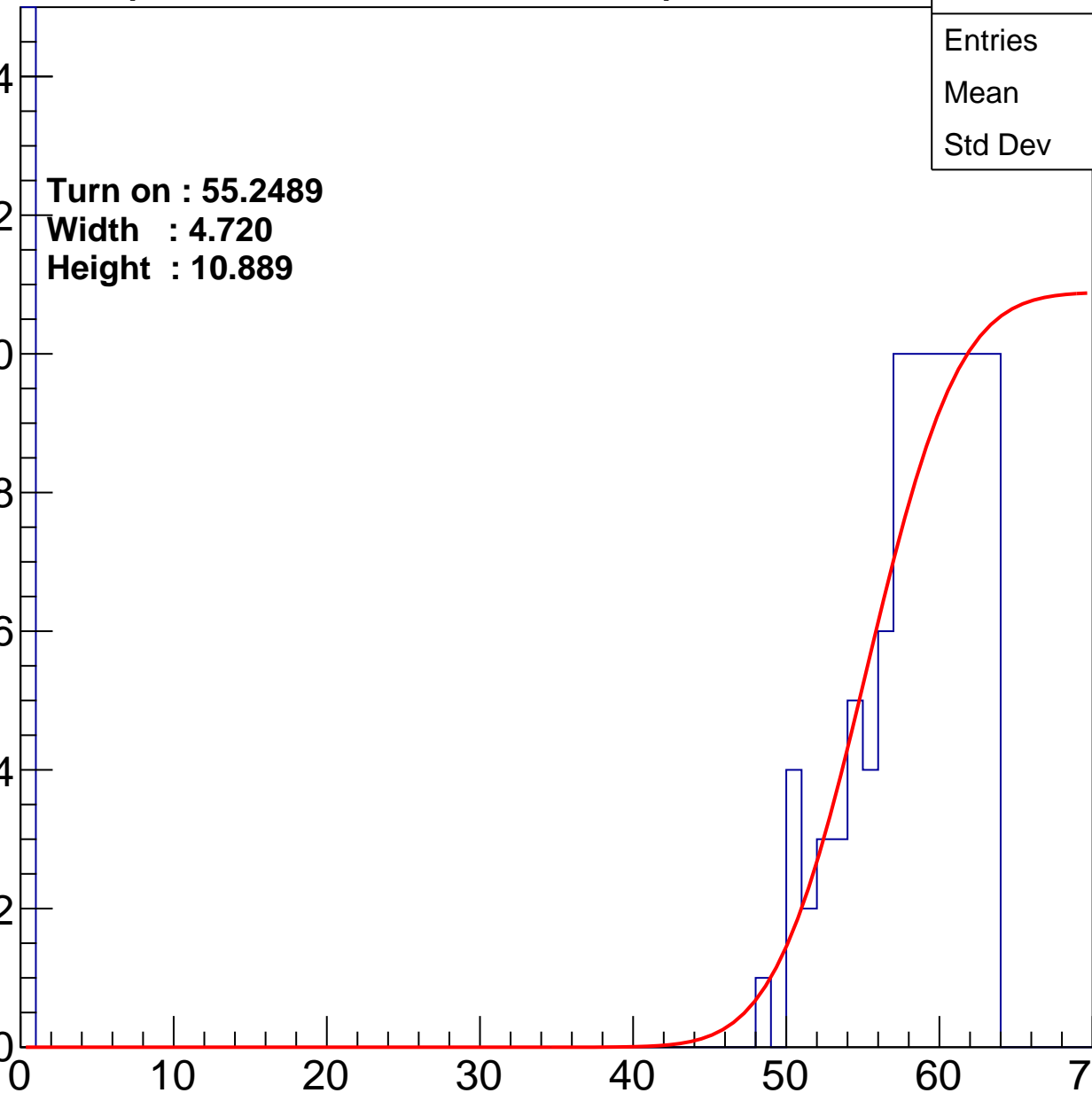
Width : 4.720

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch92

calib_packv5_033123_0516.root, FC#4, port A1

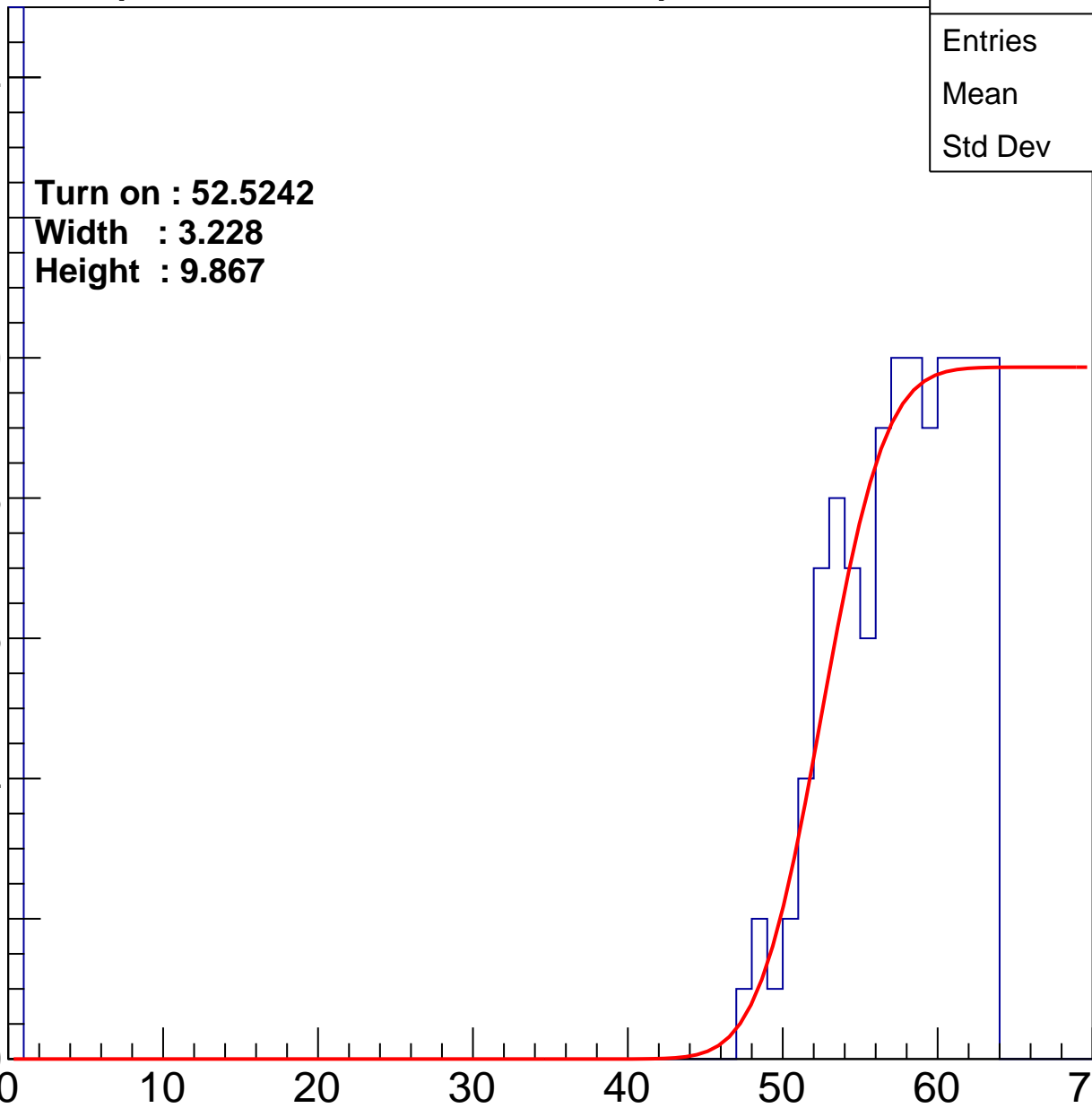
Entry

14
12
10
8
6
4
2
0

Turn on : 52.5242
Width : 3.228
Height : 9.867

Entries	188
Mean	35.3
Std Dev	27.99

ampl



B1L104S, U19-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	32.9
Std Dev	28.95

Turn on : 54.4226

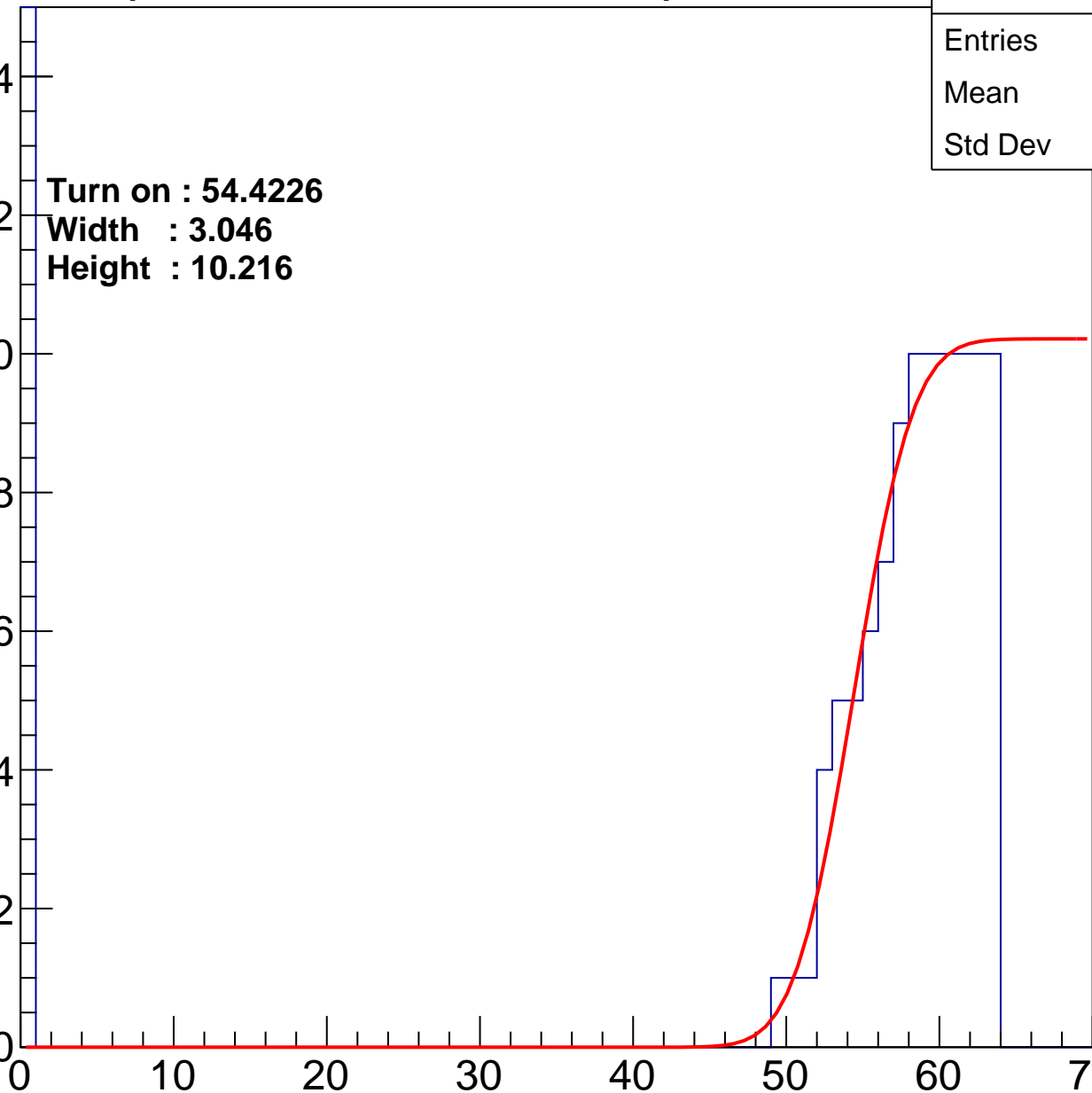
Width : 3.046

Height : 10.216

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch94

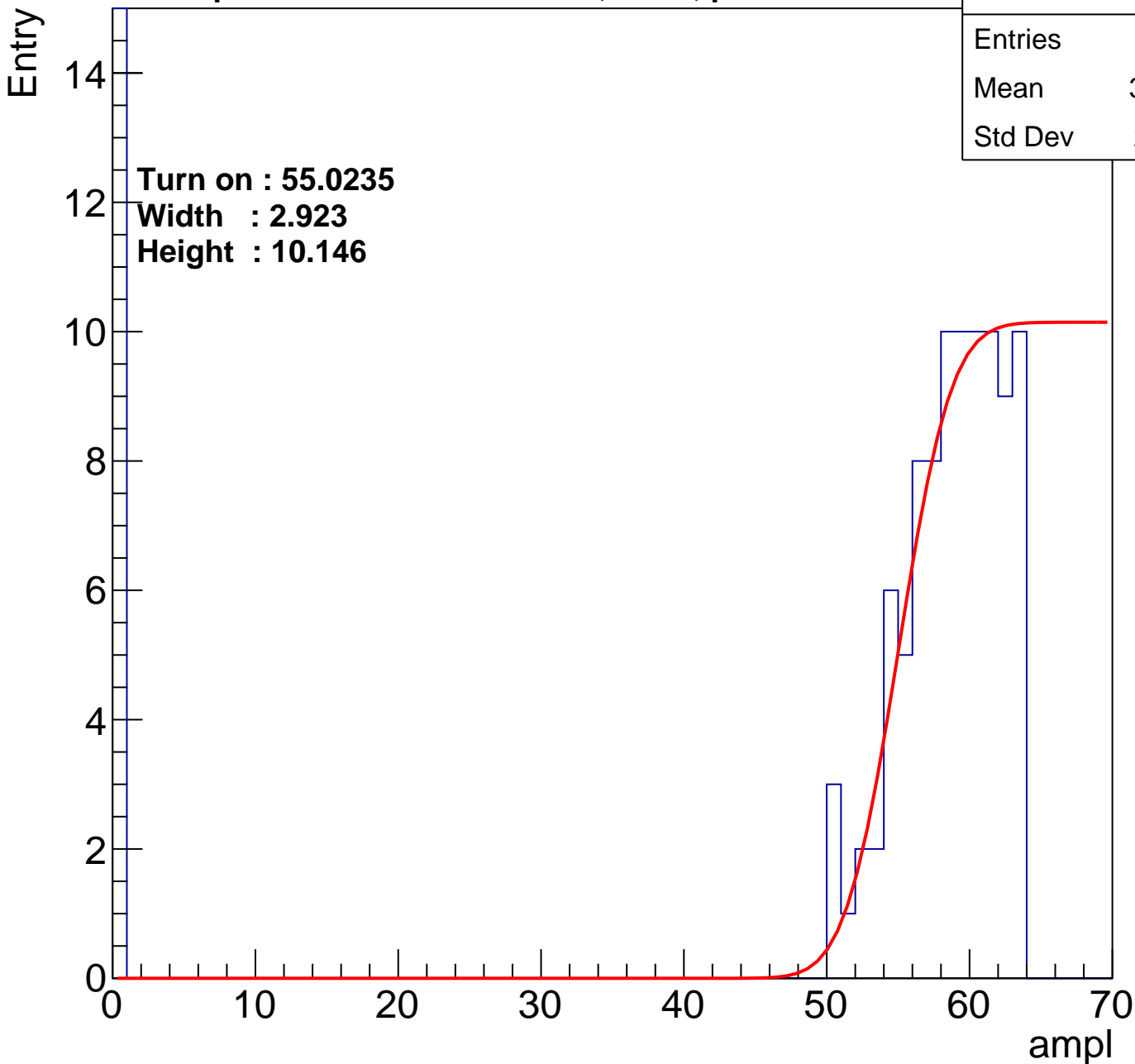
calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	30.97
Std Dev	29.21

Turn on : 55.0235

Width : 2.923

Height : 10.146



B1L104S, U19-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	31.81
Std Dev	29.28

Turn on : 56.1675

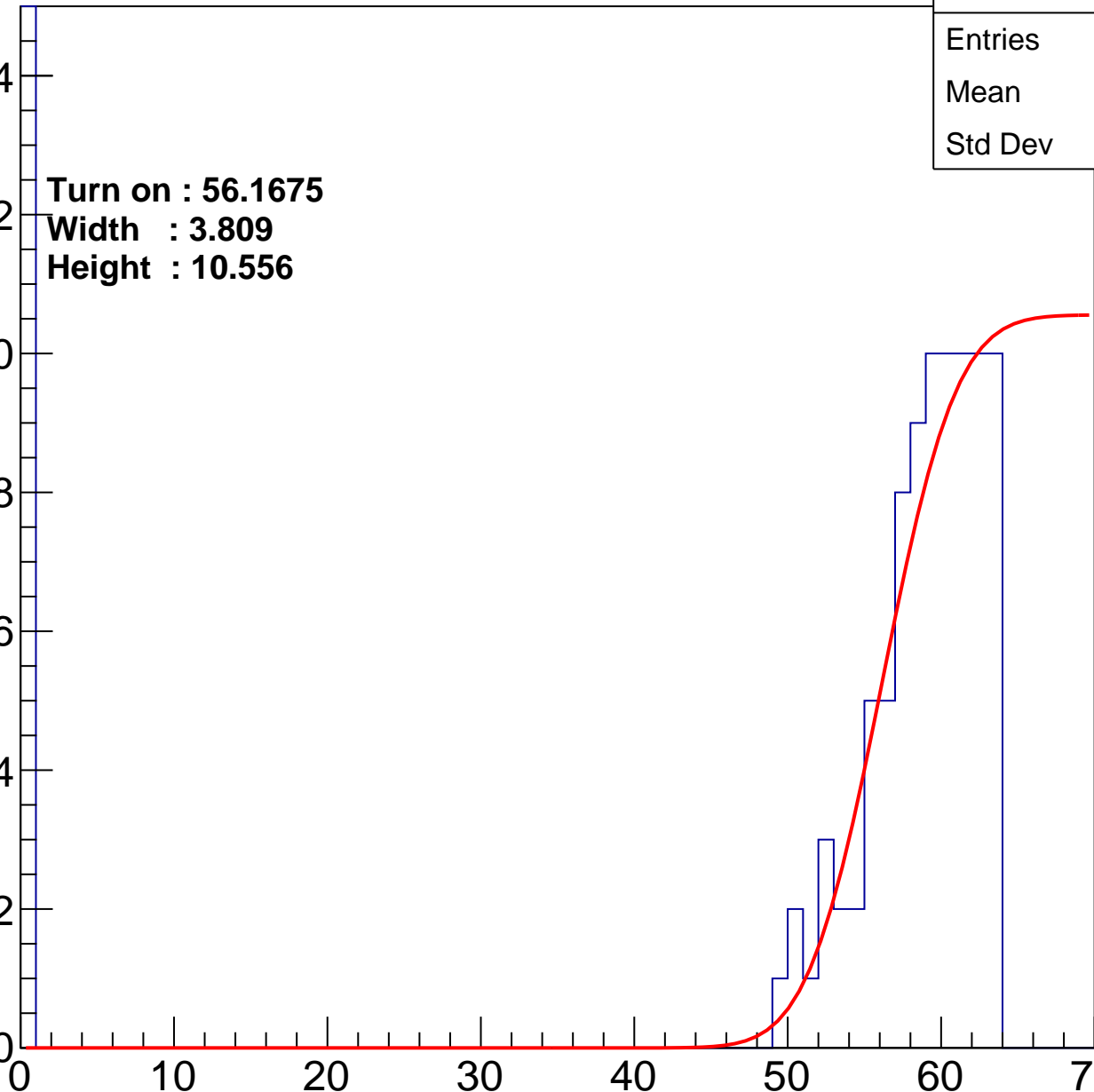
Width : 3.809

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	32.85
Std Dev	28.96

Turn on : 54.6063

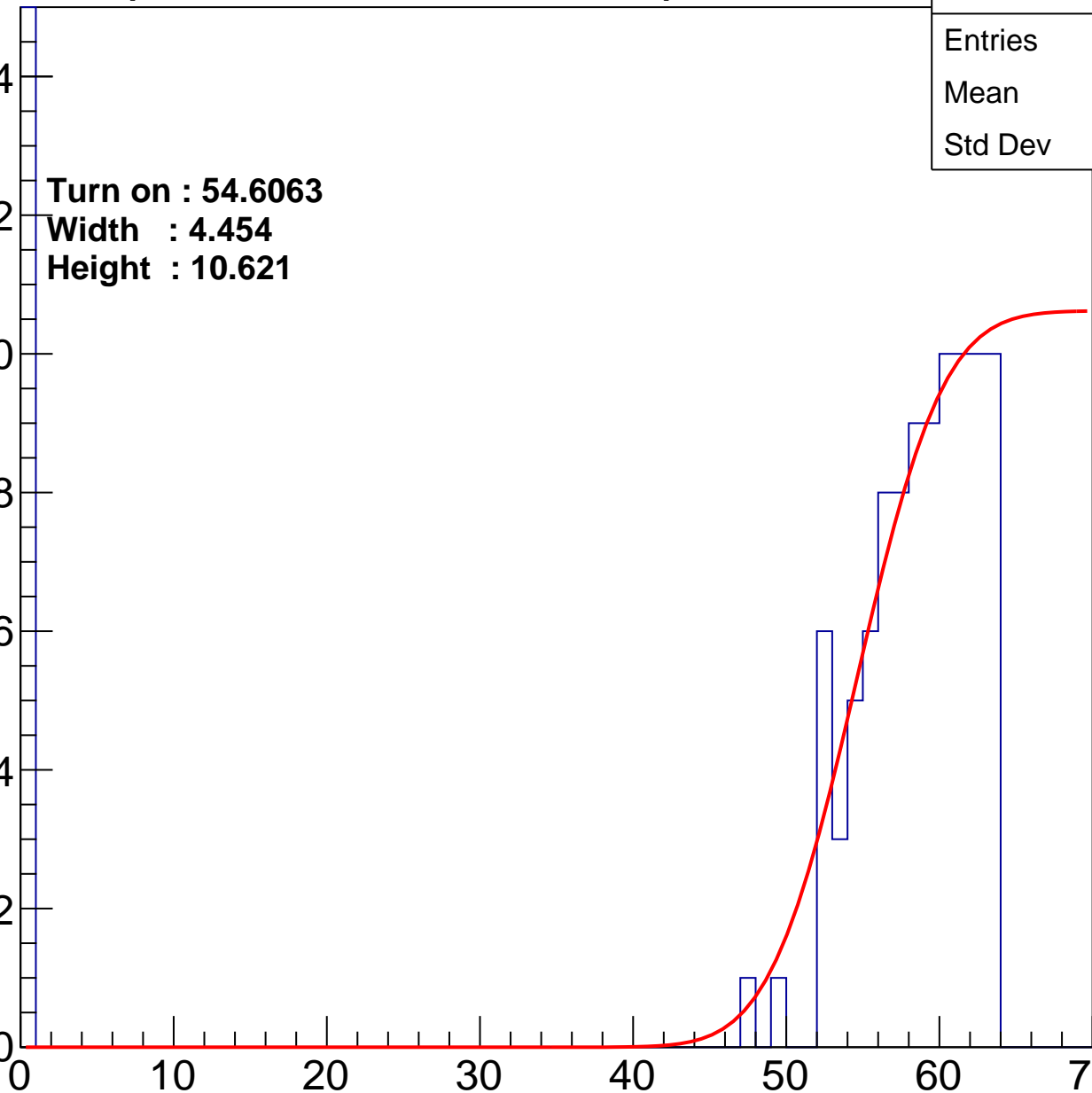
Width : 4.454

Height : 10.621

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	34.32
Std Dev	28.25

Turn on : 52.2286

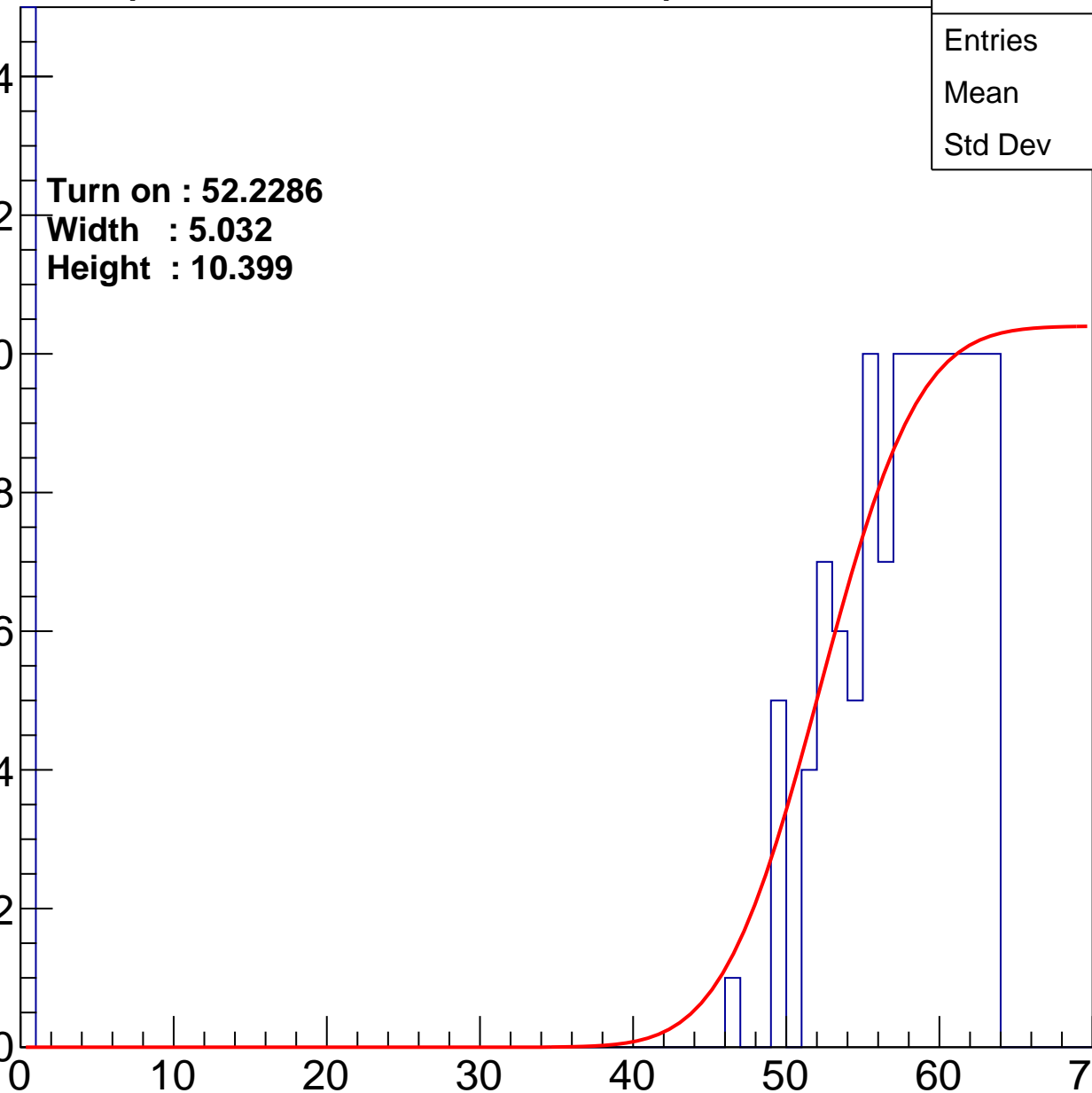
Width : 5.032

Height : 10.399

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch98

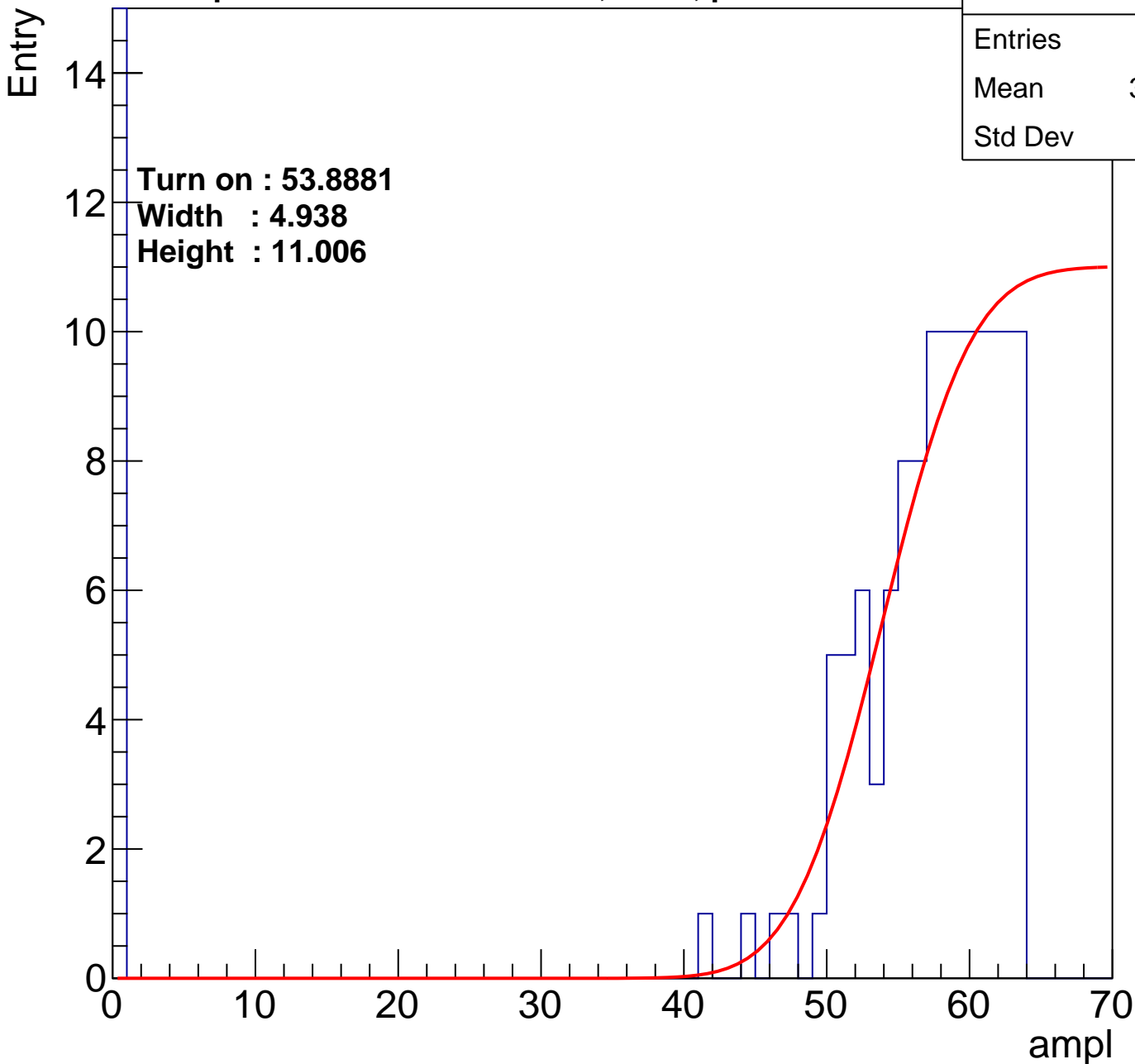
calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	31.96
Std Dev	28.5

Turn on : 53.8881

Width : 4.938

Height : 11.006



B1L104S, U19-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.48
Std Dev	28.86

Turn on : 54.0879

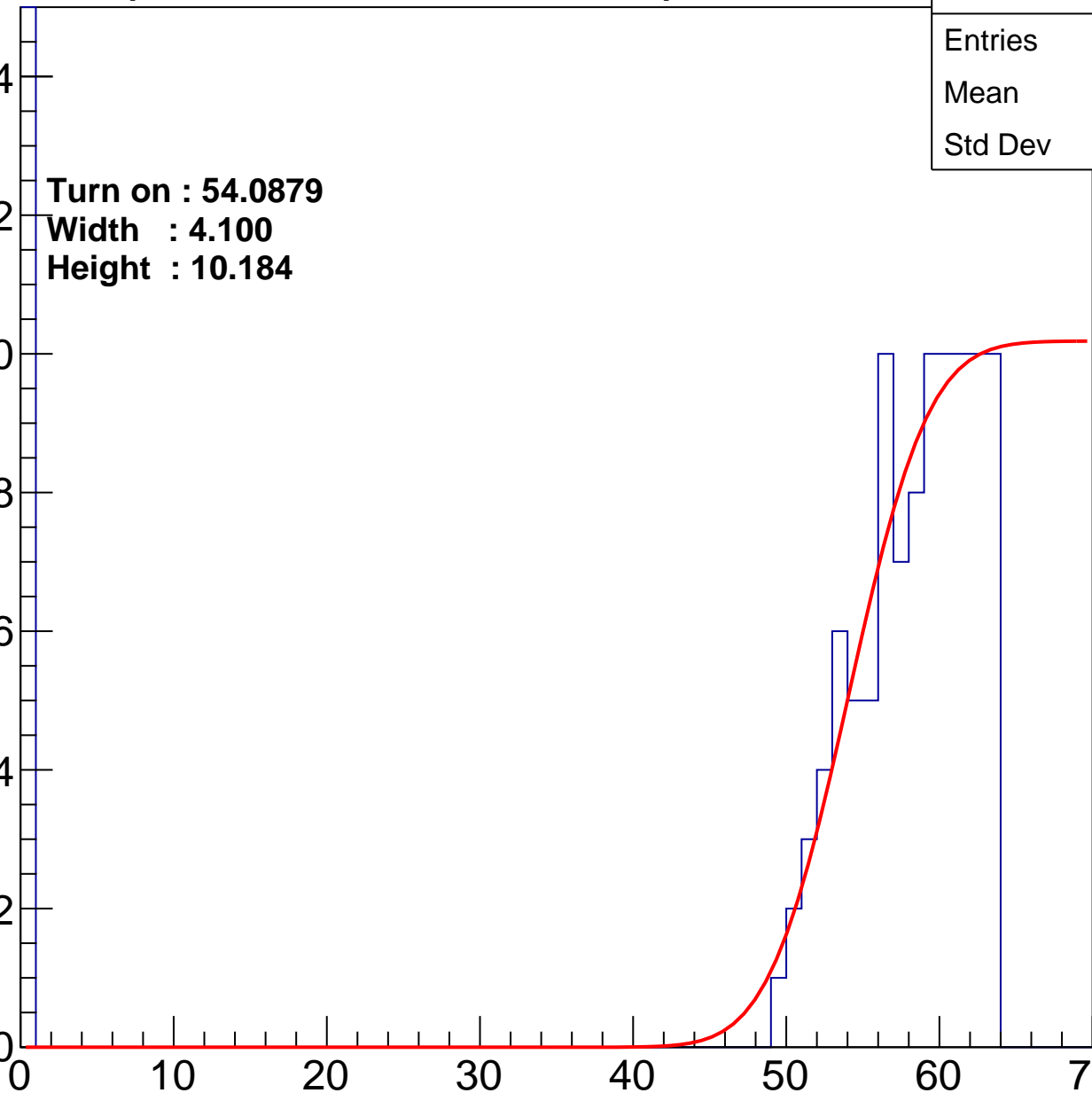
Width : 4.100

Height : 10.184

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch100

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	31.33
Std Dev	28.82

Turn on : 53.6008

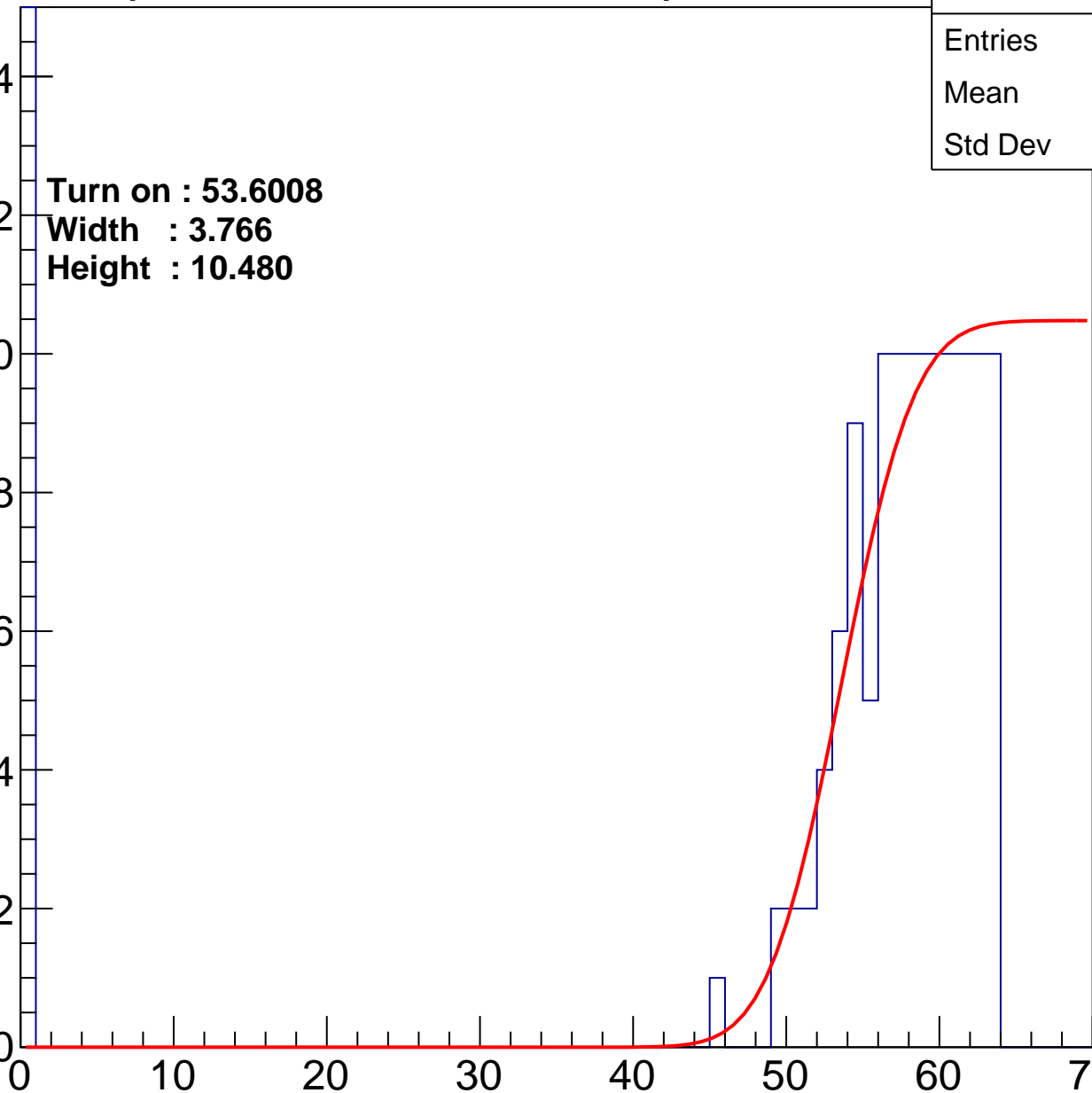
Width : 3.766

Height : 10.480

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	30.89
Std Dev	28.84

Turn on : 53.2282

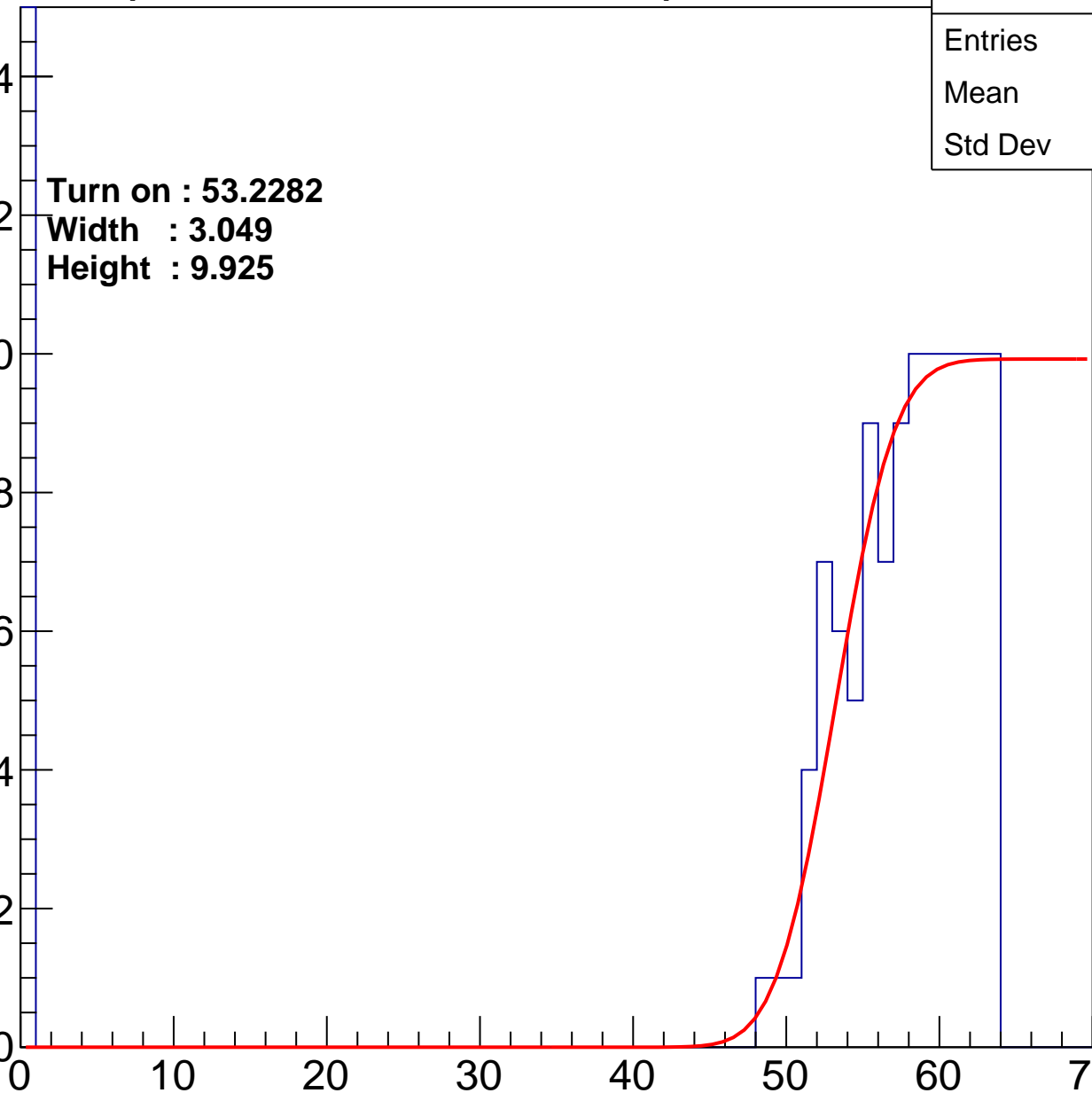
Width : 3.049

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	34.17
Std Dev	28.39

Turn on : 53.9166

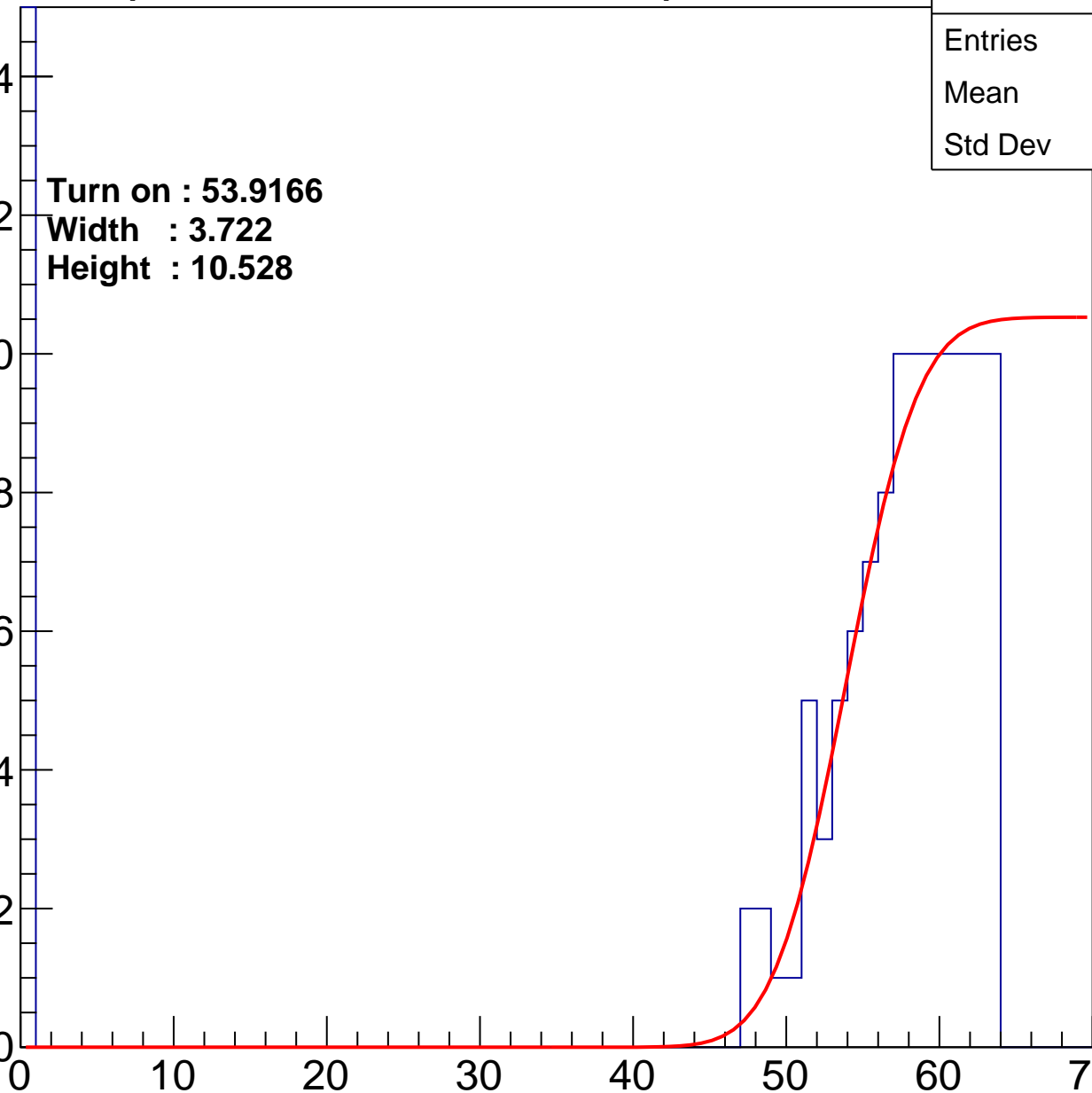
Width : 3.722

Height : 10.528

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	32.48
Std Dev	28.75

Turn on : 53.9665

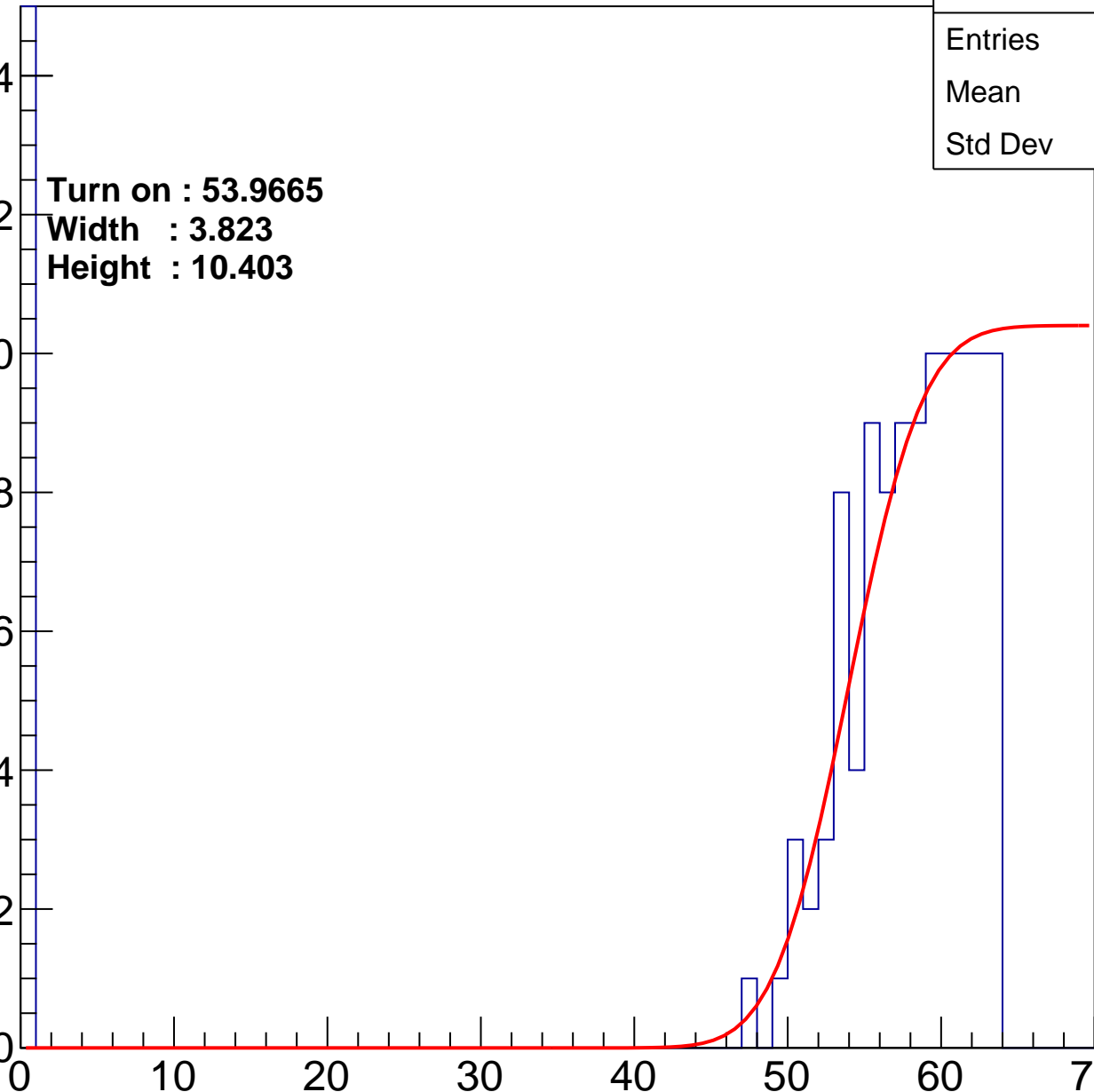
Width : 3.823

Height : 10.403

Entry

14
12
10
8
6
4
2
0

ampl



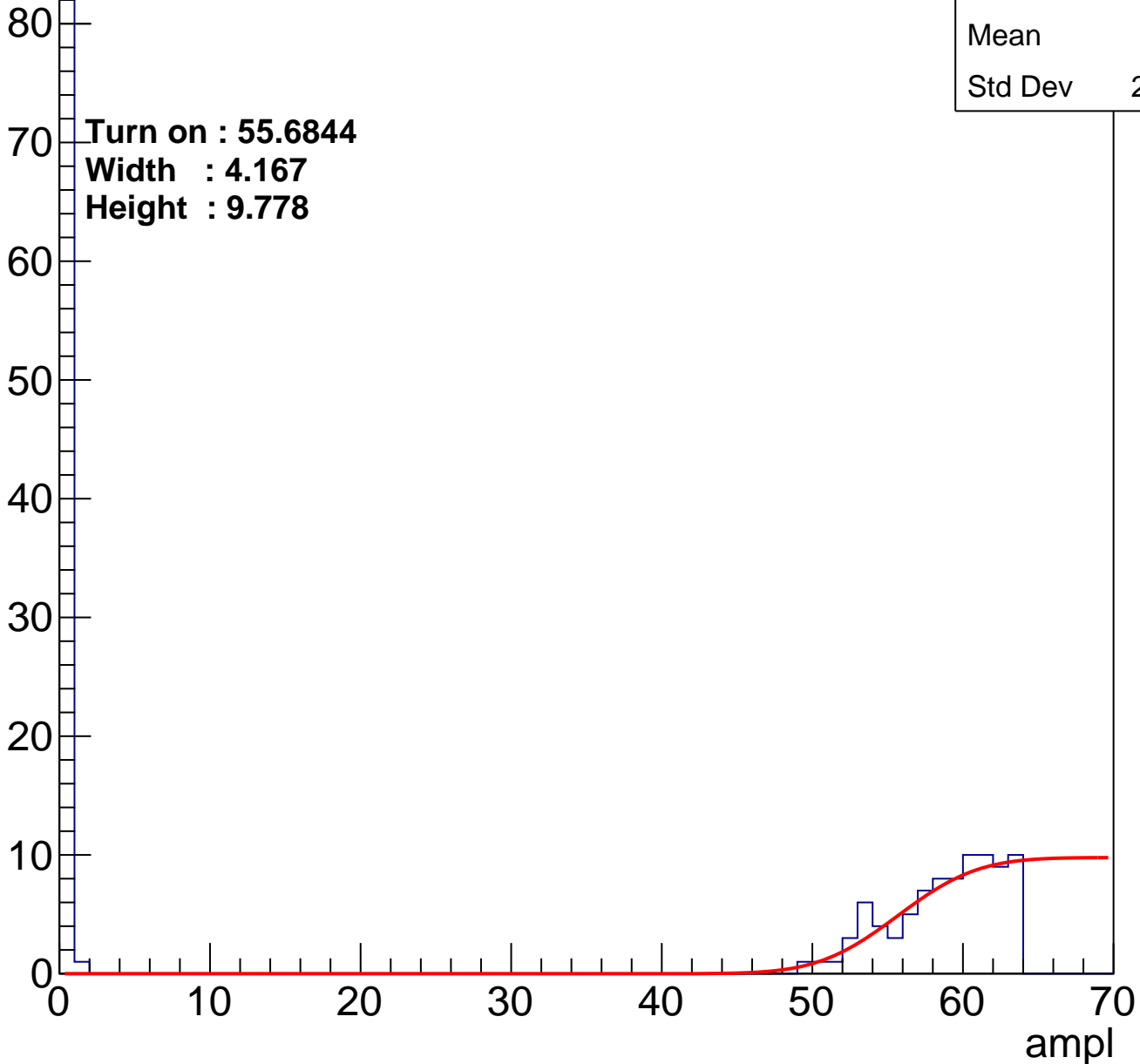
B1L104S, U19-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	29.7
Std Dev	29.27

Turn on : 55.6844
Width : 4.167
Height : 9.778

Entry



B1L104S, U19-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	38.72
Std Dev	27.19

Turn on : 53.6809

Width : 2.752

Height : 10.204

Entry

14

12

10

8

6

4

2

0

0

10

20

30

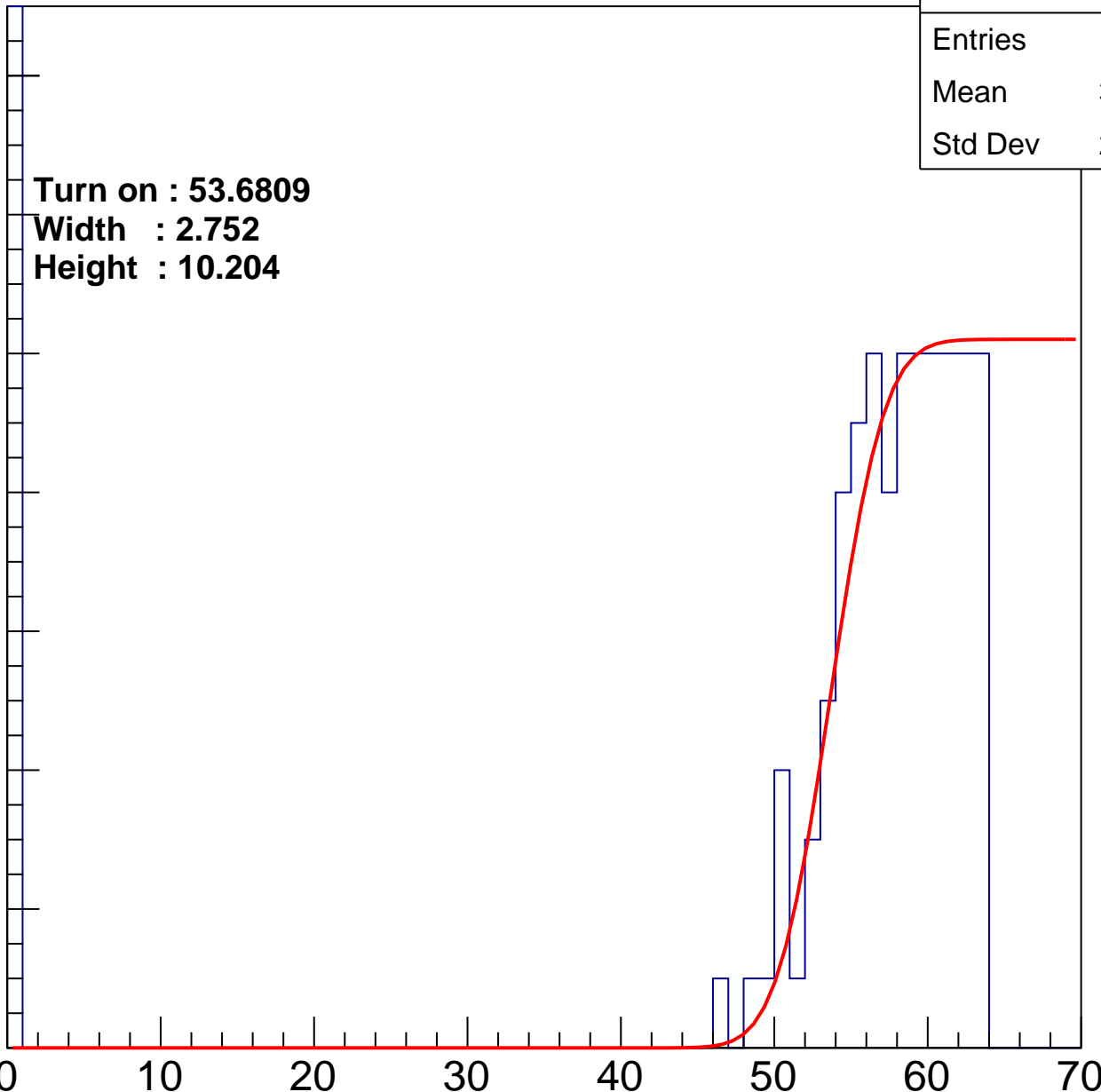
40

50

60

70

ampl



B1L104S, U19-ch106

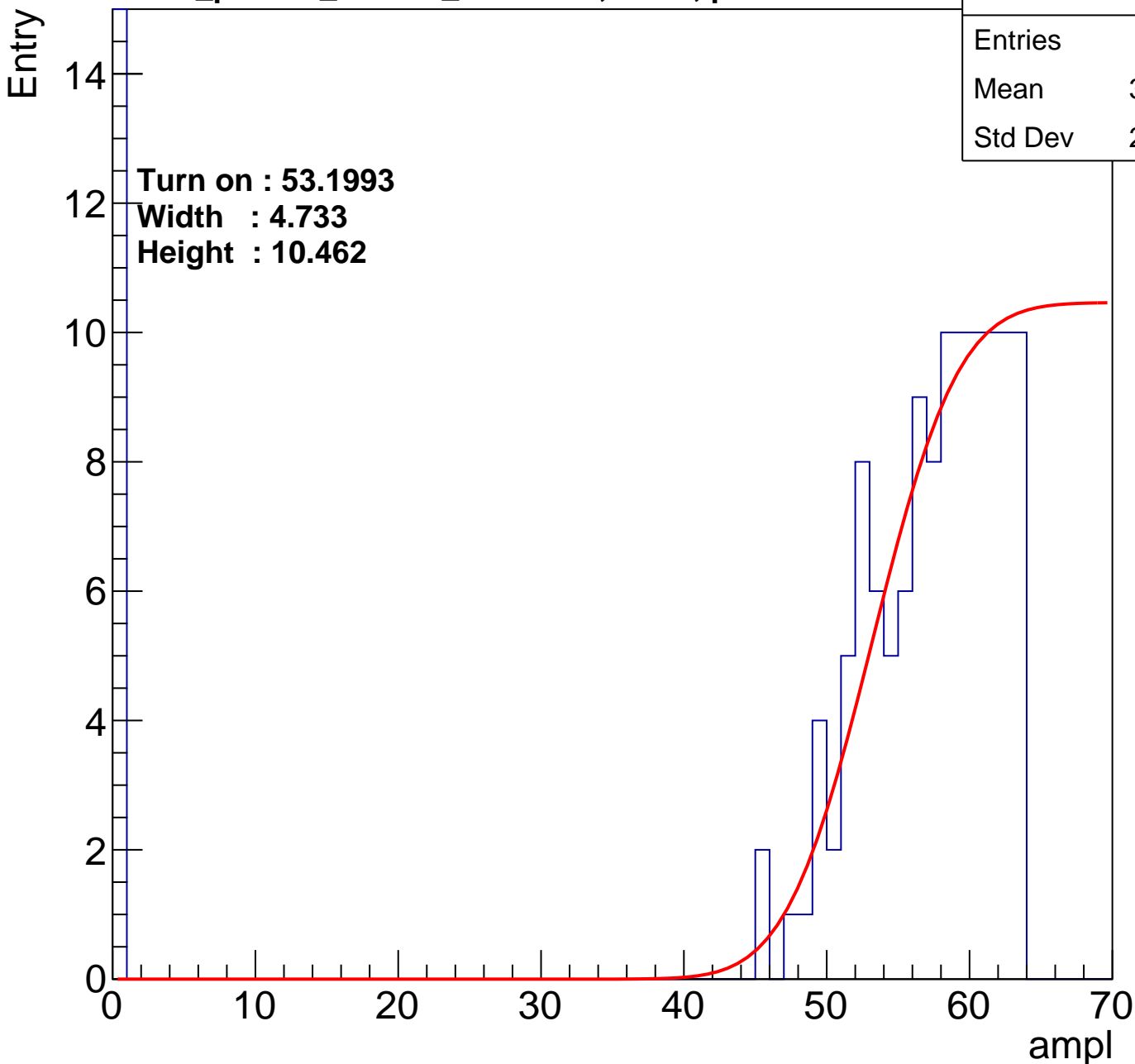
calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	30.27
Std Dev	28.59

Turn on : 53.1993

Width : 4.733

Height : 10.462



B1L104S, U19-ch107

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	32.38
Std Dev	28.45

Turn on : 52.5313

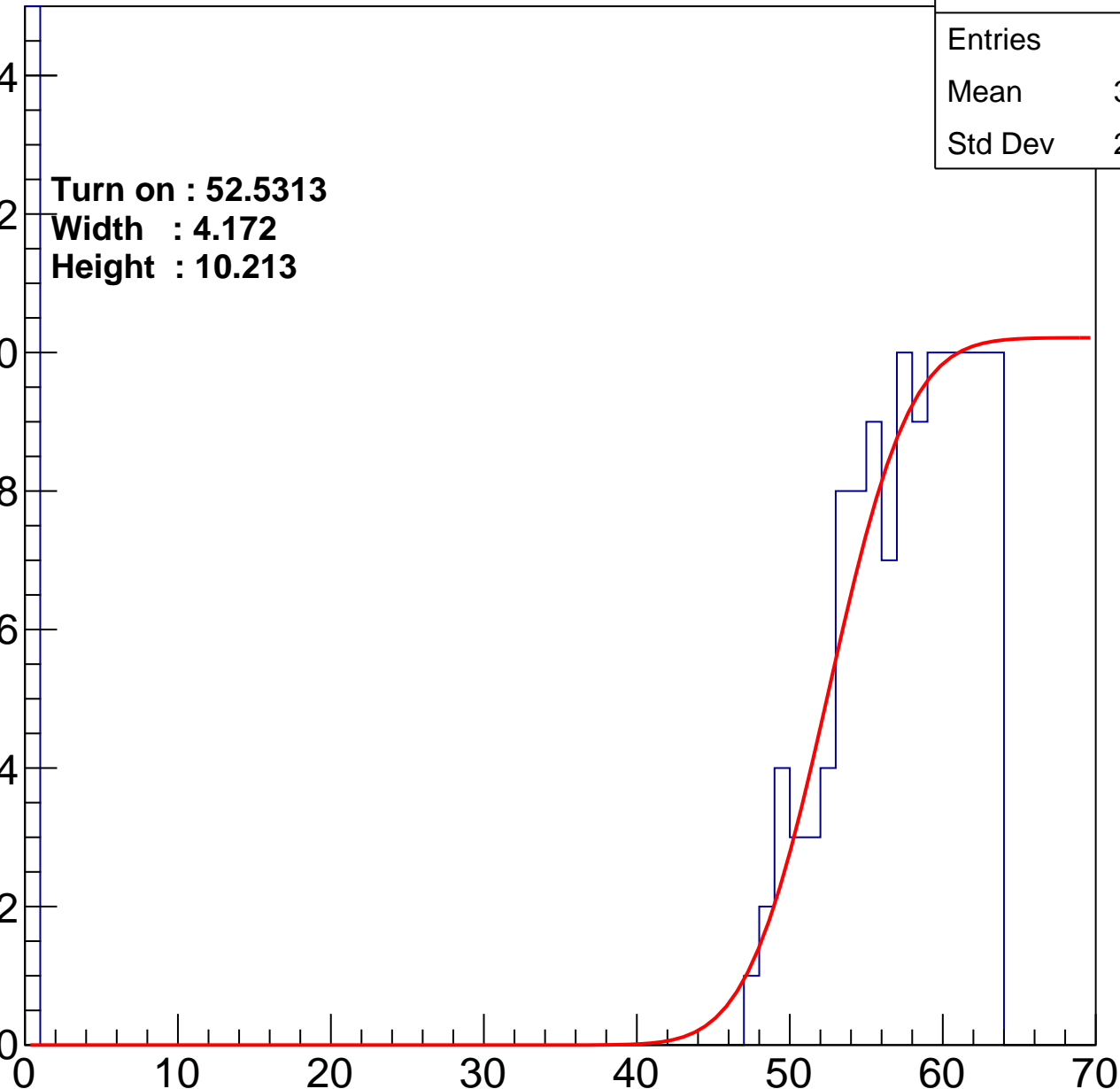
Width : 4.172

Height : 10.213

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch108

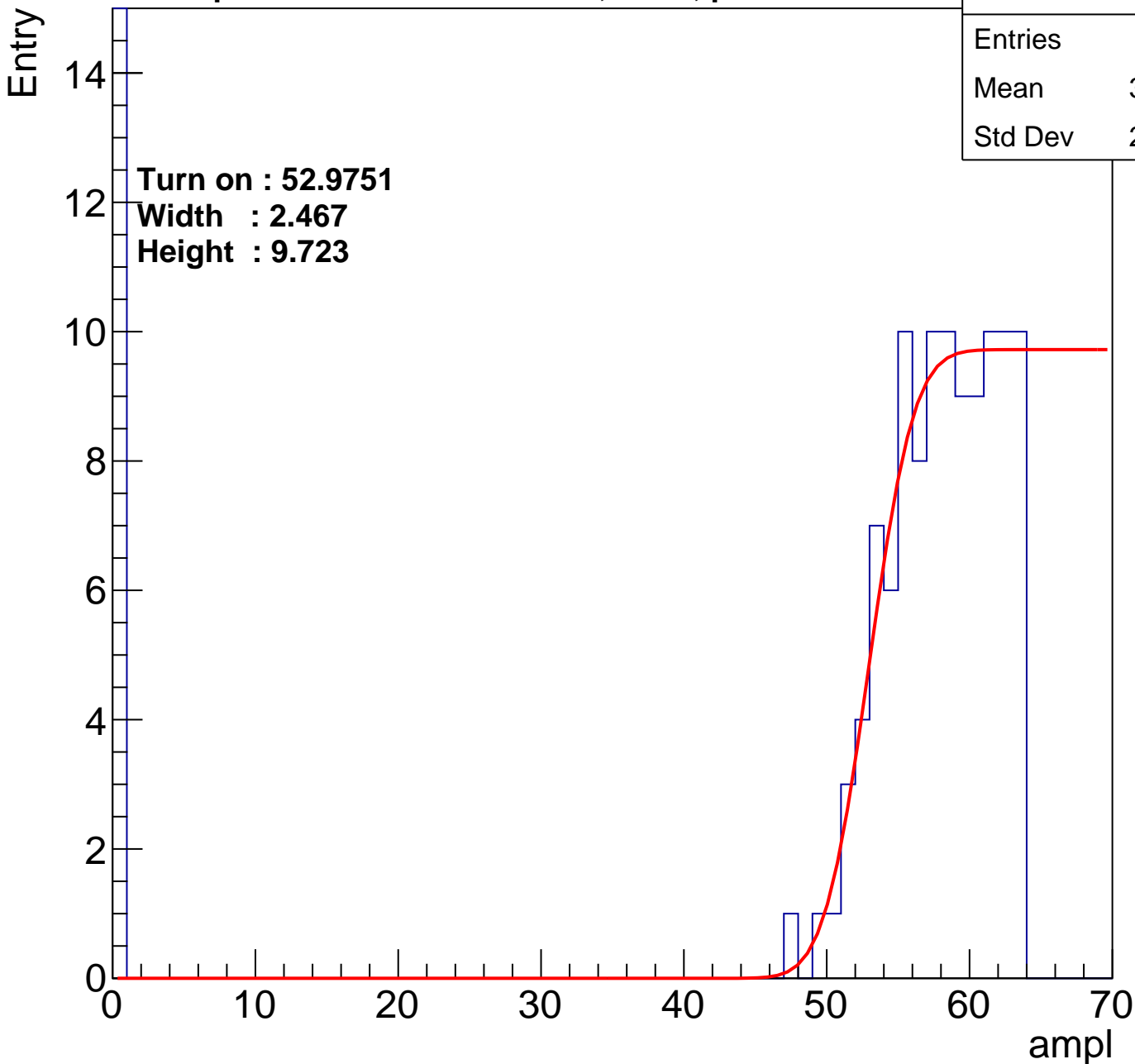
calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	35.09
Std Dev	28.27

Turn on : 52.9751

Width : 2.467

Height : 9.723



B1L104S, U19-ch109

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	33.62
Std Dev	28.65

Turn on : 53.6974

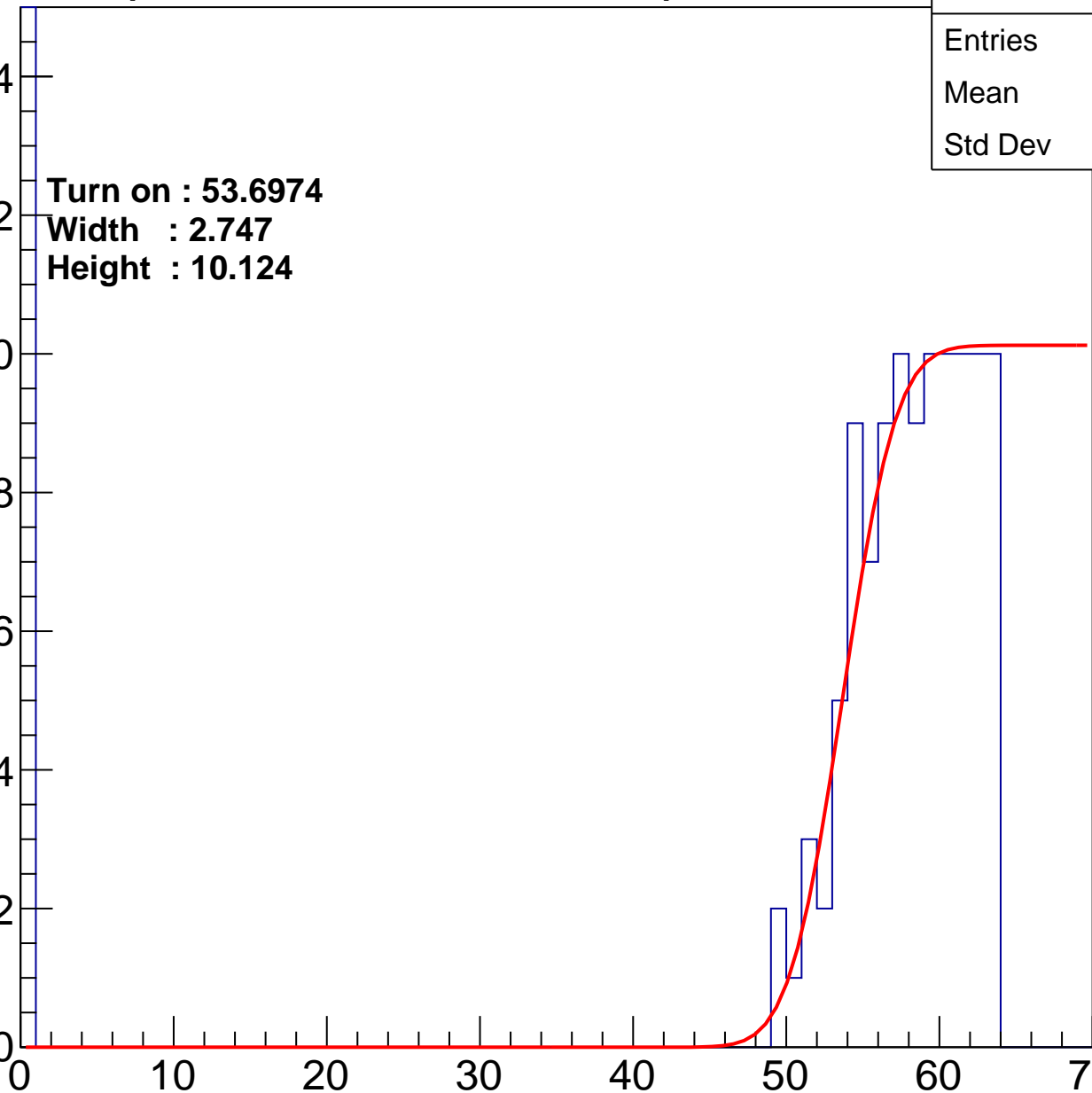
Width : 2.747

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	32.11
Std Dev	28.89

Turn on : 53.8283

Width : 3.825

Height : 10.059

Entry

14

12

10

8

6

4

2

0

0

10

20

30

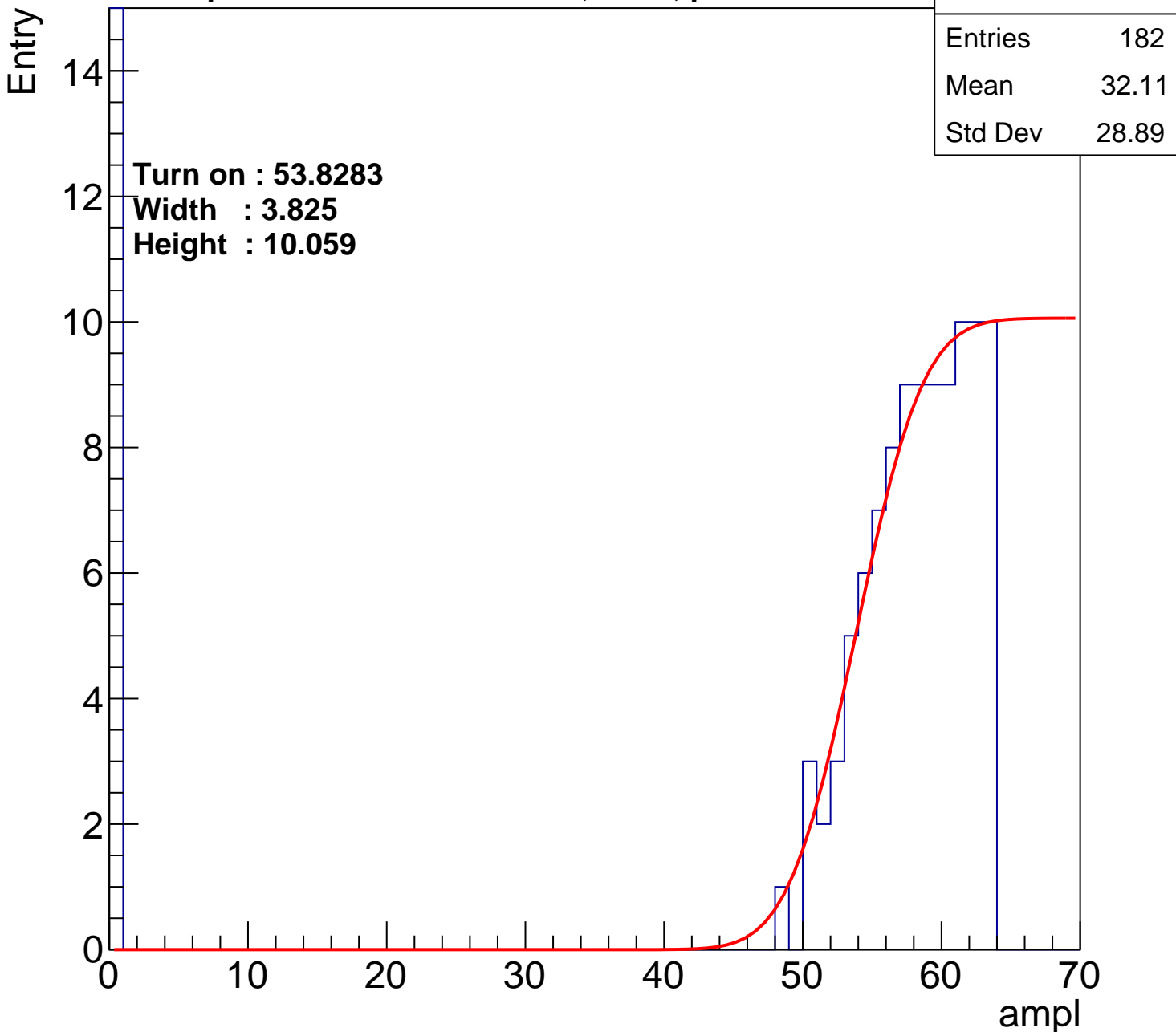
40

50

60

70

ampl



B1L104S, U19-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	30.18
Std Dev	28.79

Turn on : 51.9549

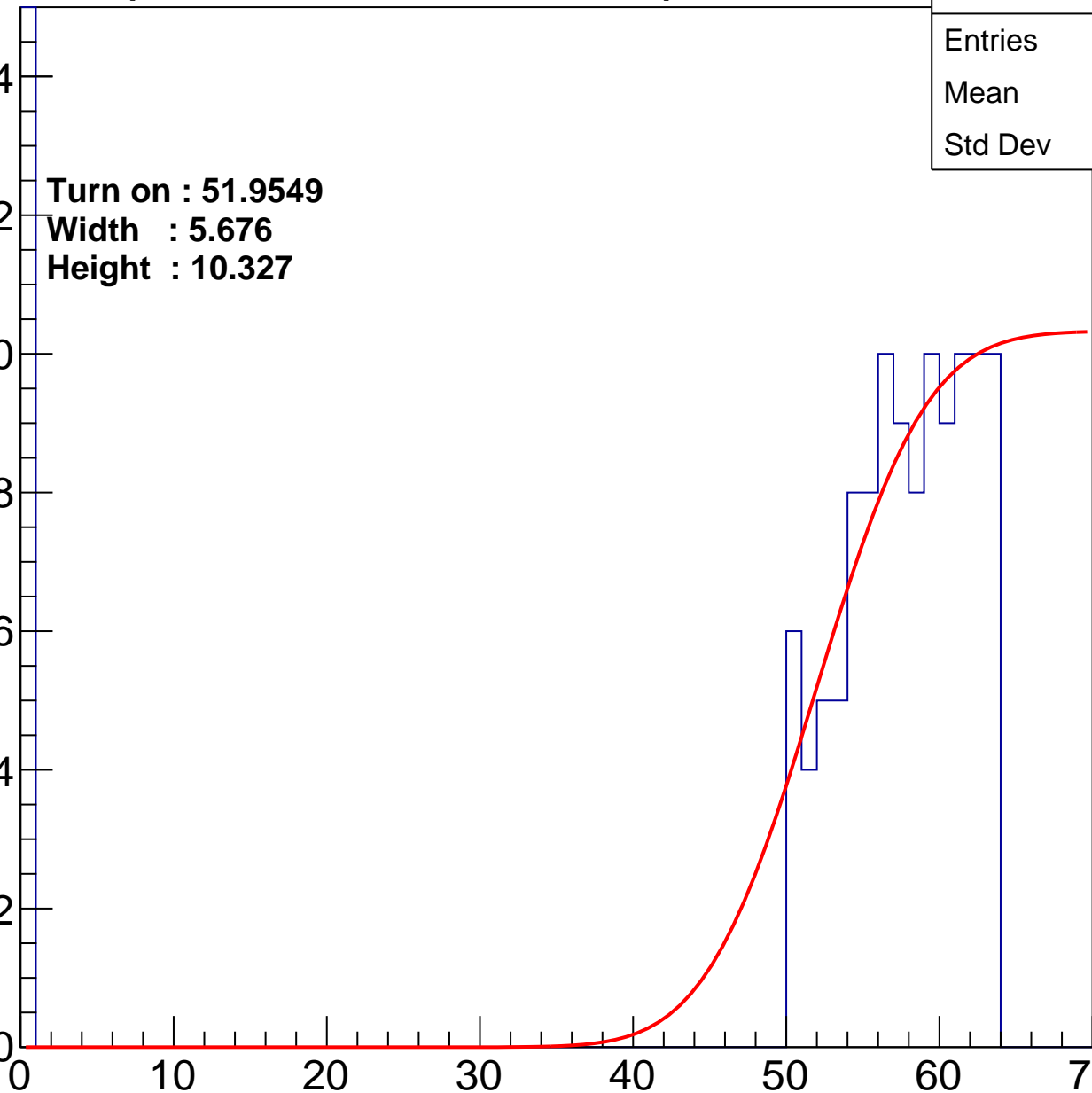
Width : 5.676

Height : 10.327

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	36.72
Std Dev	27.38

Turn on : 51.1423

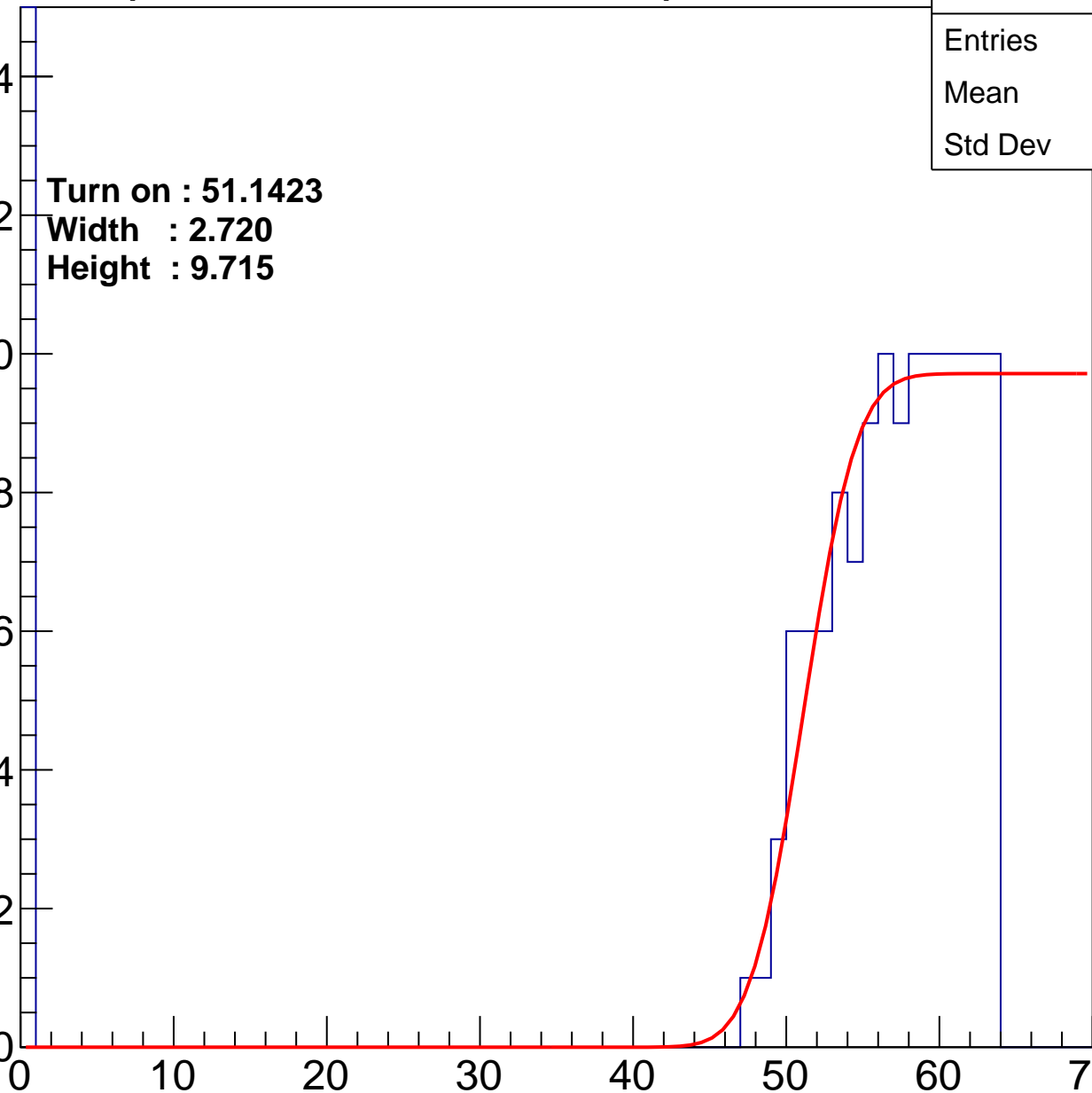
Width : 2.720

Height : 9.715

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	31.49
Std Dev	28.72

Turn on : 53.6445

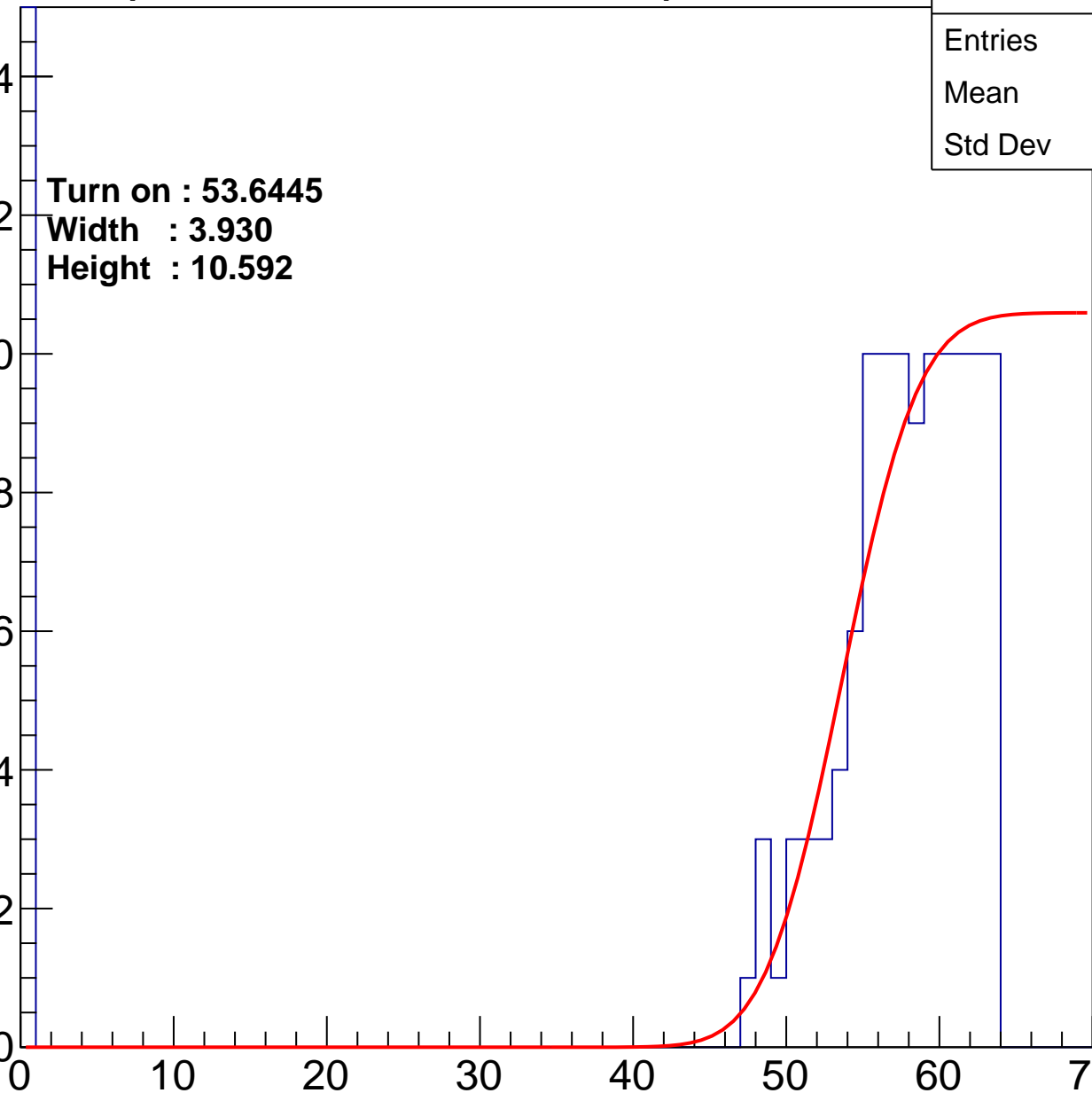
Width : 3.930

Height : 10.592

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	30.7
Std Dev	29.08

Turn on : 55.3152

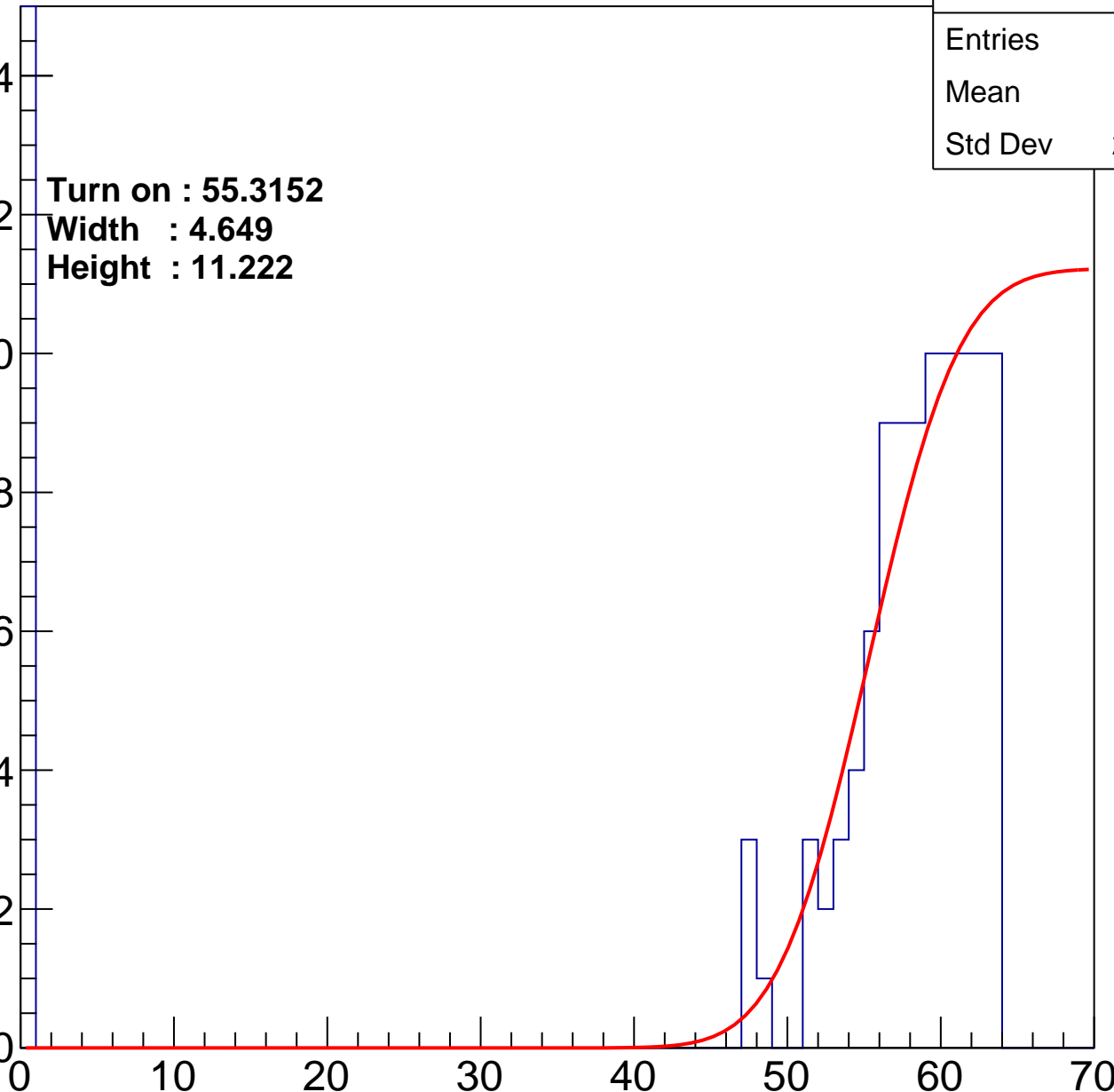
Width : 4.649

Height : 11.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	33.69
Std Dev	28.65

Turn on : 53.5226

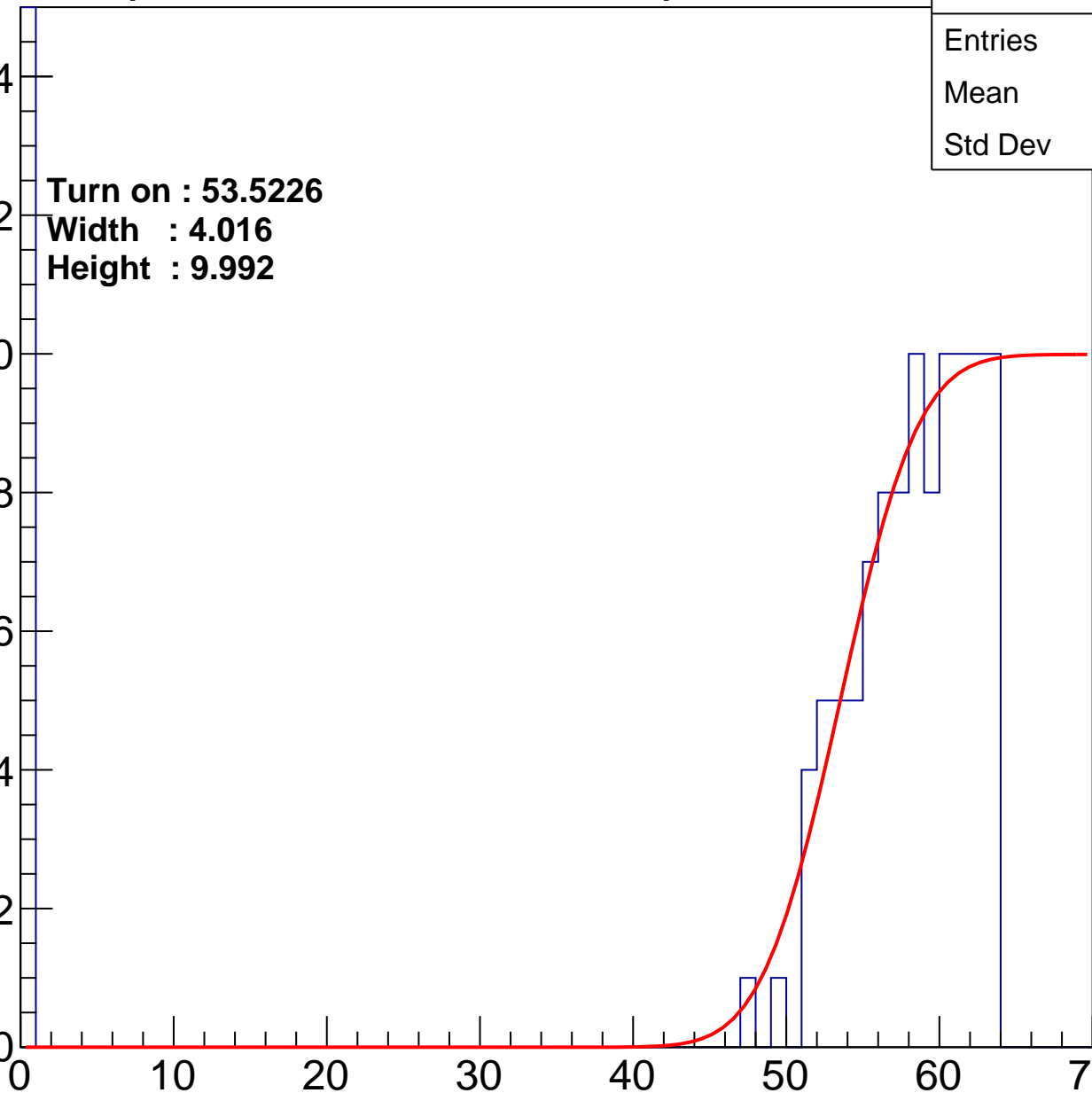
Width : 4.016

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	29.36
Std Dev	29.3

Turn on : 55.7890

Width : 3.216

Height : 10.282

Entry

14

12

10

8

6

4

2

0

0

10

20

30

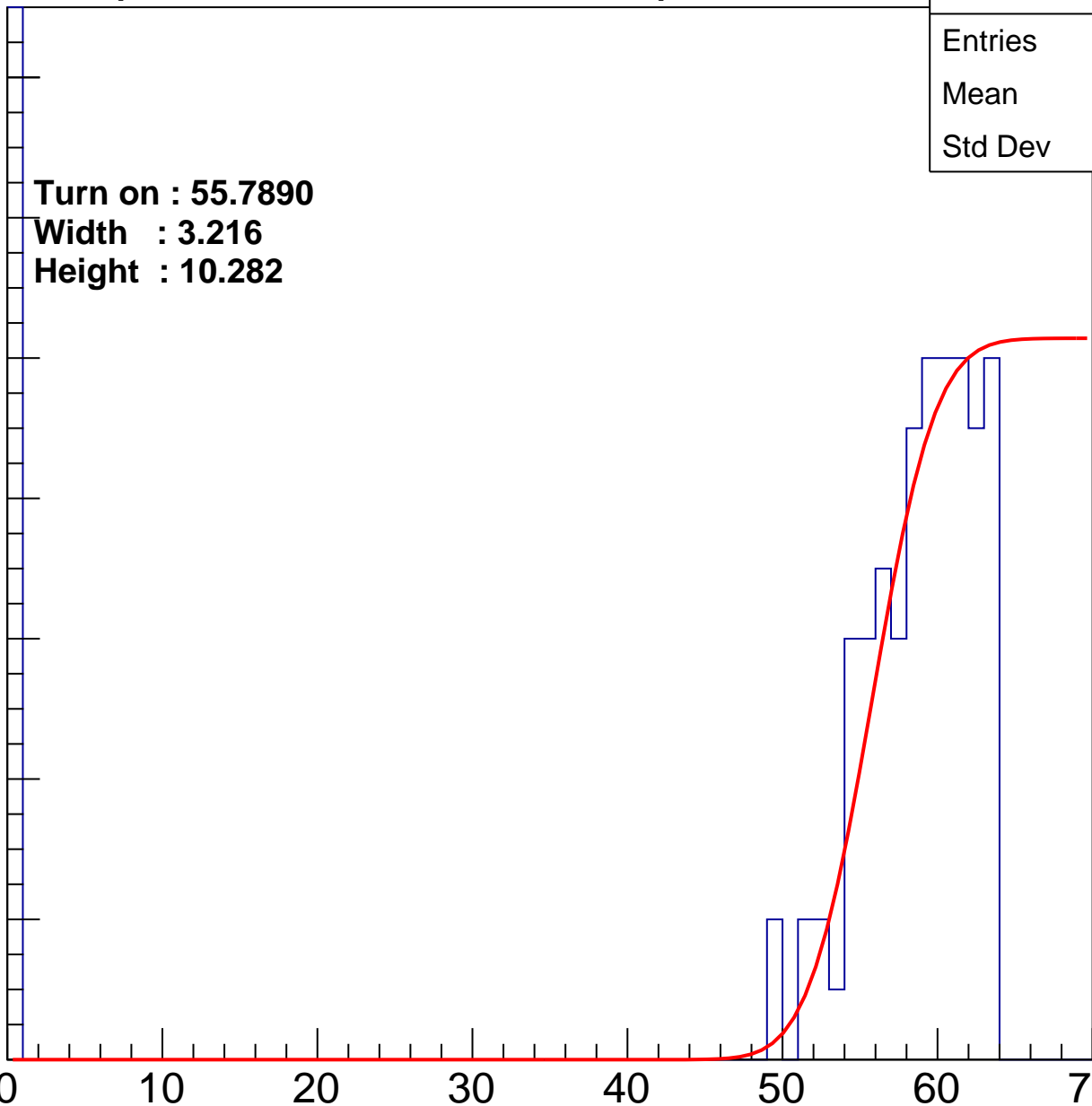
40

50

60

70

ampl



B1L104S, U19-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	34.47
Std Dev	27.99

Turn on : 51.9339

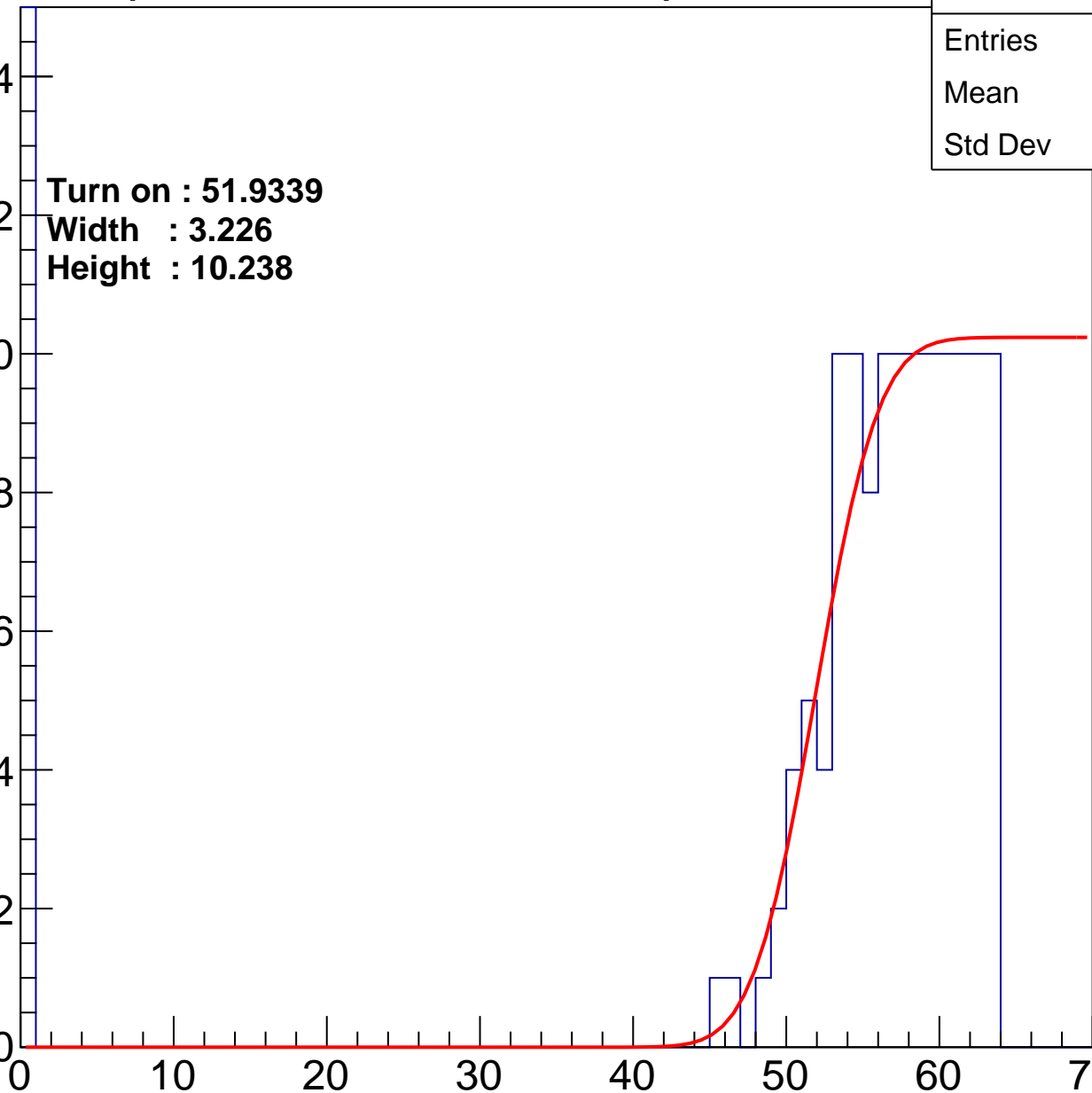
Width : 3.226

Height : 10.238

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	28.12
Std Dev	29.03

Turn on : 53.7395

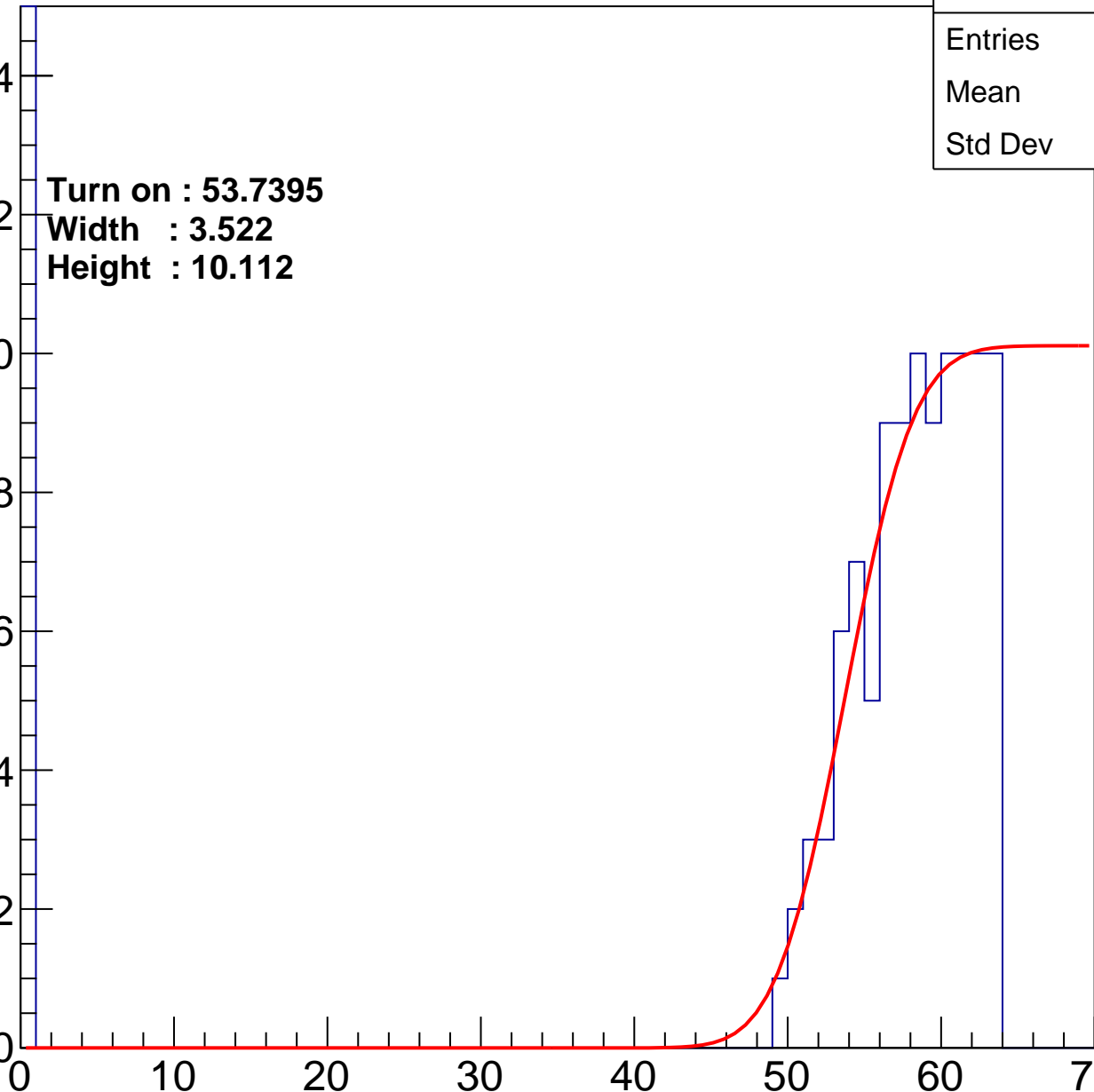
Width : 3.522

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	34.15
Std Dev	28.22

Turn on : 51.7966

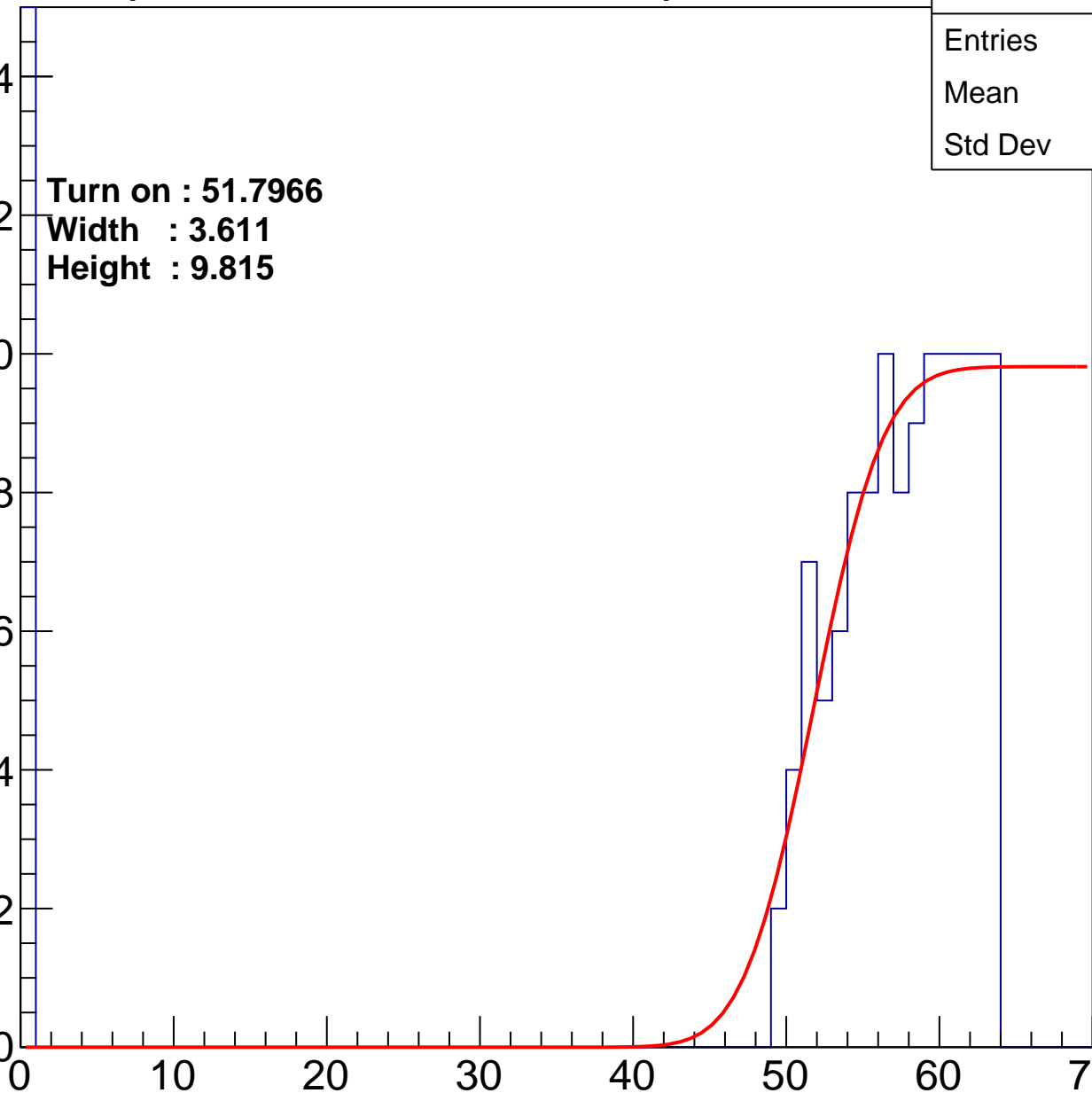
Width : 3.611

Height : 9.815

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	31.51
Std Dev	28.52

Turn on : 55.3337

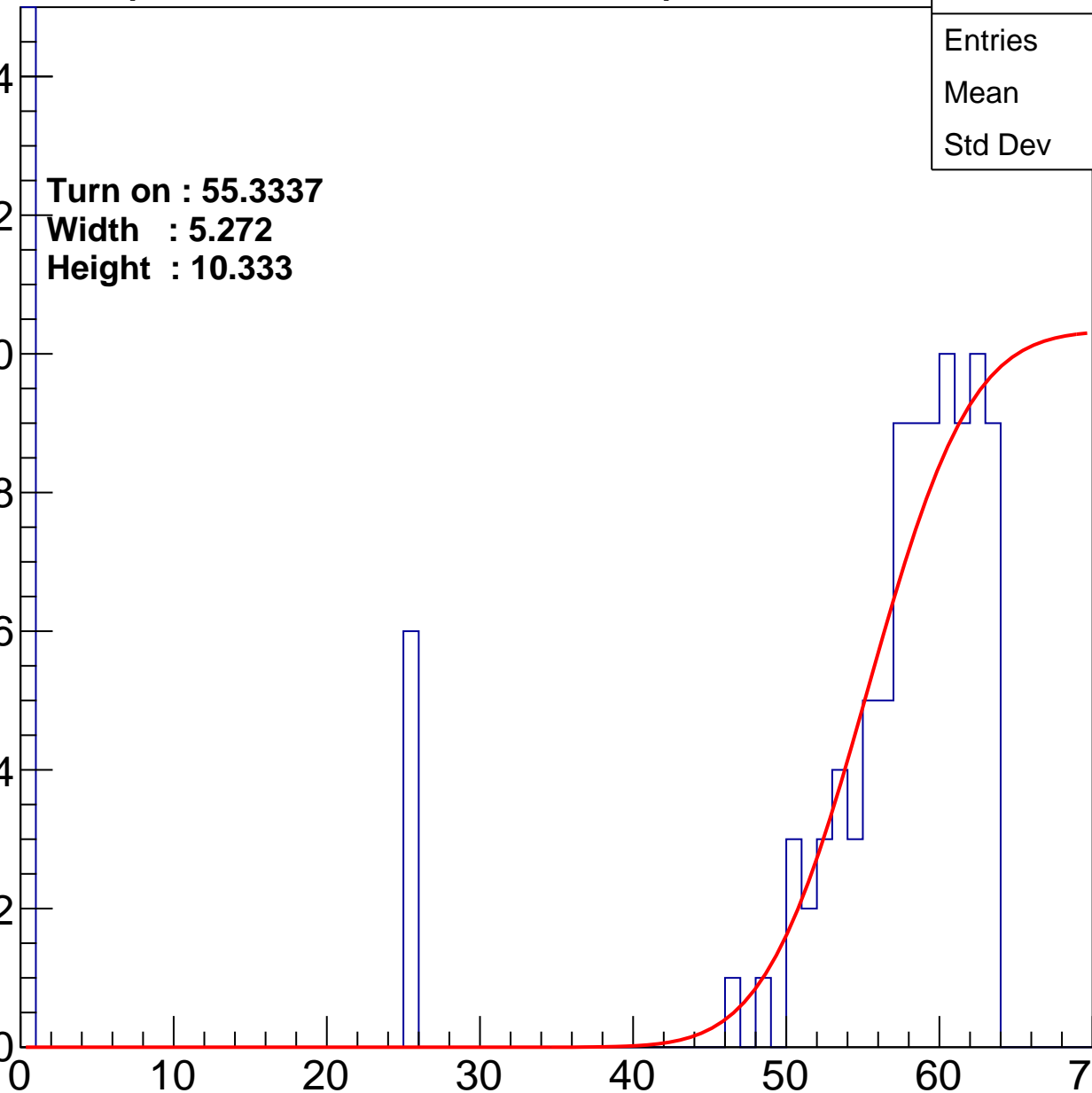
Width : 5.272

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	29.9
Std Dev	29.01

Turn on : 53.1992

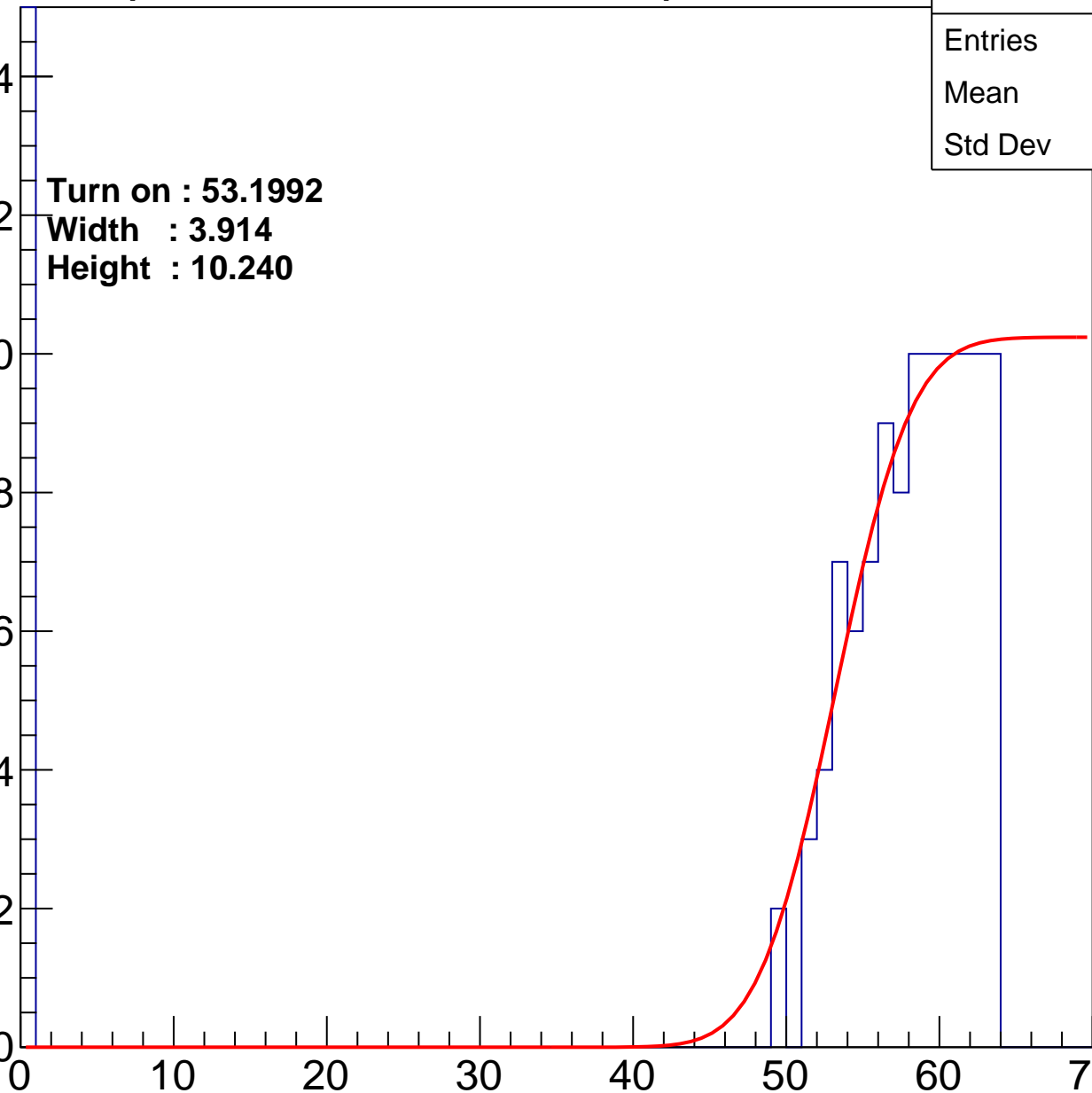
Width : 3.914

Height : 10.240

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	29.85
Std Dev	28.68

Turn on : 54.3143

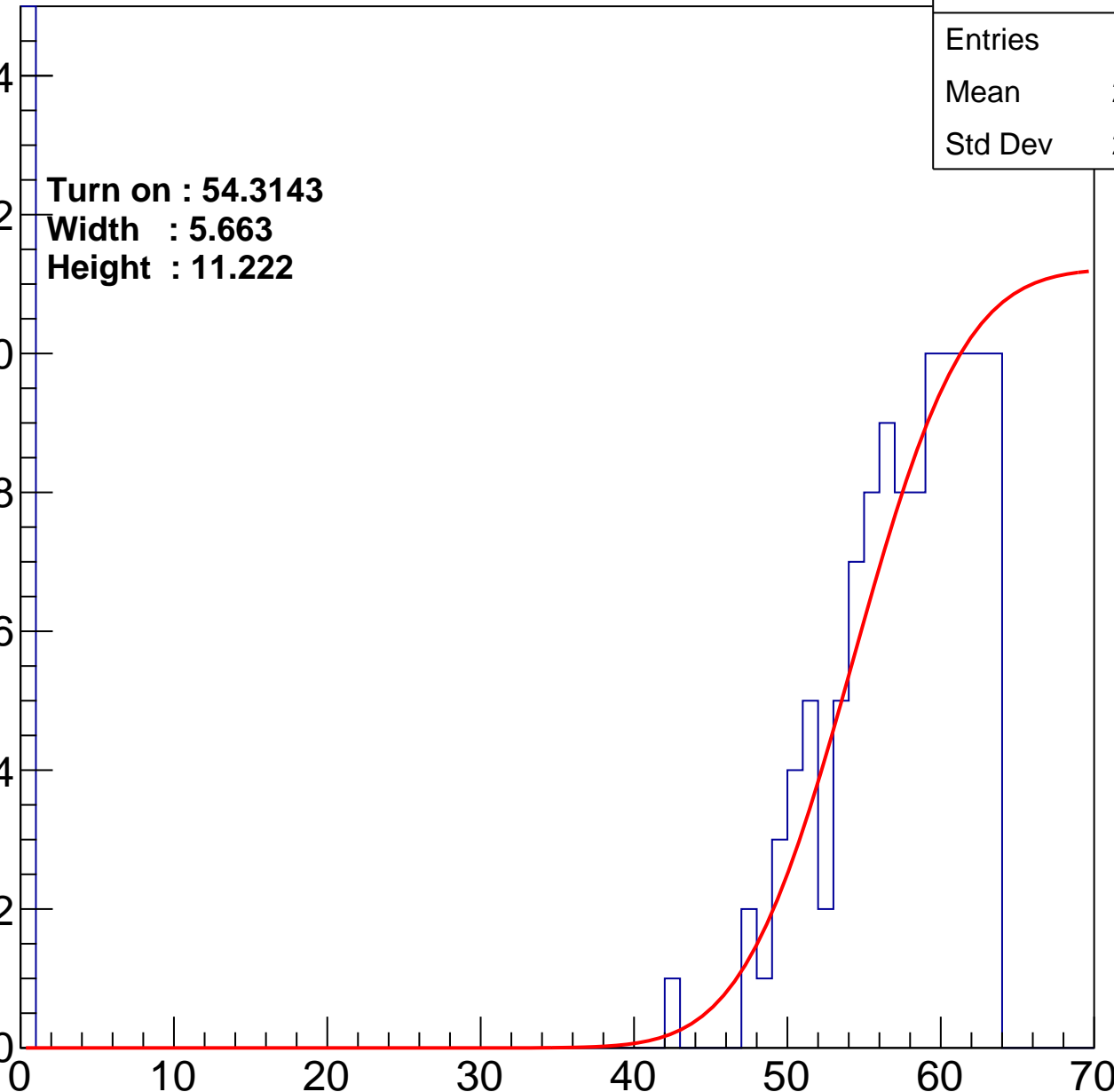
Width : 5.663

Height : 11.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	33.6
Std Dev	28.17

Turn on : 51.7427

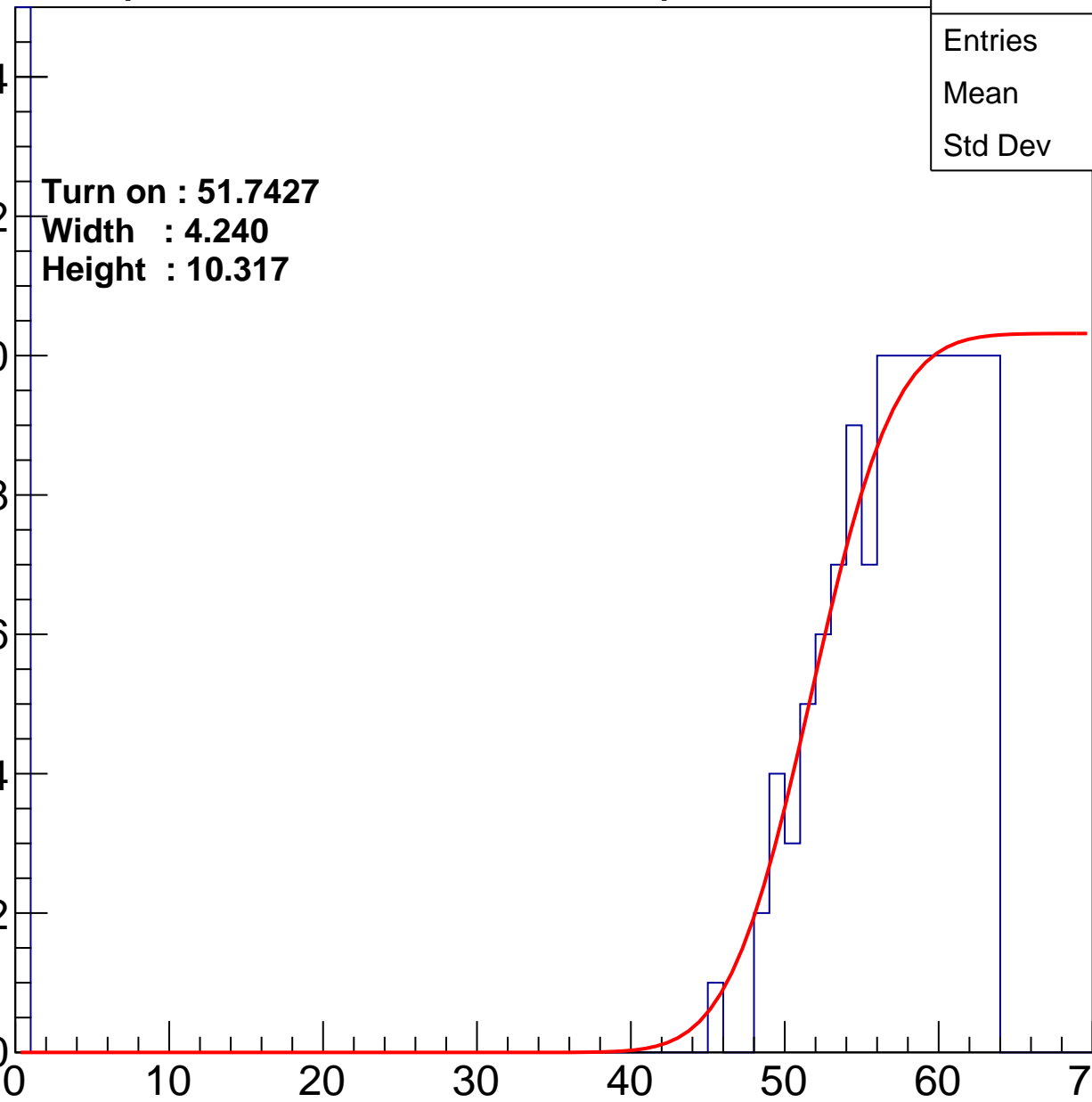
Width : 4.240

Height : 10.317

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	227
Mean	28.9
Std Dev	28.68

Turn on : 52.6453

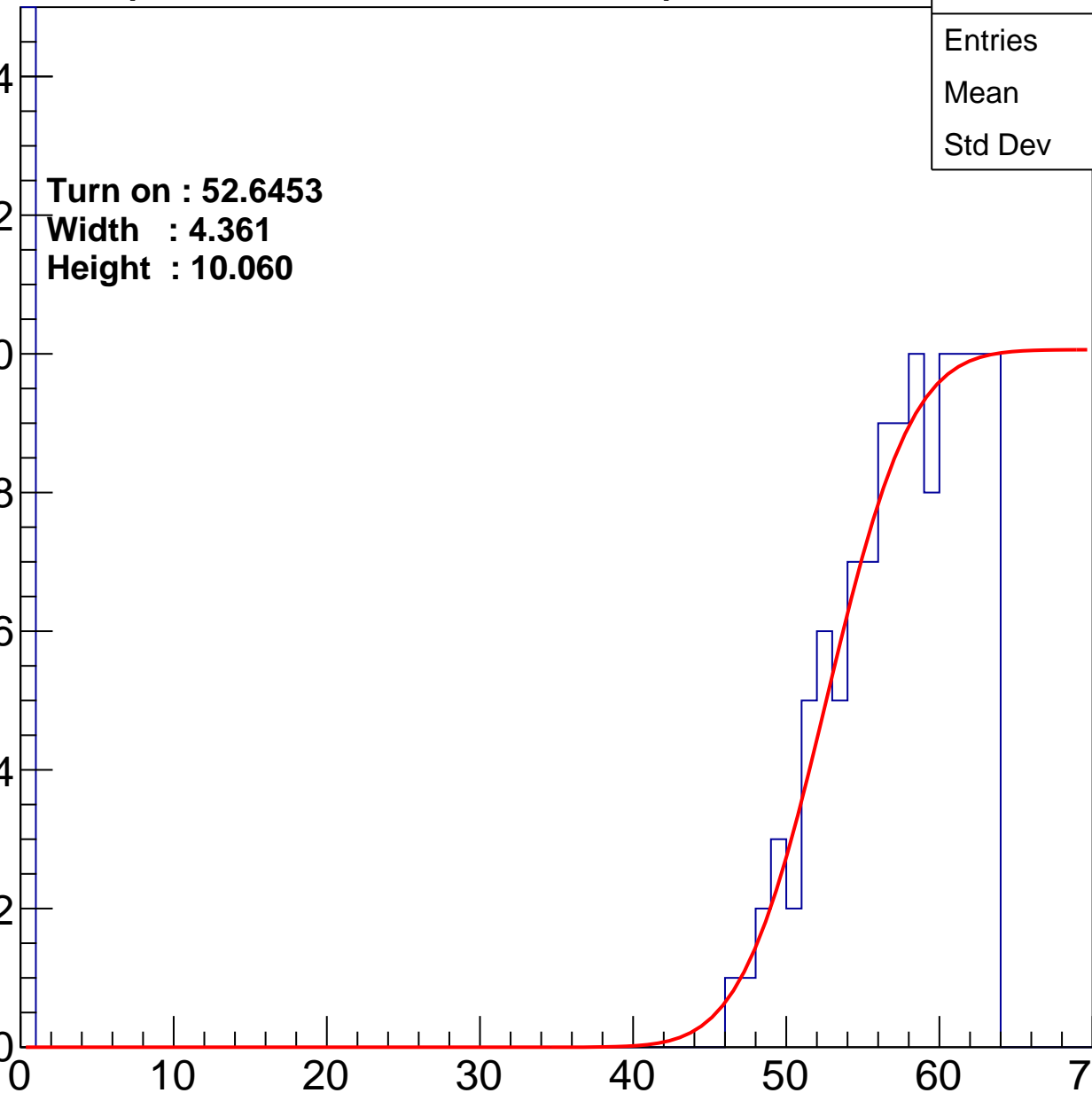
Width : 4.361

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	32.53
Std Dev	29.12

Turn on : 55.2987

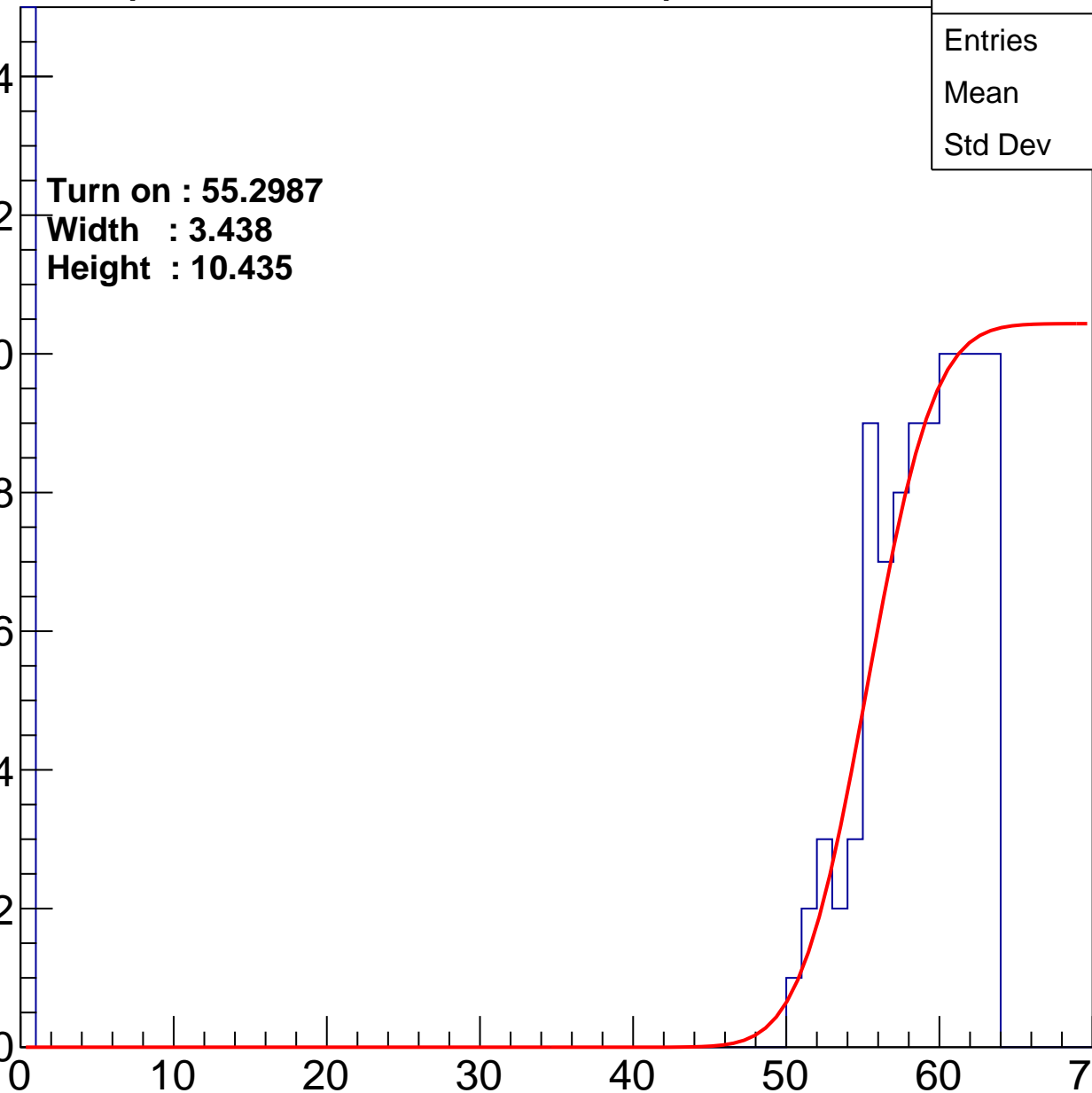
Width : 3.438

Height : 10.435

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	34.8
Std Dev	28.12

Turn on : 54.8686

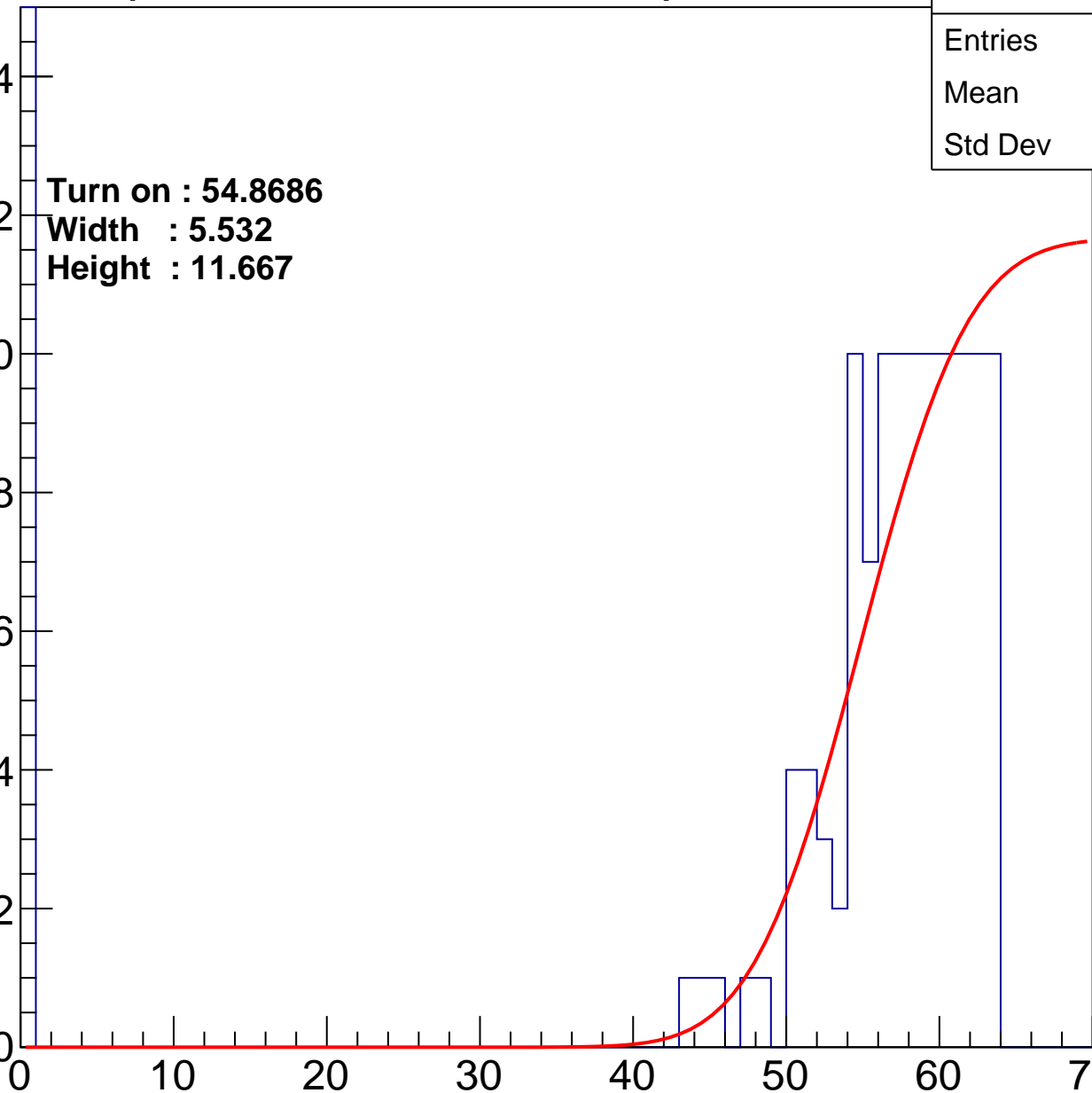
Width : 5.532

Height : 11.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U19-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	34.49
Std Dev	28

Turn on : 51.9982

Width : 3.599

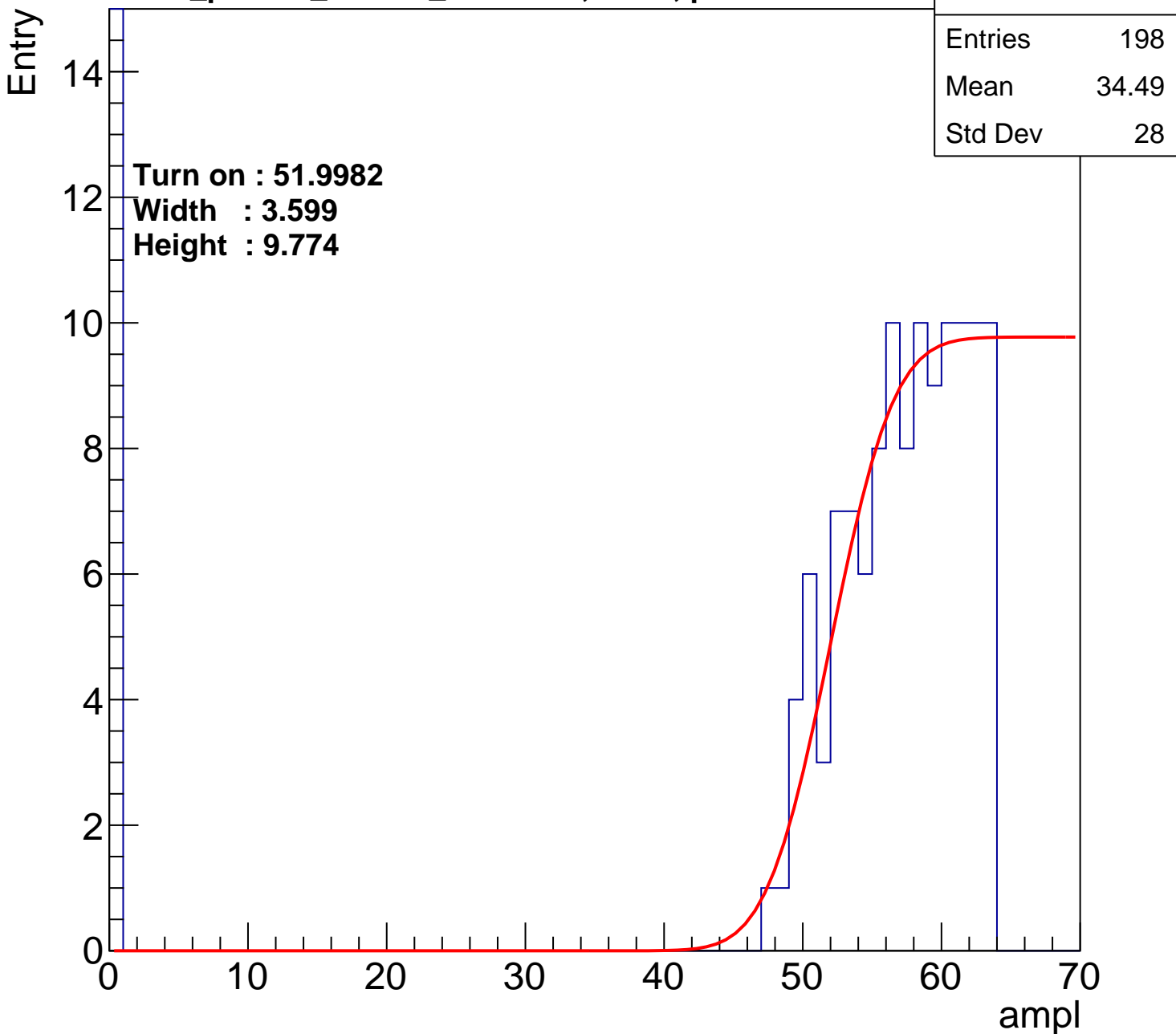
Height : 9.774

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U19-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	34.49
Std Dev	28

Turn on : 51.9982

Width : 3.599

Height : 9.774

Entry

14
12
10
8
6
4
2
0

ampl

