

B1L104S, U22-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	34.48
Std Dev	27.75

Turn on : 51.2744

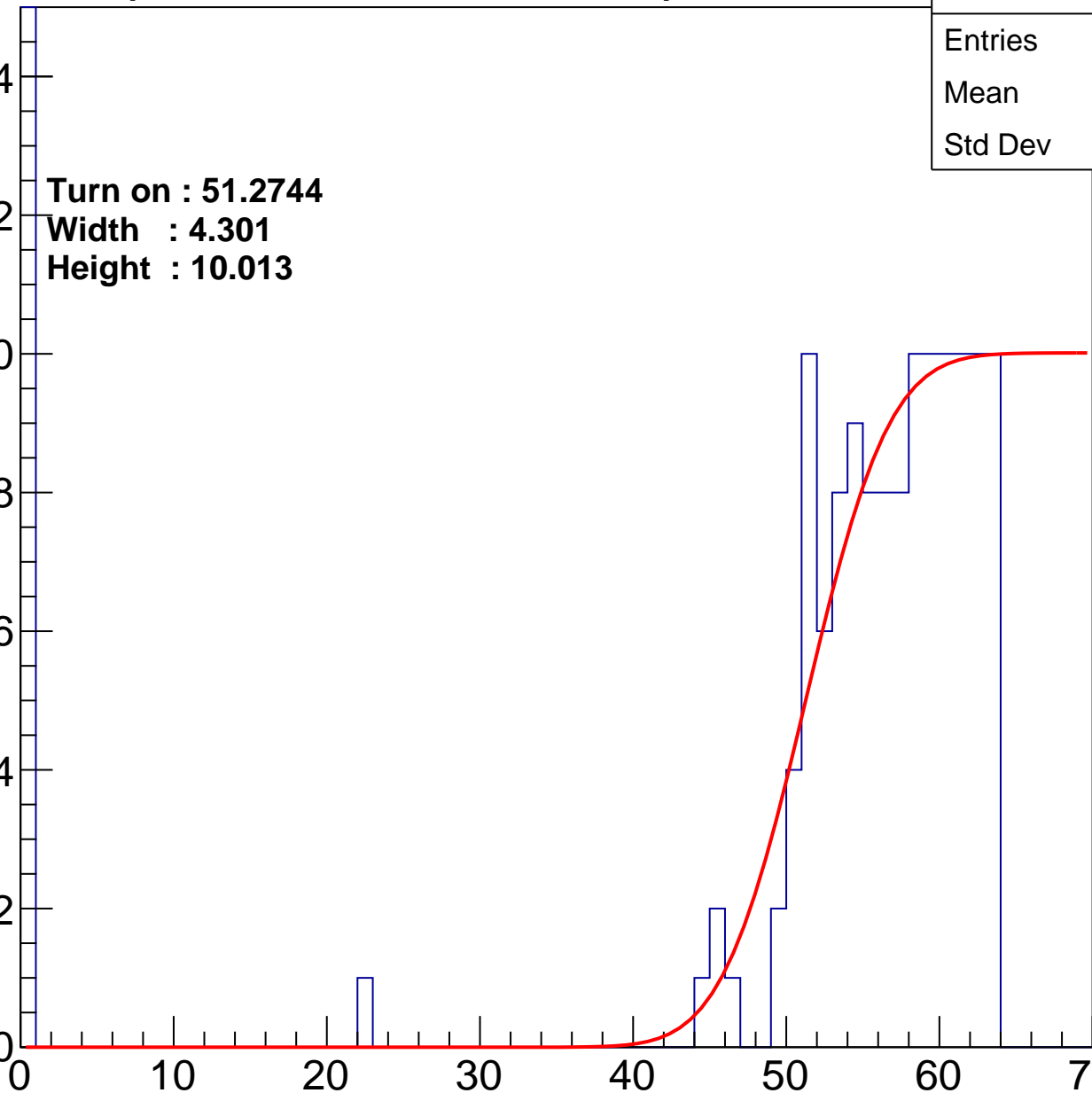
Width : 4.301

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	29.55
Std Dev	29.32

Turn on : 55.7899

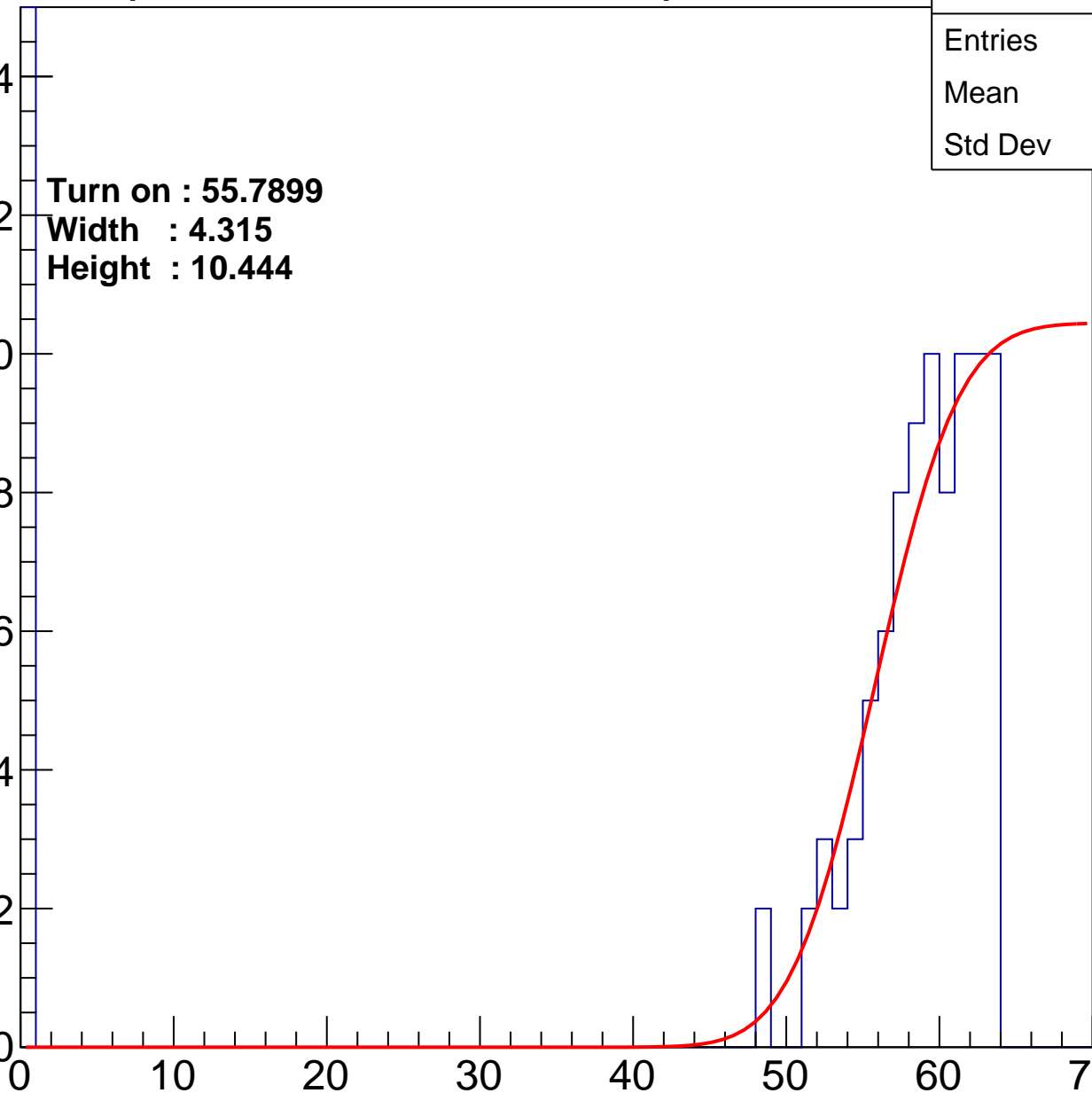
Width : 4.315

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	35.97
Std Dev	27.65

Turn on : 51.7314

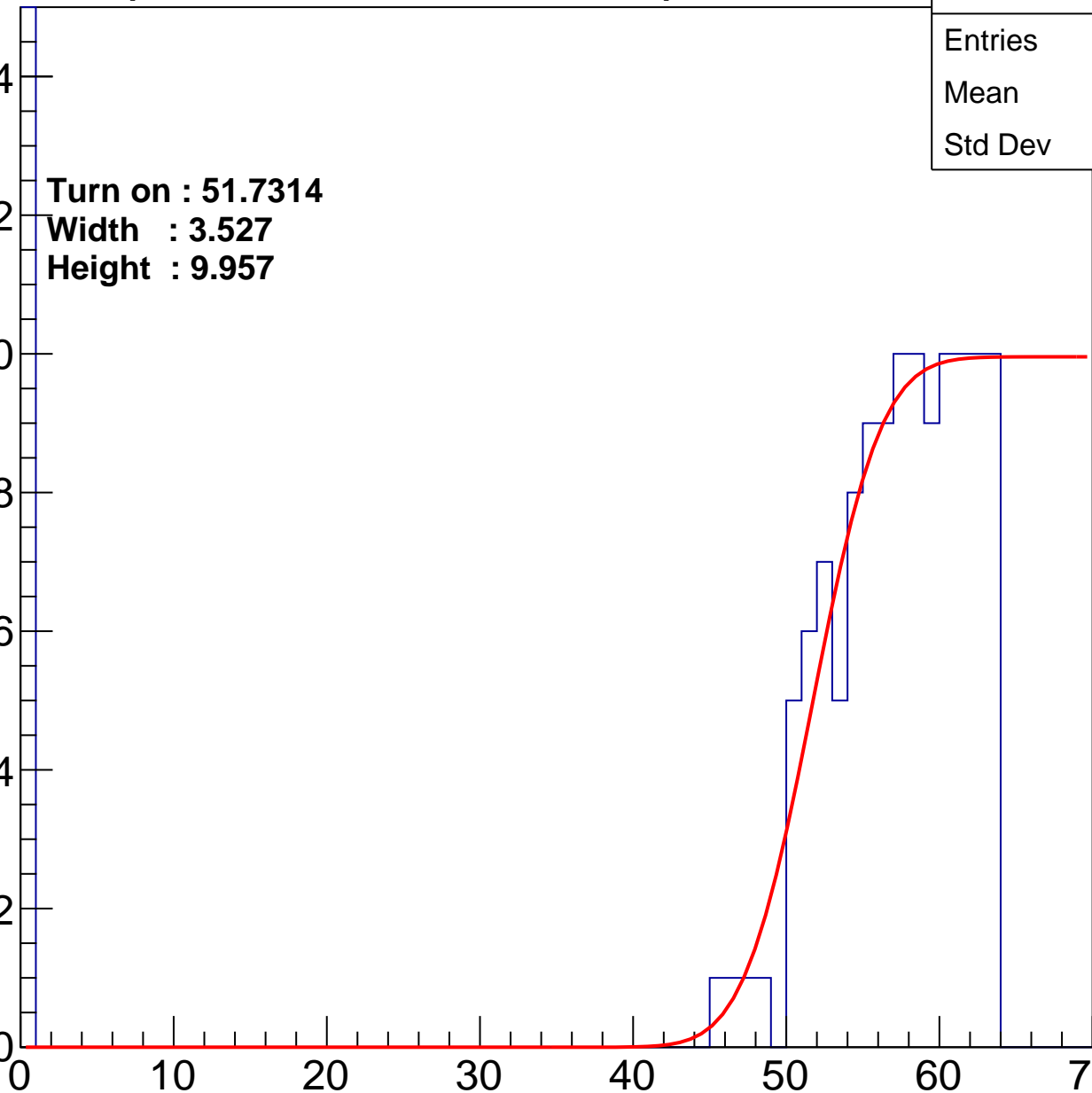
Width : 3.527

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	36.92
Std Dev	27.79

Turn on : 54.3059

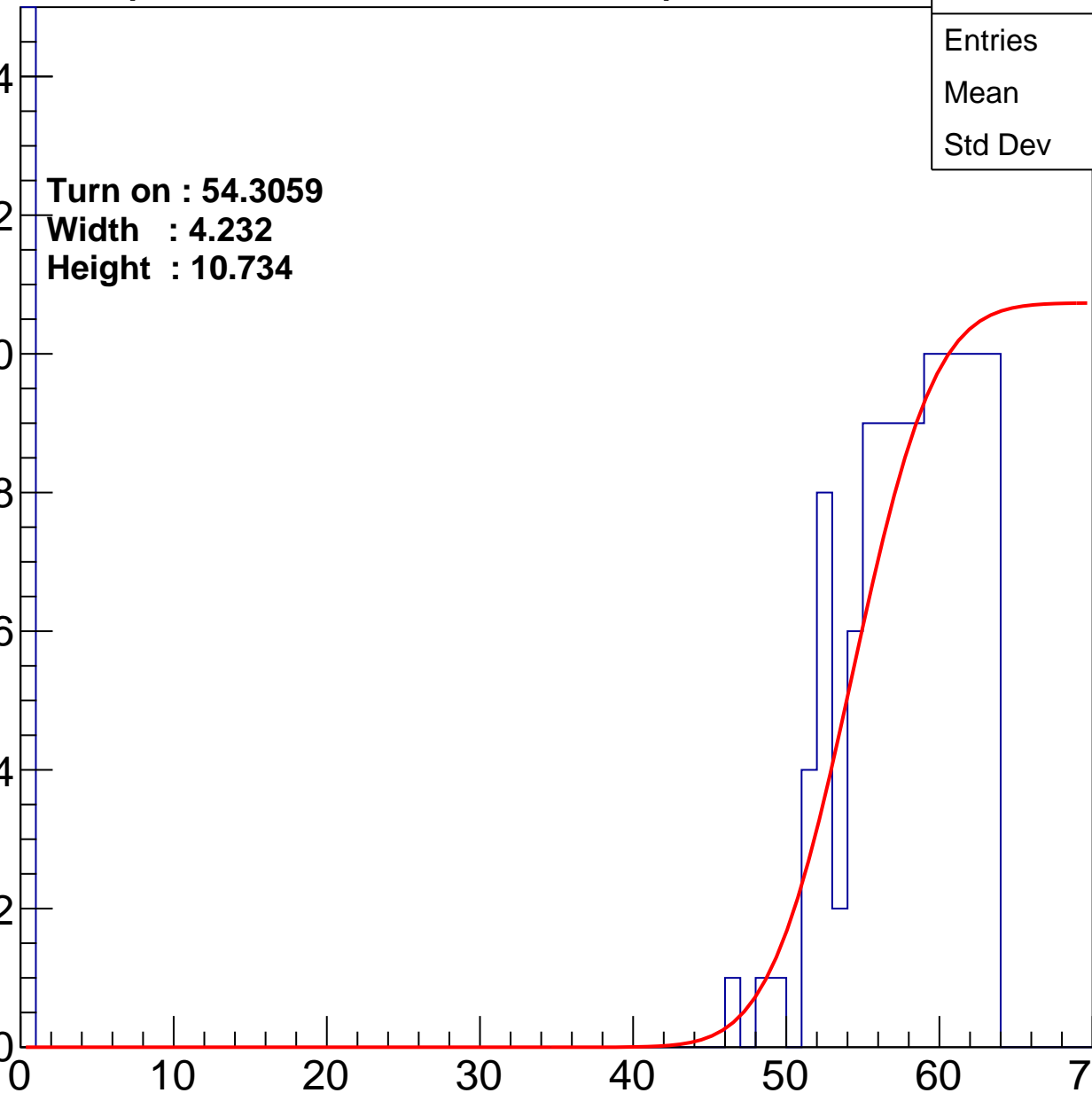
Width : 4.232

Height : 10.734

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	33.3
Std Dev	28.28

Turn on : 55.0191

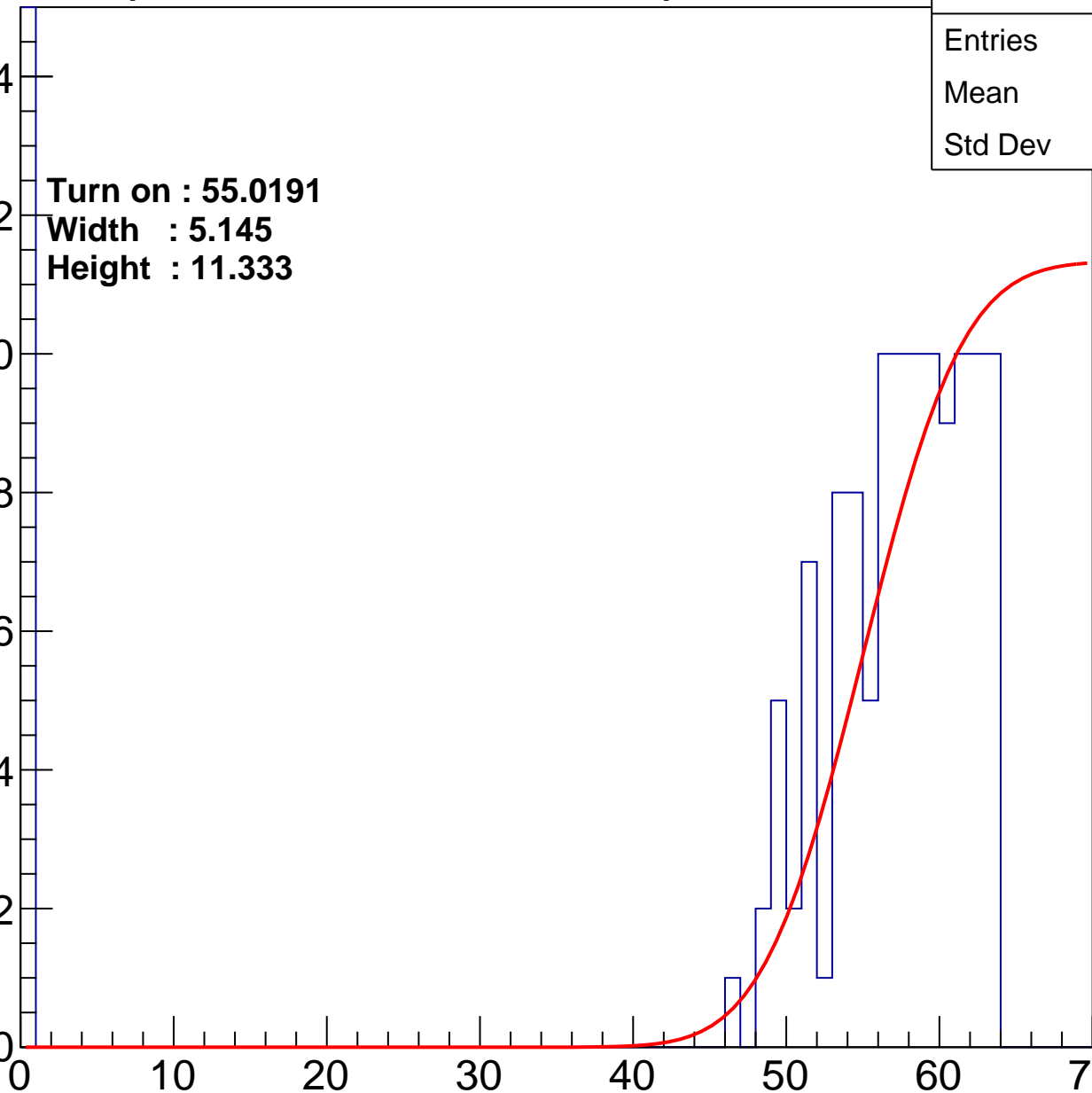
Width : 5.145

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	38.27
Std Dev	27.26

Turn on : 54.6626

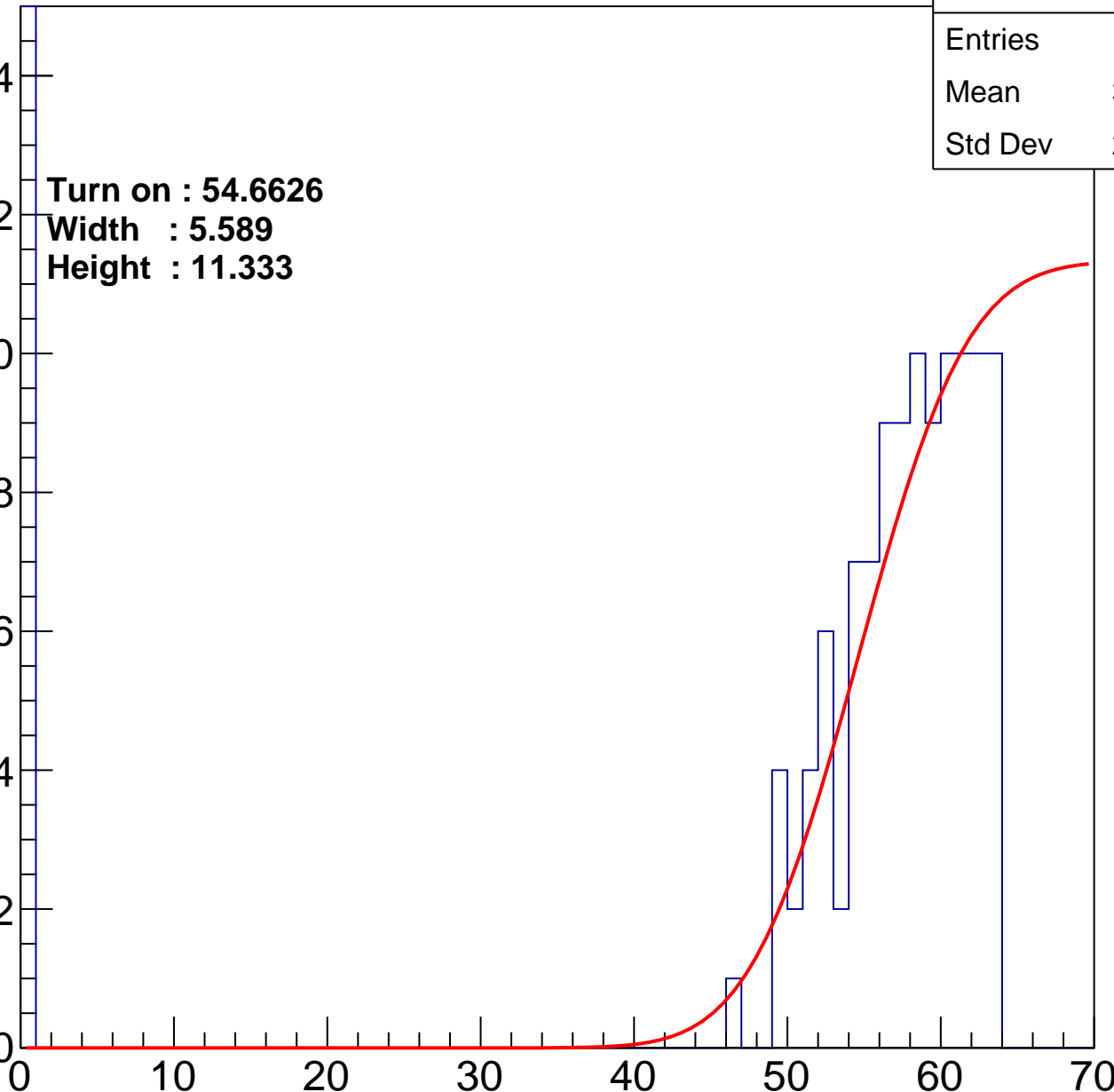
Width : 5.589

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	30
Std Dev	28.95

Turn on : 54.8349

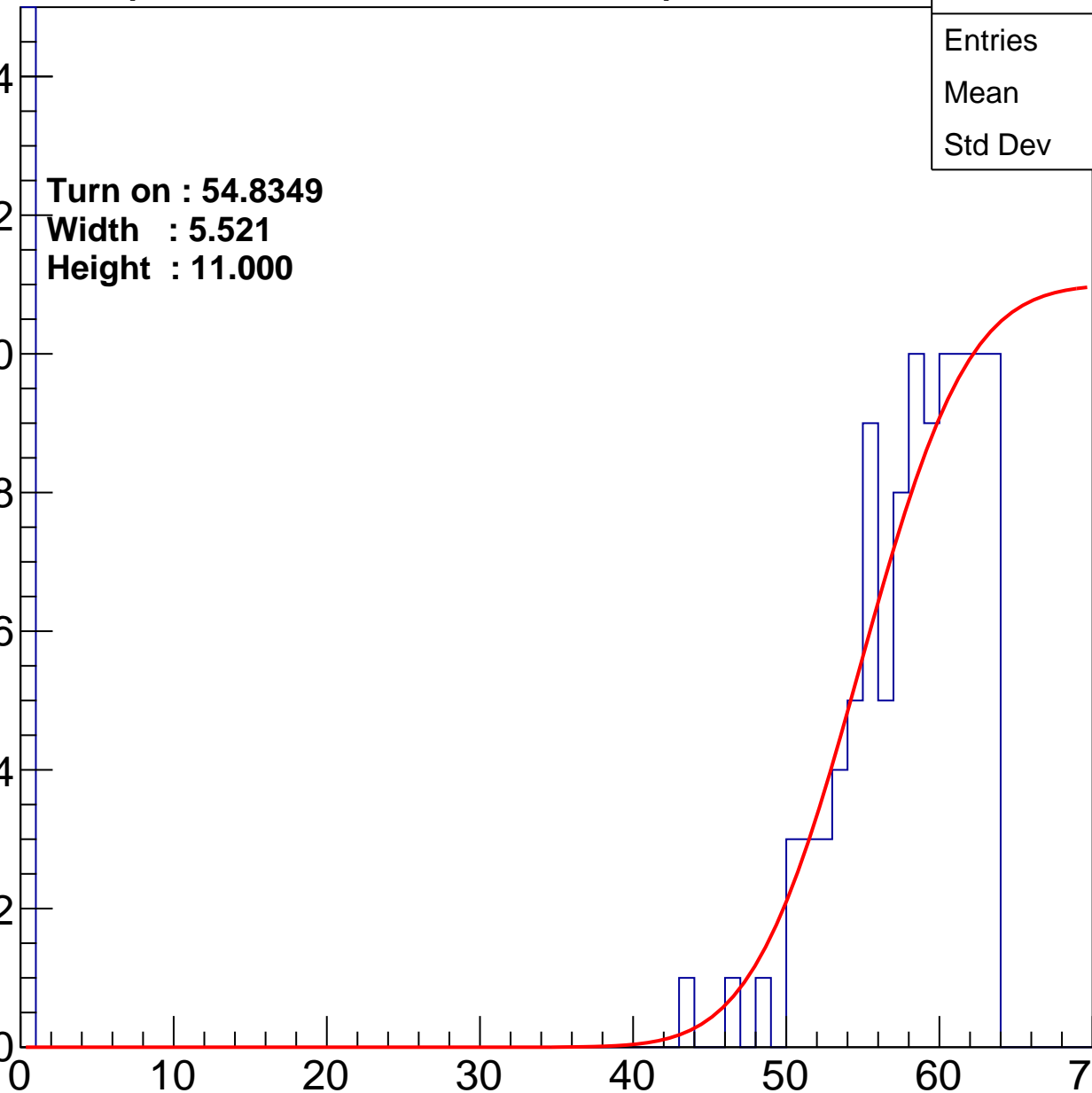
Width : 5.521

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	36.8
Std Dev	28.04

Turn on : 54.4076

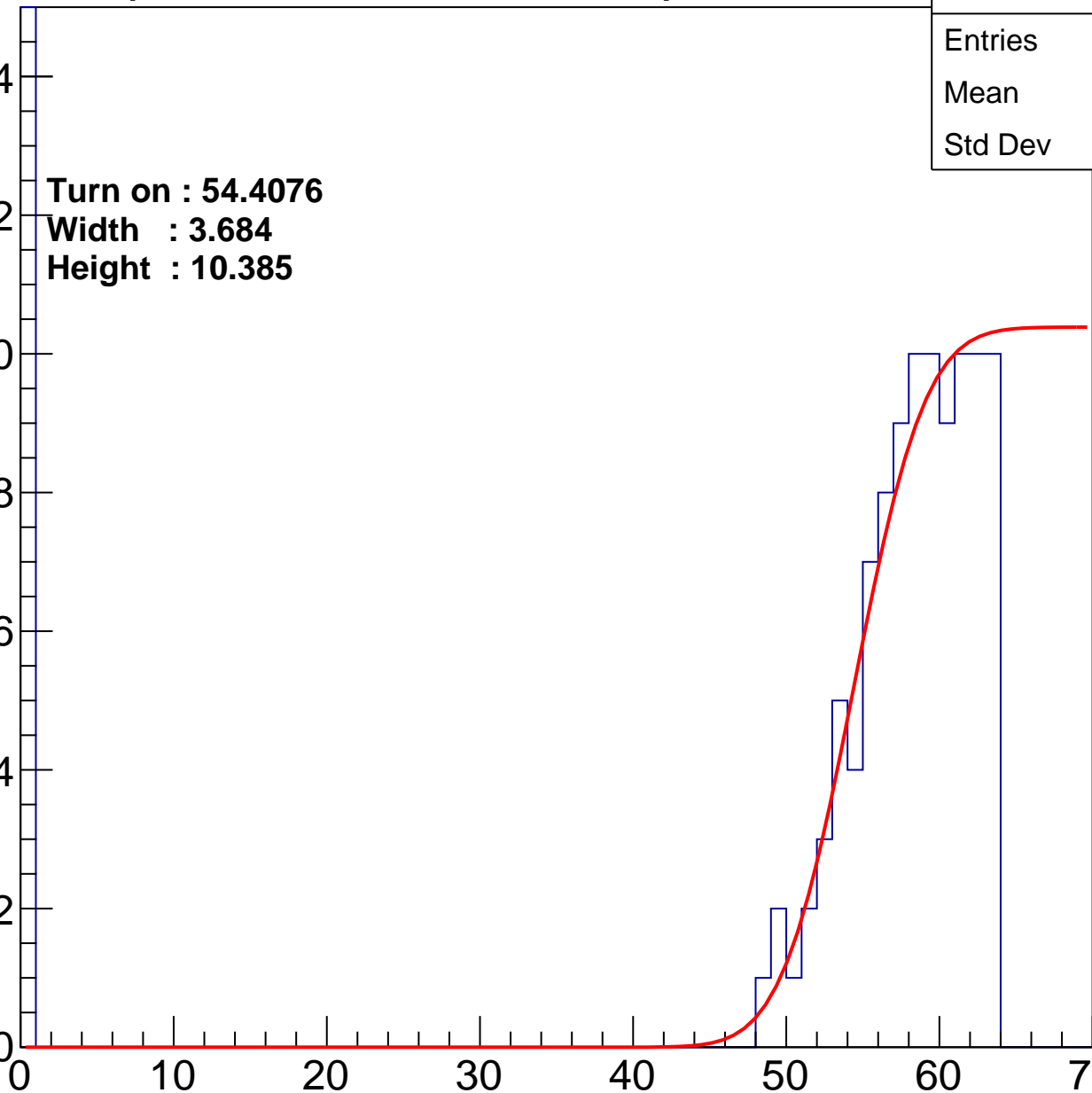
Width : 3.684

Height : 10.385

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch8

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	32.4
Std Dev	28.99

Turn on : 55.9259

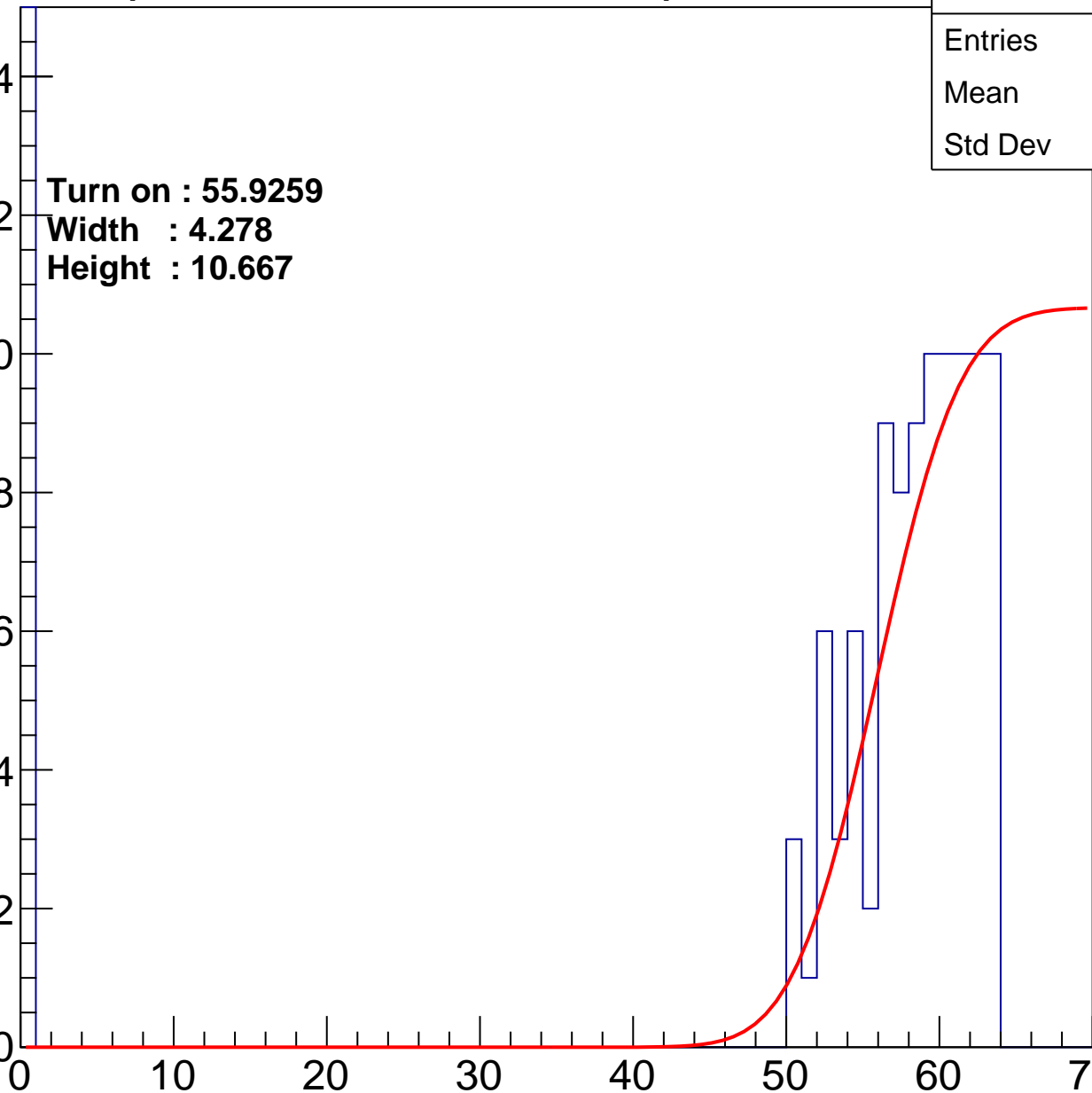
Width : 4.278

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch9

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	37.85
Std Dev	27.52

Turn on : 52.5430

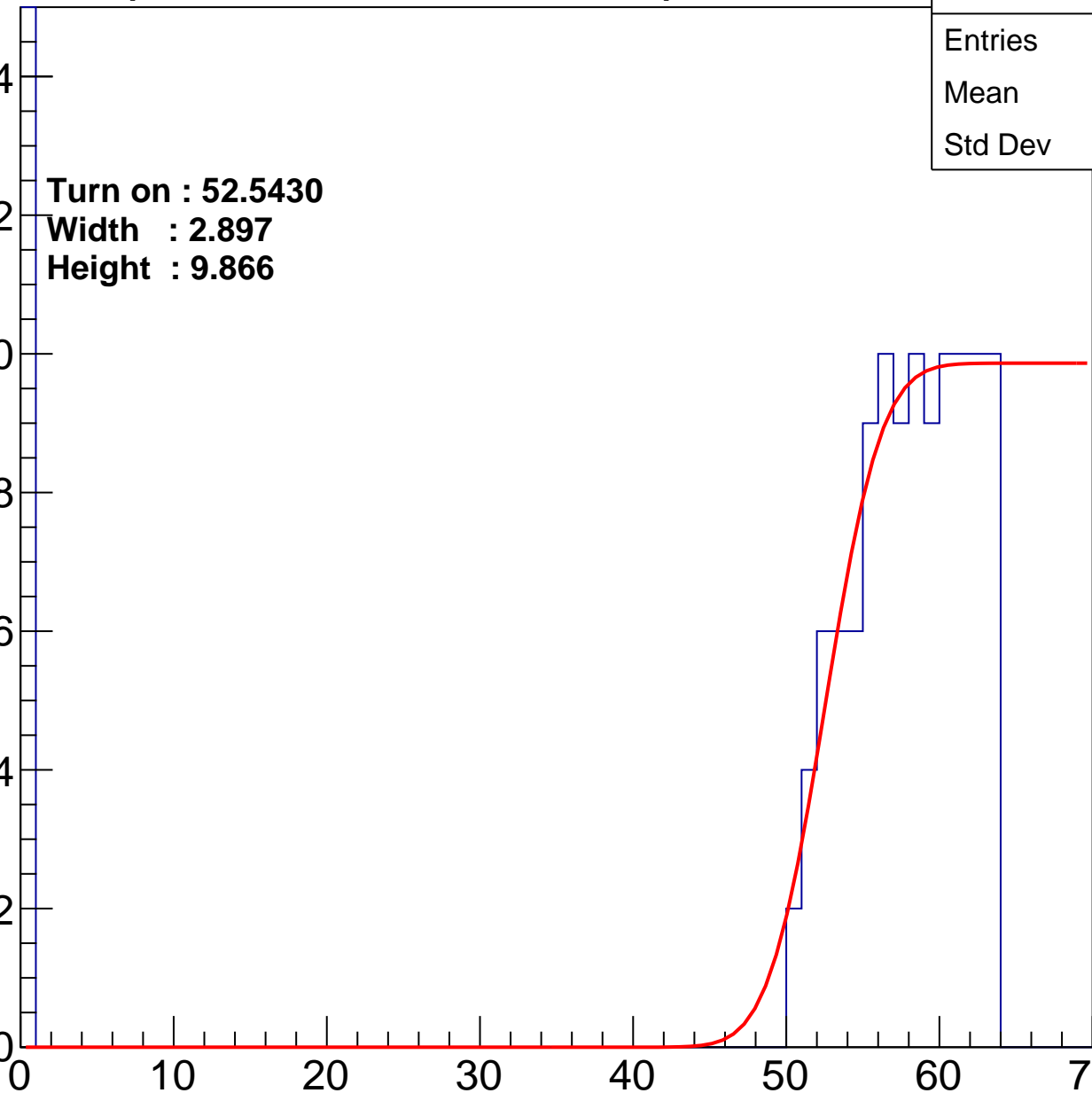
Width : 2.897

Height : 9.866

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	151
Mean	36.58
Std Dev	28.22

Turn on : 54.8008

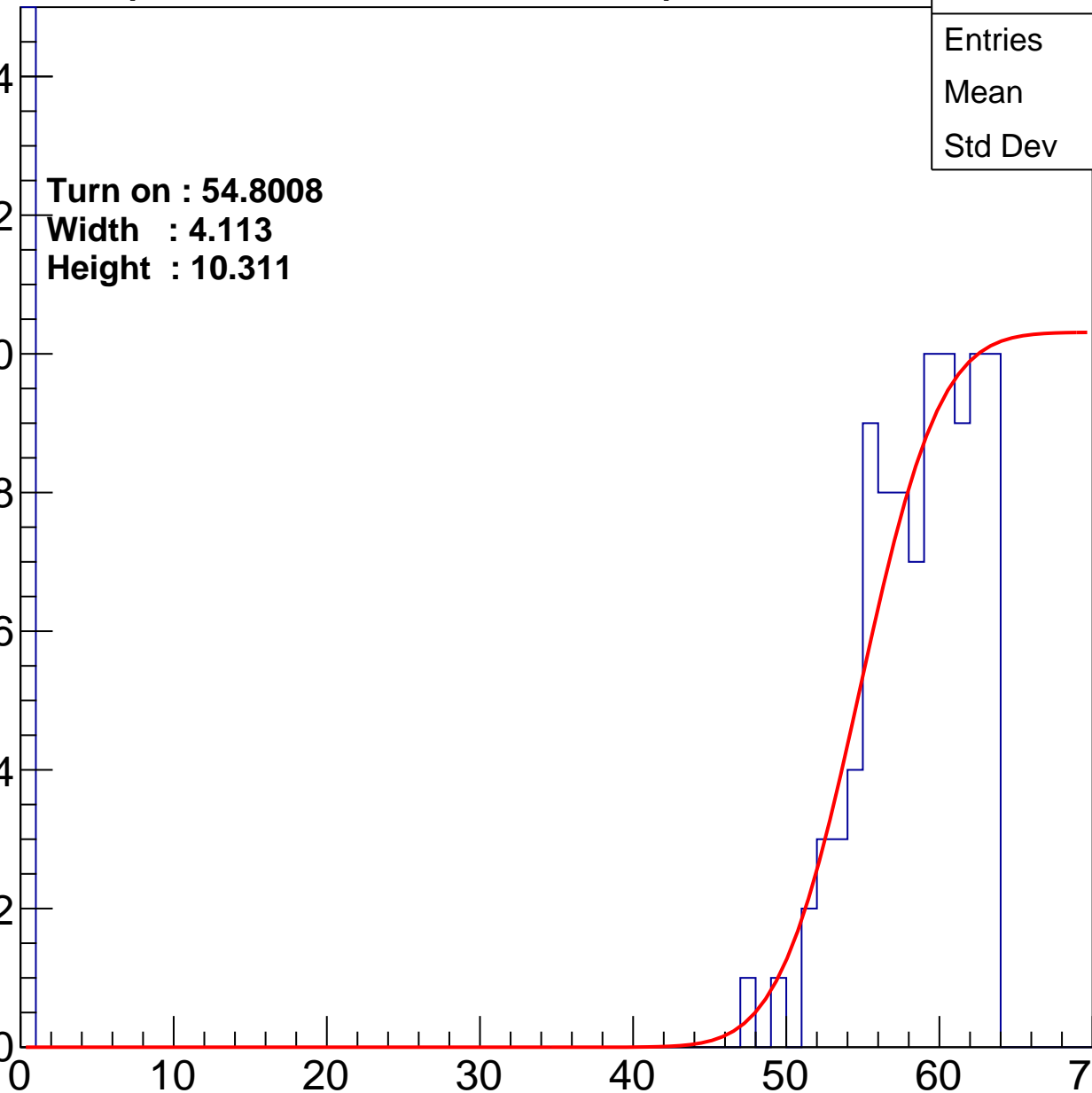
Width : 4.113

Height : 10.311

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	40.88
Std Dev	25.78

Turn on : 51.5748

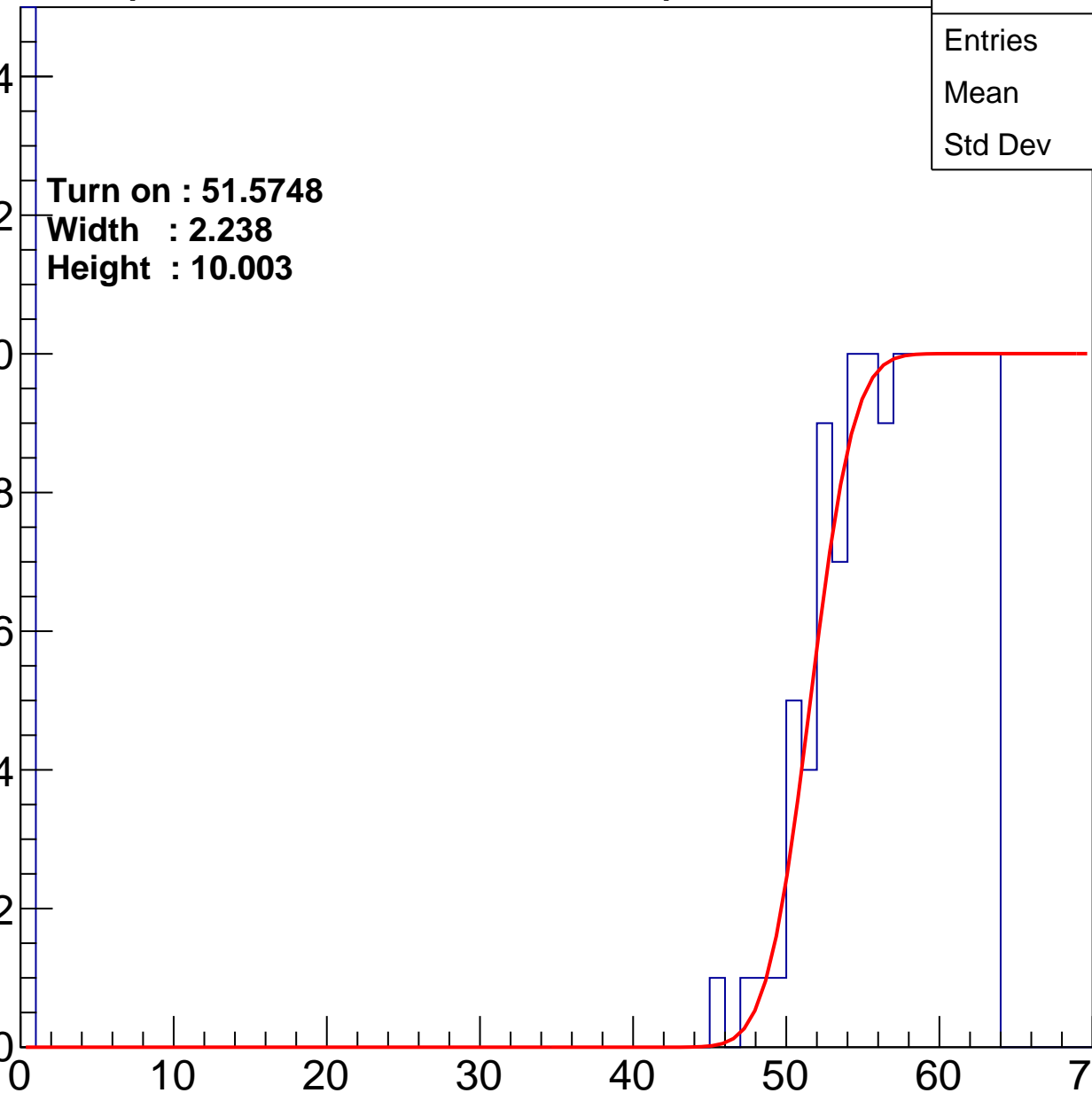
Width : 2.238

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	36.72
Std Dev	27.41

Turn on : 51.6670

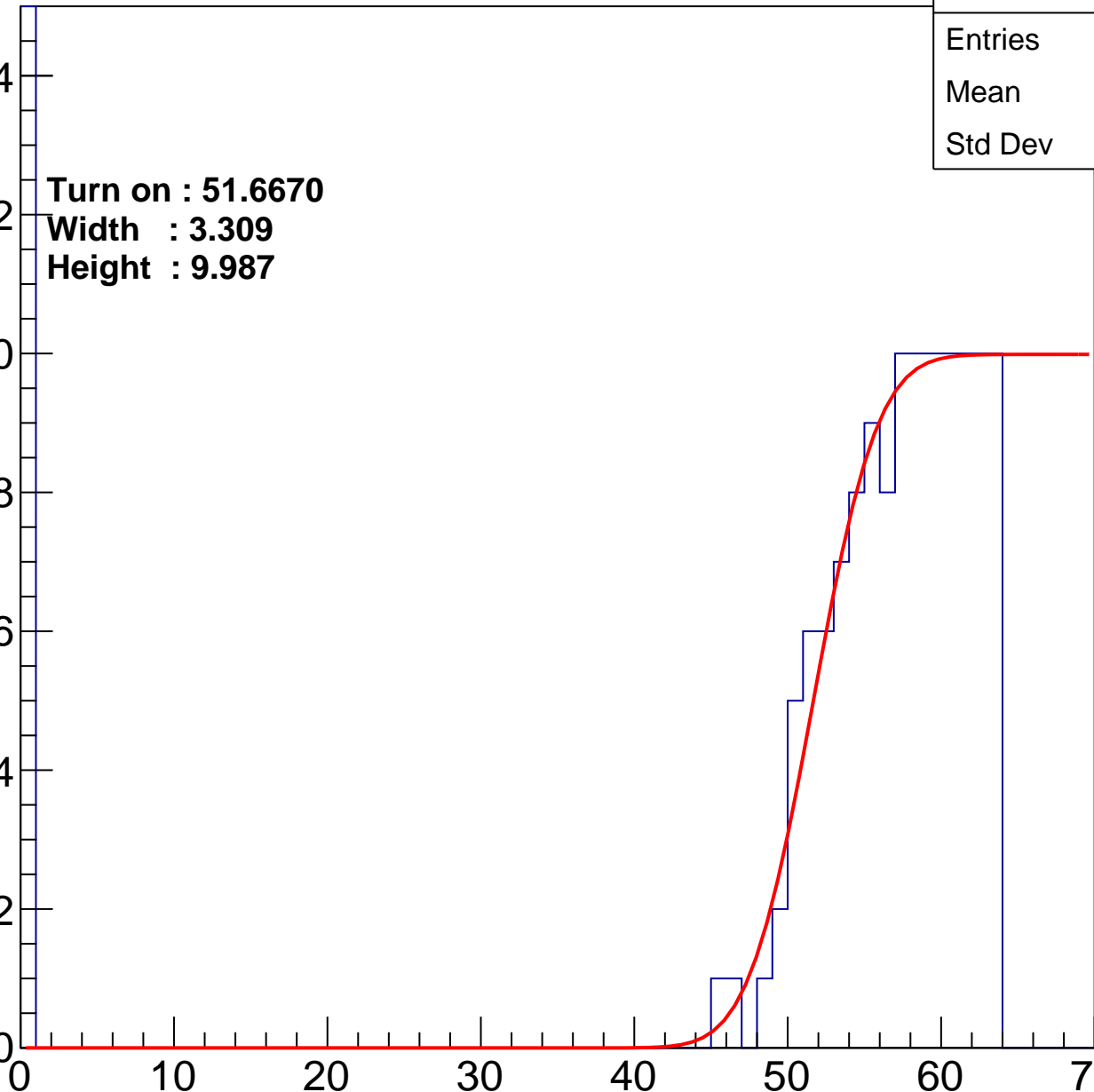
Width : 3.309

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	33.01
Std Dev	28.72

Turn on : 53.9272

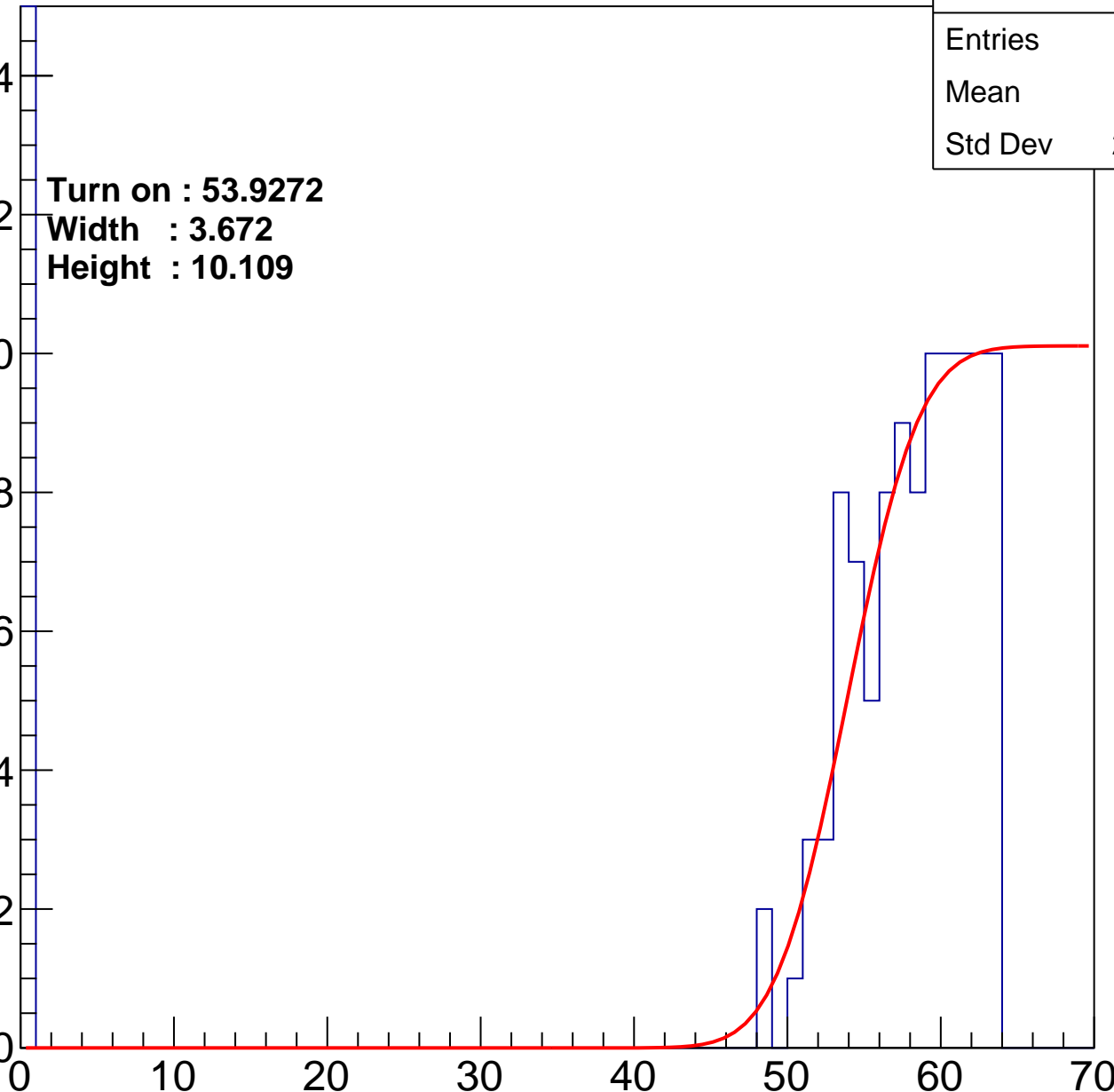
Width : 3.672

Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch14

calib_packv5_033123_0516.root, FC#4, port A1

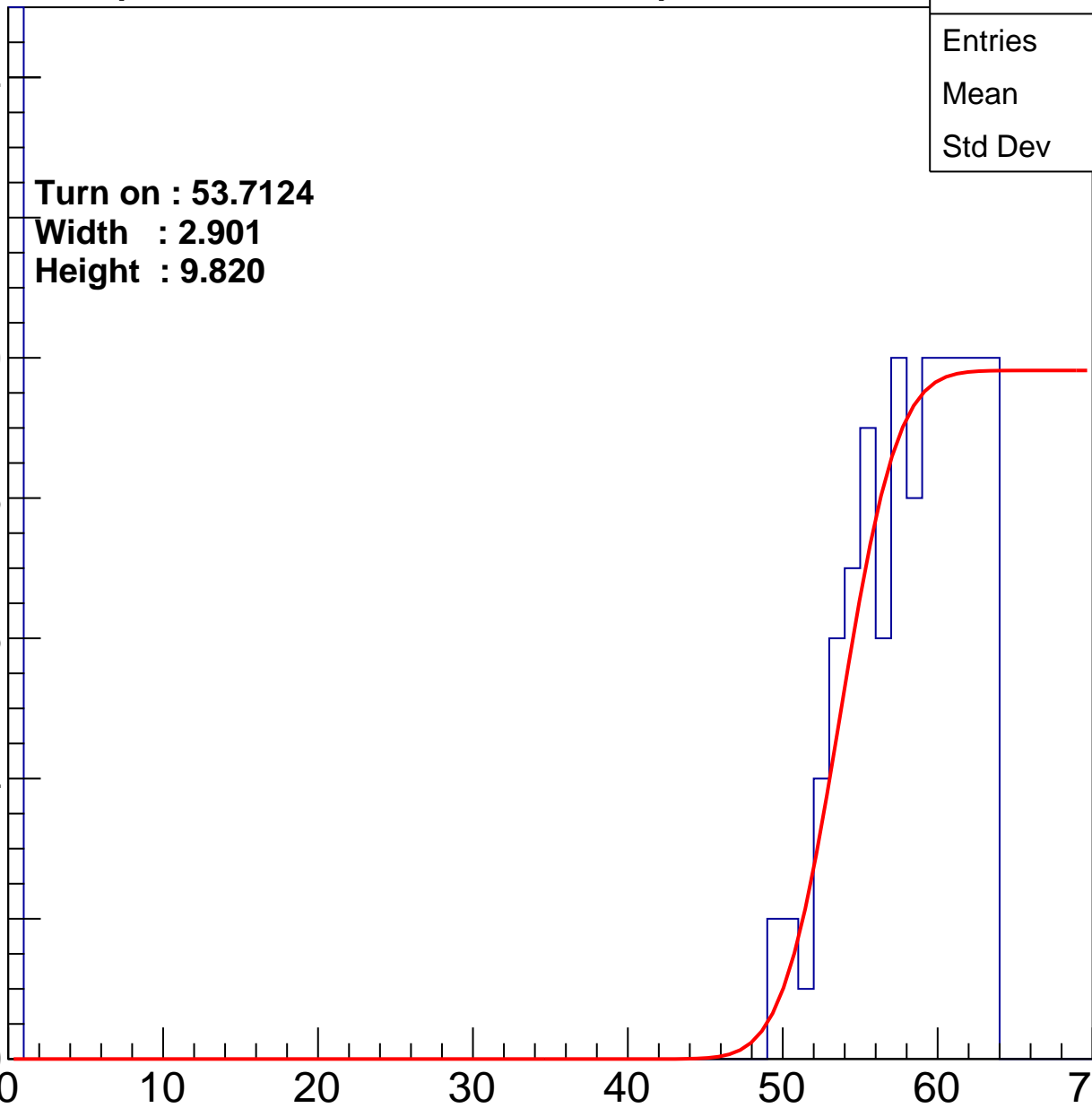
Entry

14
12
10
8
6
4
2
0

Turn on : 53.7124
Width : 2.901
Height : 9.820

Entries	169
Mean	35.91
Std Dev	28.18

ampl



B1L104S, U22-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	32.87
Std Dev	29.08

Turn on : 55.2664

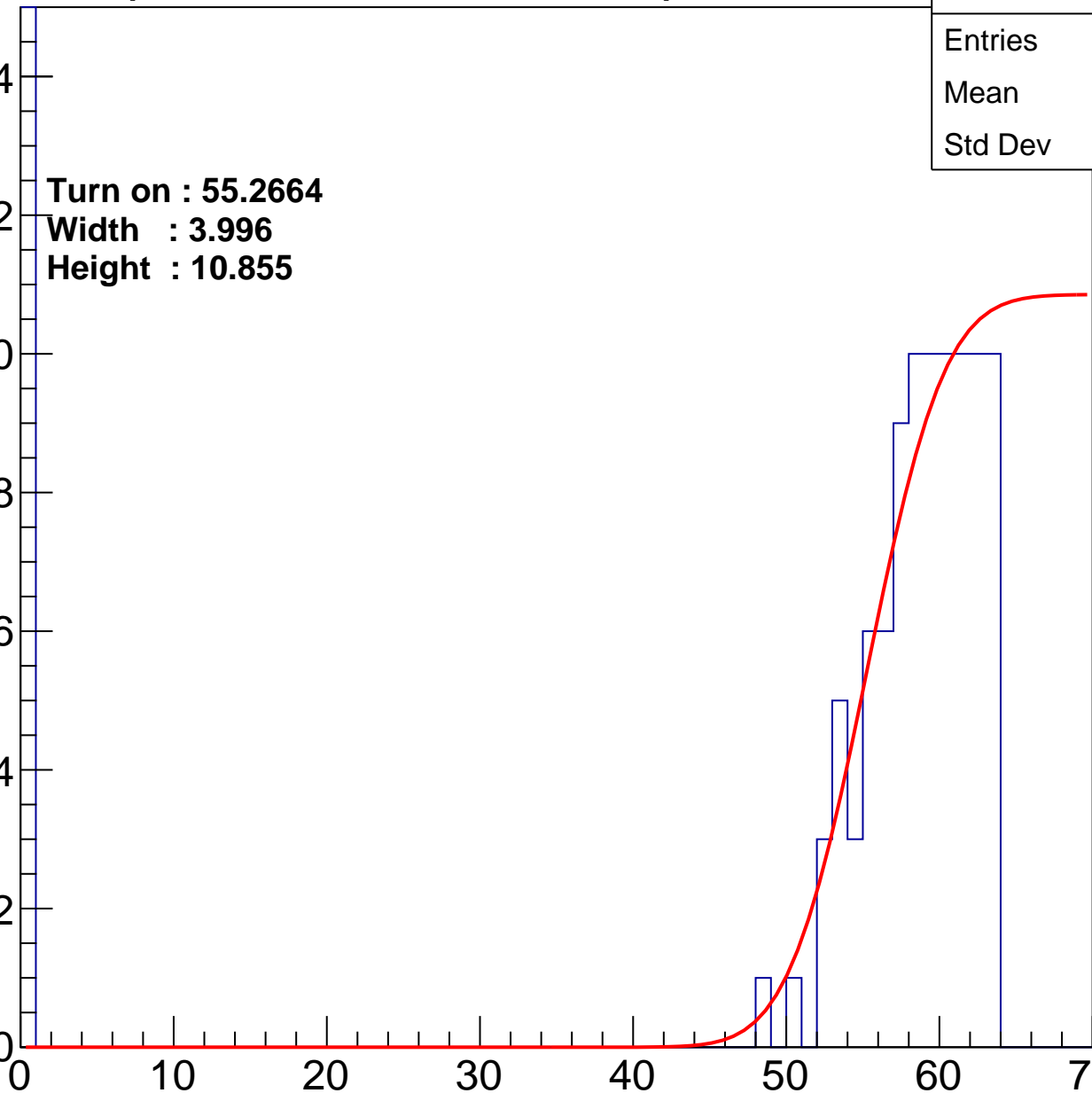
Width : 3.996

Height : 10.855

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	39.52
Std Dev	25.83

Turn on : 51.5089

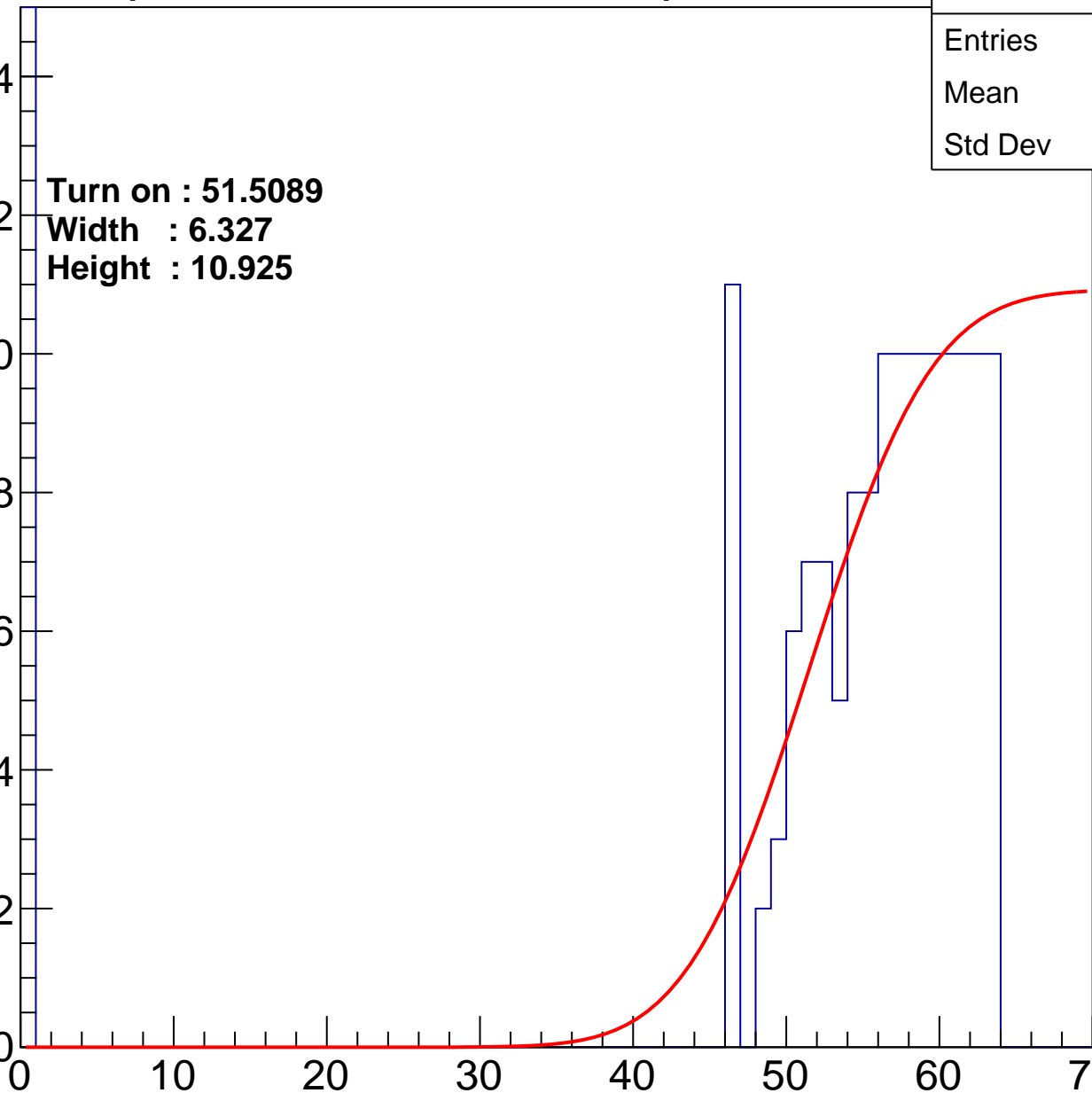
Width : 6.327

Height : 10.925

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	30.37
Std Dev	29.38

Turn on : 55.4348

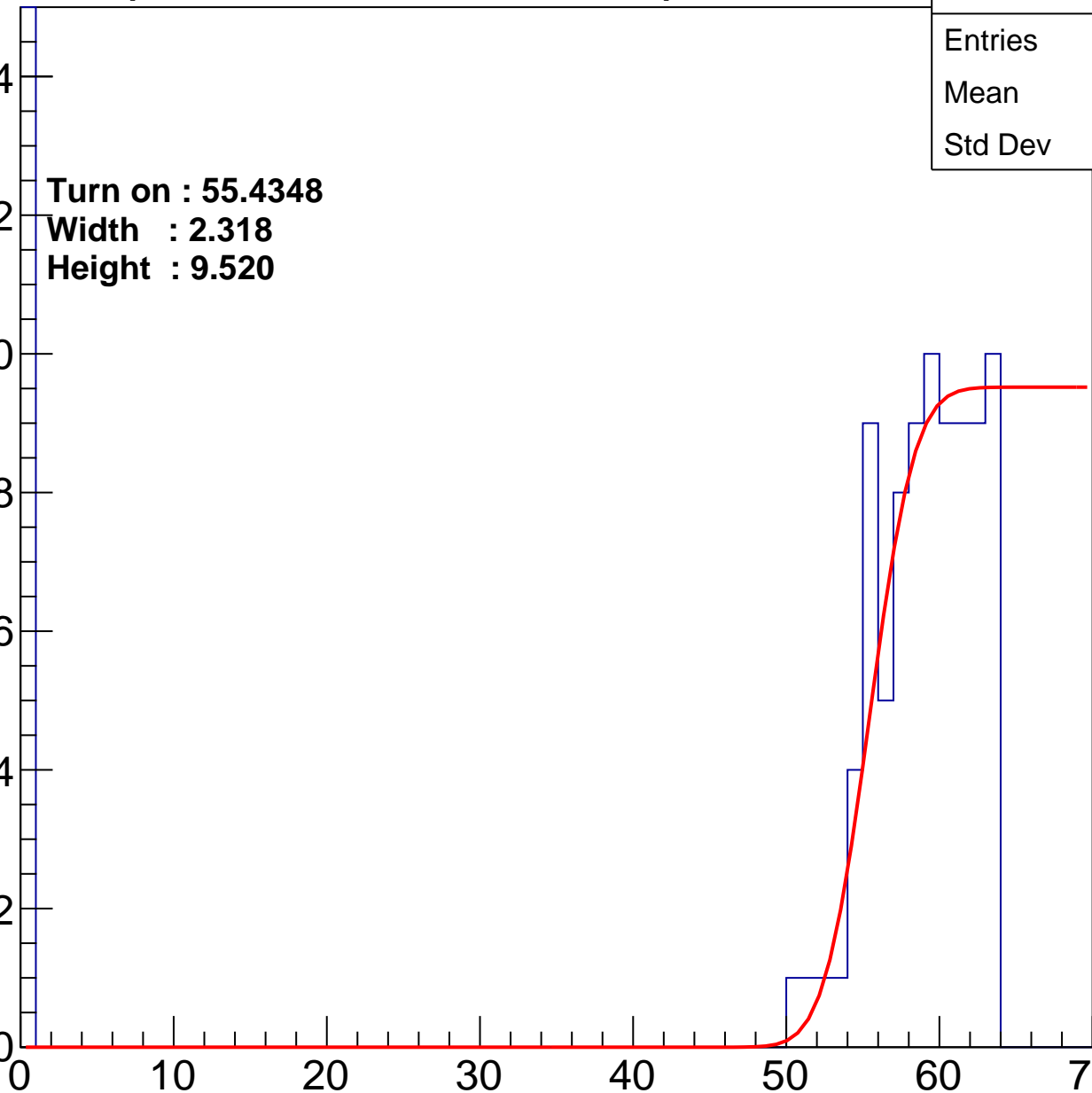
Width : 2.318

Height : 9.520

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	36.54
Std Dev	27.67

Turn on : 52.7682

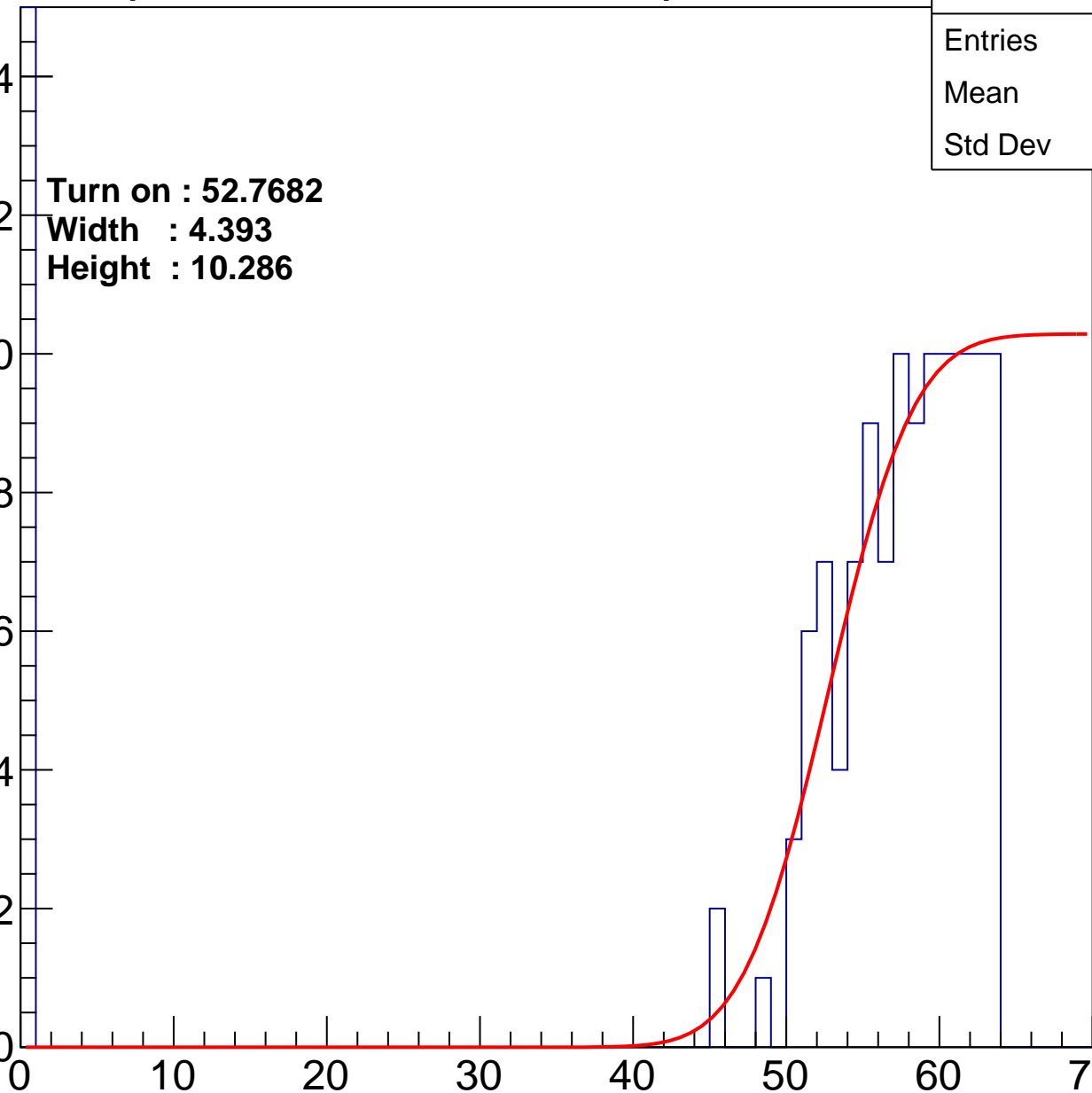
Width : 4.393

Height : 10.286

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	35.15
Std Dev	28.42

Turn on : 53.4554

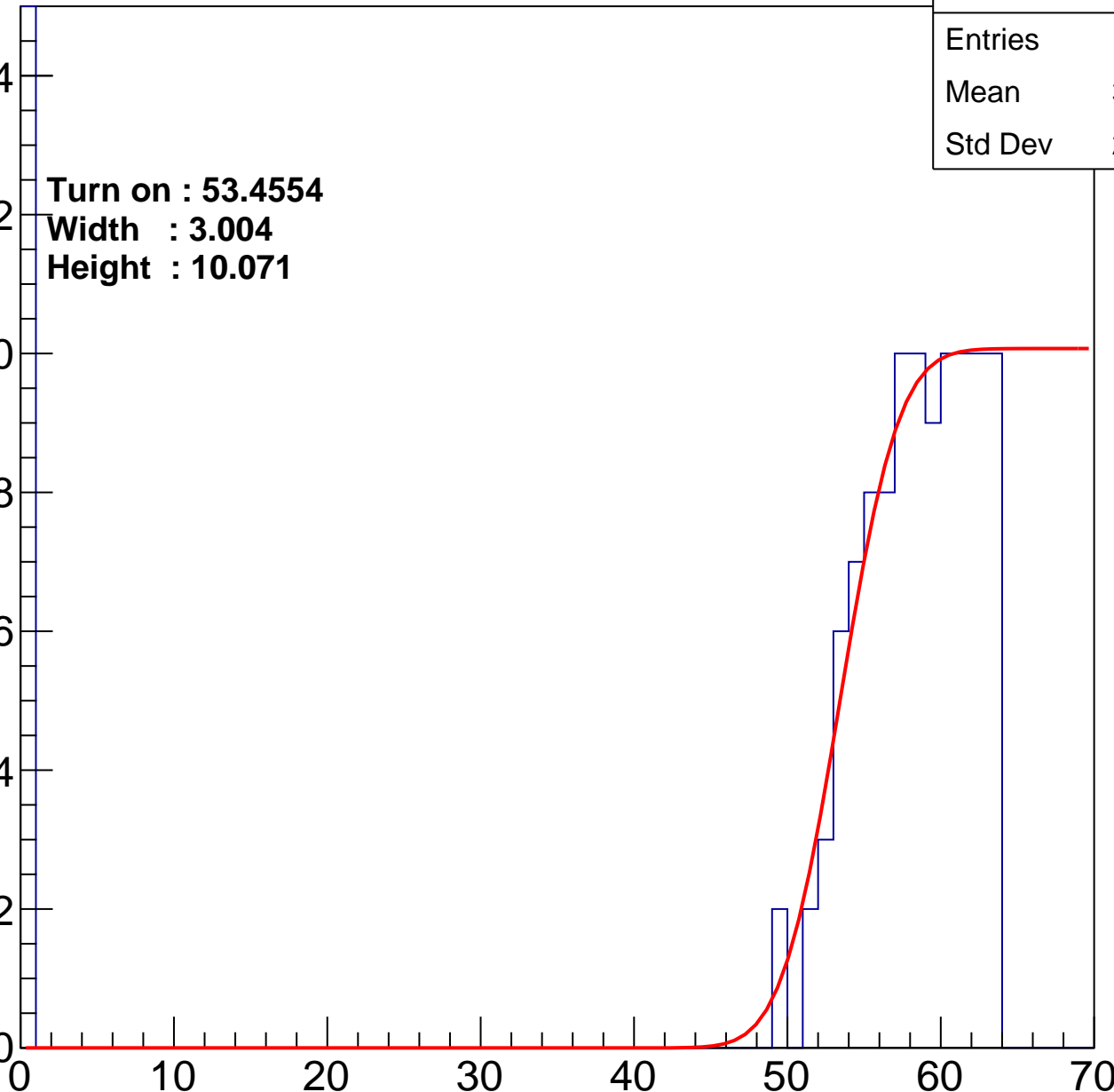
Width : 3.004

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch20

calib_packv5_033123_0516.root, FC#4, port A1

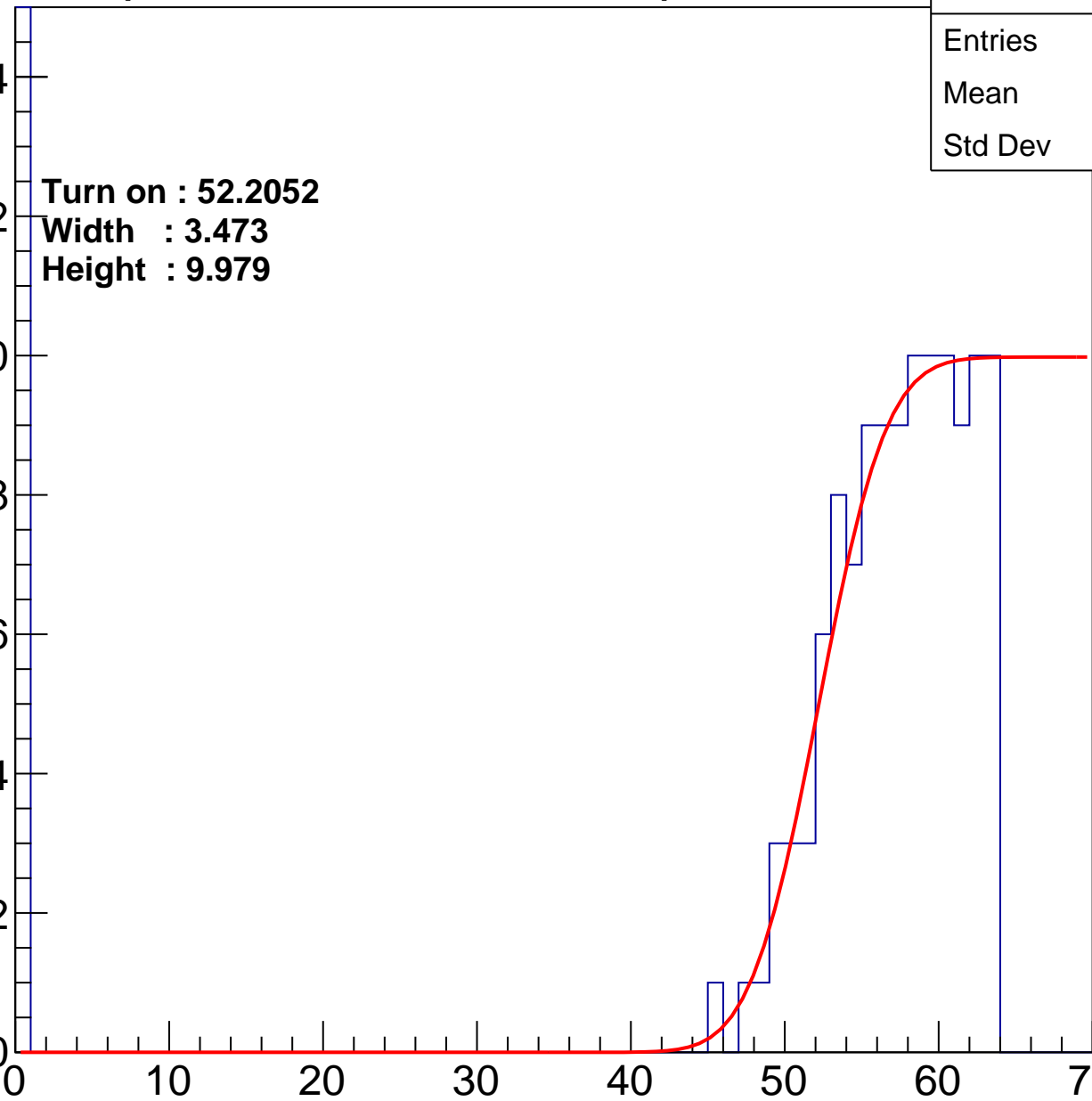
Entry

14
12
10
8
6
4
2
0

Turn on : 52.2052
Width : 3.473
Height : 9.979

Entries	204
Mean	33.26
Std Dev	28.29

ampl



B1L104S, U22-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	37.33
Std Dev	27.82

Turn on : 54.7177

Width : 5.216

Height : 11.000

Entry

14

12

10

8

6

4

2

0

0

10

20

30

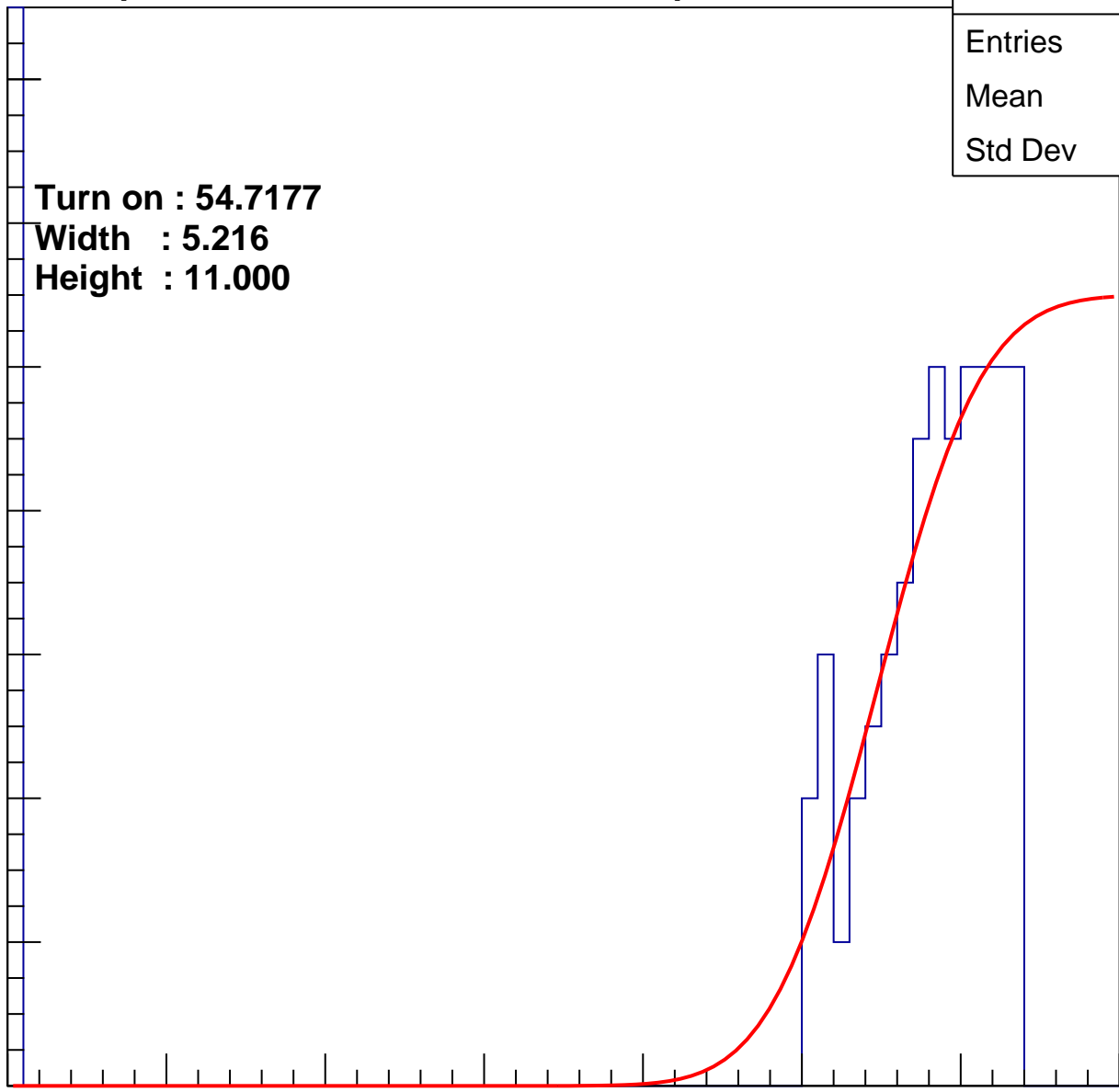
40

50

60

70

ampl



B1L104S, U22-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	33.52
Std Dev	28.33

Turn on : 52.4763

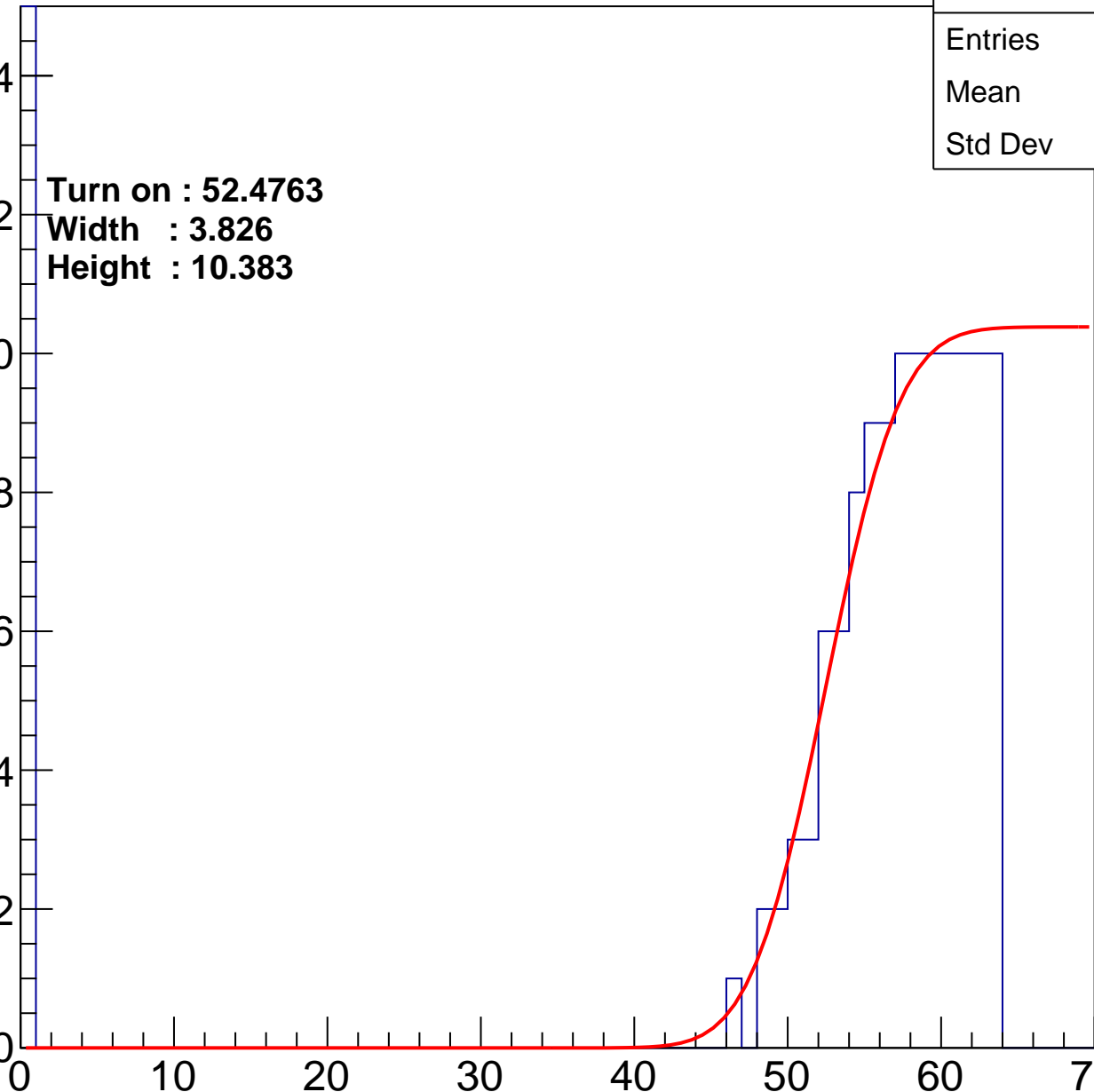
Width : 3.826

Height : 10.383

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch23

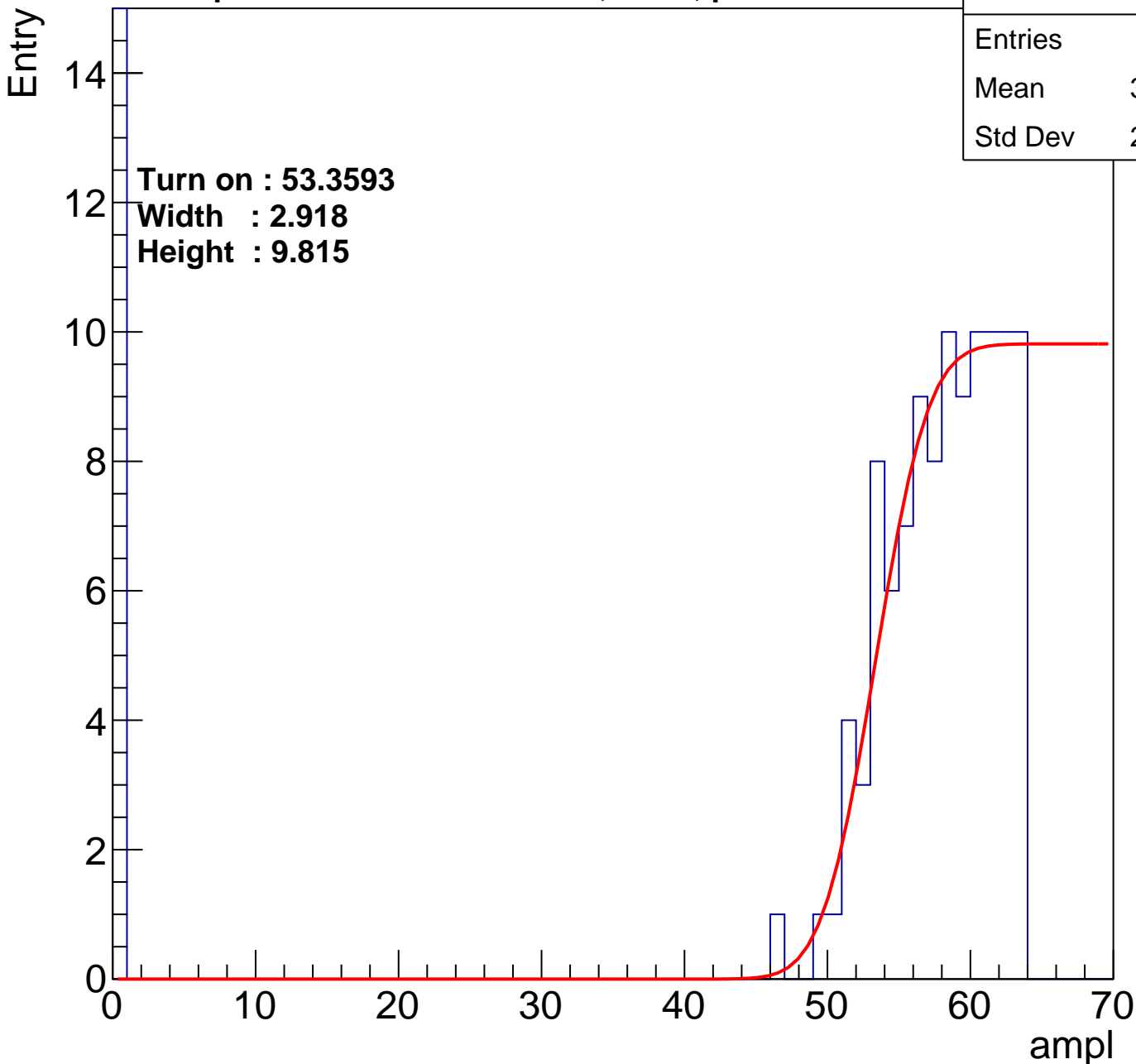
calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	33.17
Std Dev	28.64

Turn on : 53.3593

Width : 2.918

Height : 9.815



B1L104S, U22-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	36.26
Std Dev	27.62

Turn on : 51.8128

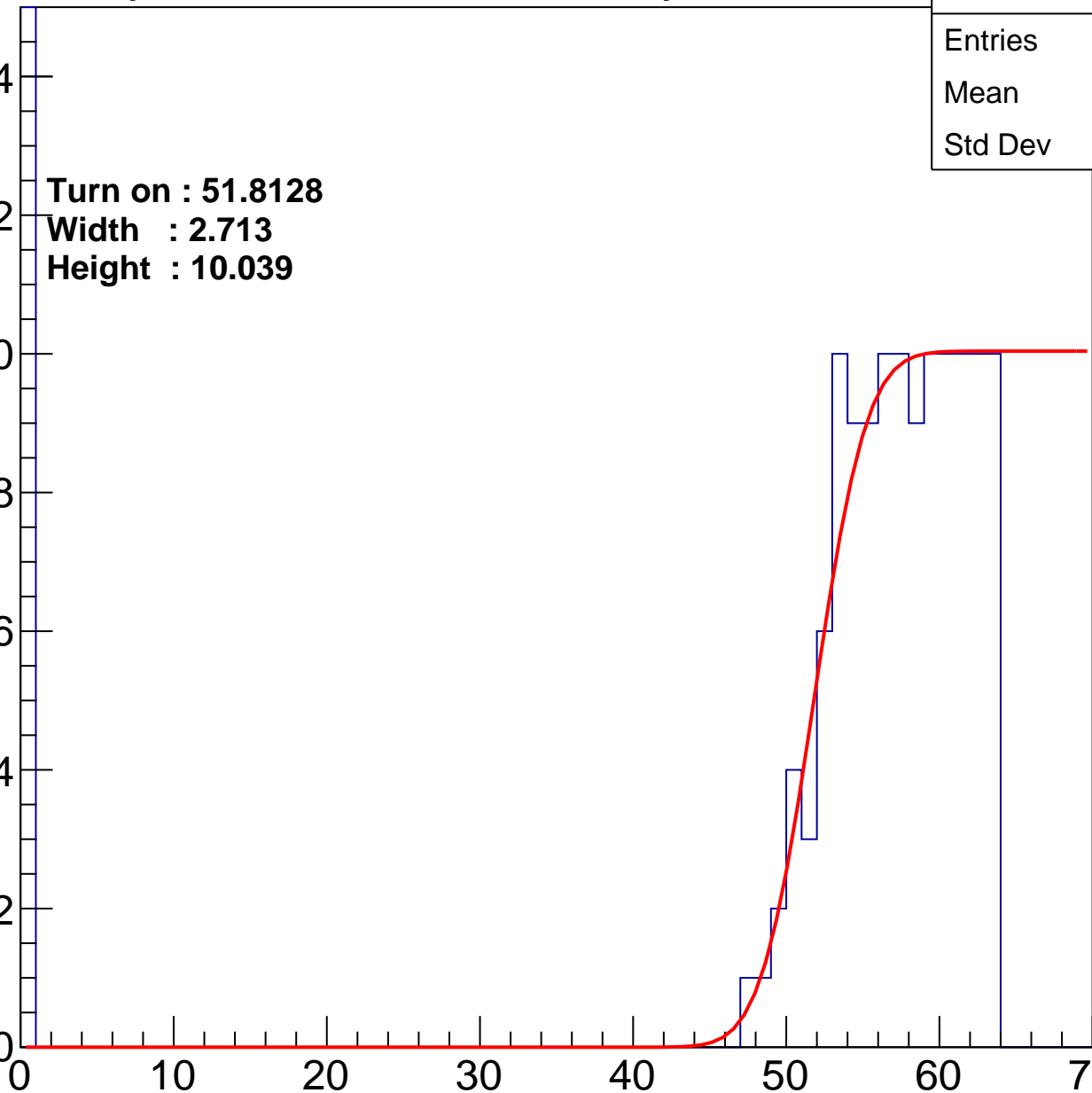
Width : 2.713

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch25

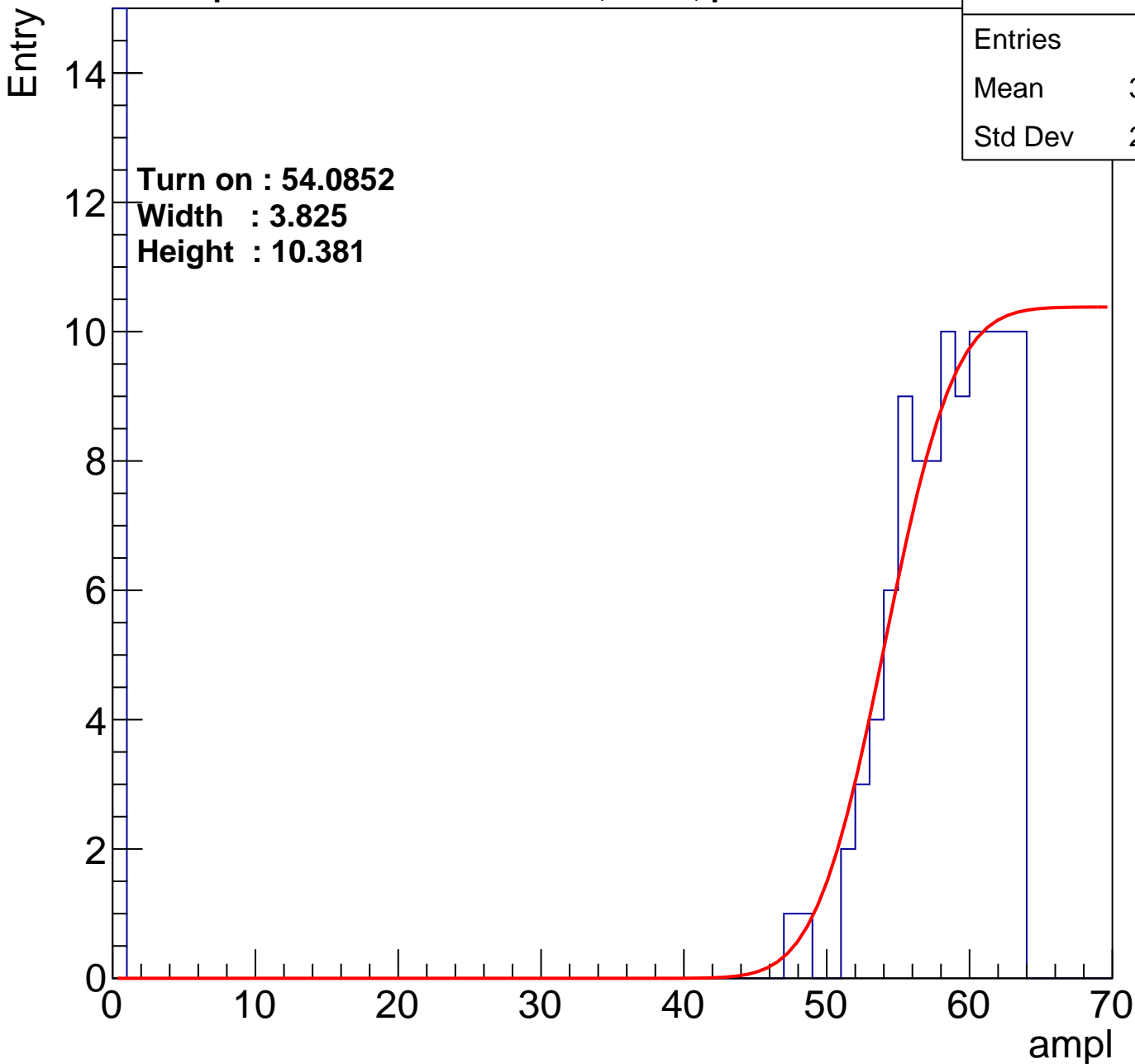
calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	32.19
Std Dev	28.95

Turn on : 54.0852

Width : 3.825

Height : 10.381



B1L104S, U22-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	35.65
Std Dev	27.61

Turn on : 51.8470

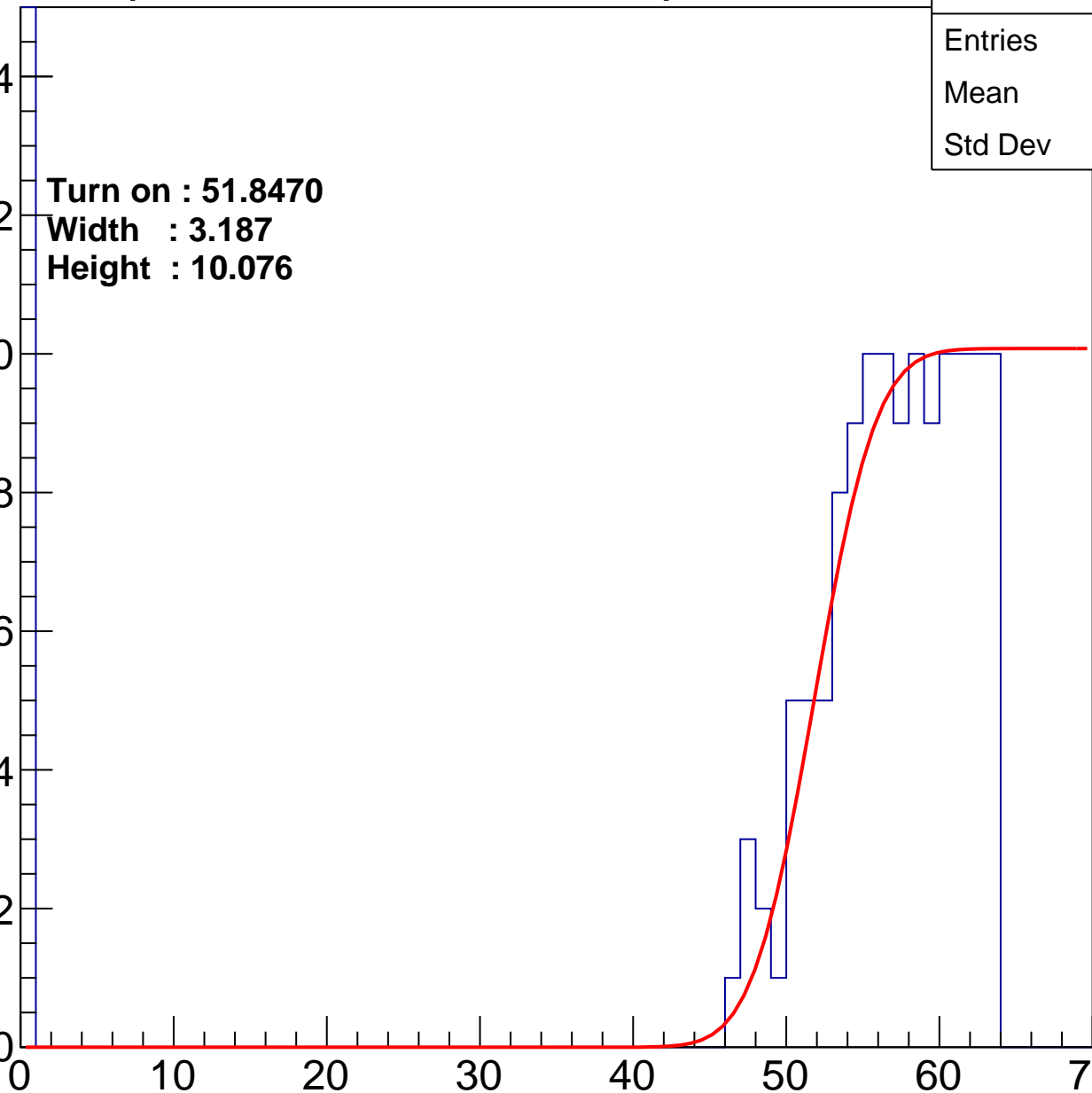
Width : 3.187

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	32.87
Std Dev	28.65

Turn on : 53.3895

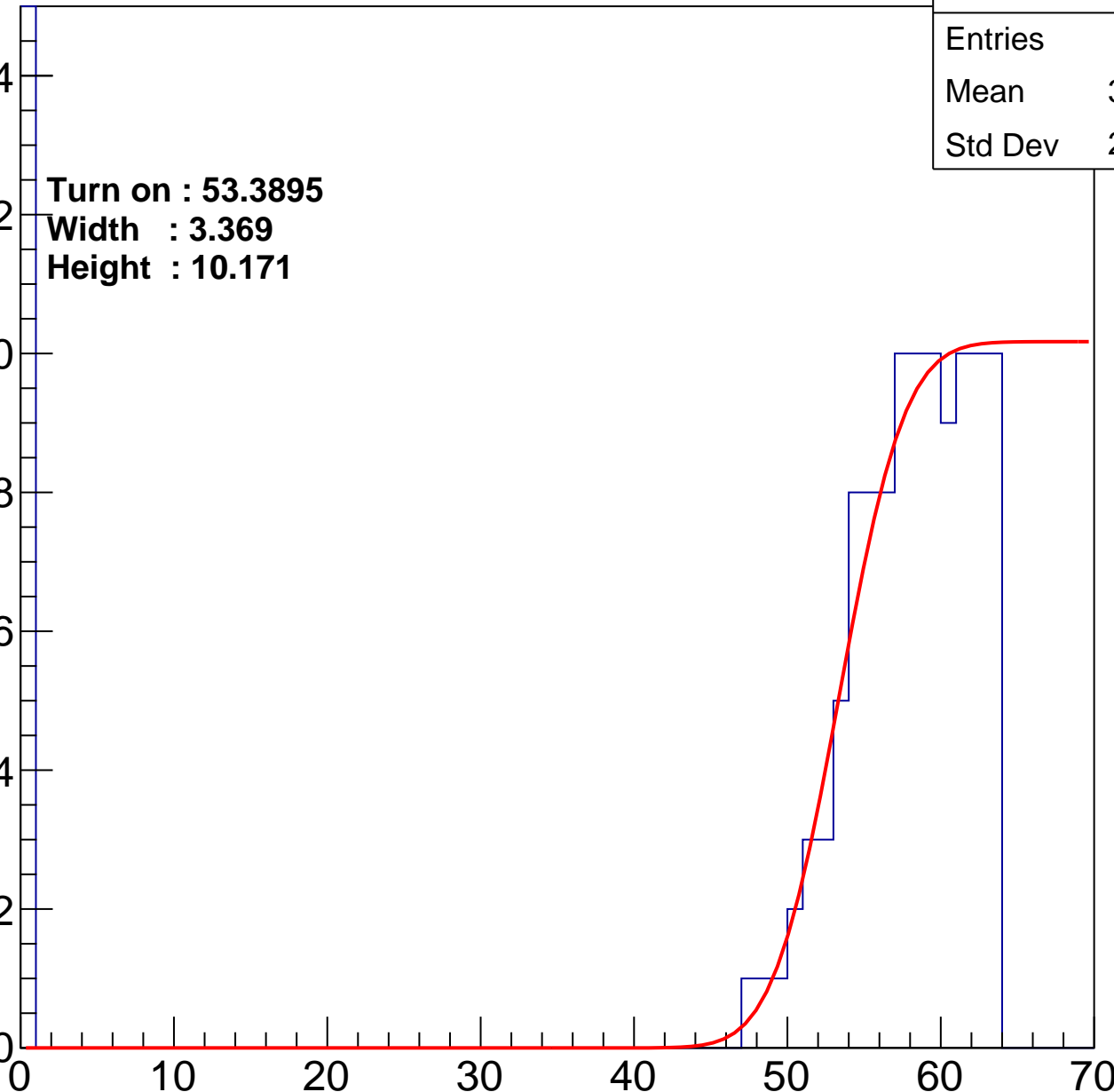
Width : 3.369

Height : 10.171

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	30.25
Std Dev	29.07

Turn on : 54.6824

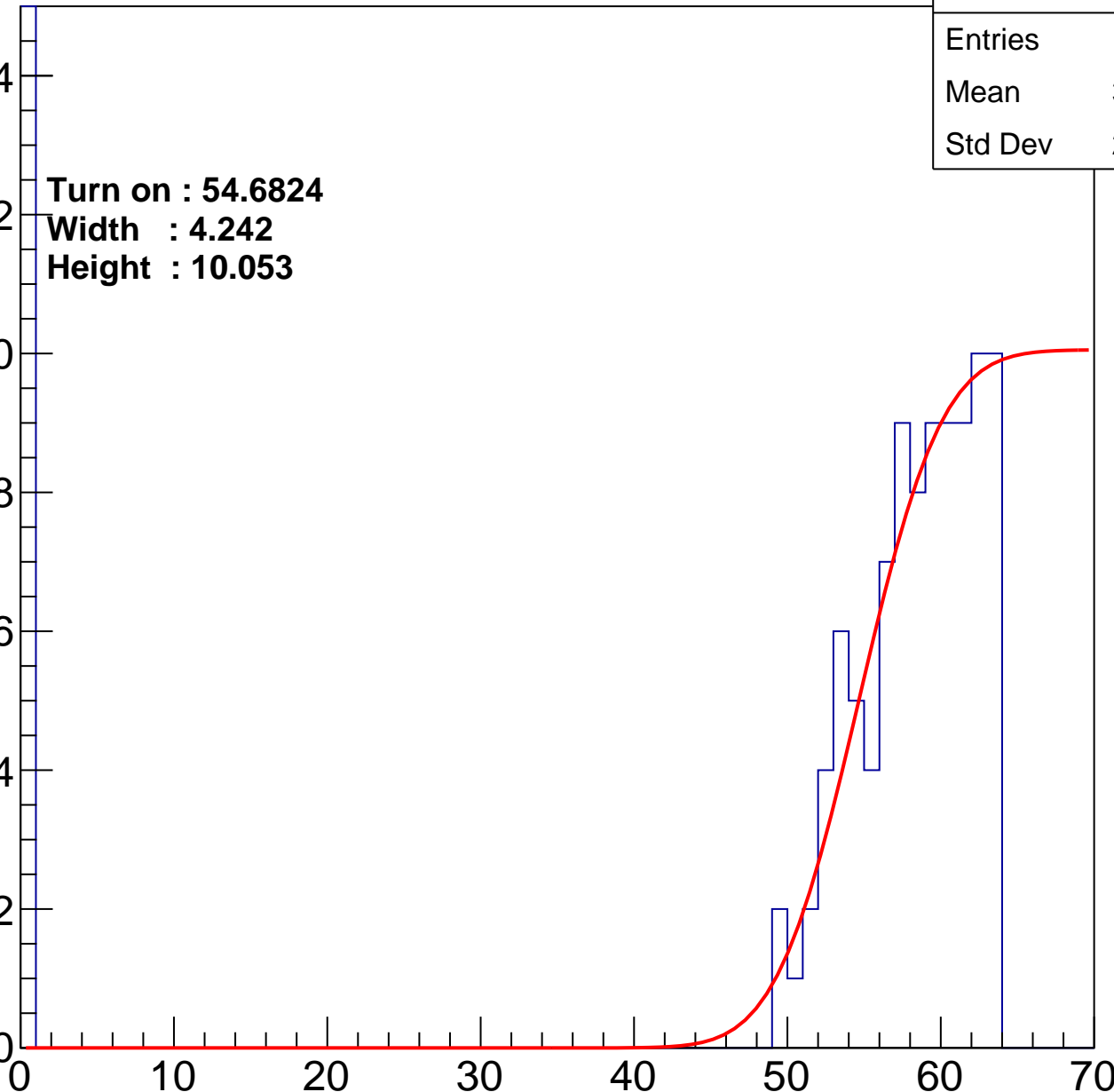
Width : 4.242

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	29.25
Std Dev	29.2

Turn on : 54.3936

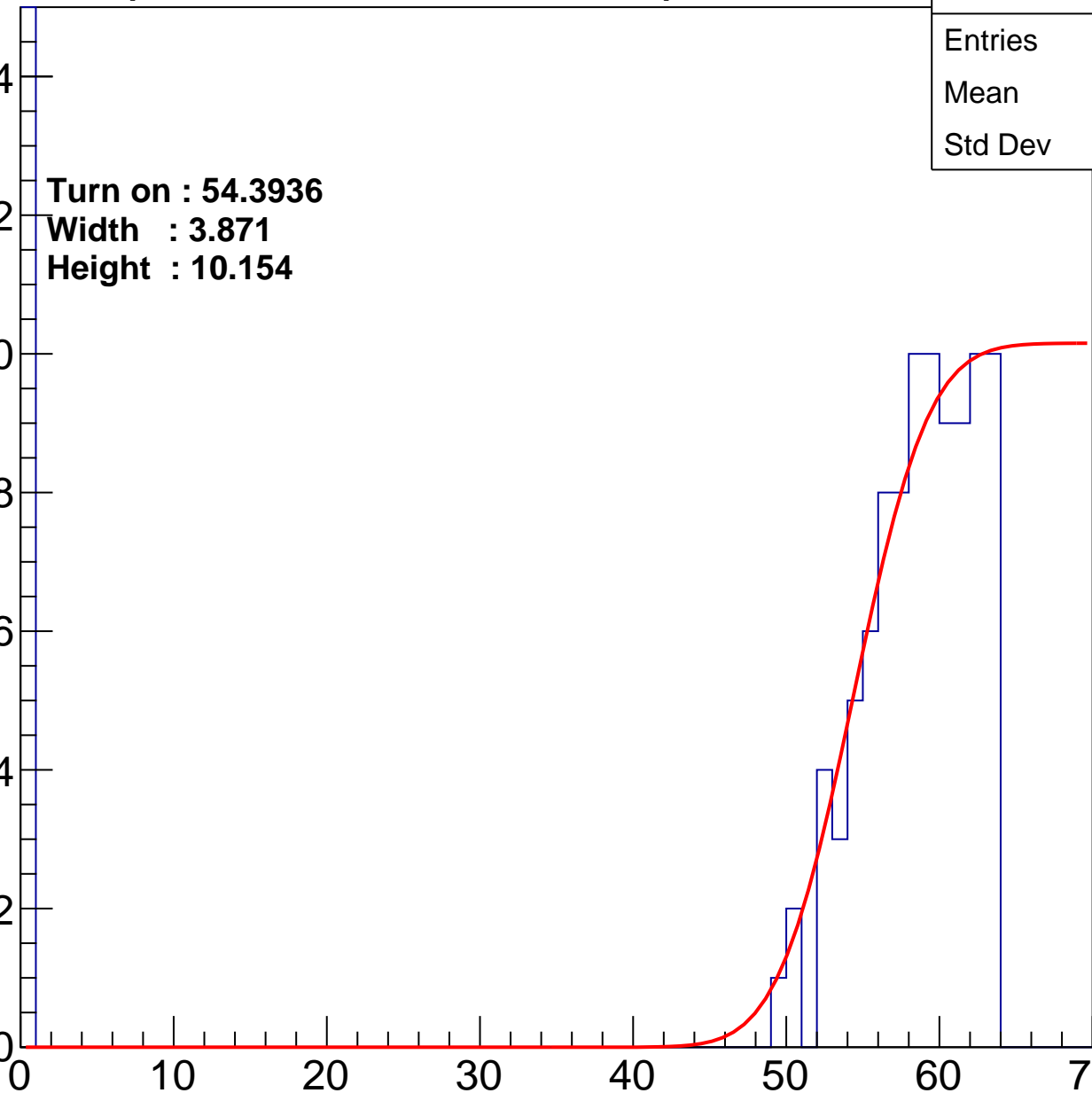
Width : 3.871

Height : 10.154

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	31.18
Std Dev	28.99

Turn on : 54.3218

Width : 1.956

Height : 10.152

Entry

14

12

10

8

6

4

2

0

0

10

20

30

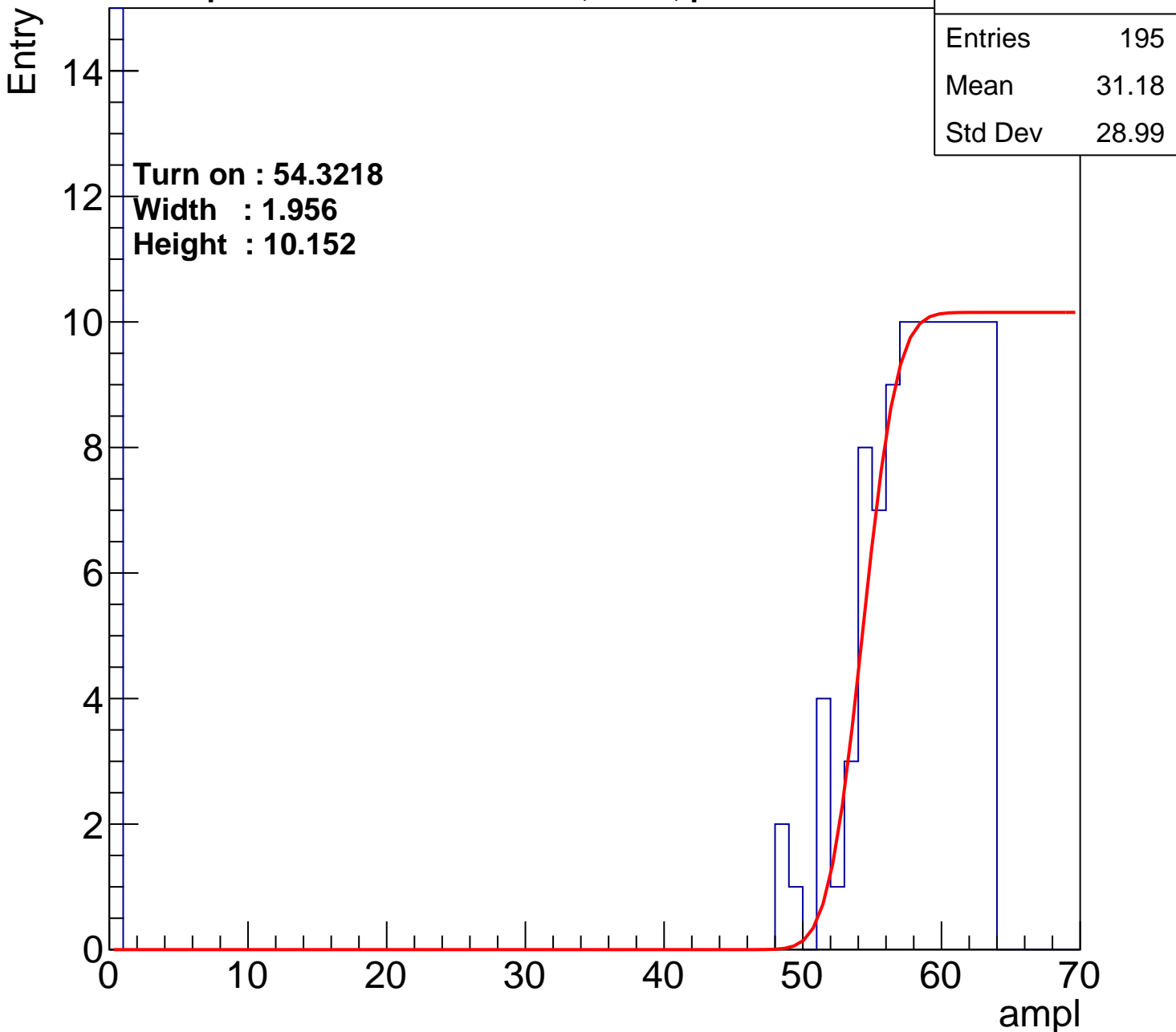
40

50

60

70

ampl



B1L104S, U22-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	28.4
Std Dev	29.07

Turn on : 53.9053

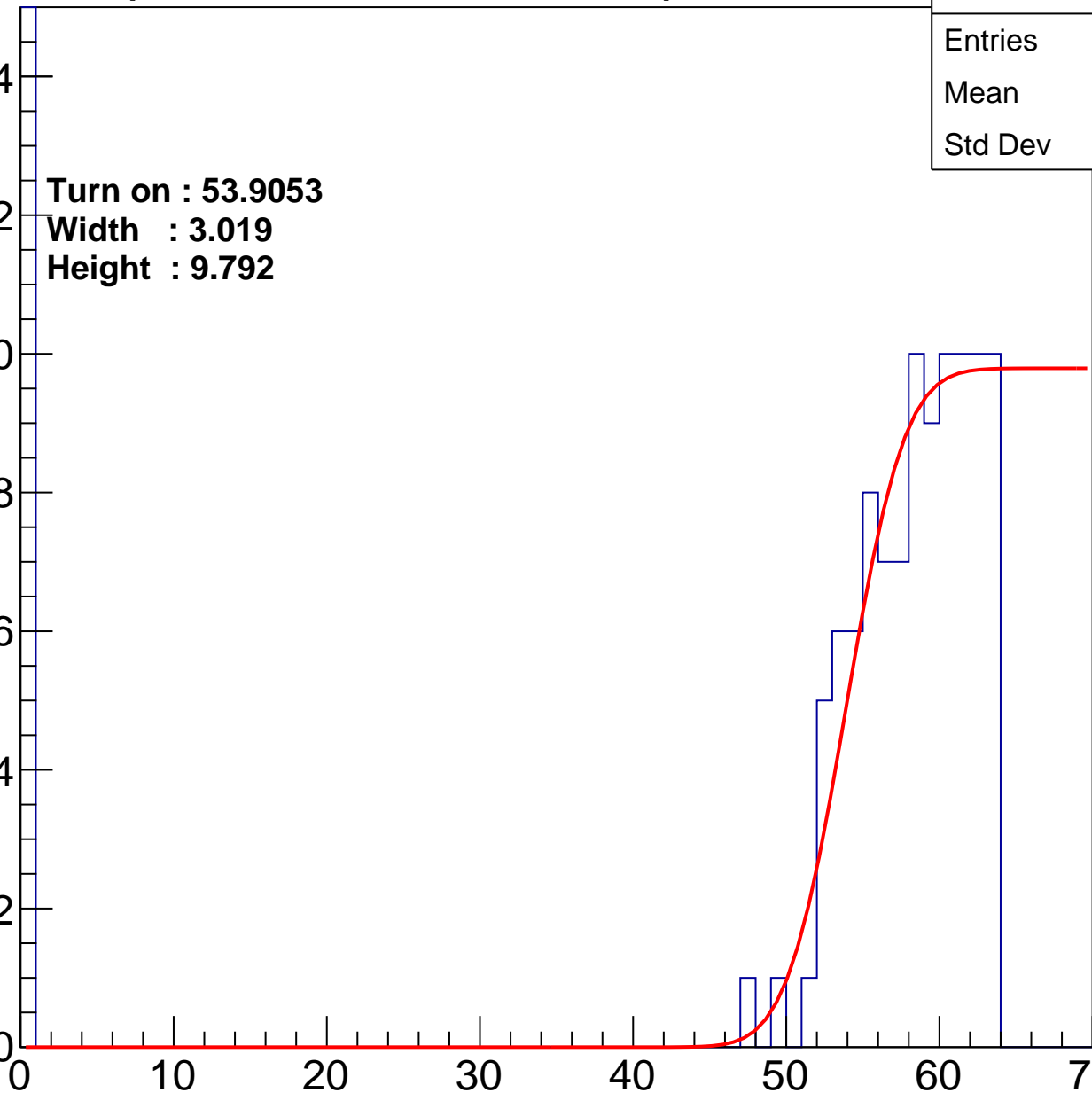
Width : 3.019

Height : 9.792

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	31.22
Std Dev	28.81

Turn on : 53.7959

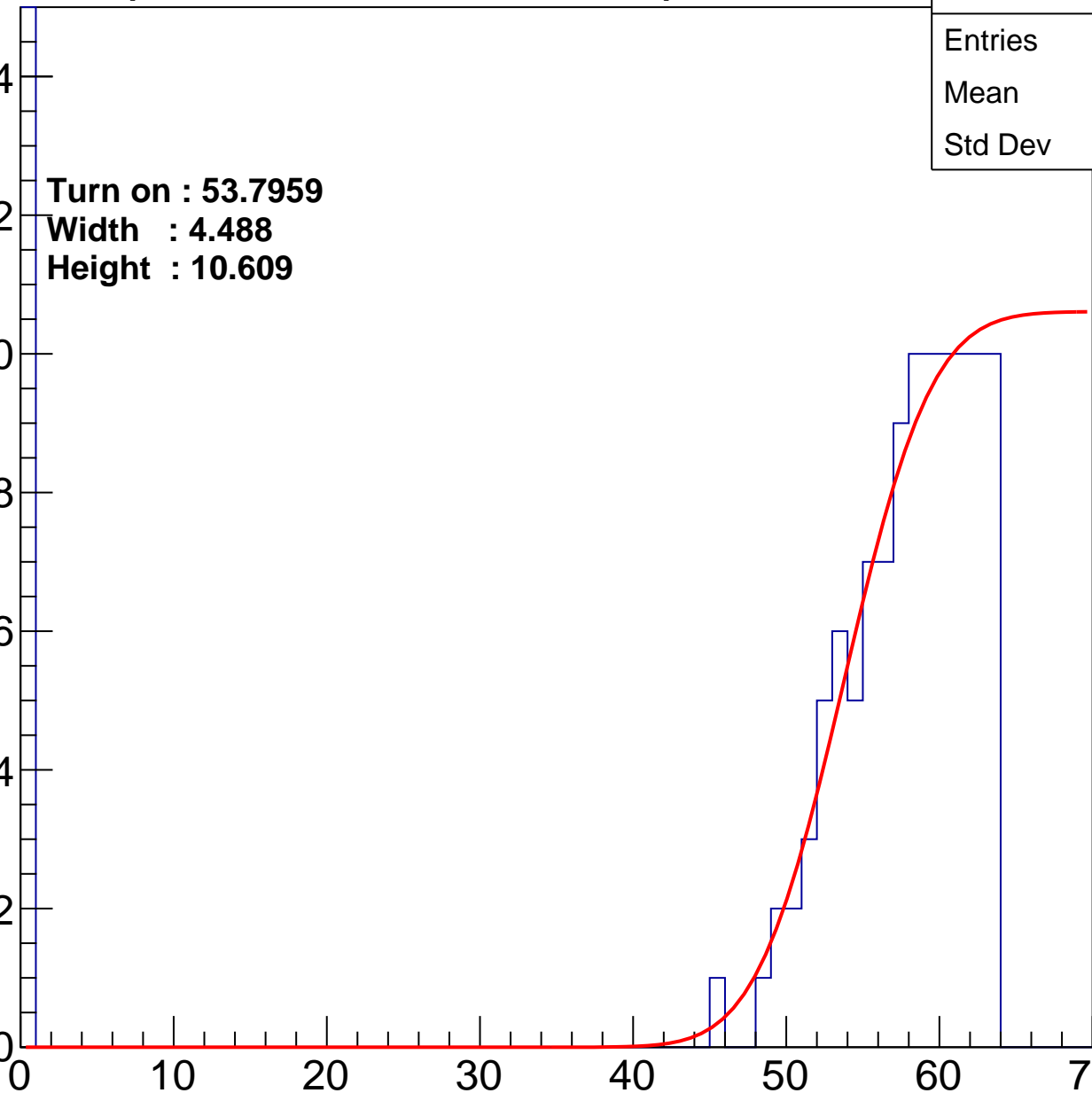
Width : 4.488

Height : 10.609

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch33

calib_packv5_033123_0516.root, FC#4, port A1

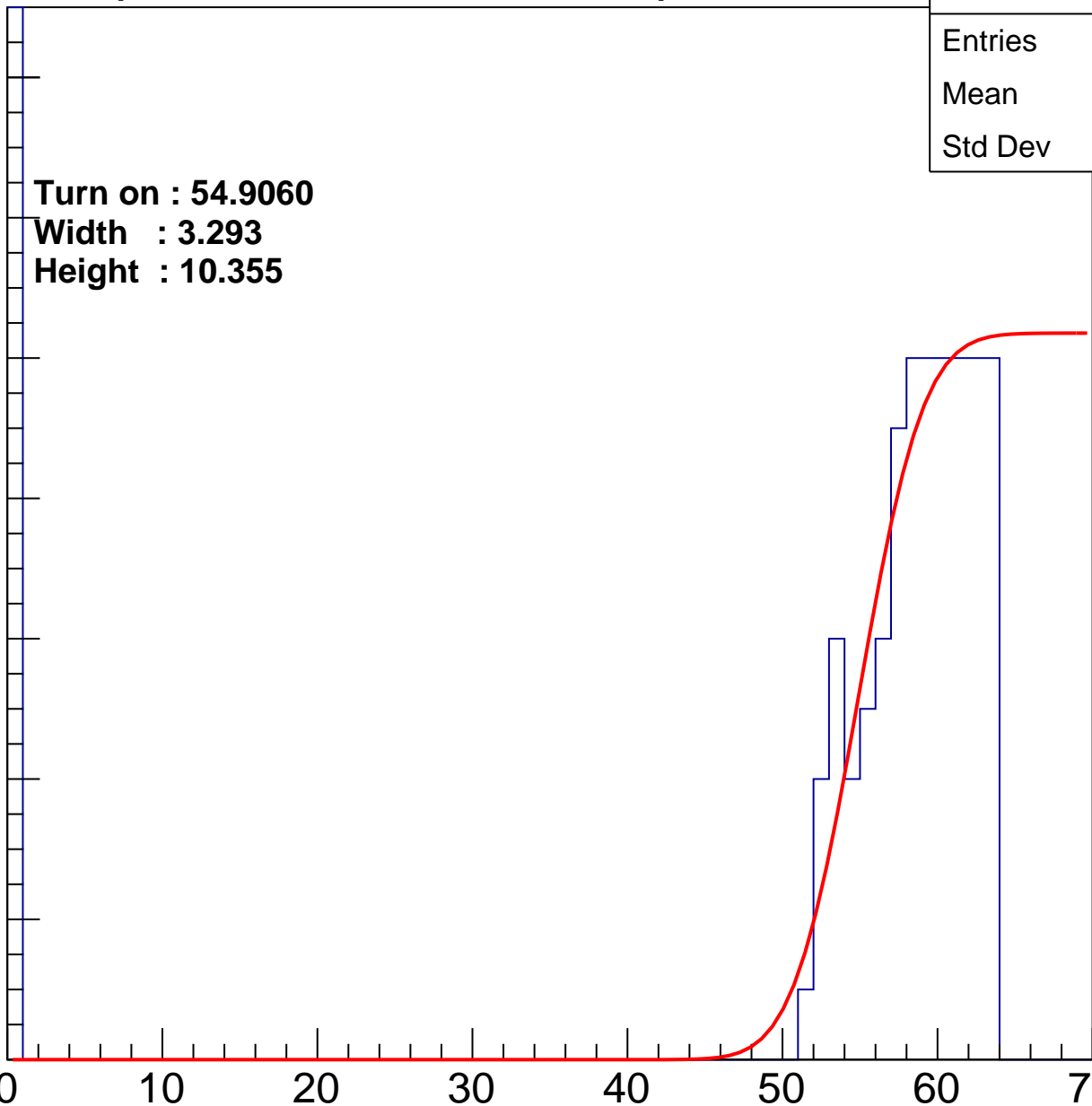
Entry

14
12
10
8
6
4
2
0

Turn on : 54.9060
Width : 3.293
Height : 10.355

Entries	180
Mean	30.82
Std Dev	29.25

ampl



B1L104S, U22-ch34

calib_packv5_033123_0516.root, FC#4, port A1

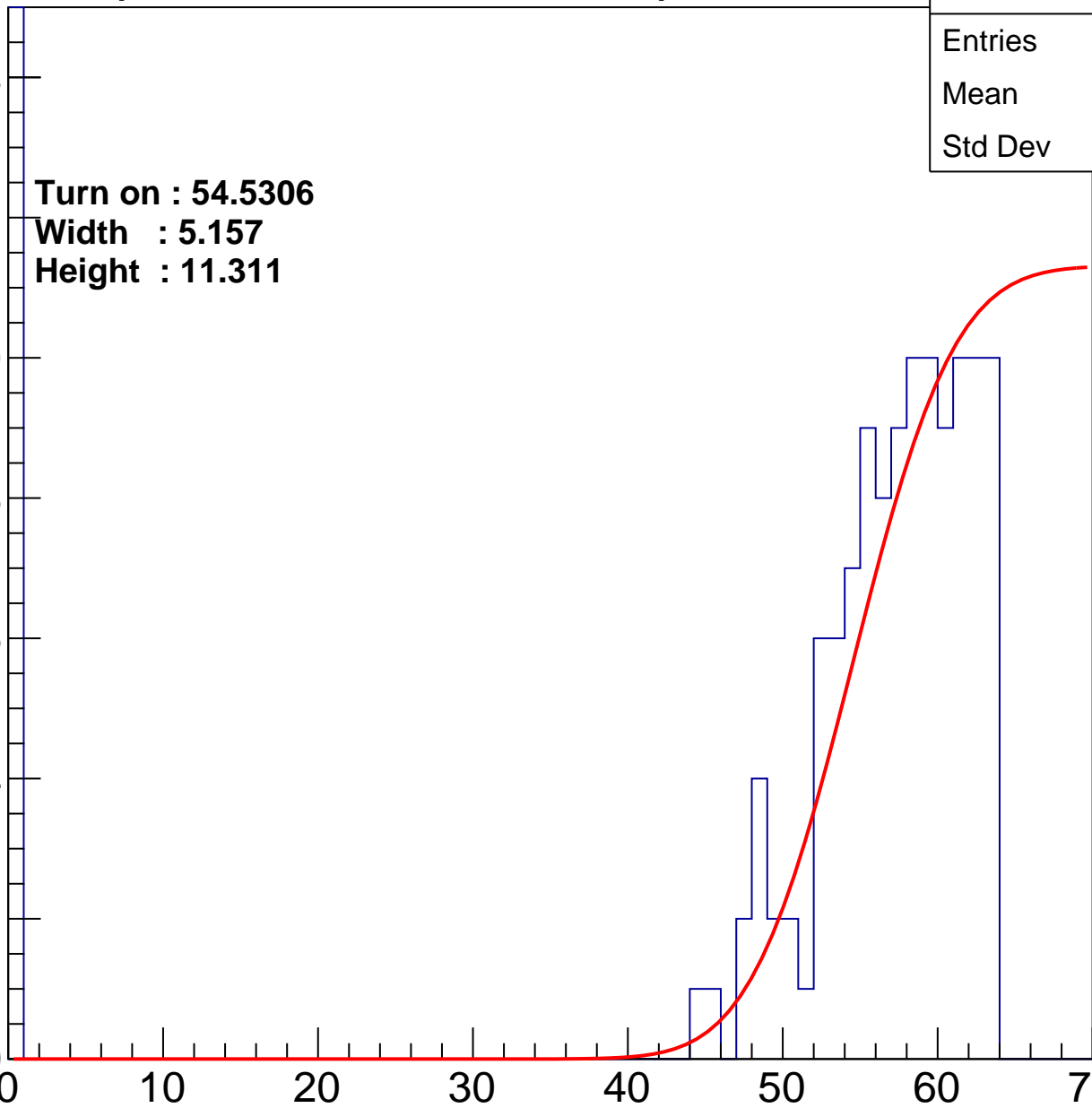
Entry

14
12
10
8
6
4
2
0

Turn on : 54.5306
Width : 5.157
Height : 11.311

Entries	164
Mean	40.6
Std Dev	26.01

ampl



B1L104S, U22-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	28.59
Std Dev	29.3

Turn on : 54.5681

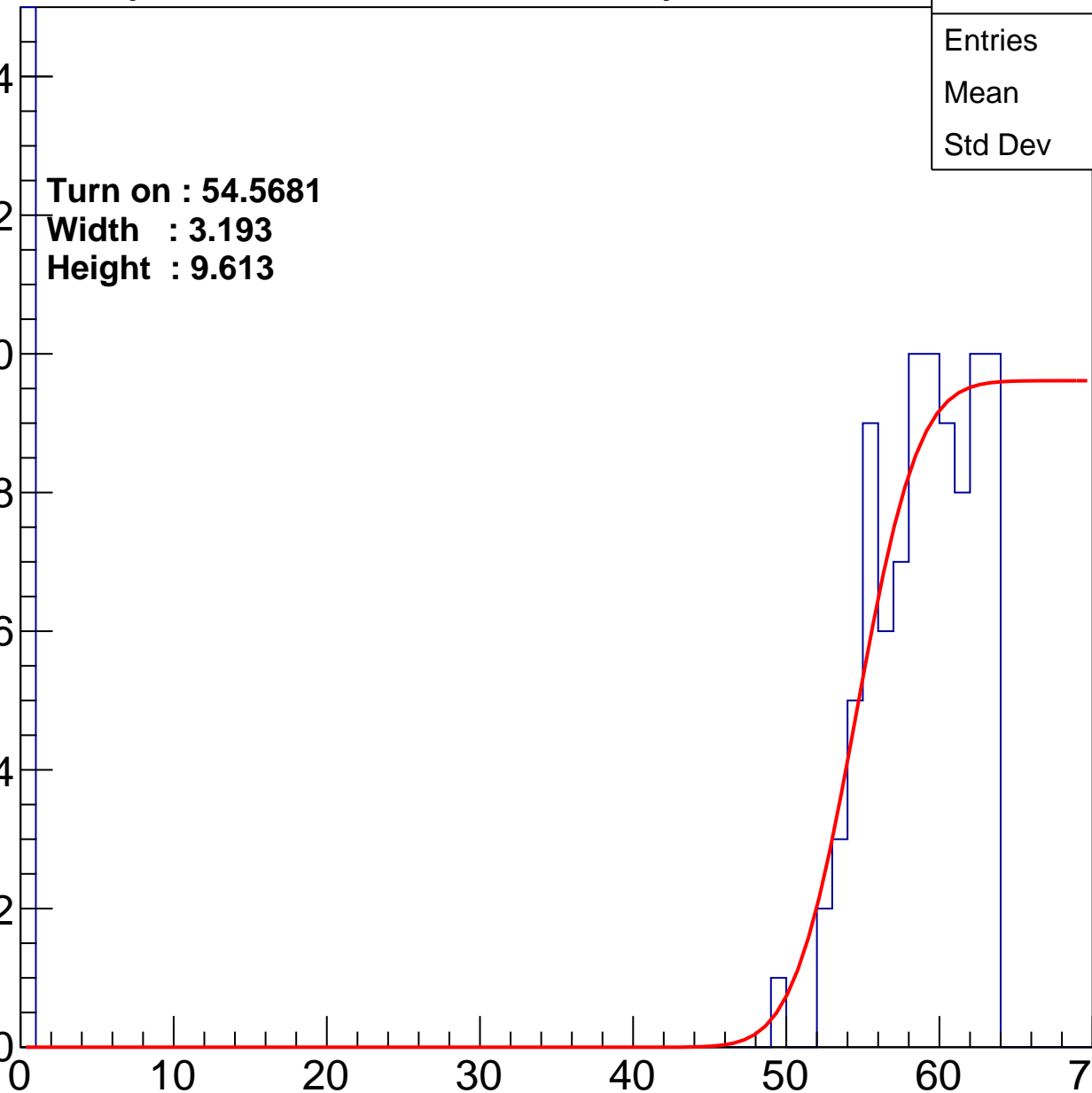
Width : 3.193

Height : 9.613

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	30.04
Std Dev	29.1

Turn on : 53.8129

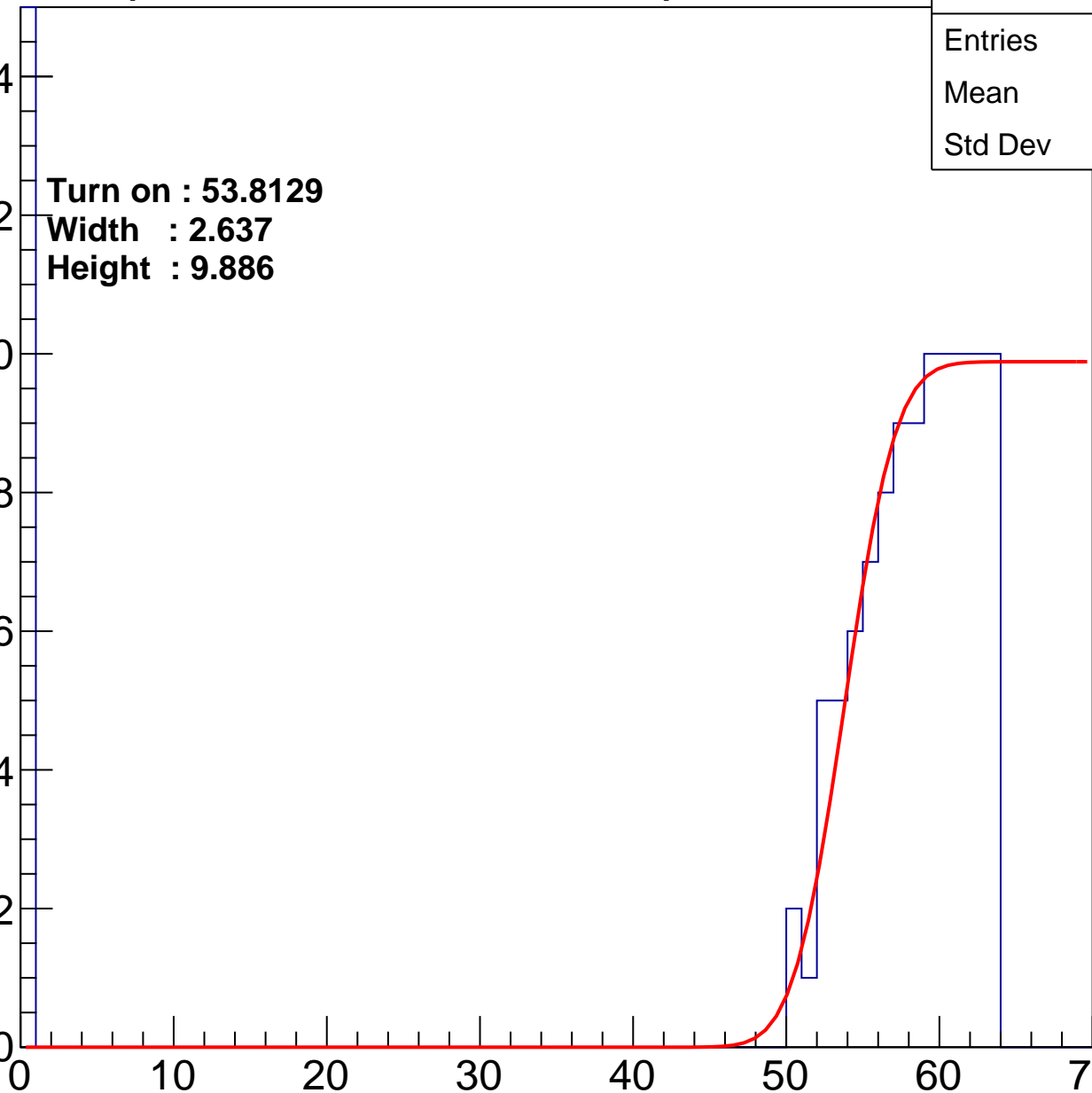
Width : 2.637

Height : 9.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch37

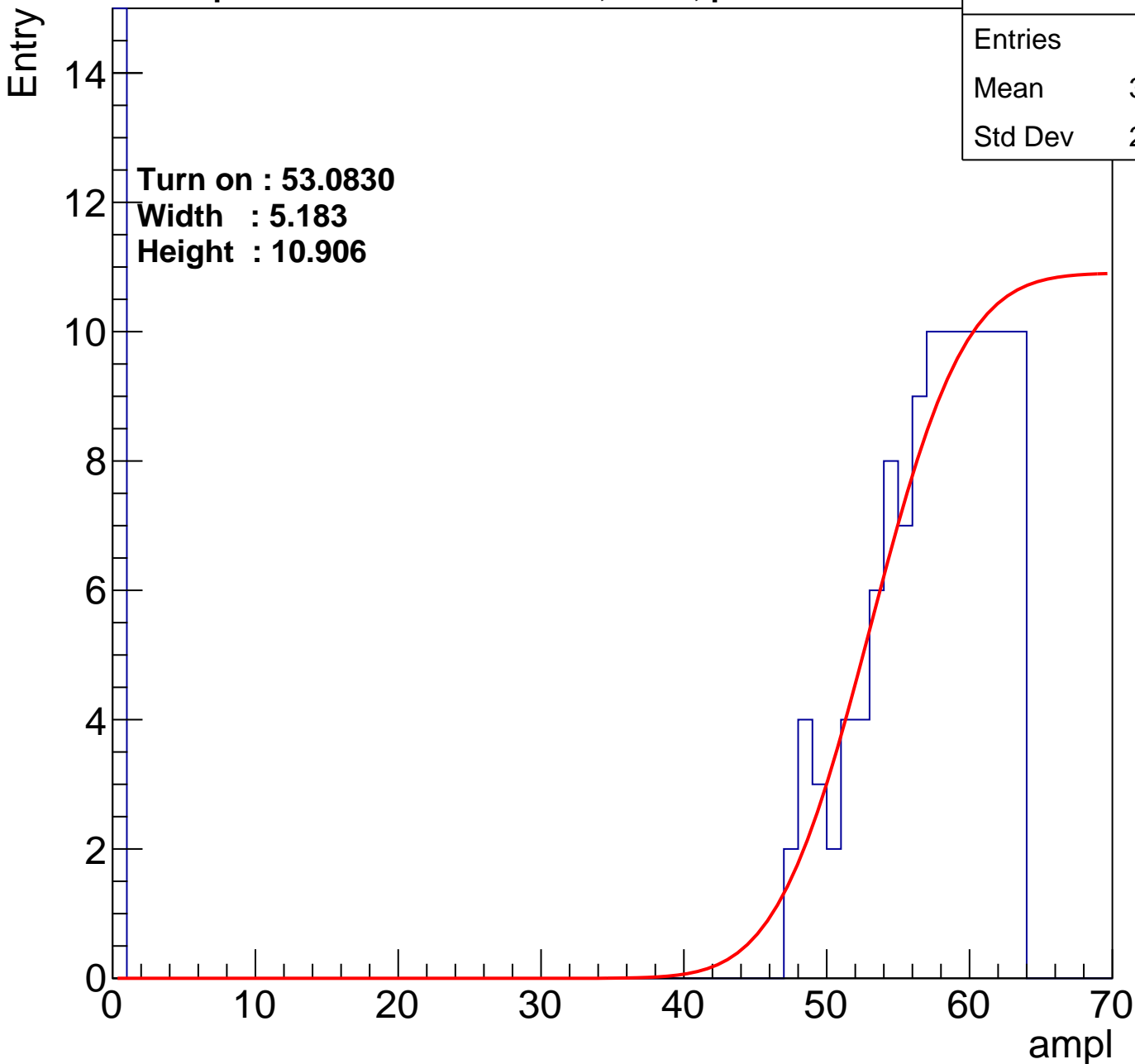
calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	31.55
Std Dev	28.52

Turn on : 53.0830

Width : 5.183

Height : 10.906



B1L104S, U22-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	33.28
Std Dev	28.47

Turn on : 54.2408

Width : 5.741

Height : 11.111

Entry

14

12

10

8

6

4

2

0

0

10

20

30

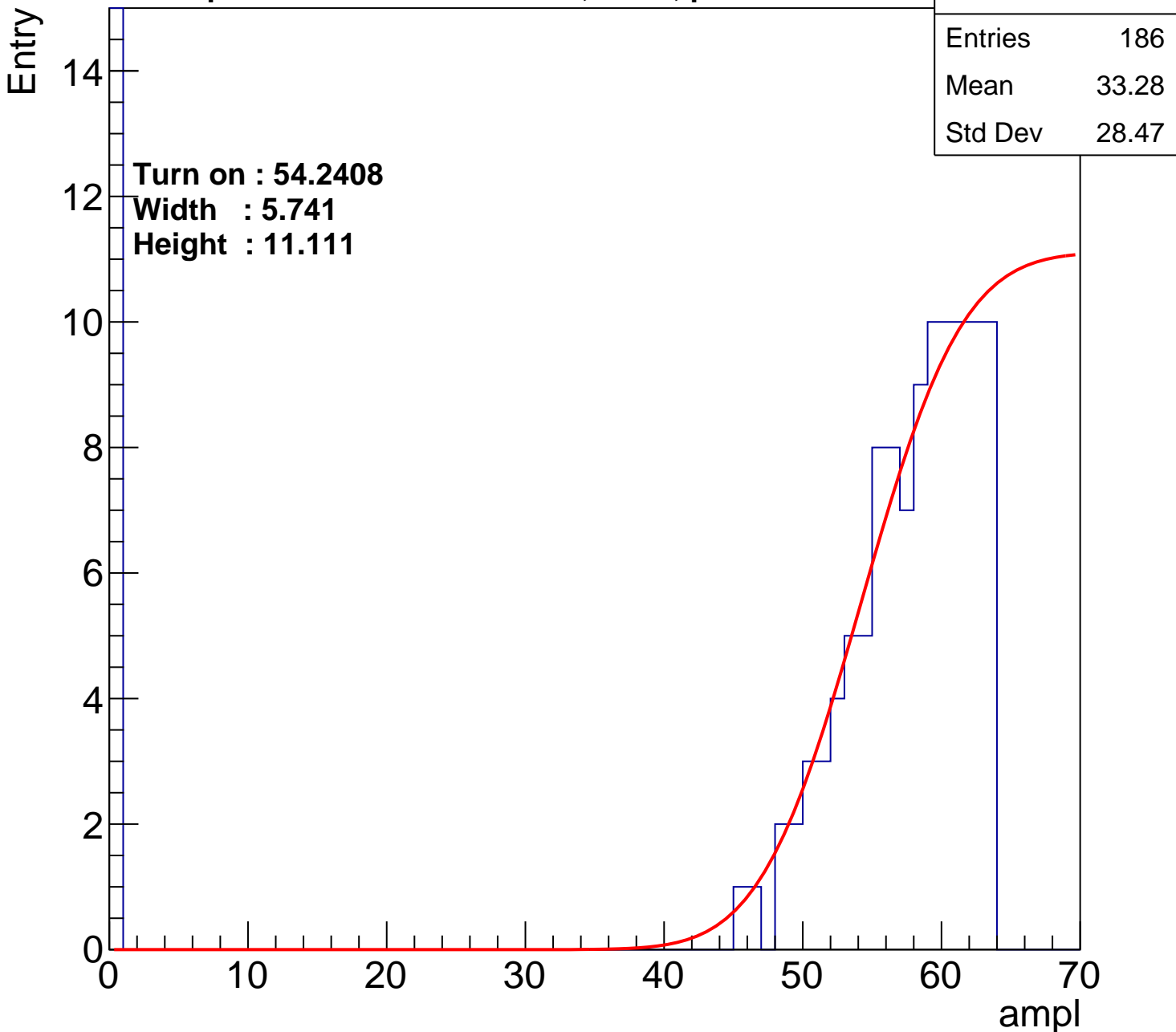
40

50

60

70

ampl



B1L104S, U22-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	36.68
Std Dev	28.2

Turn on : 54.1802

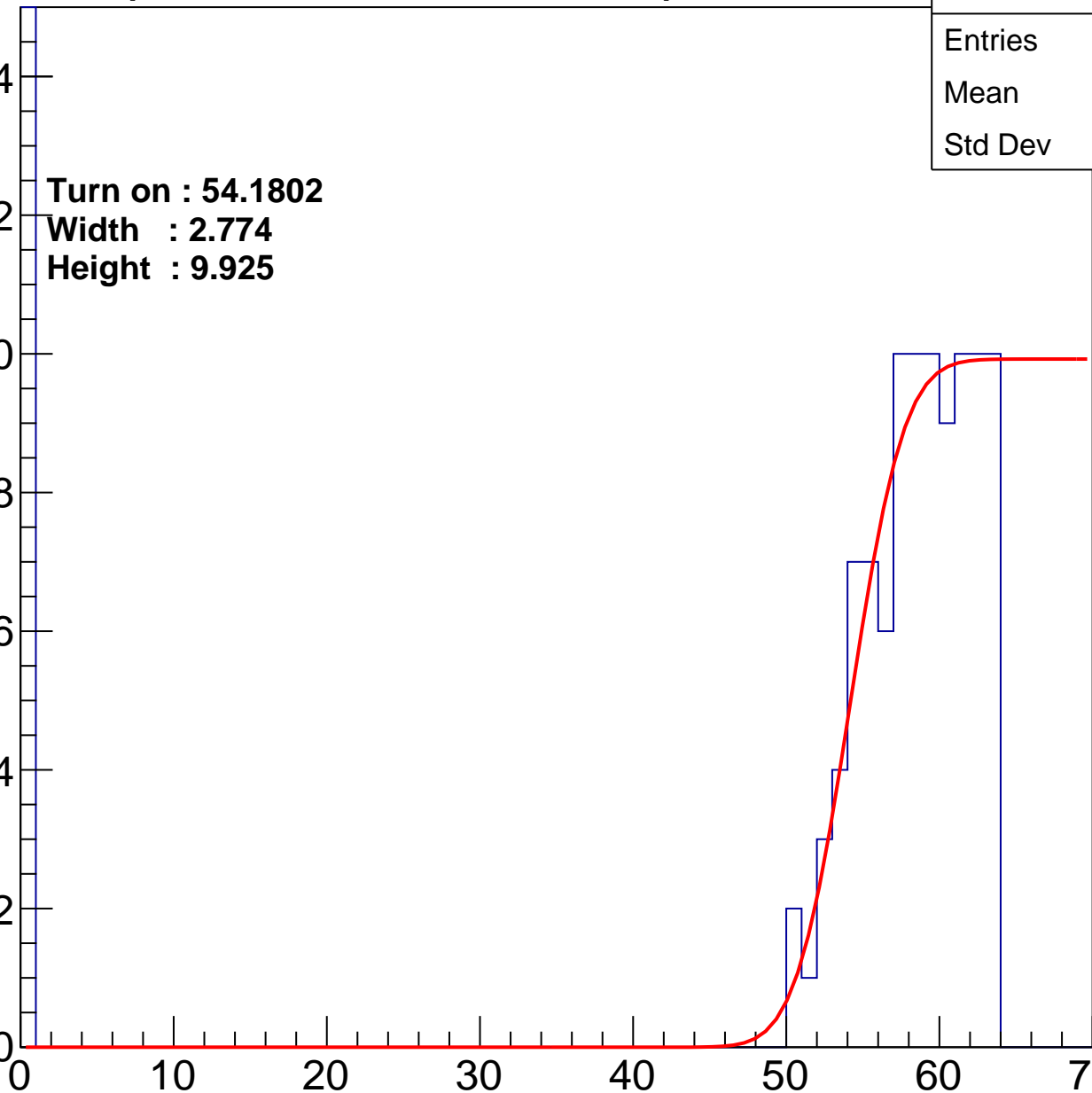
Width : 2.774

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	36.47
Std Dev	27.54

Turn on : 52.4061

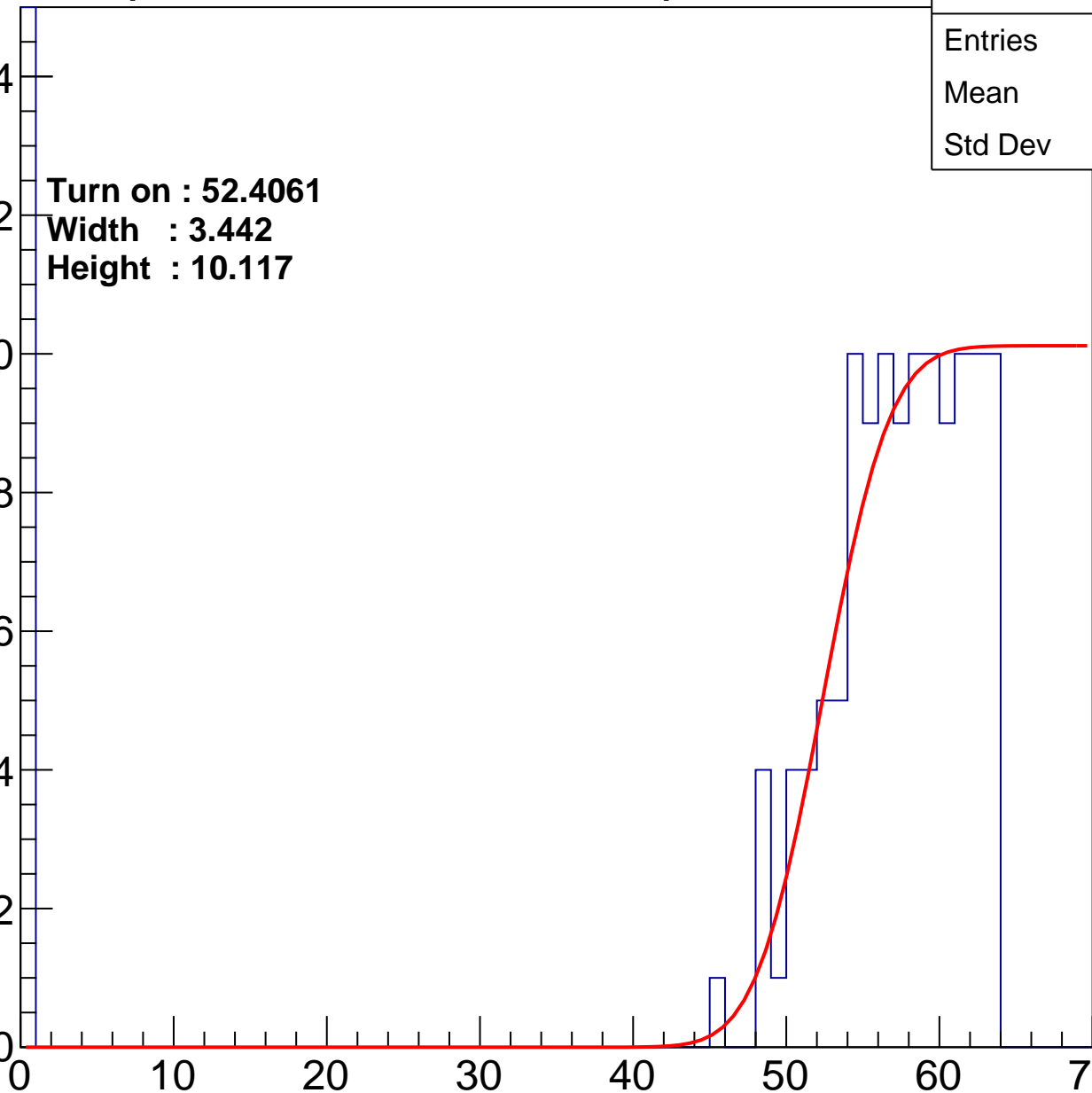
Width : 3.442

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	34.49
Std Dev	28.73

Turn on : 55.1779

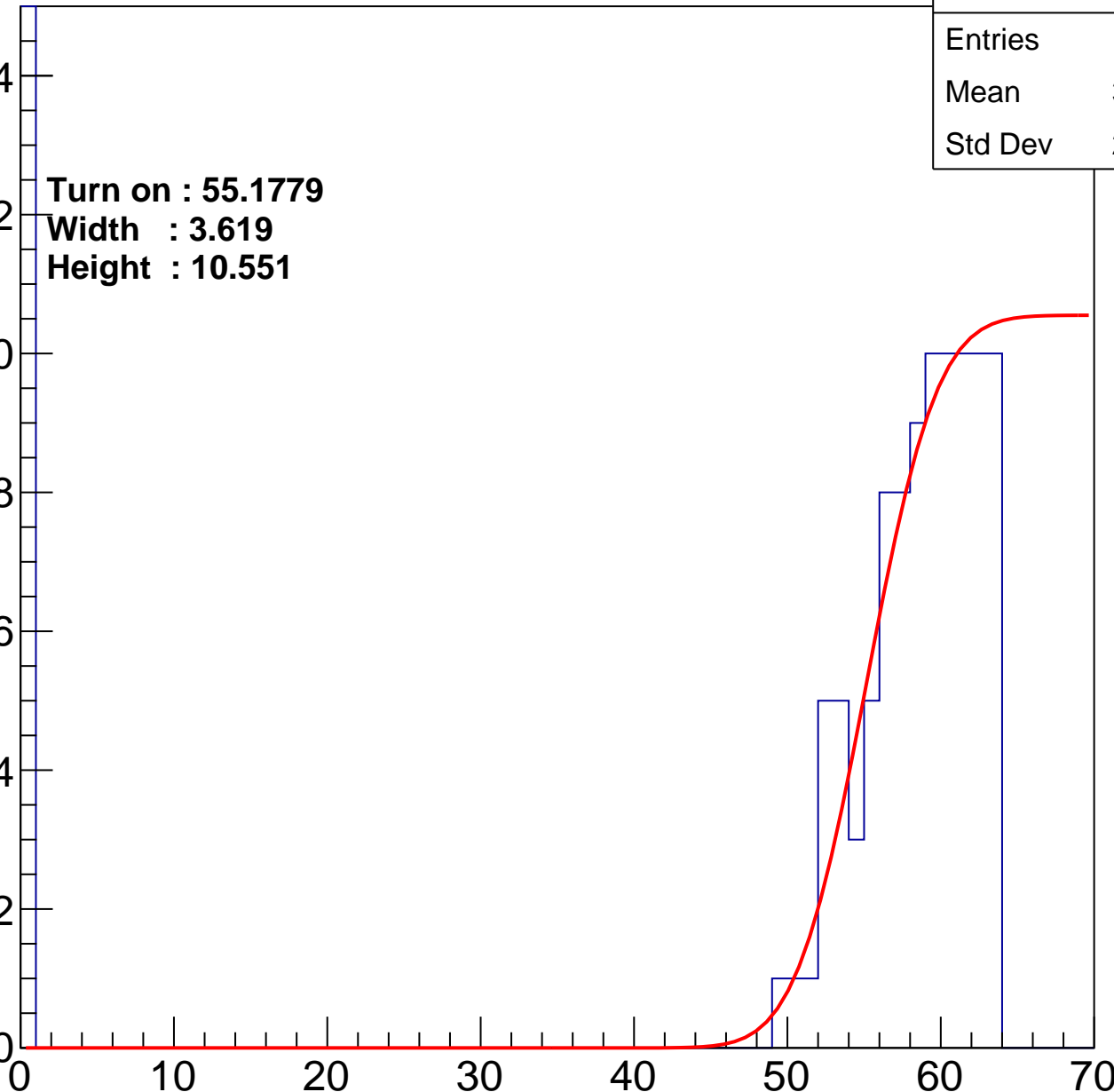
Width : 3.619

Height : 10.551

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	28.25
Std Dev	29.02

Turn on : 53.7381

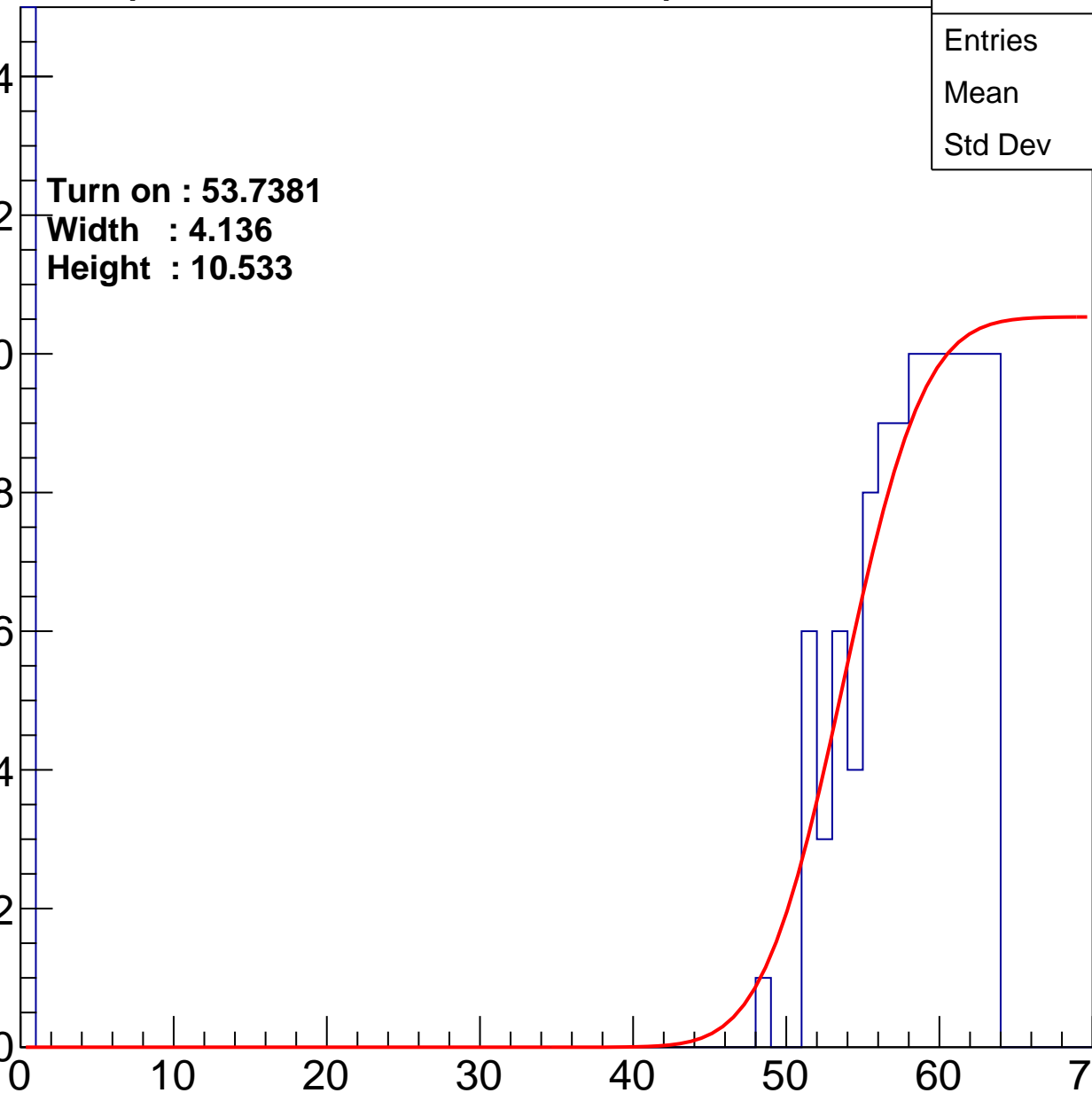
Width : 4.136

Height : 10.533

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch43

calib_packv5_033123_0516.root, FC#4, port A1

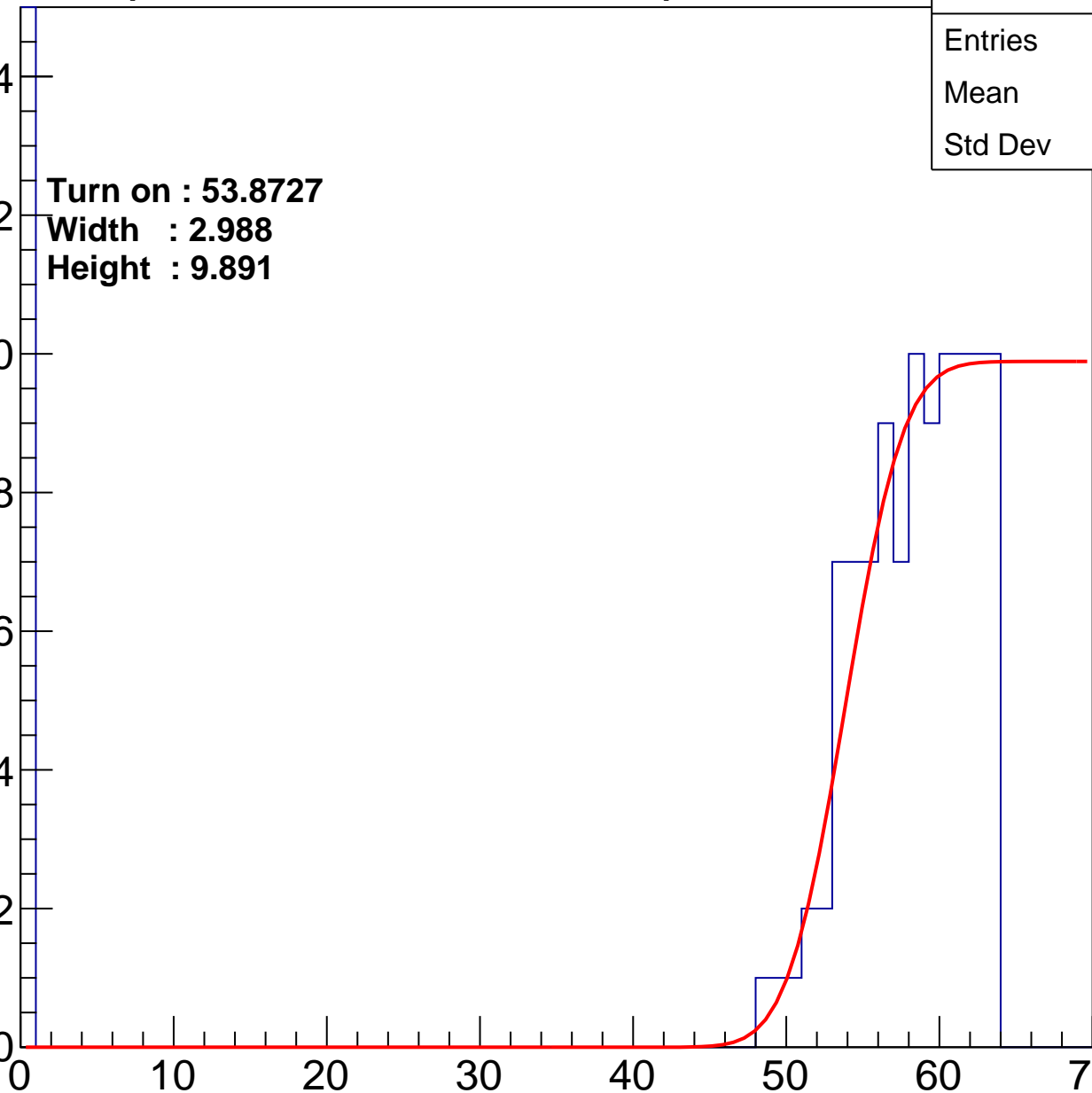
Entry

14
12
10
8
6
4
2
0

Turn on : 53.8727
Width : 2.988
Height : 9.891

Entries	173
Mean	34.46
Std Dev	28.54

ampl



B1L104S, U22-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	38.24
Std Dev	27.33

Turn on : 52.8643

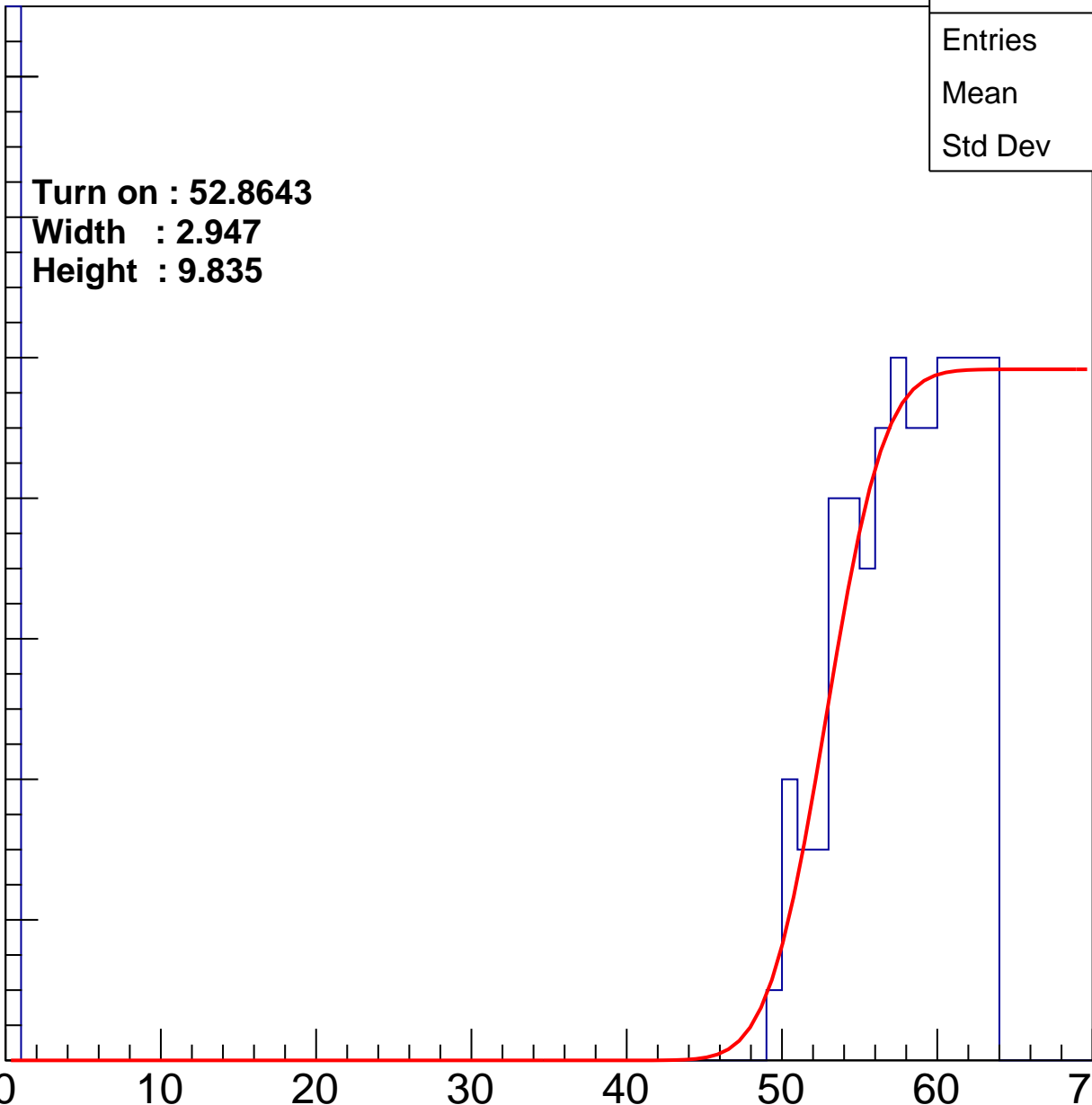
Width : 2.947

Height : 9.835

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	34.37
Std Dev	28.58

Turn on : 53.4305

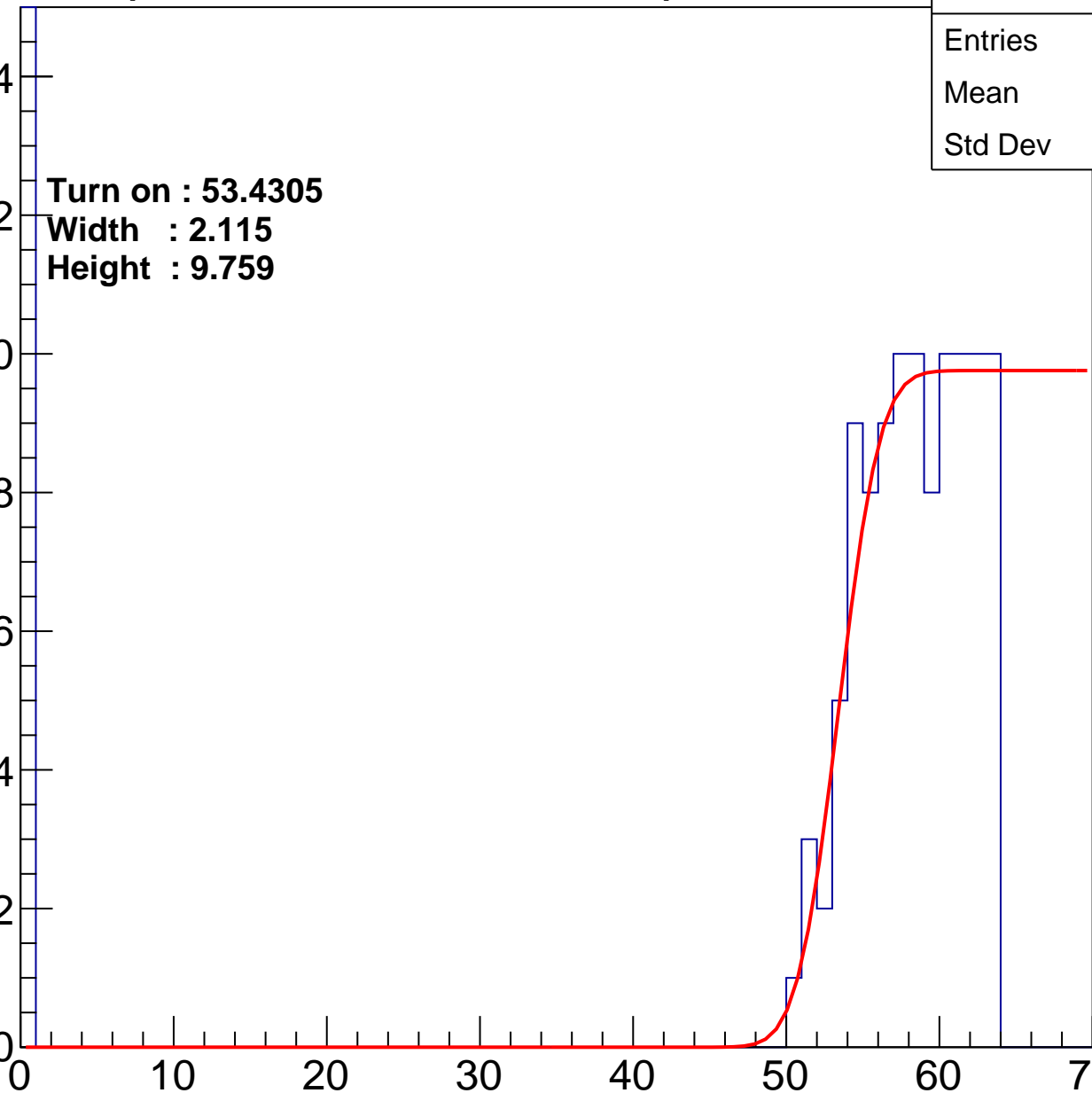
Width : 2.115

Height : 9.759

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	36.27
Std Dev	27.69

Turn on : 53.2250

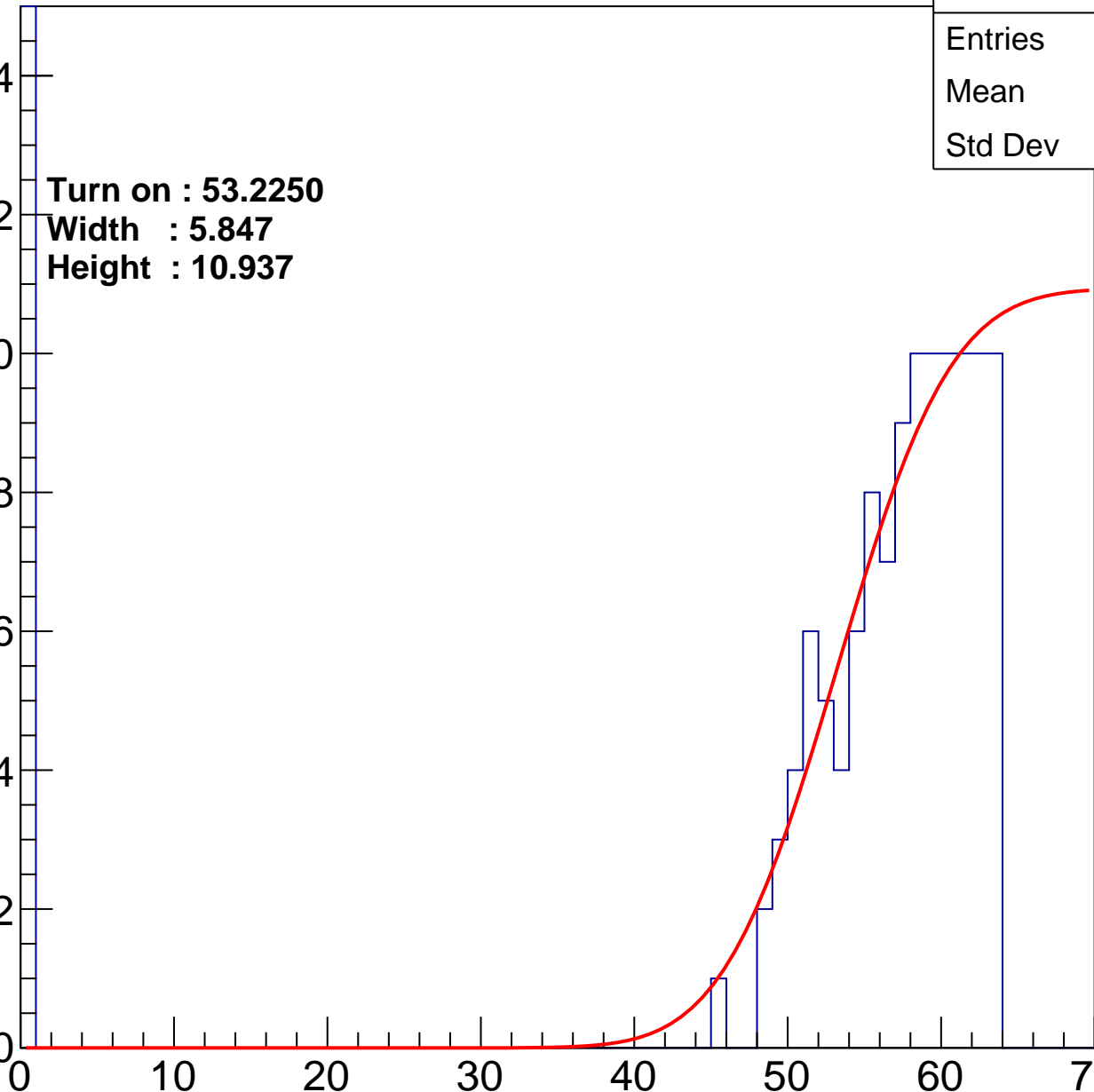
Width : 5.847

Height : 10.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	34.67
Std Dev	28.52

Turn on : 53.9002

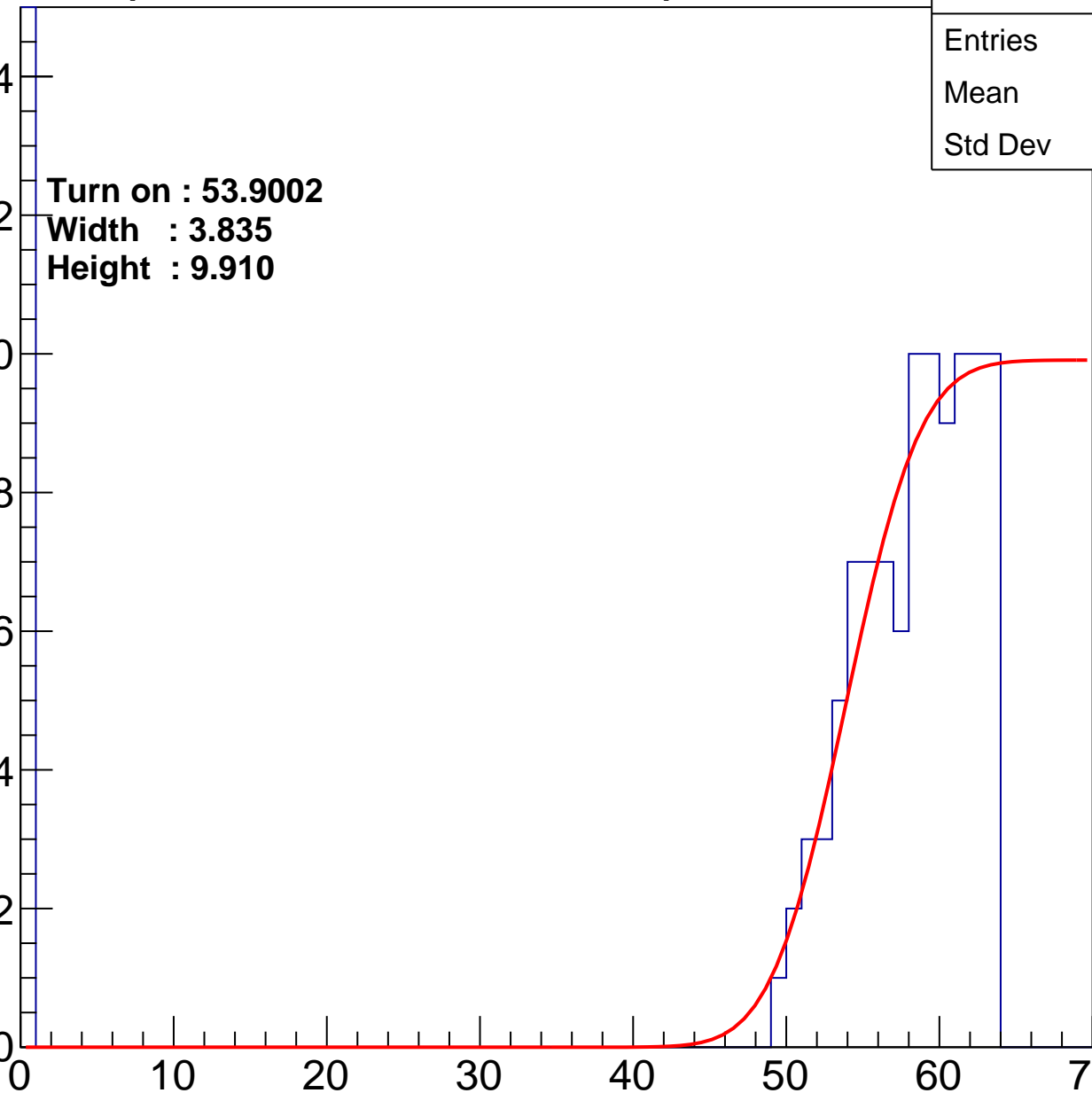
Width : 3.835

Height : 9.910

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	34.59
Std Dev	28.69

Turn on : 55.9286

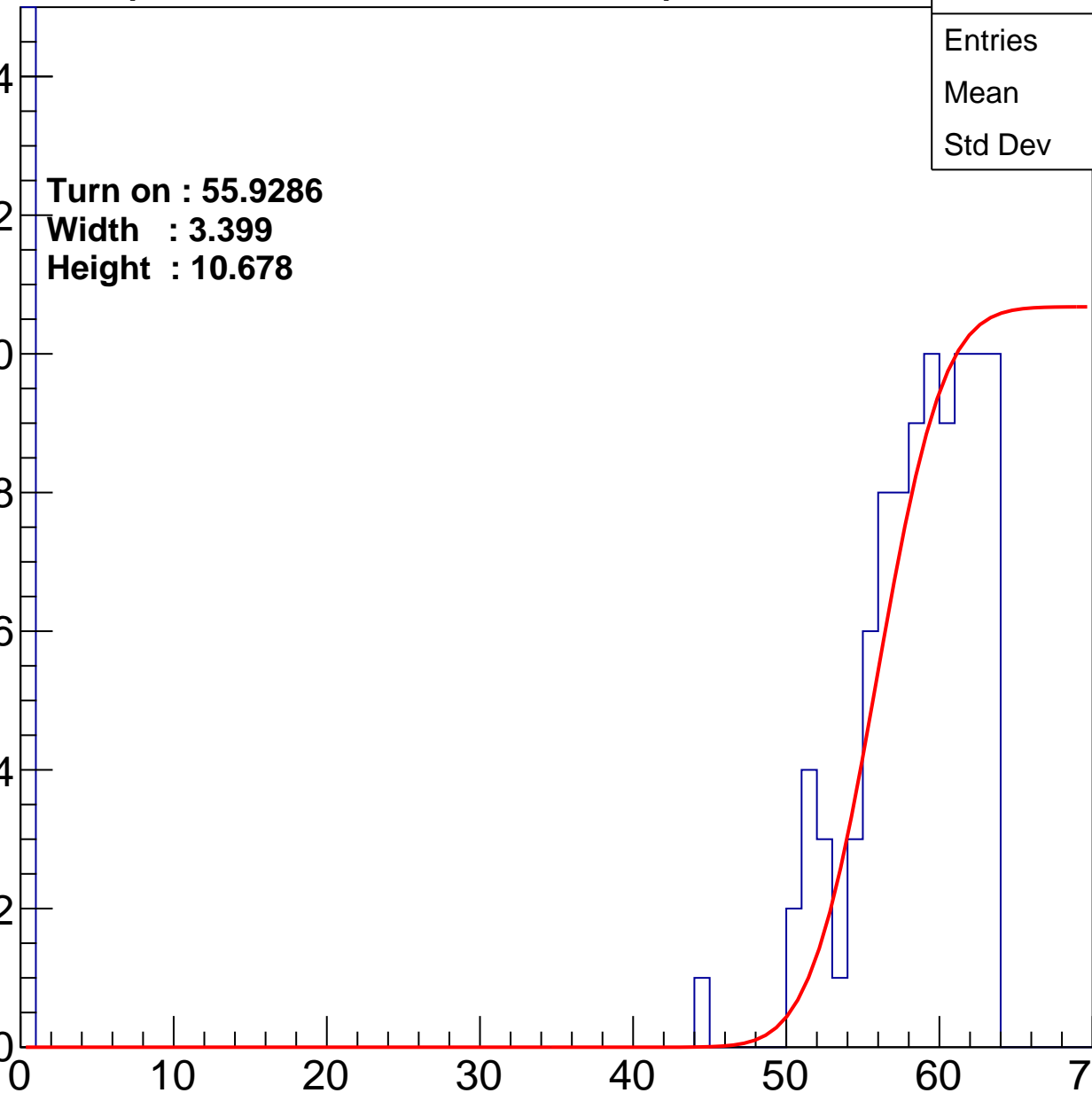
Width : 3.399

Height : 10.678

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	153
Mean	37.31
Std Dev	28.08

Turn on : 54.9651

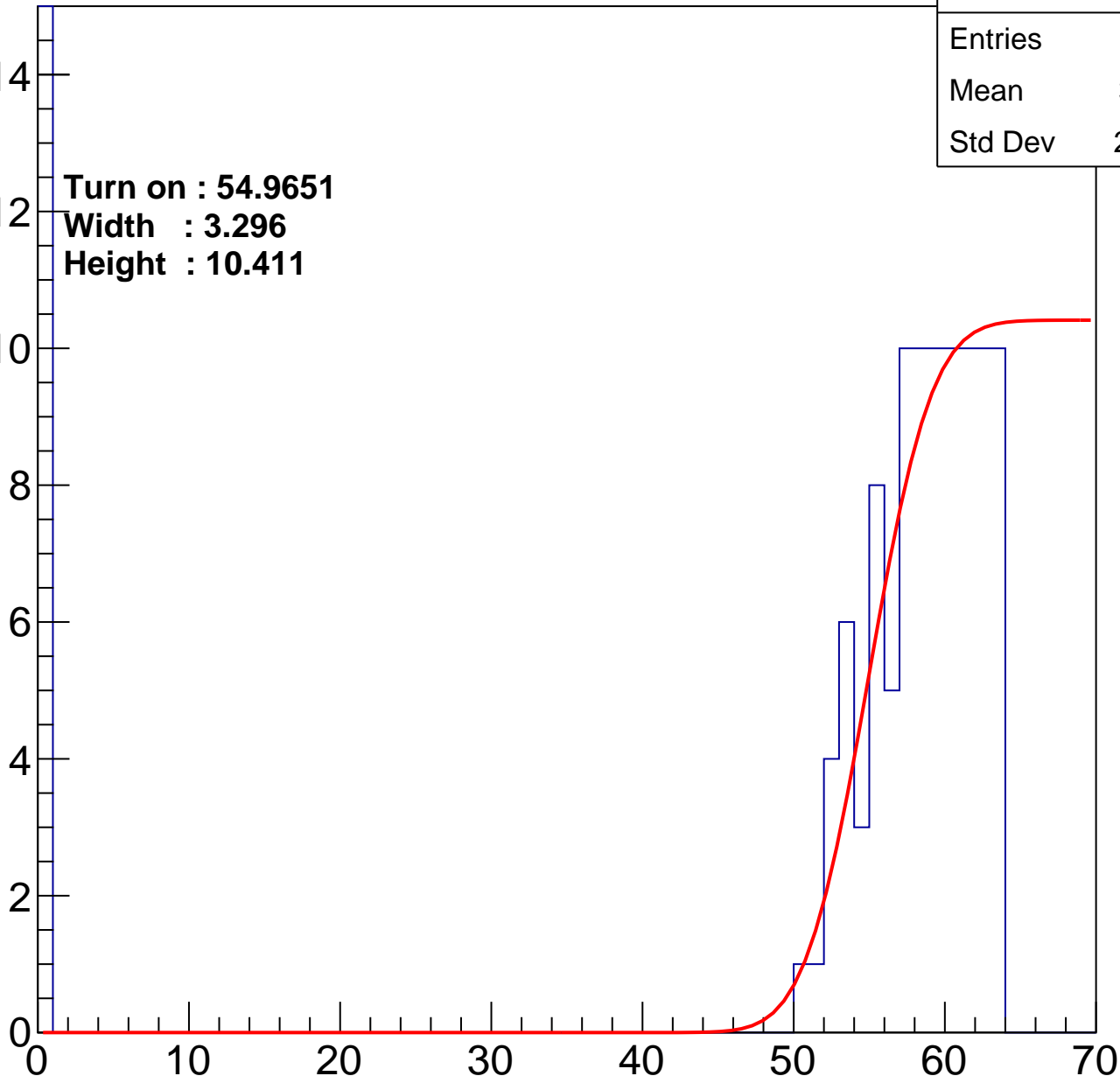
Width : 3.296

Height : 10.411

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	223
Mean	28.44
Std Dev	28.94

Turn on : 53.2901

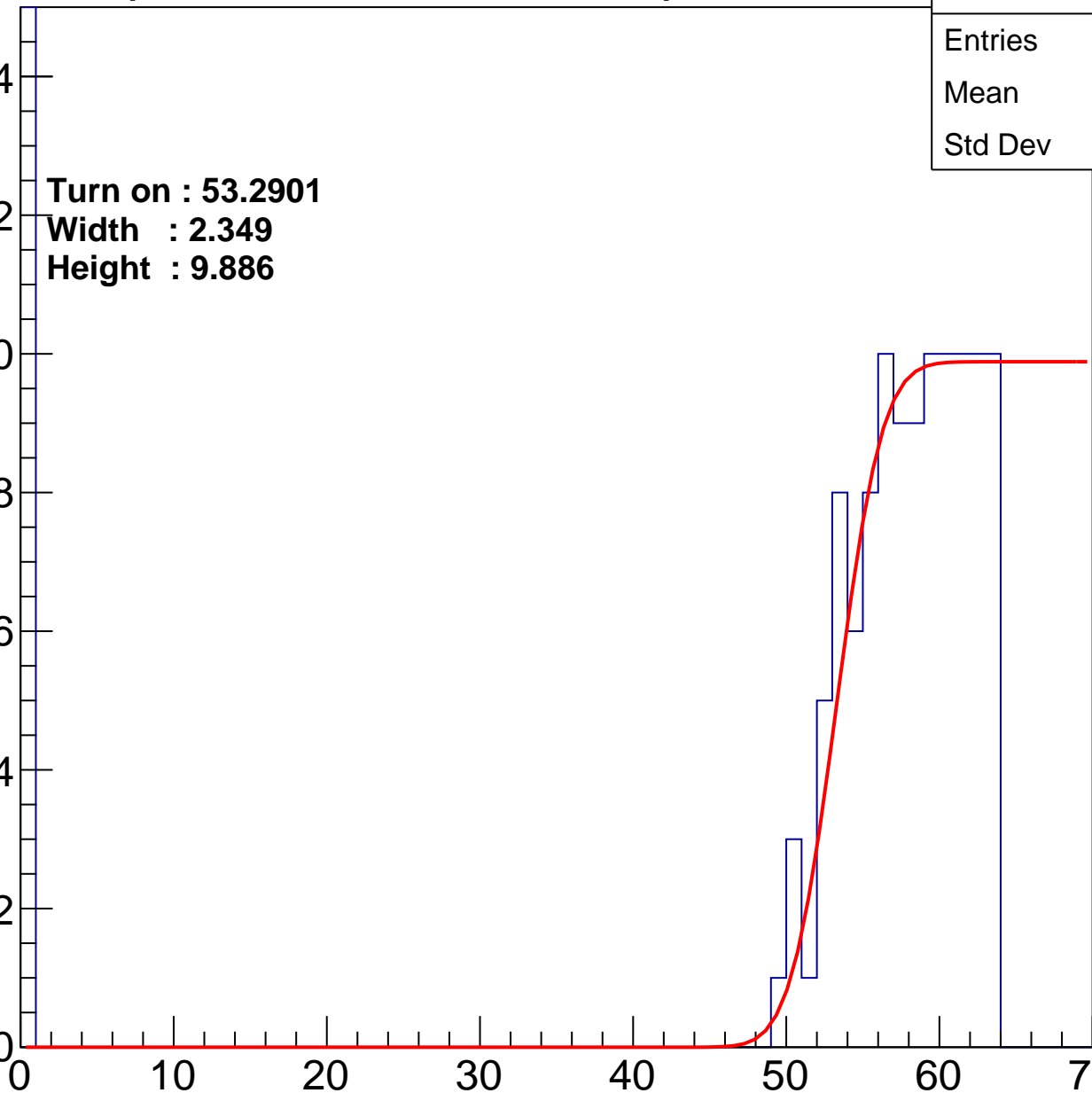
Width : 2.349

Height : 9.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch51

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	32.65
Std Dev	28.8

Turn on : 53.7819

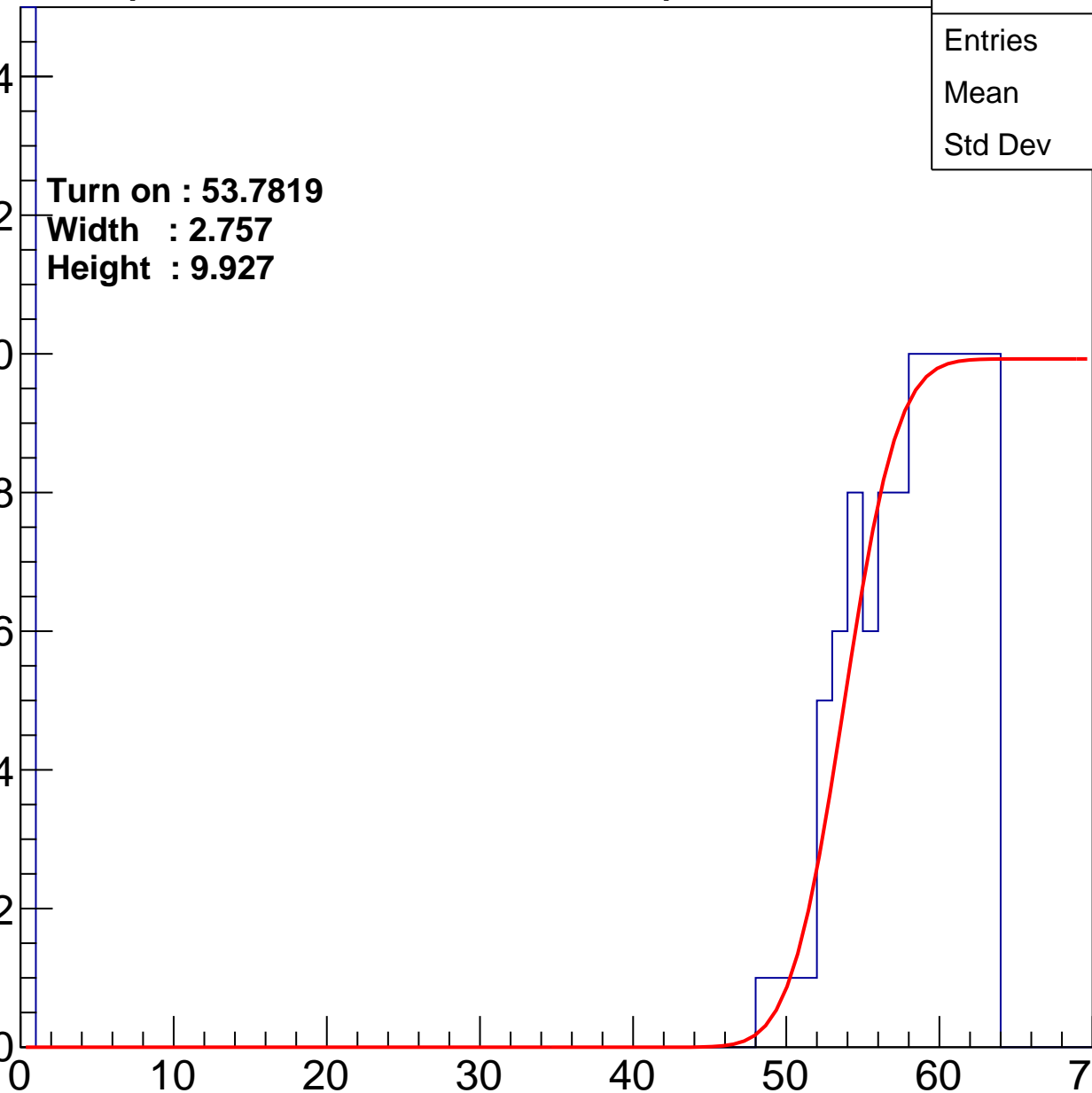
Width : 2.757

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	29.45
Std Dev	29.37

Turn on : 55.3961

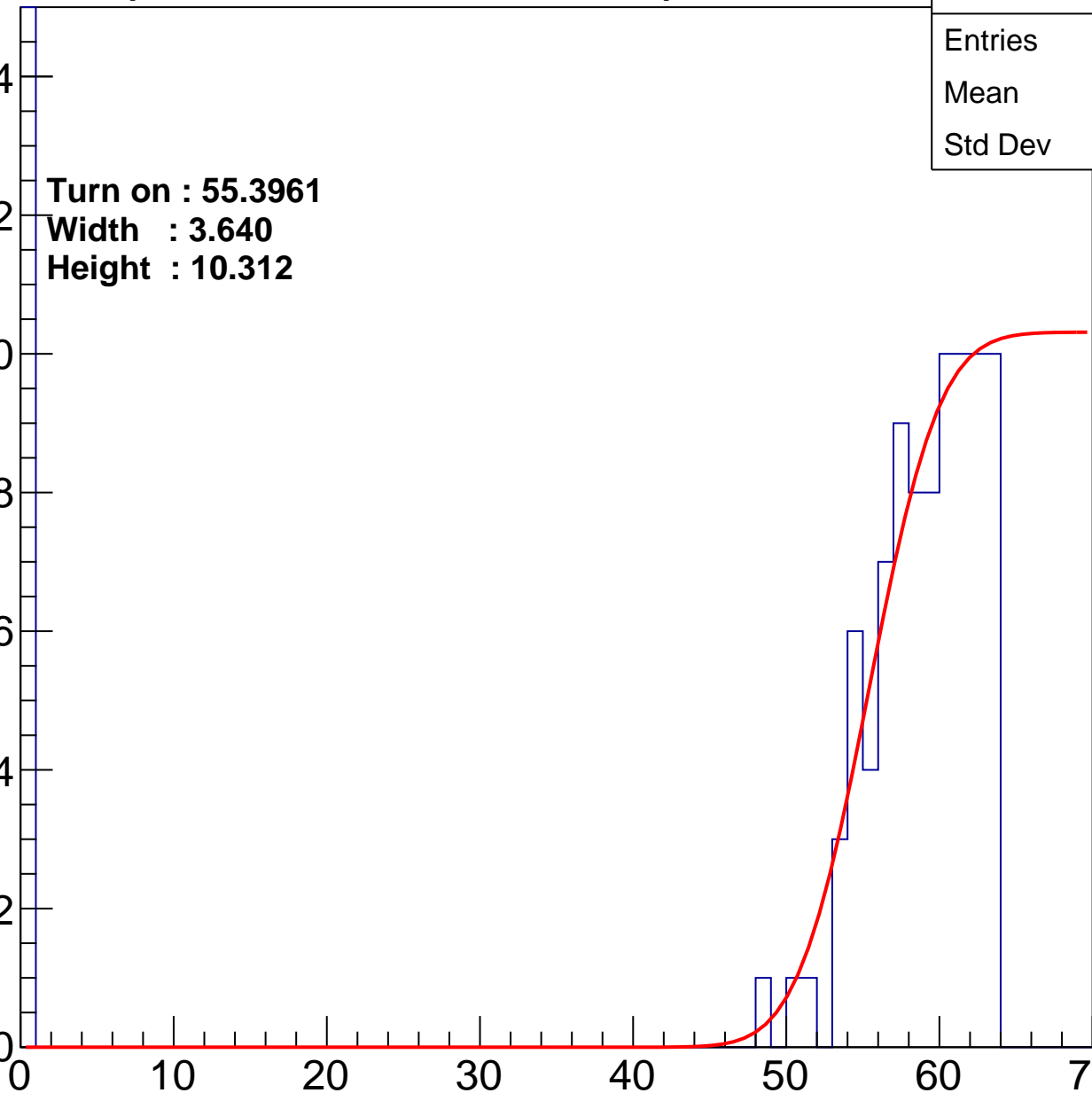
Width : 3.640

Height : 10.312

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	31.86
Std Dev	28.79

Turn on : 53.1071

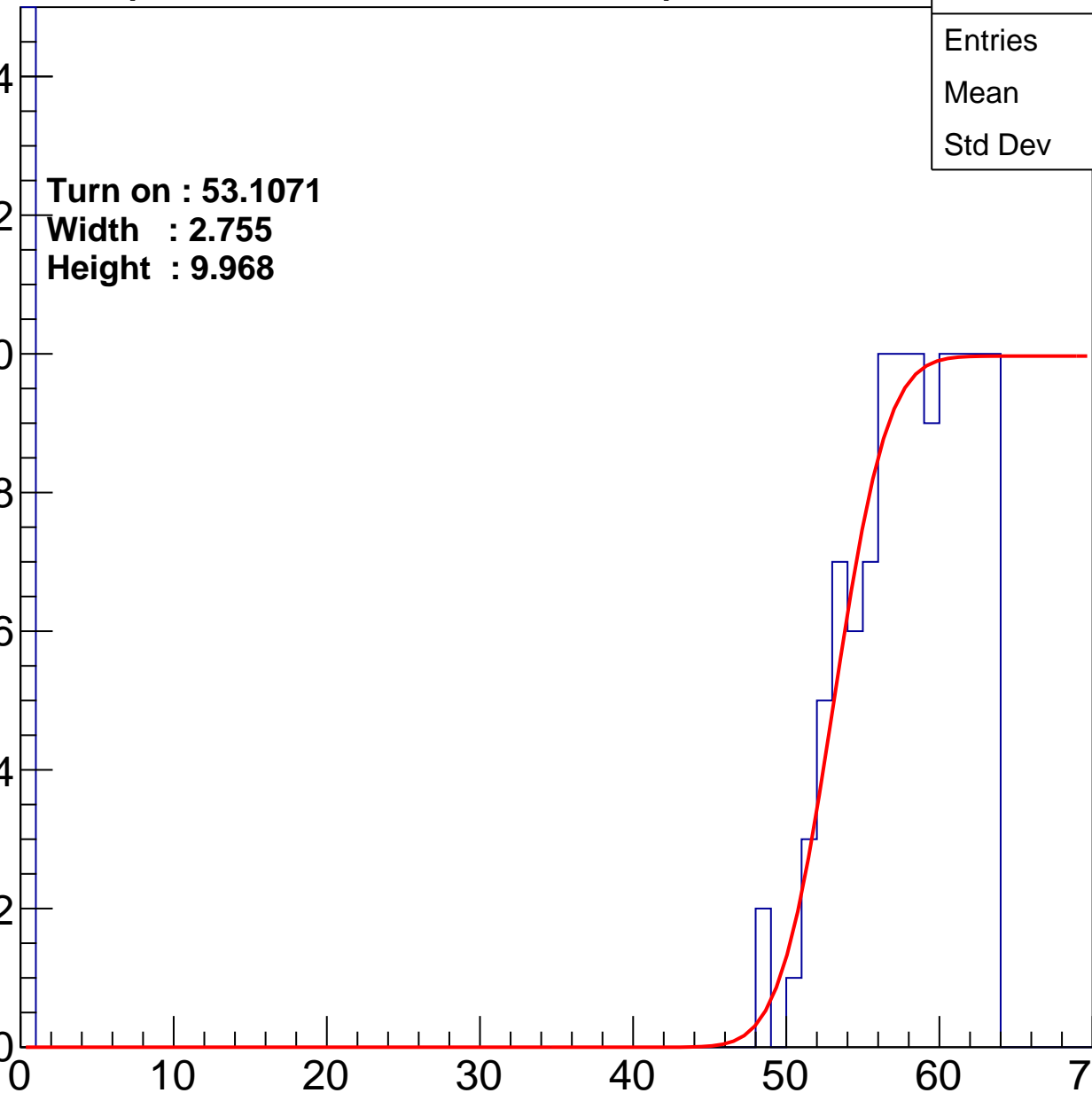
Width : 2.755

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch54

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	32.62
Std Dev	28.64

Turn on : 52.5853

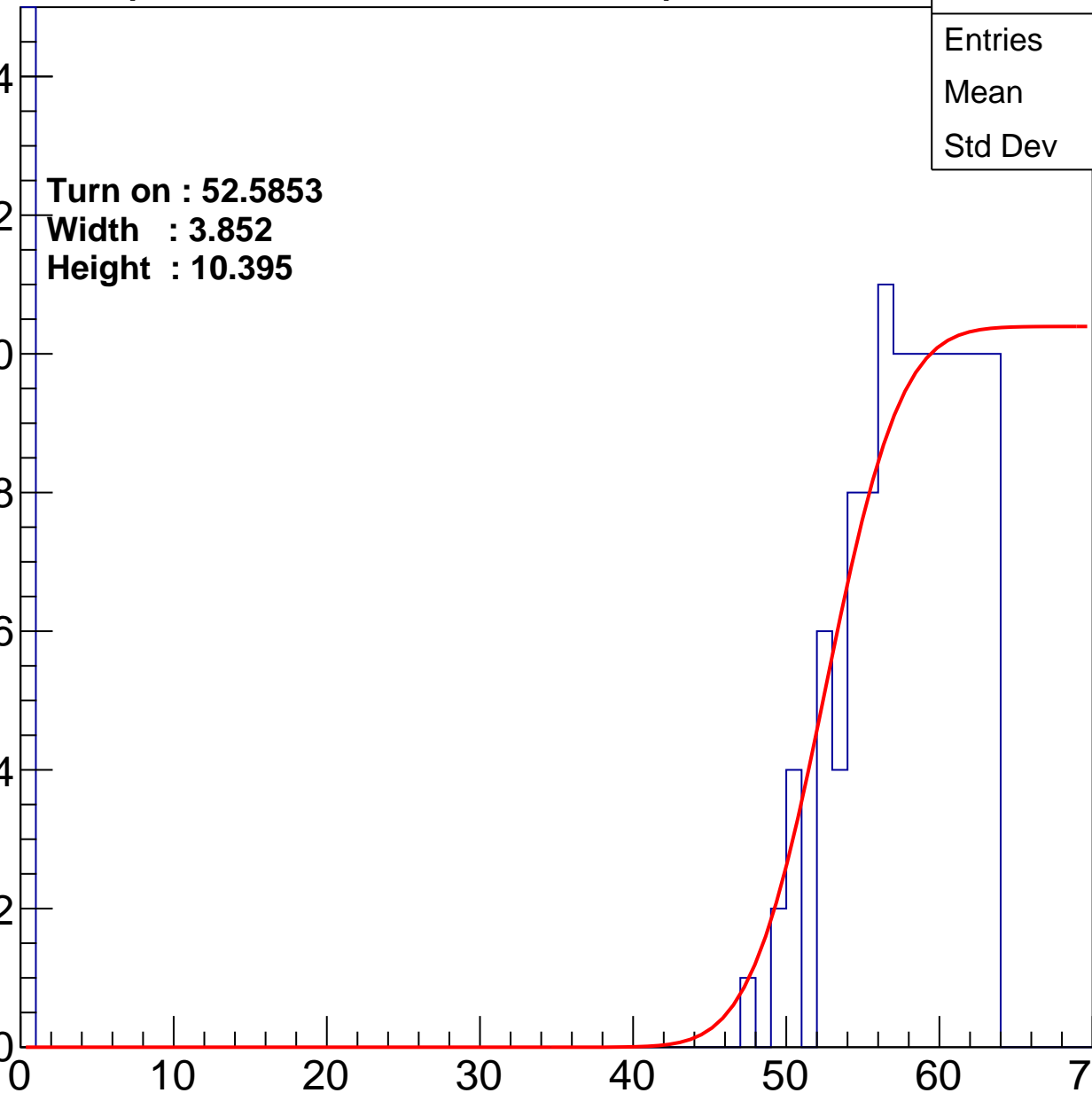
Width : 3.852

Height : 10.395

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	30.79
Std Dev	29.24

Turn on : 54.8247

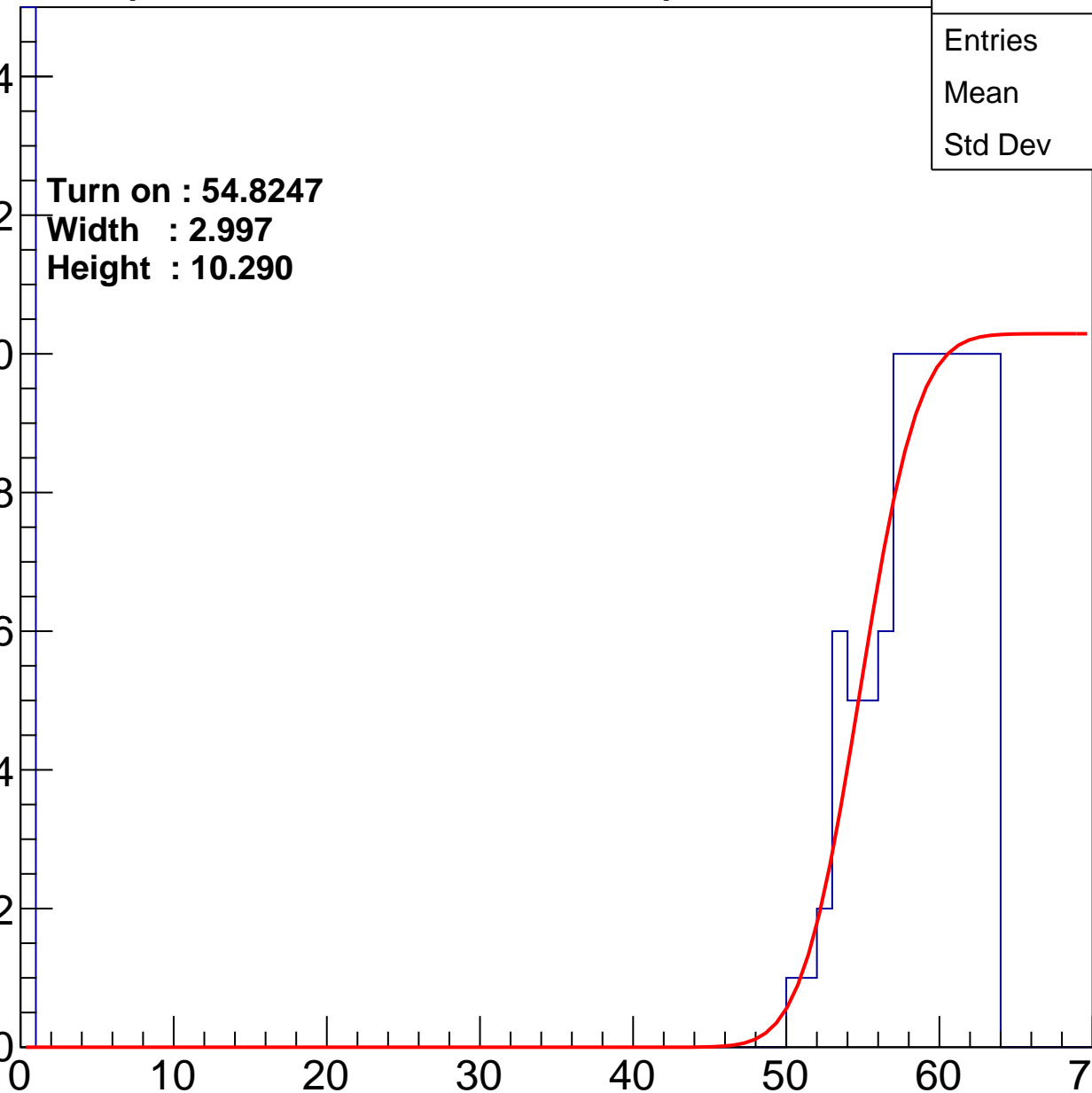
Width : 2.997

Height : 10.290

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	37.98
Std Dev	27.05

Turn on : 51.5028

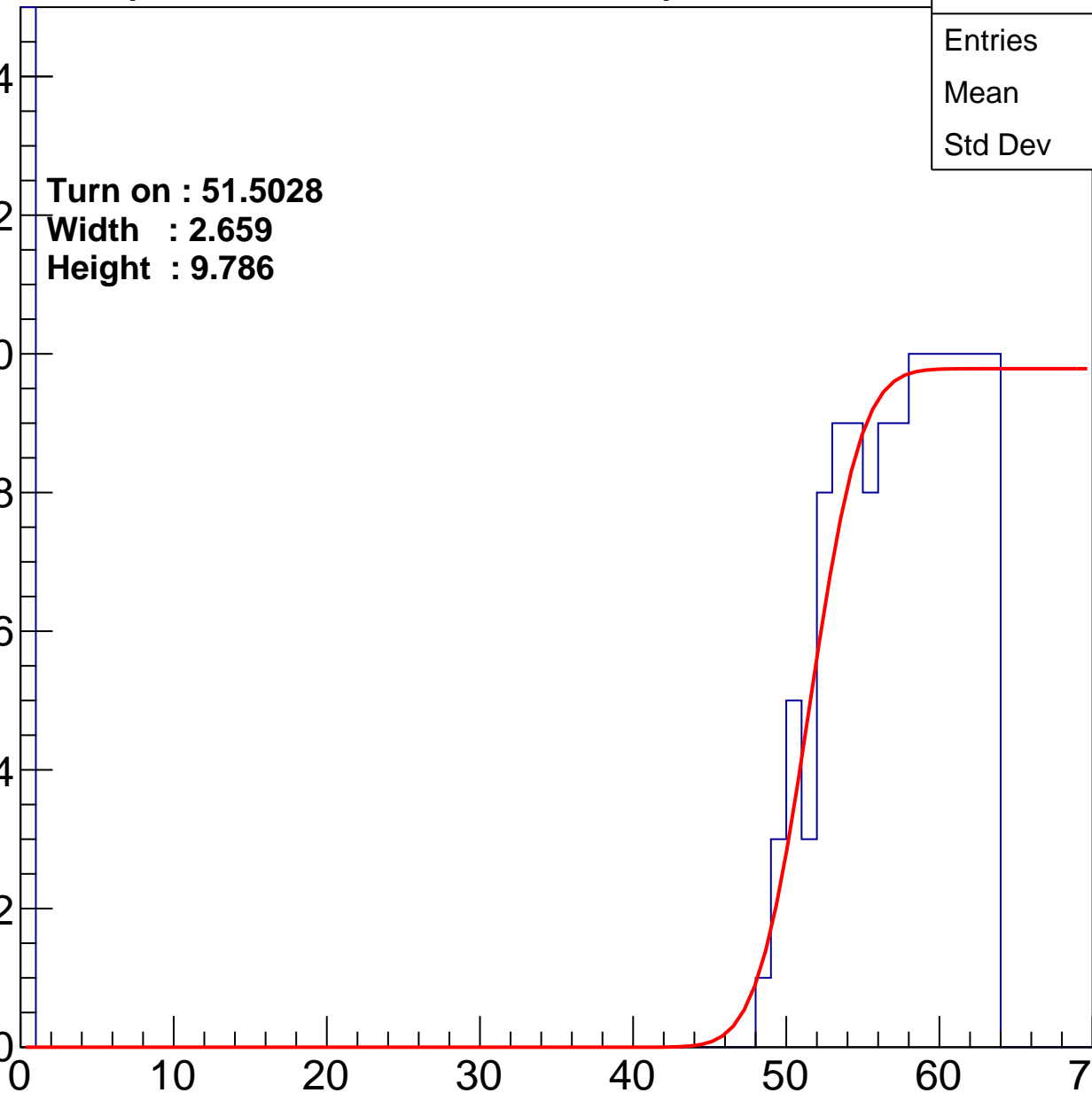
Width : 2.659

Height : 9.786

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch57

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	33.74
Std Dev	28.8

Turn on : 54.0317

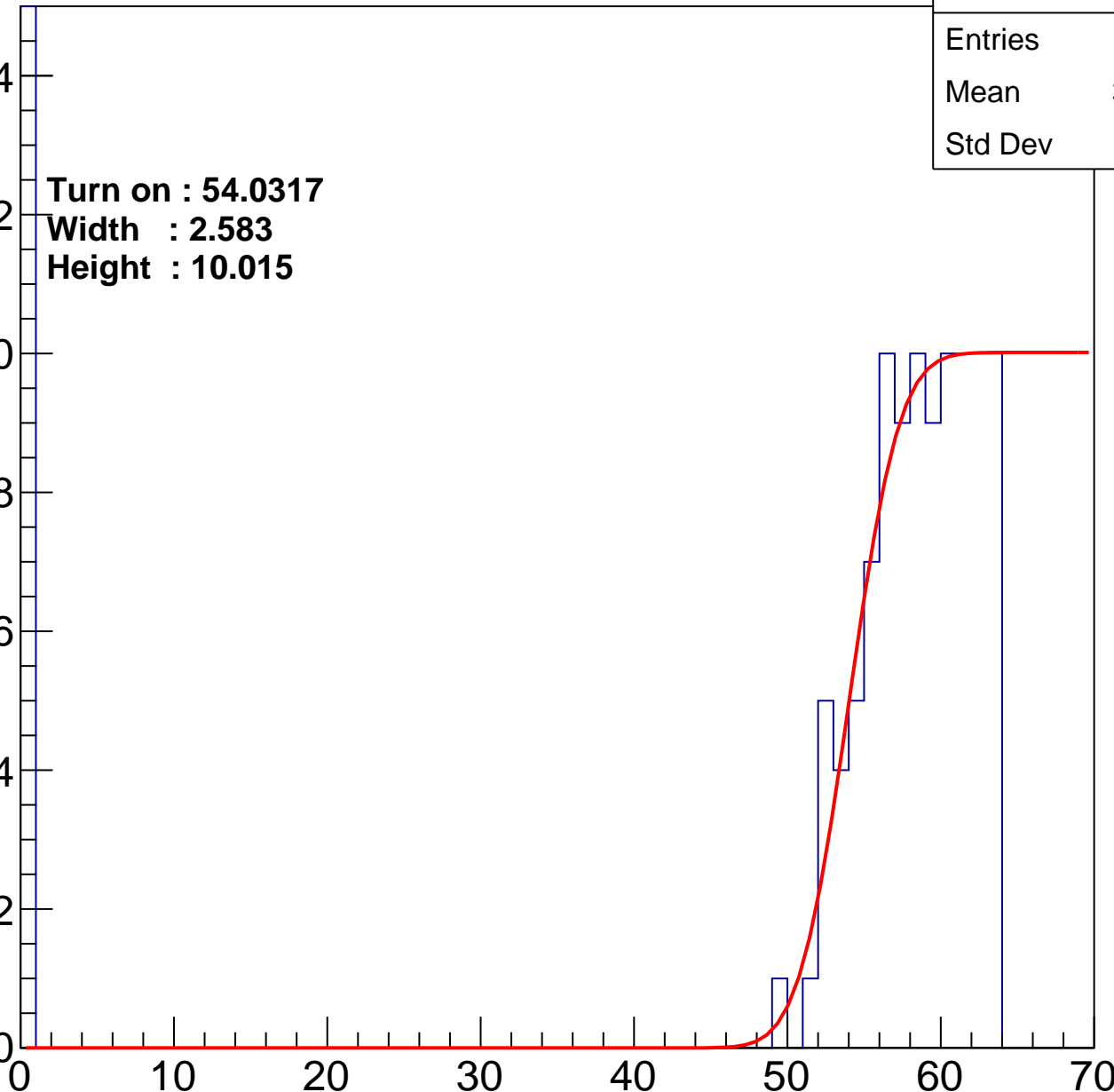
Width : 2.583

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch58

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	35.18
Std Dev	27.65

Turn on : 52.1926

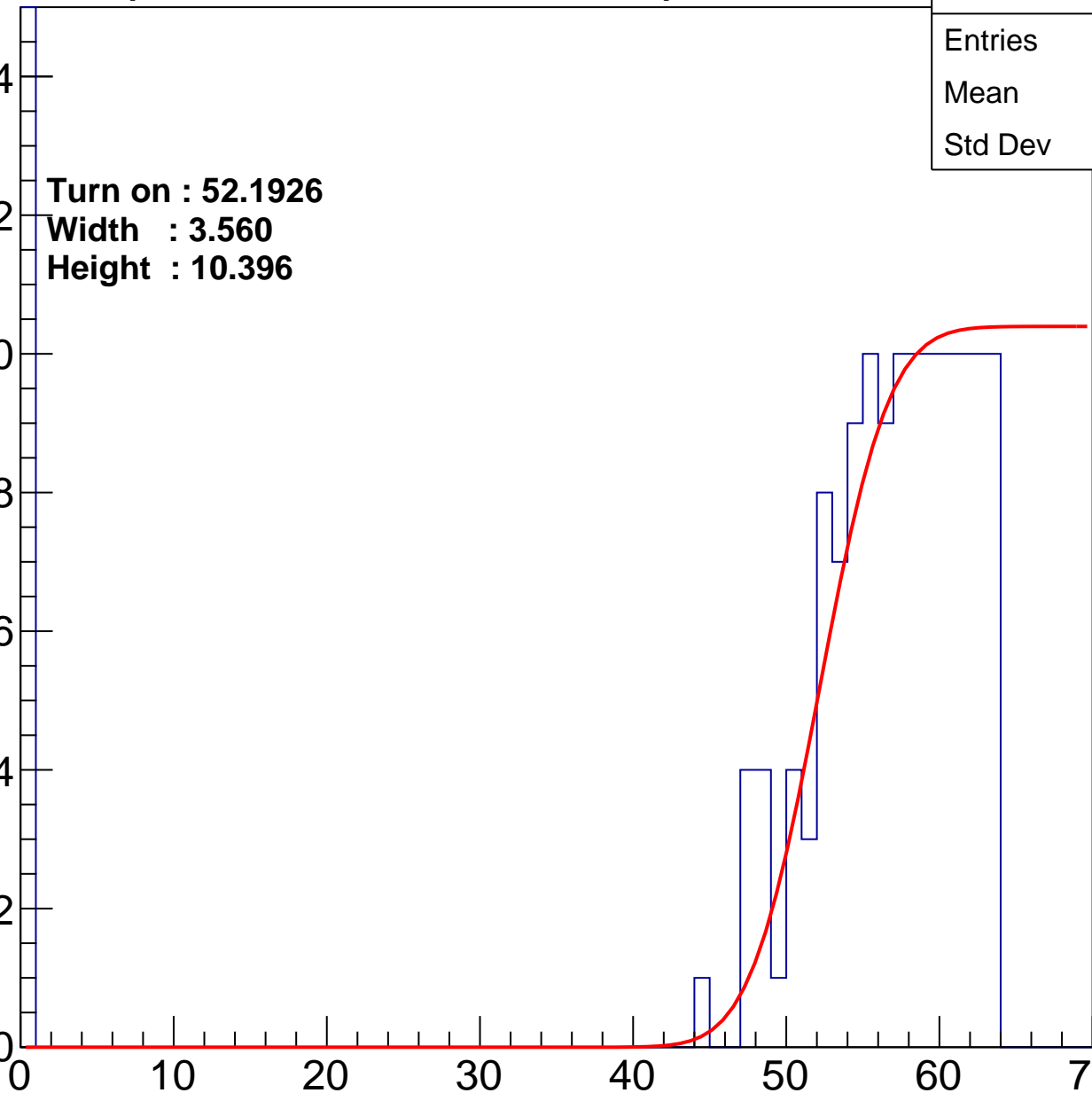
Width : 3.560

Height : 10.396

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch59

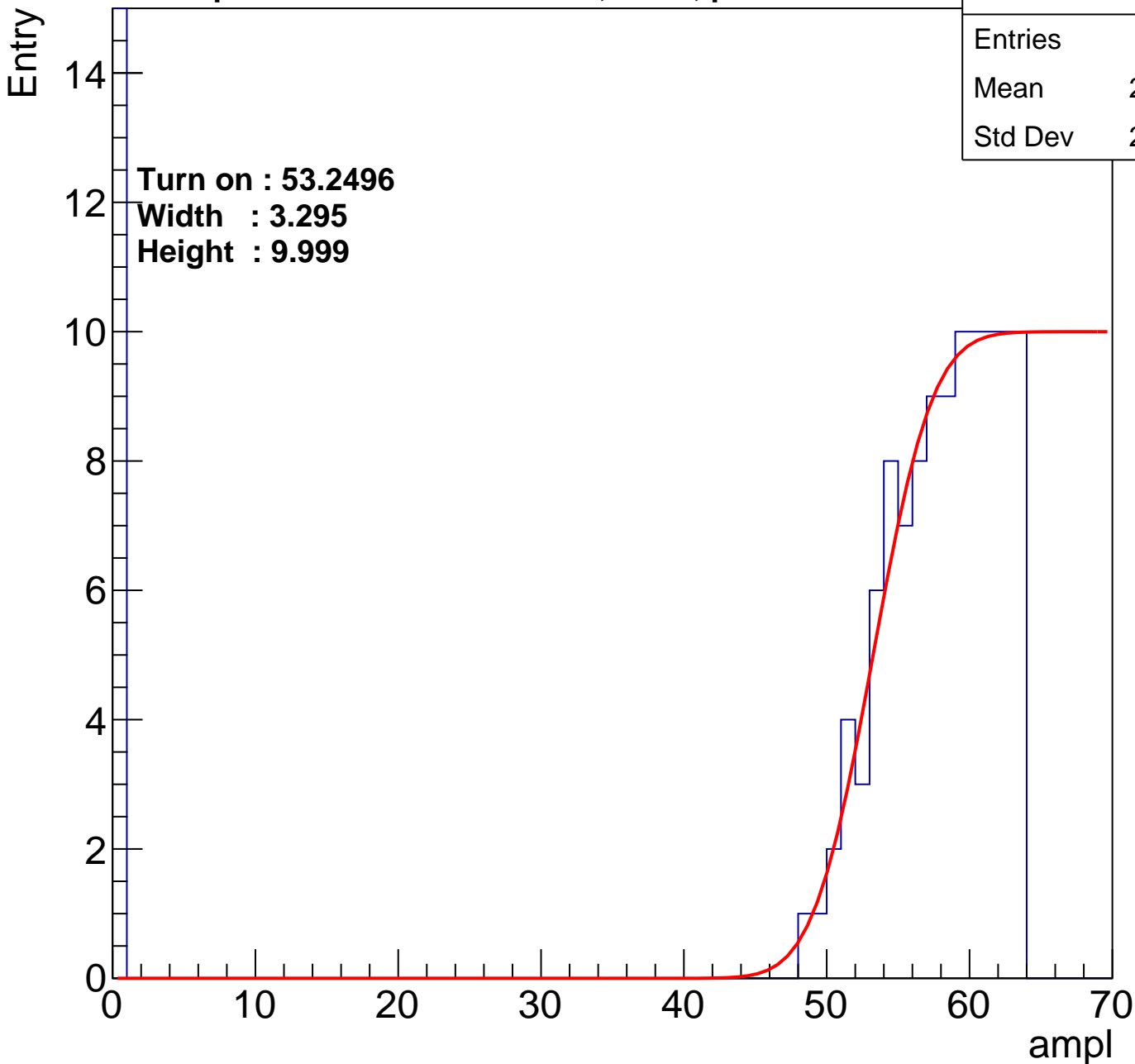
calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	29.23
Std Dev	28.94

Turn on : 53.2496

Width : 3.295

Height : 9.999



B1L104S, U22-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.84
Std Dev	28.84

Turn on : 53.3445

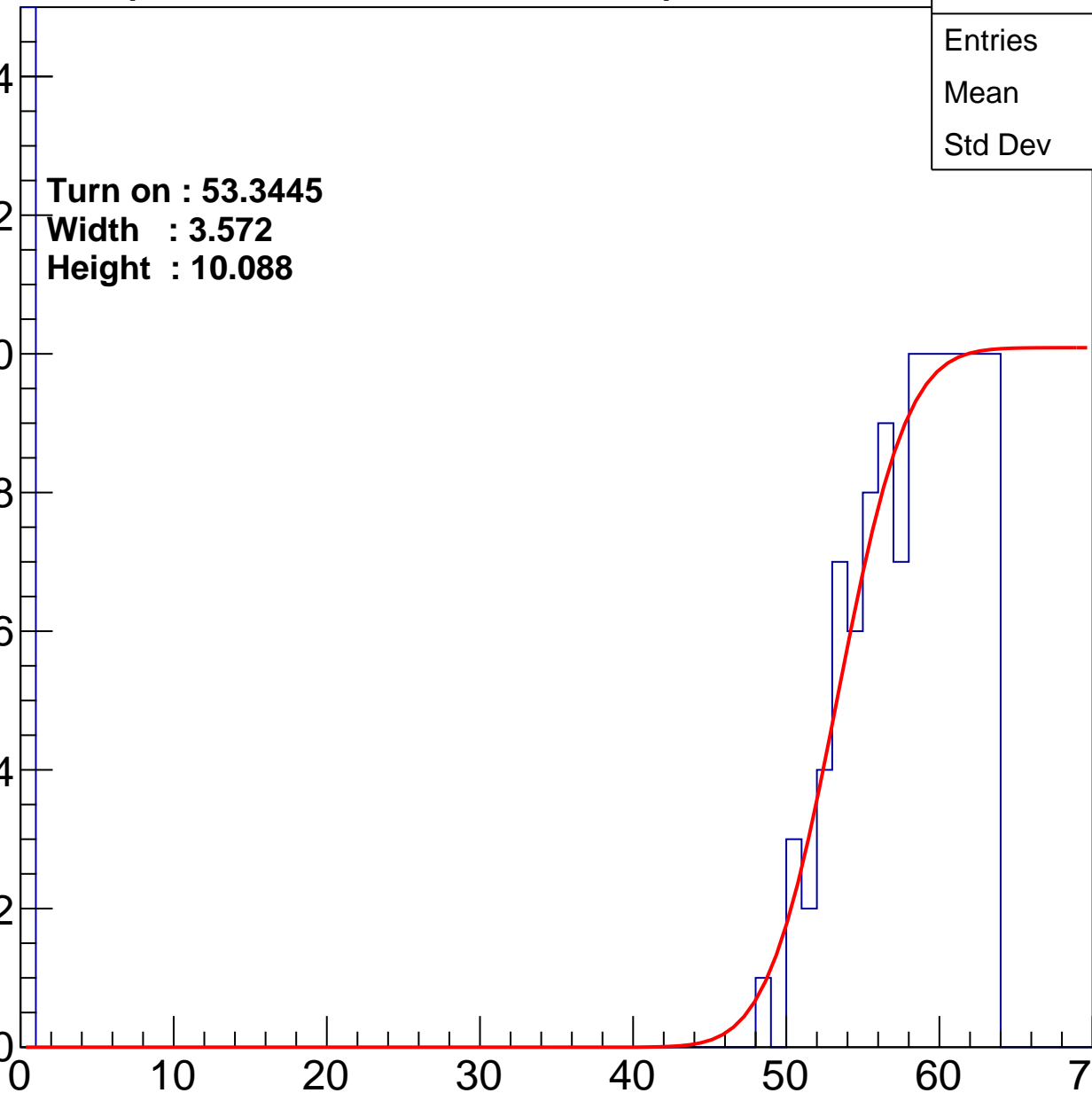
Width : 3.572

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	34.57
Std Dev	27.91

Turn on : 51.0335

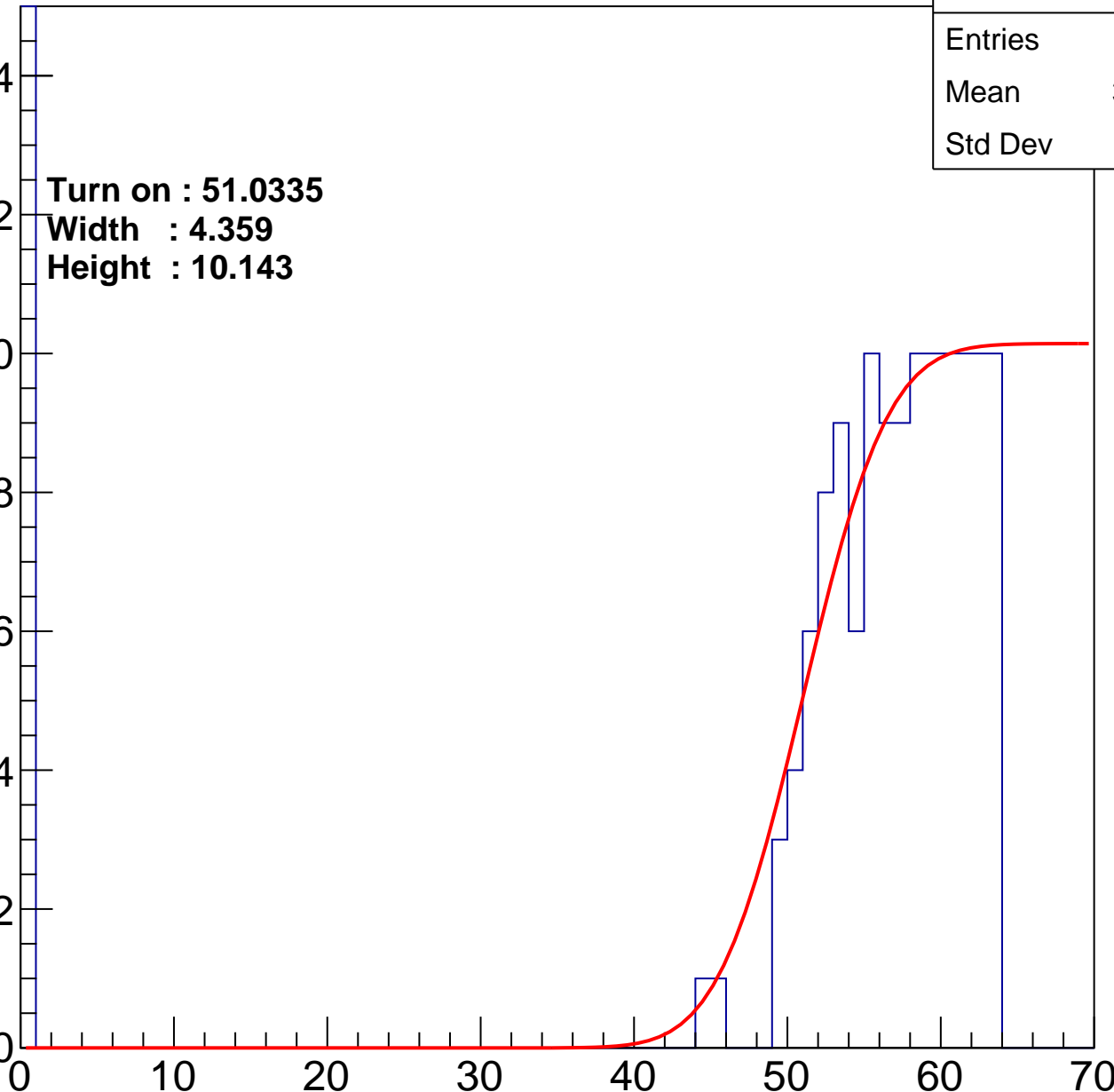
Width : 4.359

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	34.2
Std Dev	28.38

Turn on : 53.1673

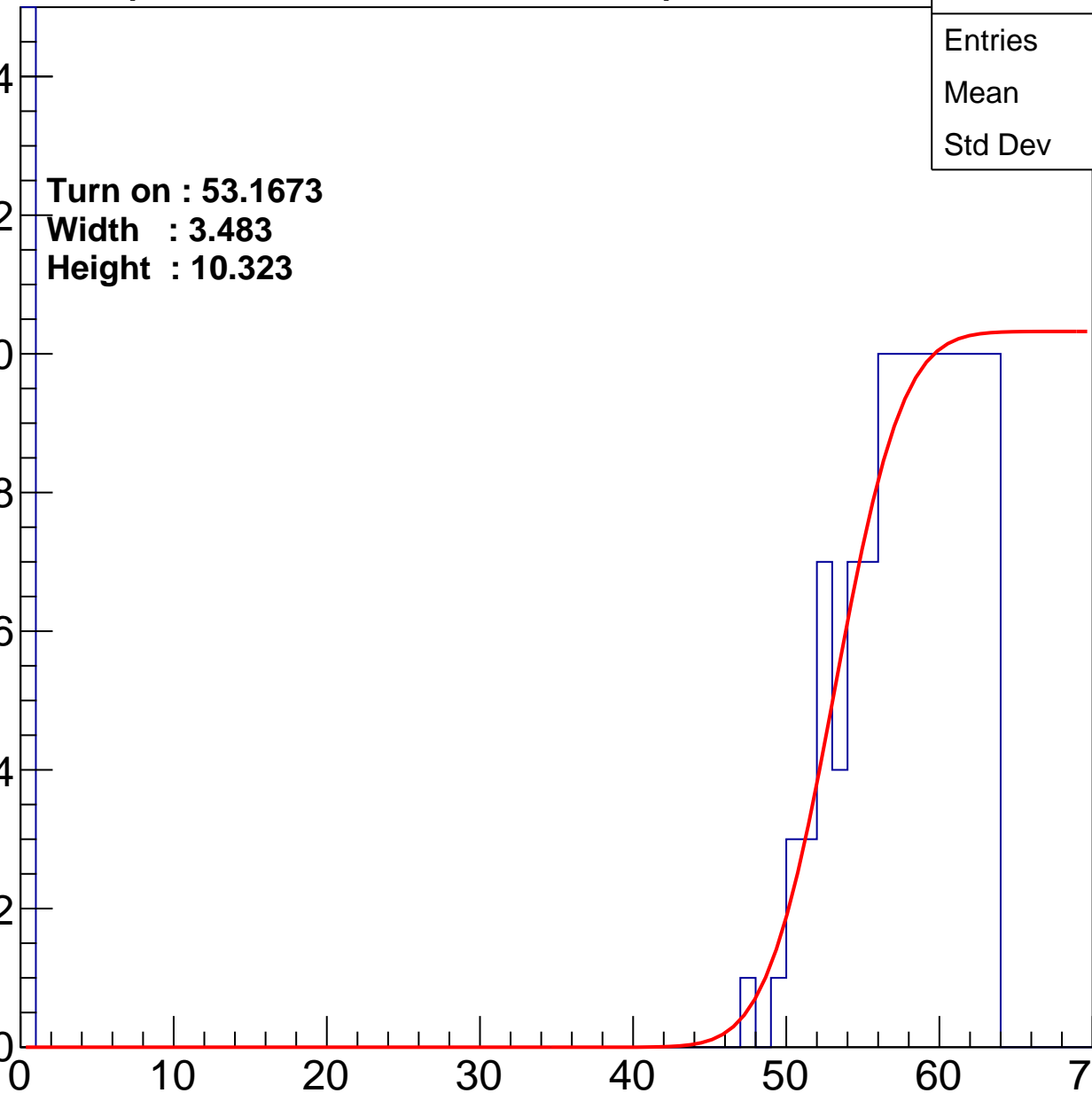
Width : 3.483

Height : 10.323

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	32.72
Std Dev	28.82

Turn on : 53.2966

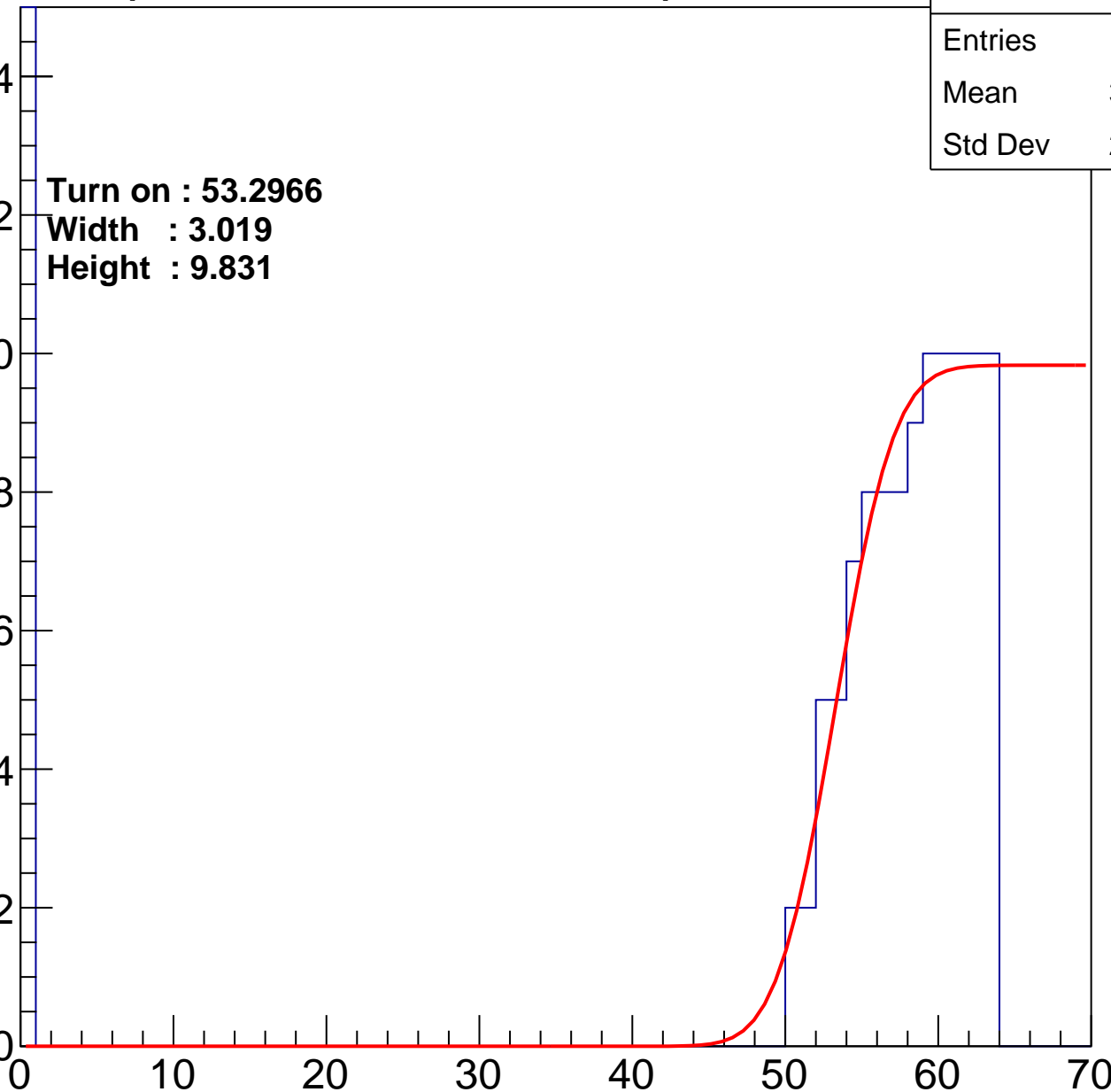
Width : 3.019

Height : 9.831

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	33.29
Std Dev	28.31

Turn on : 52.1199

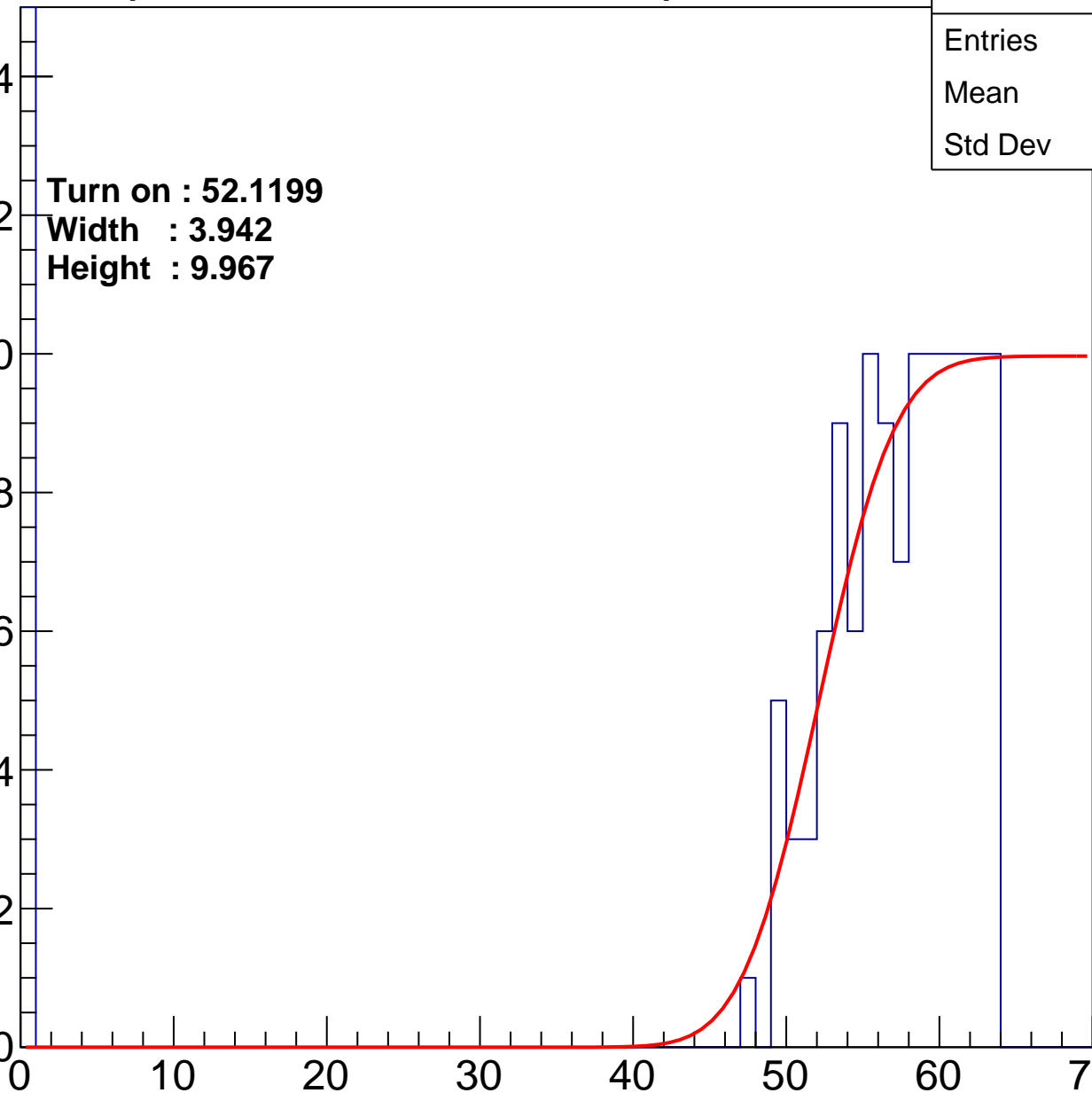
Width : 3.942

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	34.63
Std Dev	28.49

Turn on : 54.0180

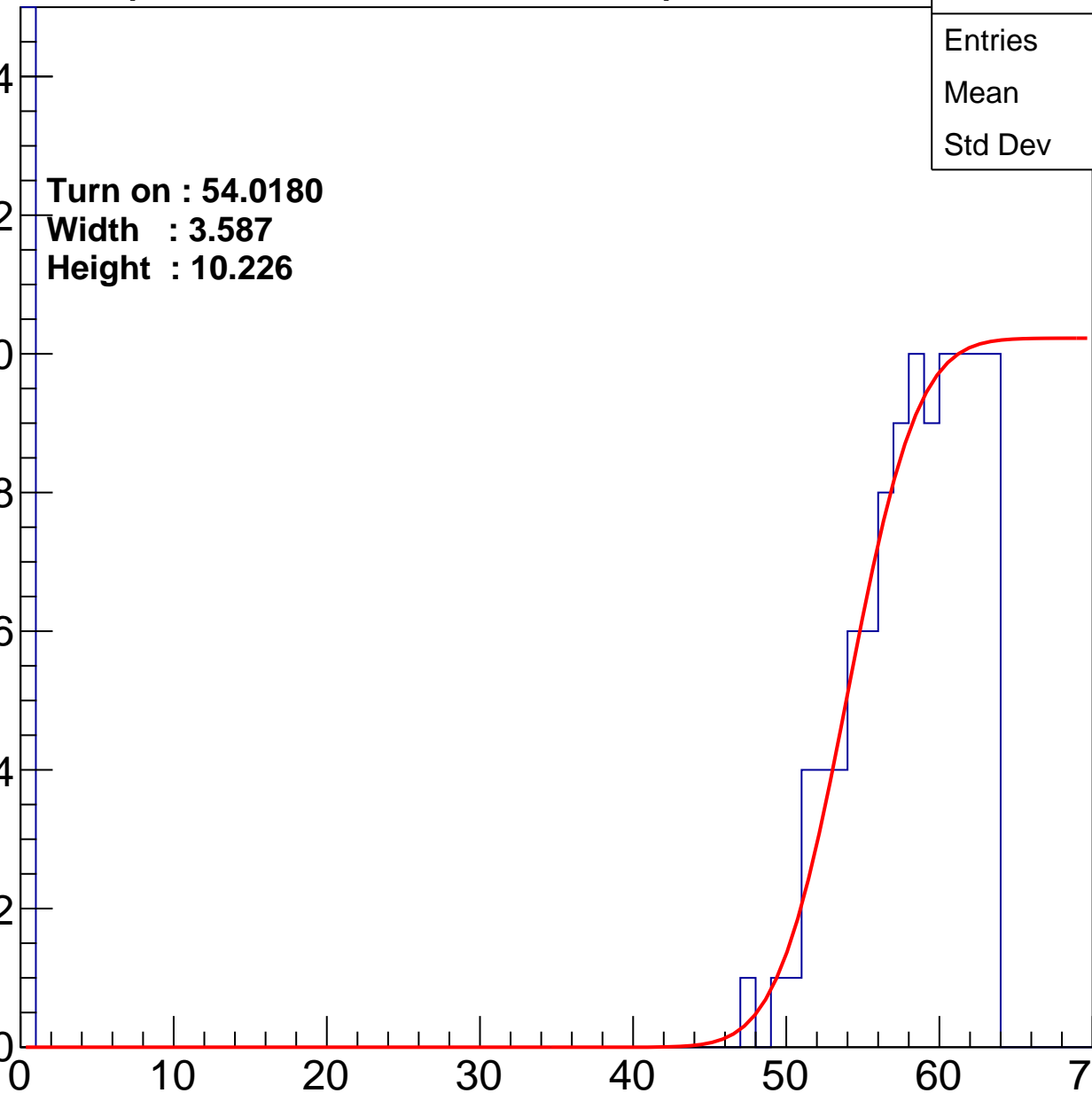
Width : 3.587

Height : 10.226

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch66

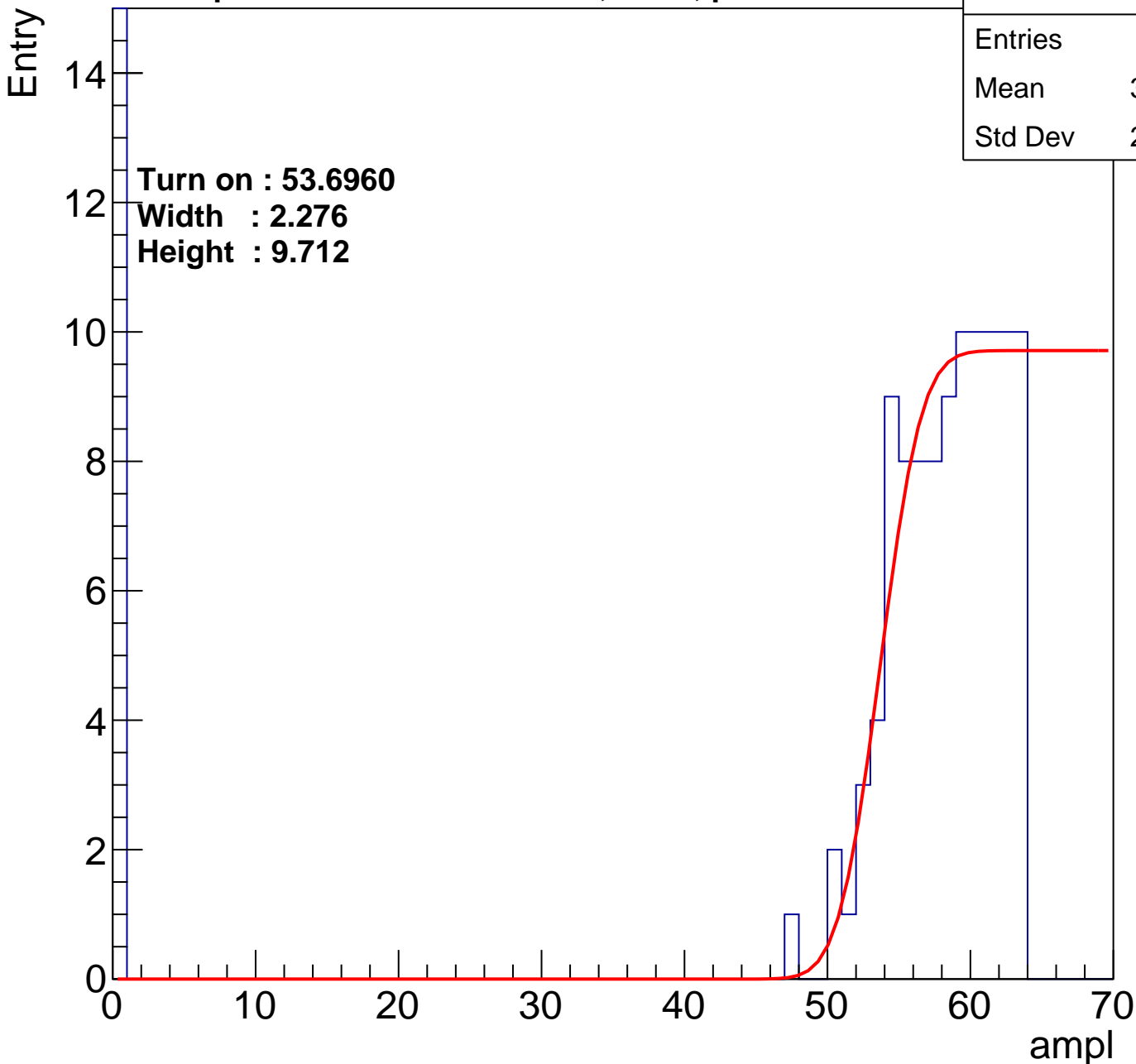
calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	32.79
Std Dev	28.84

Turn on : 53.6960

Width : 2.276

Height : 9.712



B1L104S, U22-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	35.42
Std Dev	28.21

Turn on : 53.7728

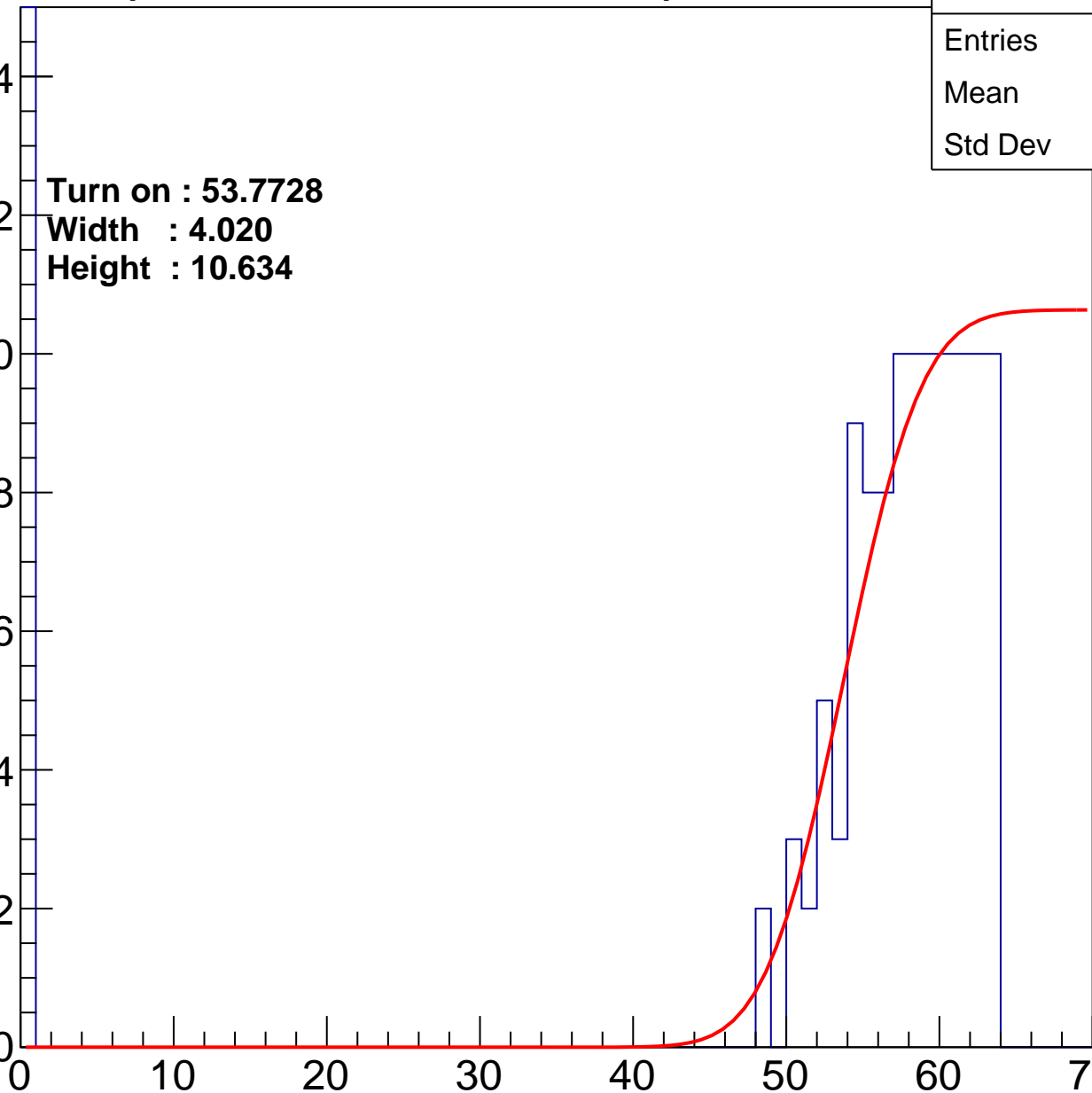
Width : 4.020

Height : 10.634

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	35.12
Std Dev	27.81

Turn on : 52.8280

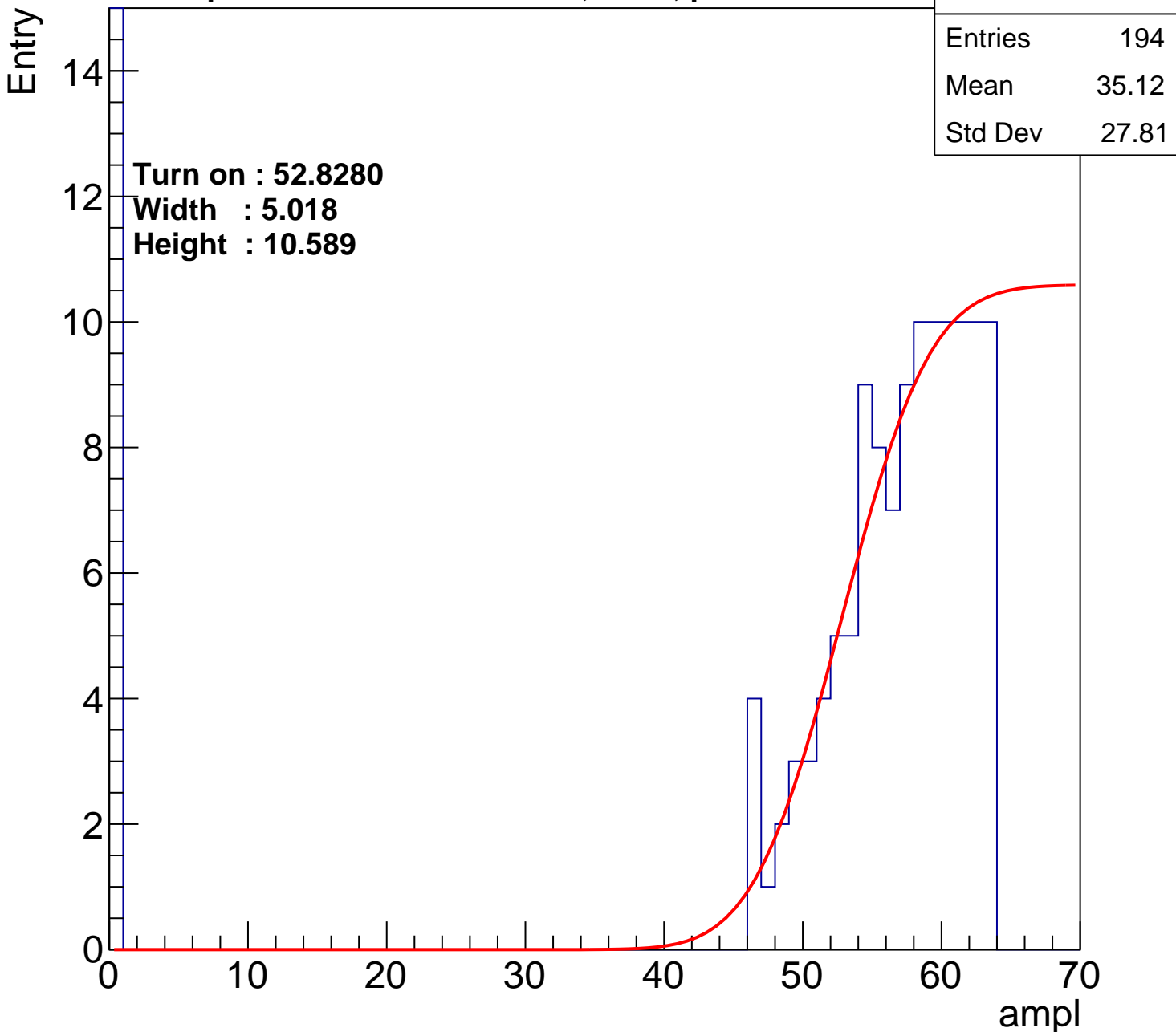
Width : 5.018

Height : 10.589

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch69

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	31.98
Std Dev	28.83

Turn on : 53.3157

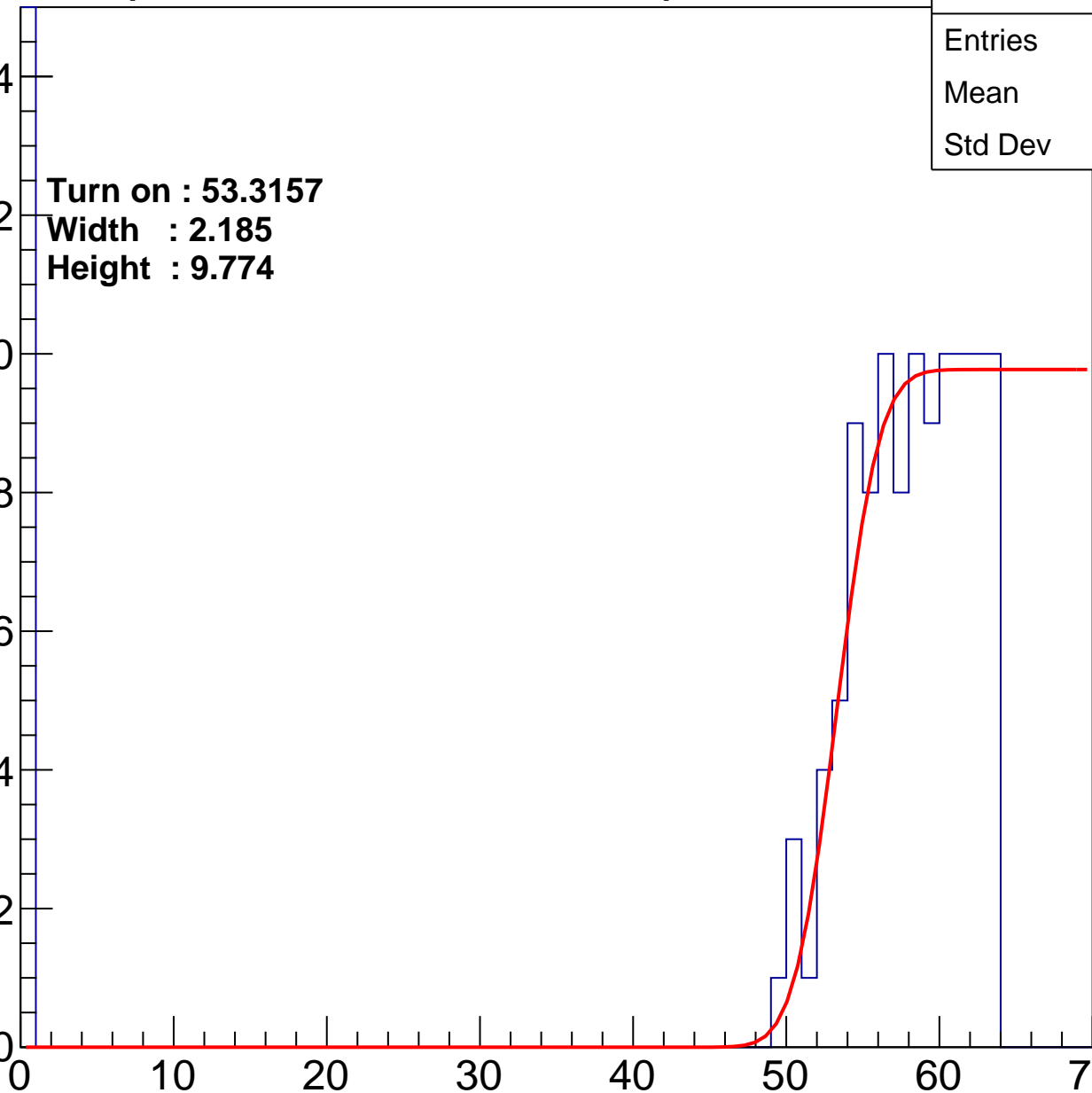
Width : 2.185

Height : 9.774

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	31.96
Std Dev	28.59

Turn on : 52.7198

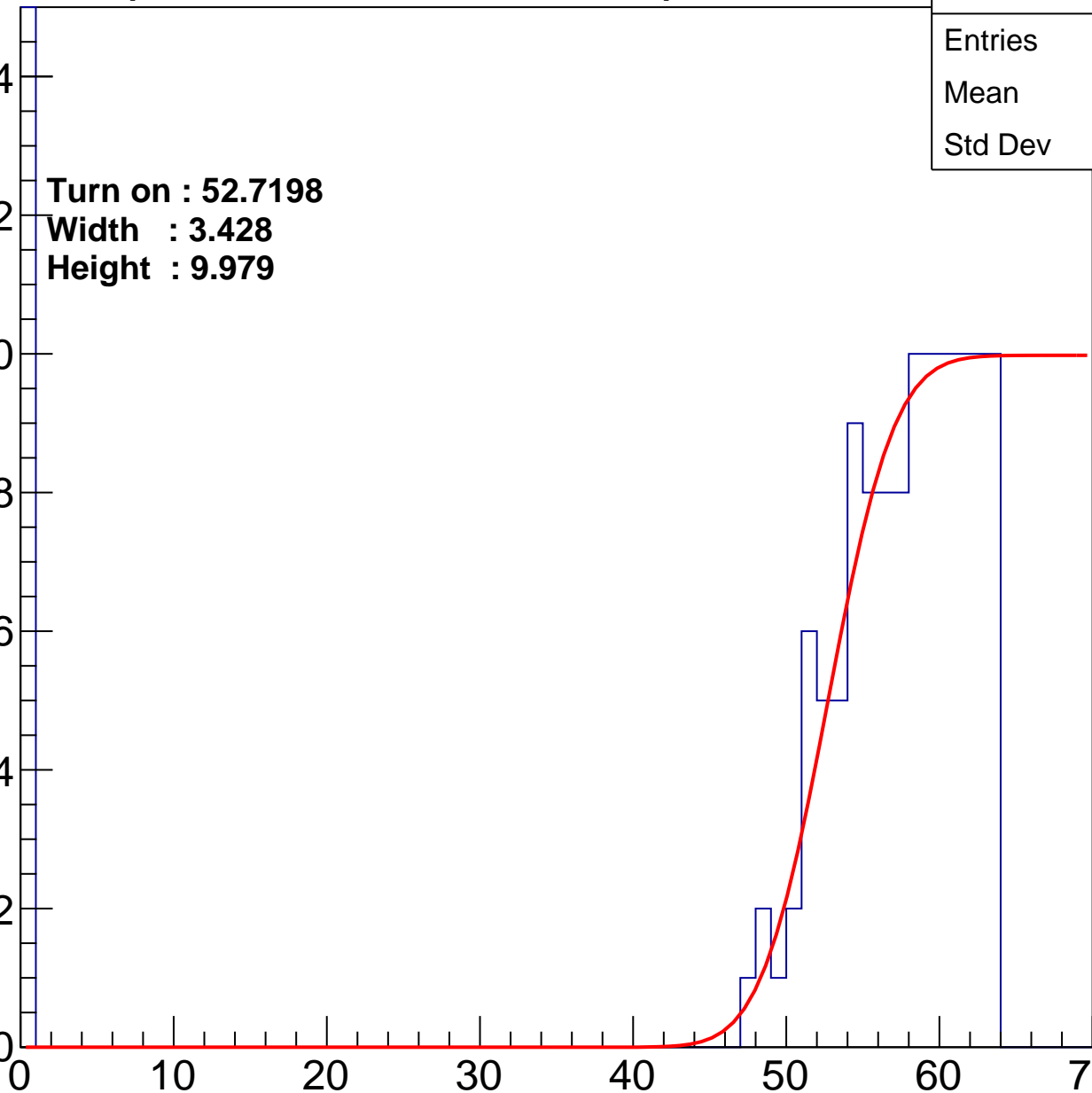
Width : 3.428

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	34.21
Std Dev	28.53

Turn on : 53.7542

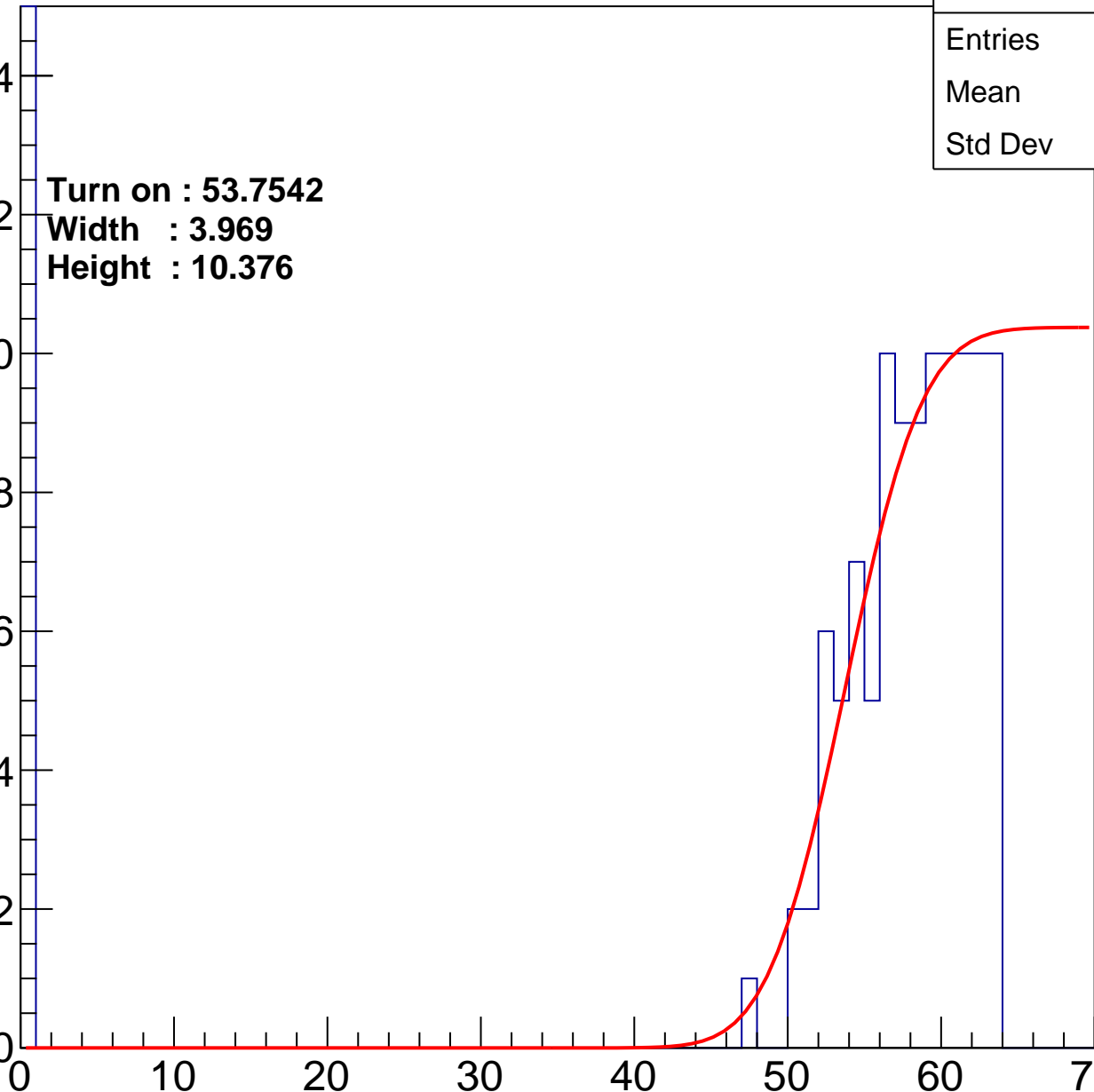
Width : 3.969

Height : 10.376

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	35.5
Std Dev	27.46

Turn on : 50.9650

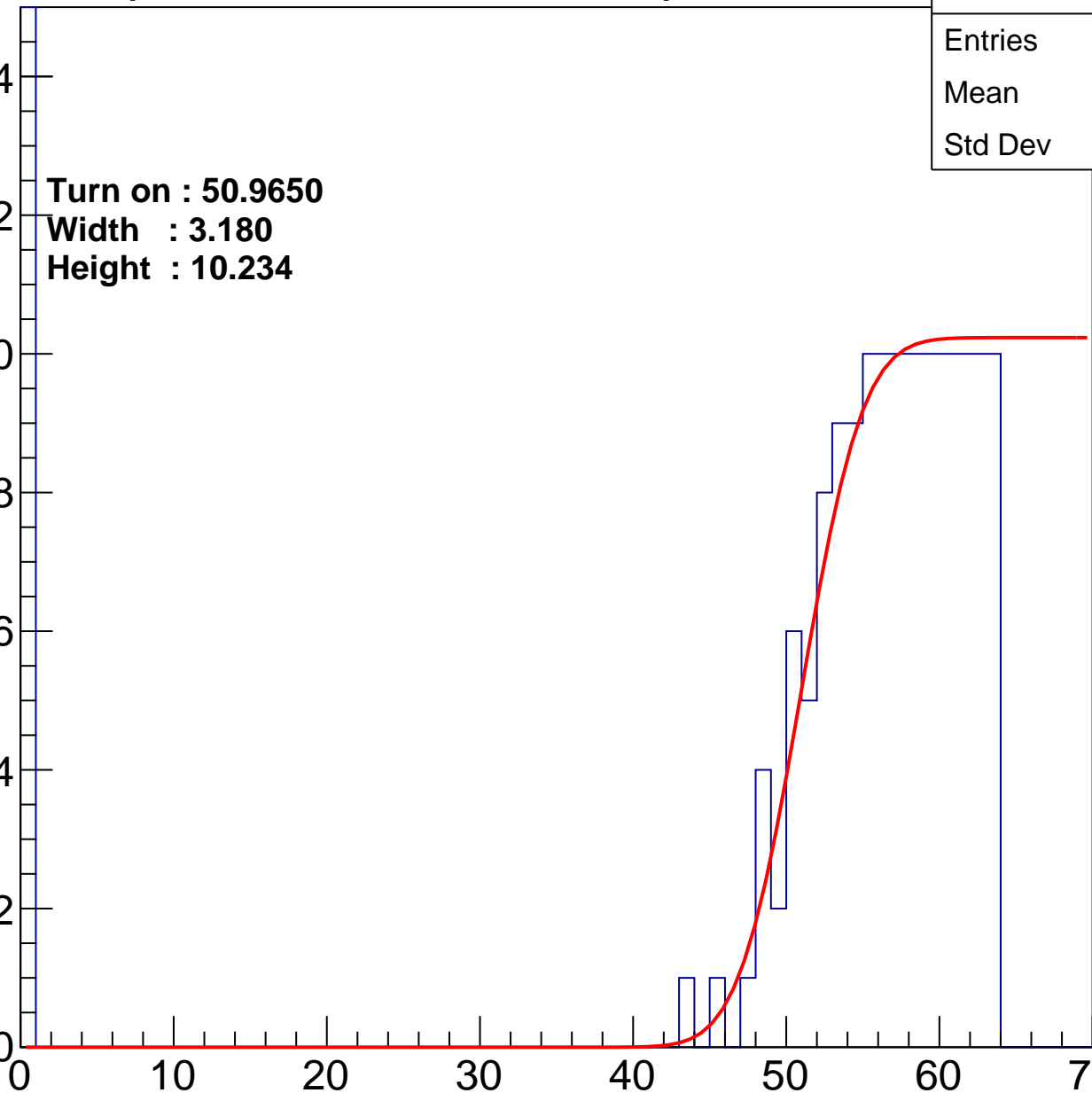
Width : 3.180

Height : 10.234

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	35.78
Std Dev	27.95

Turn on : 53.7823

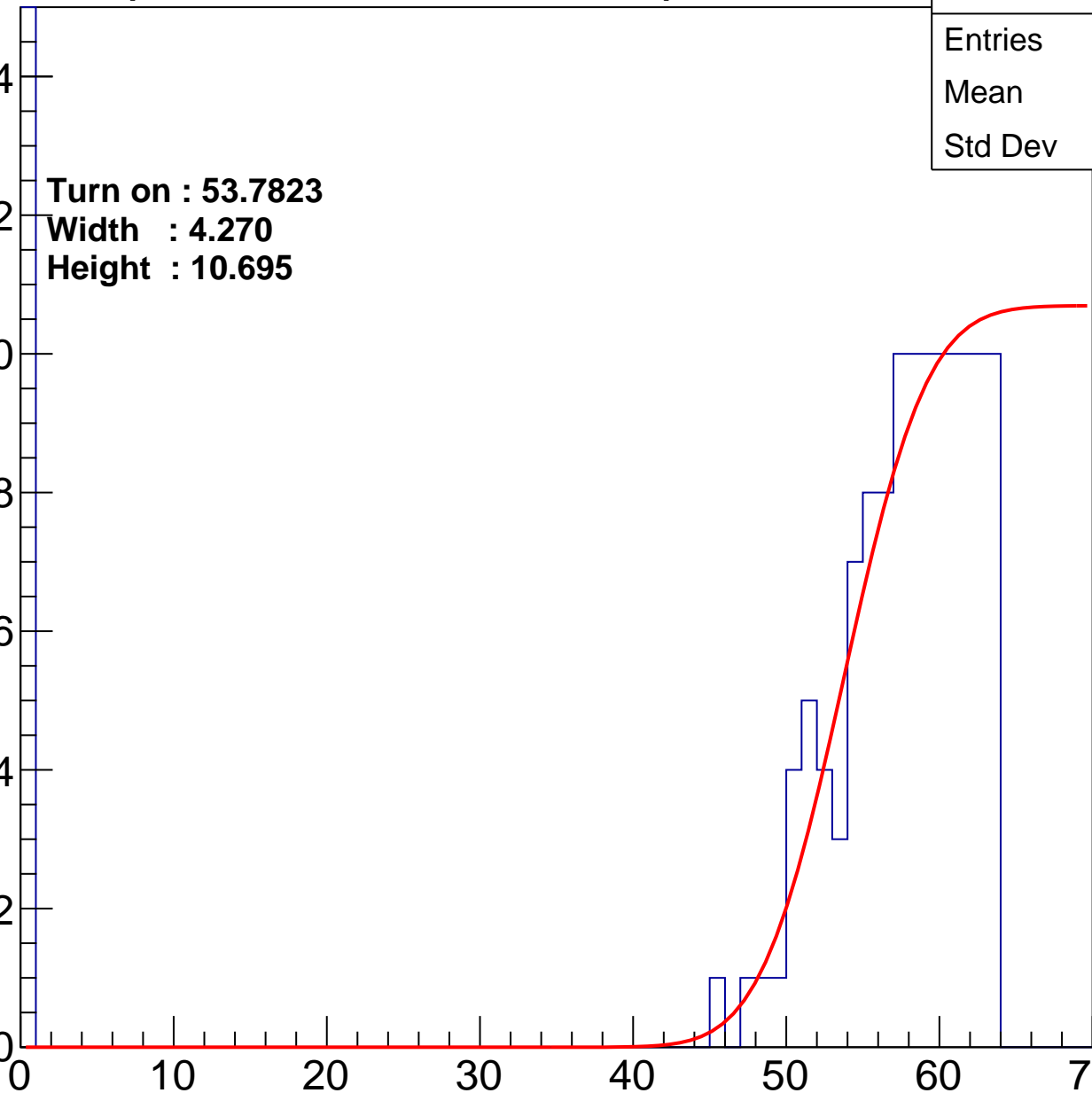
Width : 4.270

Height : 10.695

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	39.7
Std Dev	26.18

Turn on : 51.1206

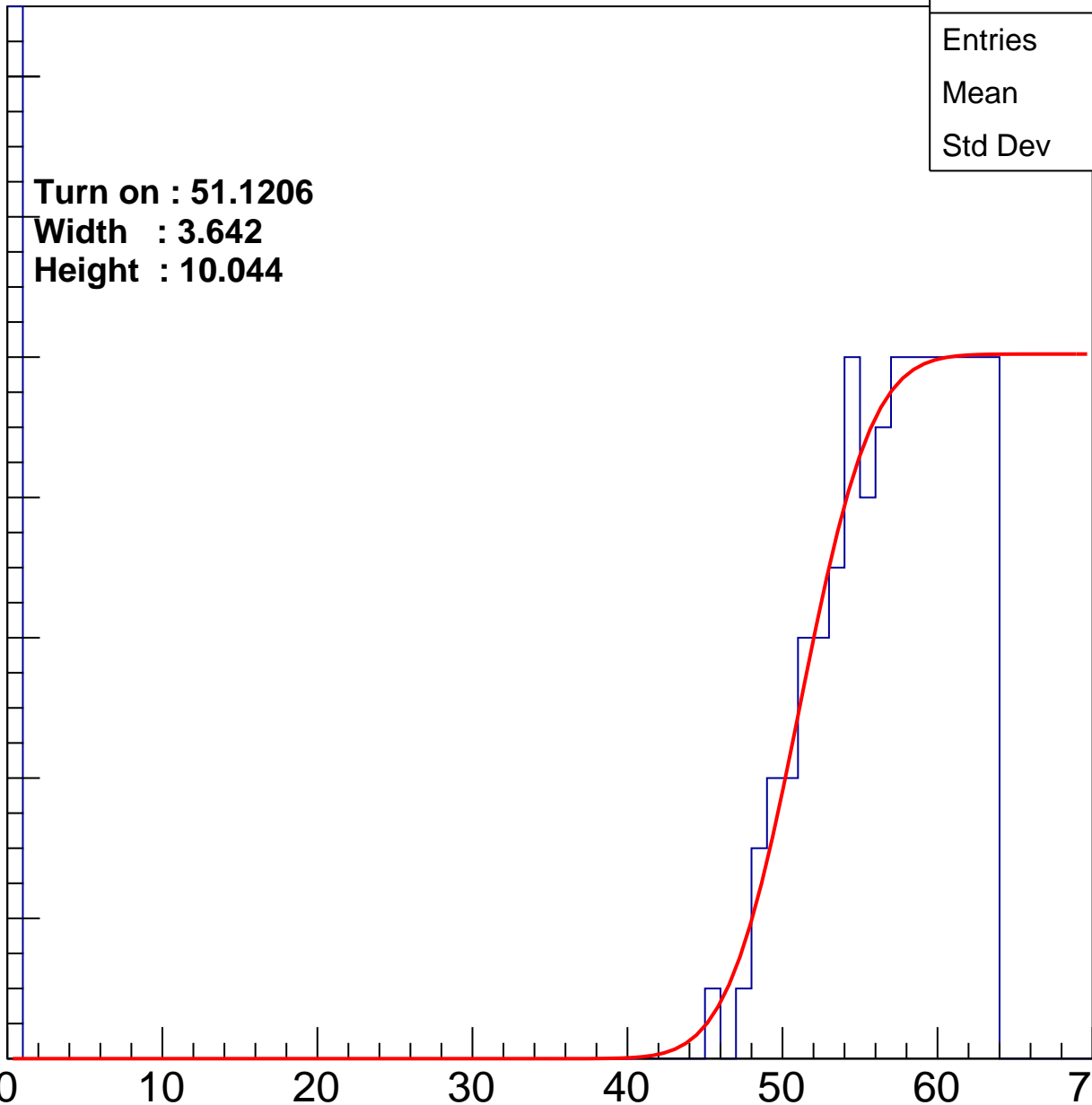
Width : 3.642

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch75

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	36.59
Std Dev	27.7

Turn on : 52.4183

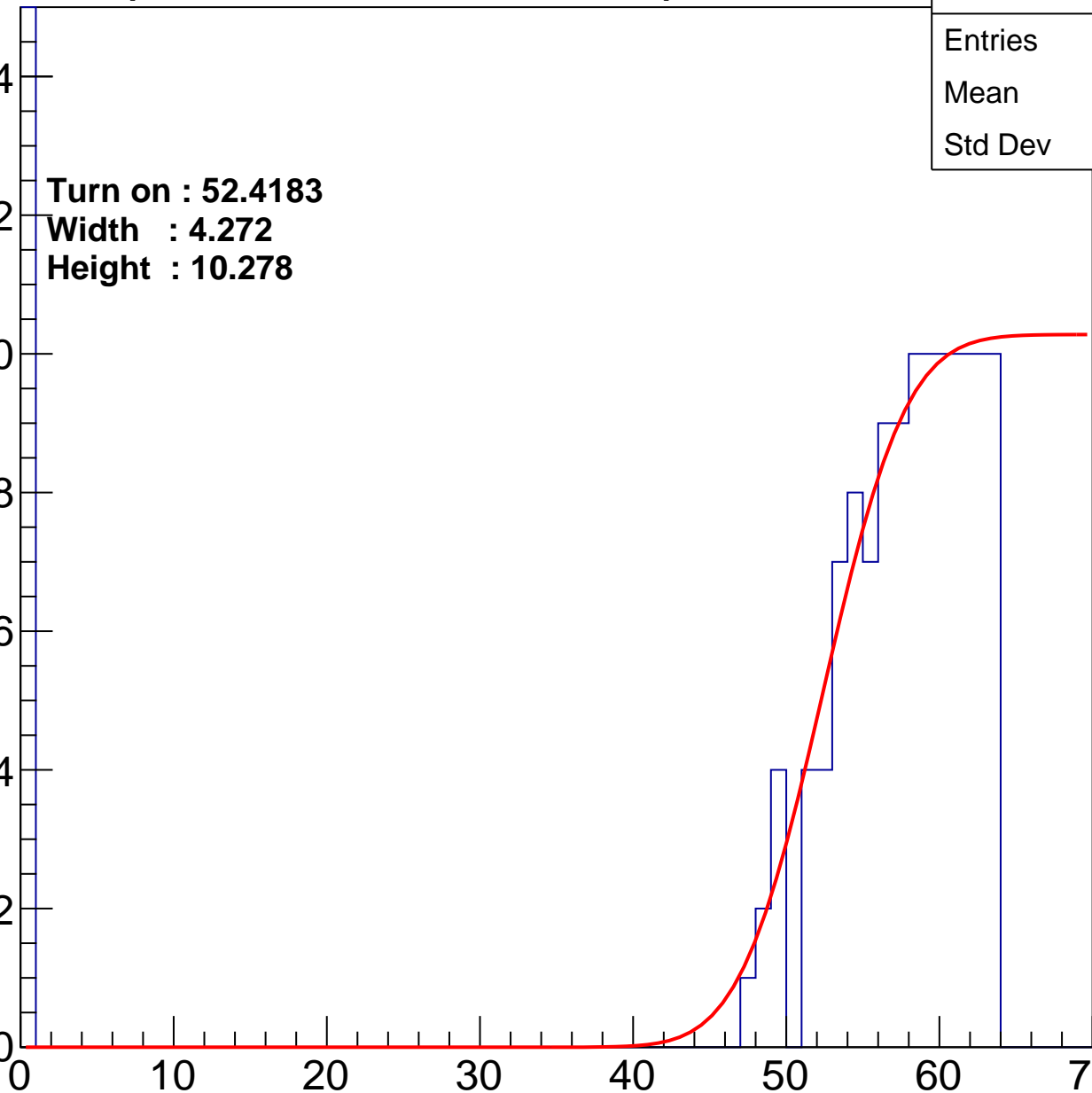
Width : 4.272

Height : 10.278

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch76

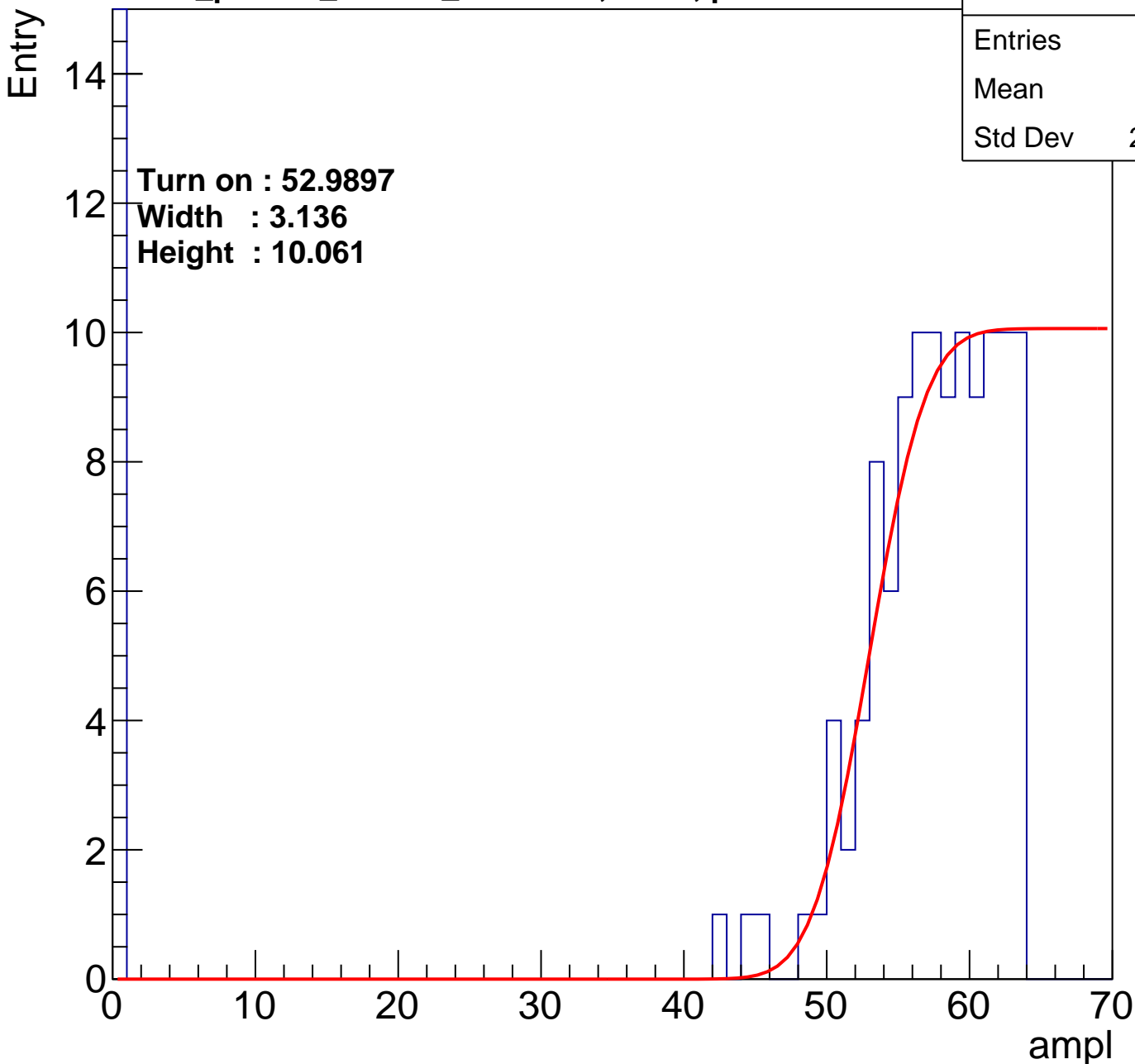
calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	35.8
Std Dev	27.82

Turn on : 52.9897

Width : 3.136

Height : 10.061



B1L104S, U22-ch77

calib_packv5_033123_0516.root, FC#4, port A1

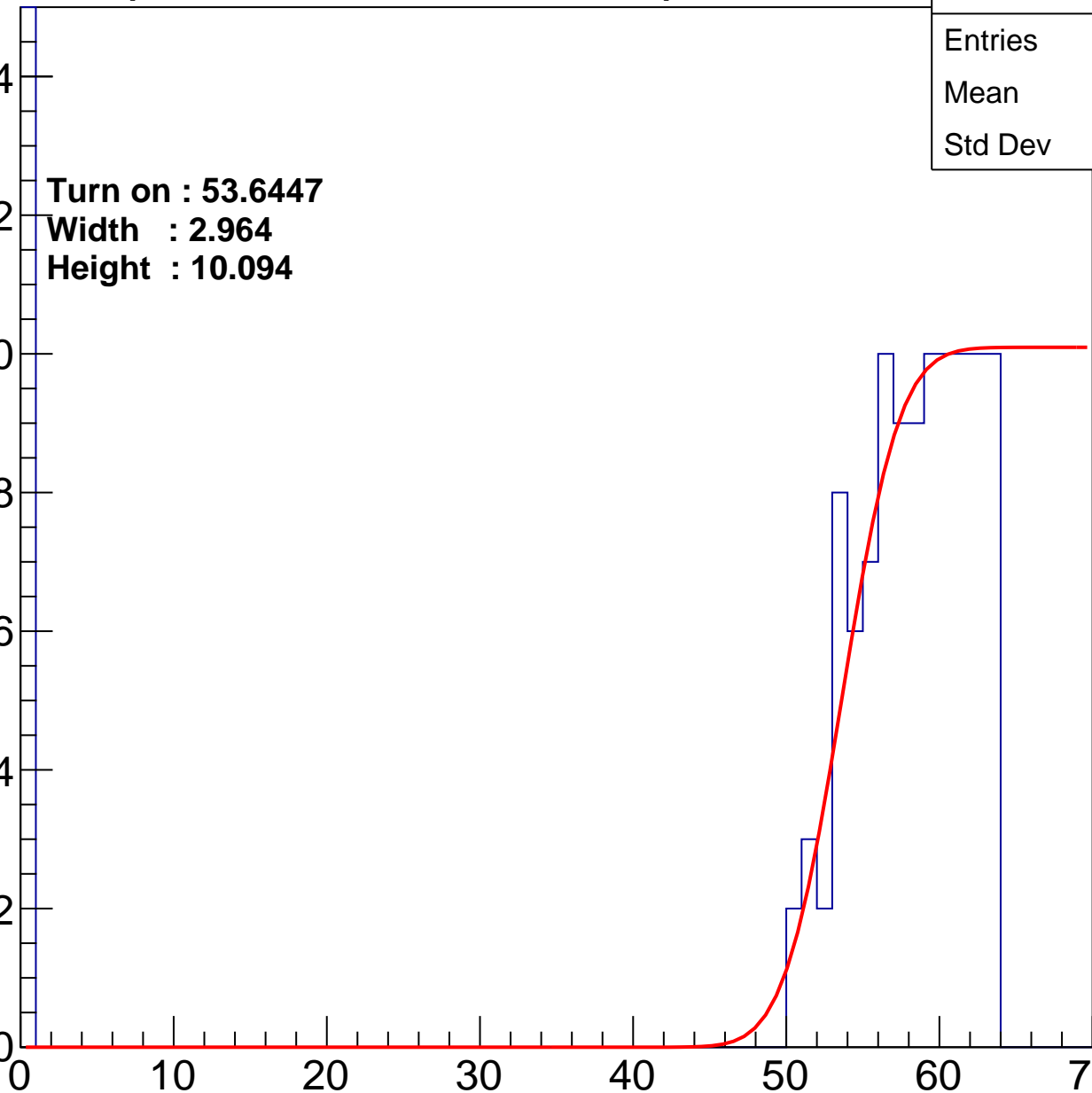
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6447
Width : 2.964
Height : 10.094

Entries	172
Mean	35.67
Std Dev	28.28

ampl



B1L104S, U22-ch78

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	34.11
Std Dev	28.33

Turn on : 53.7420

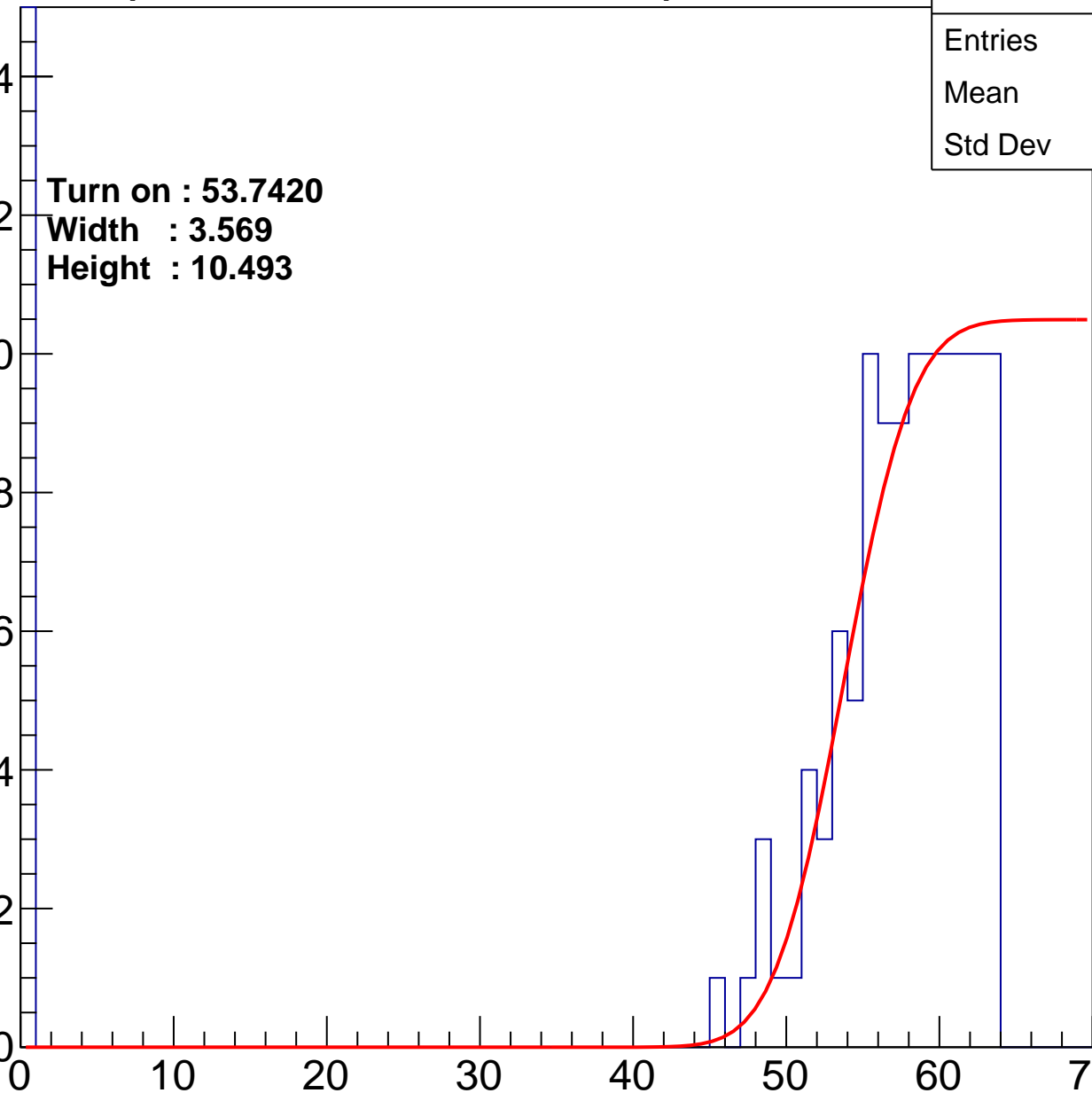
Width : 3.569

Height : 10.493

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch79

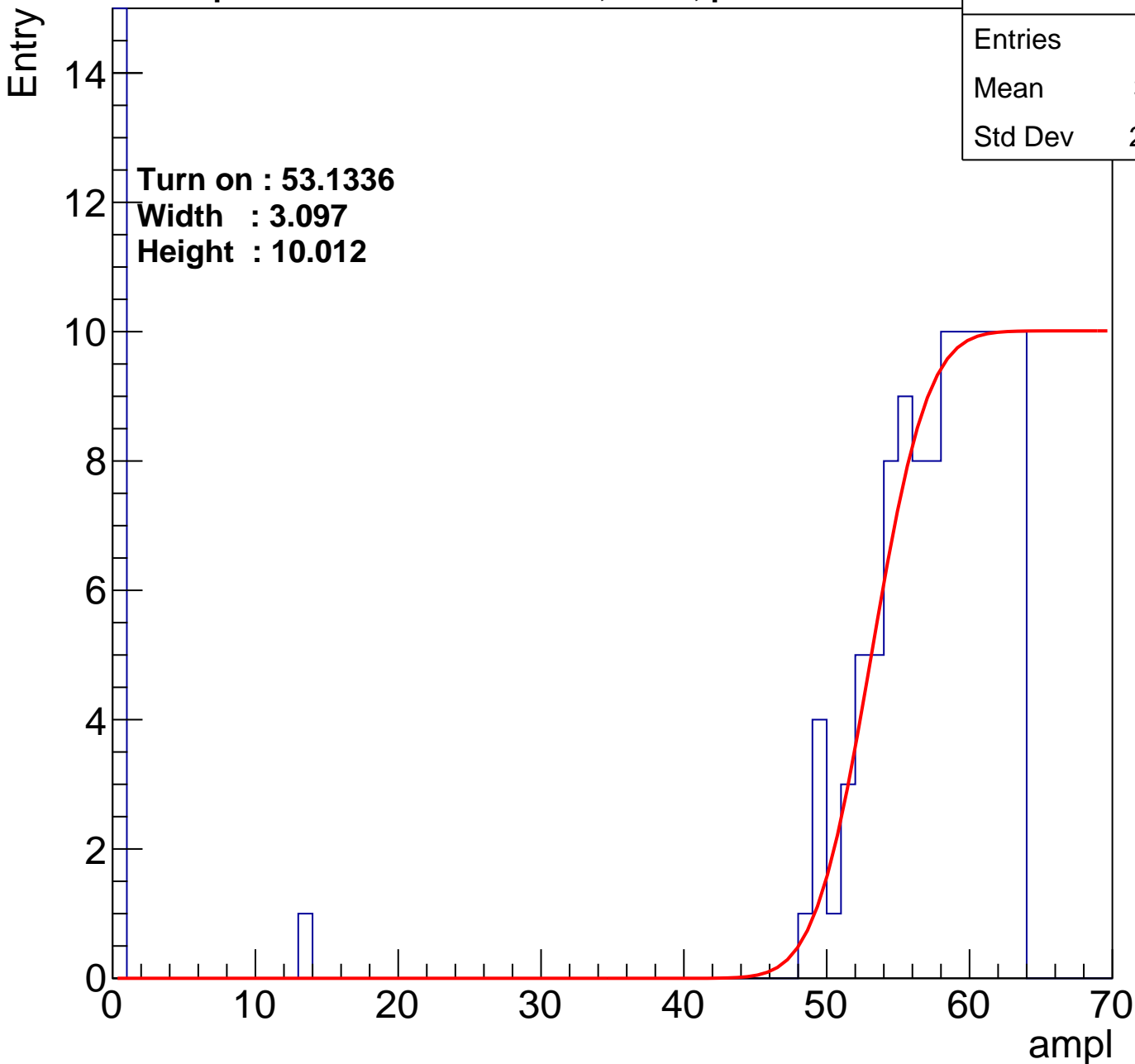
calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	36.01
Std Dev	27.89

Turn on : 53.1336

Width : 3.097

Height : 10.012



B1L104S, U22-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	33.59
Std Dev	28.22

Turn on : 51.9843

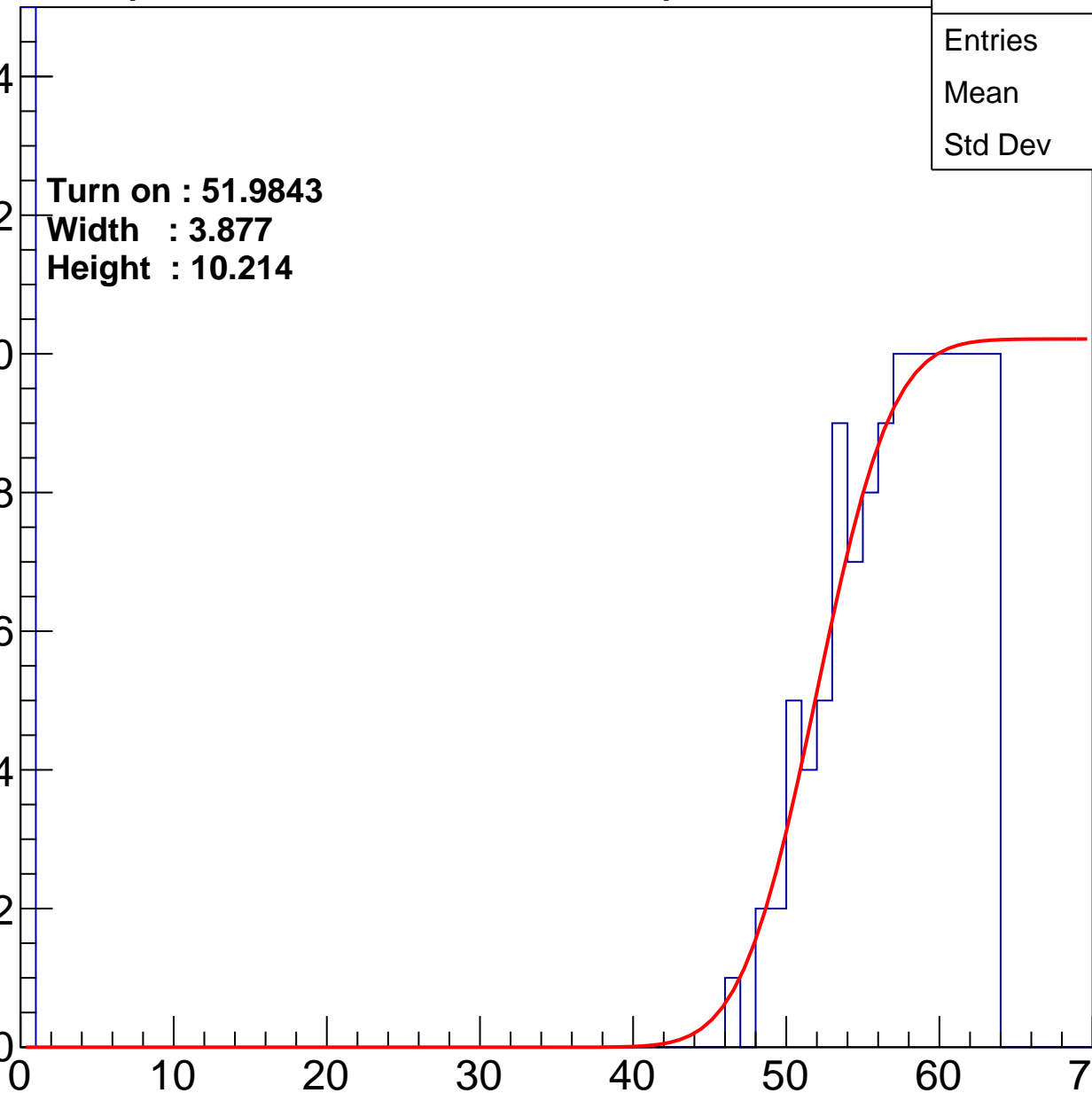
Width : 3.877

Height : 10.214

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	222
Mean	30.16
Std Dev	28.72

Turn on : 51.7775

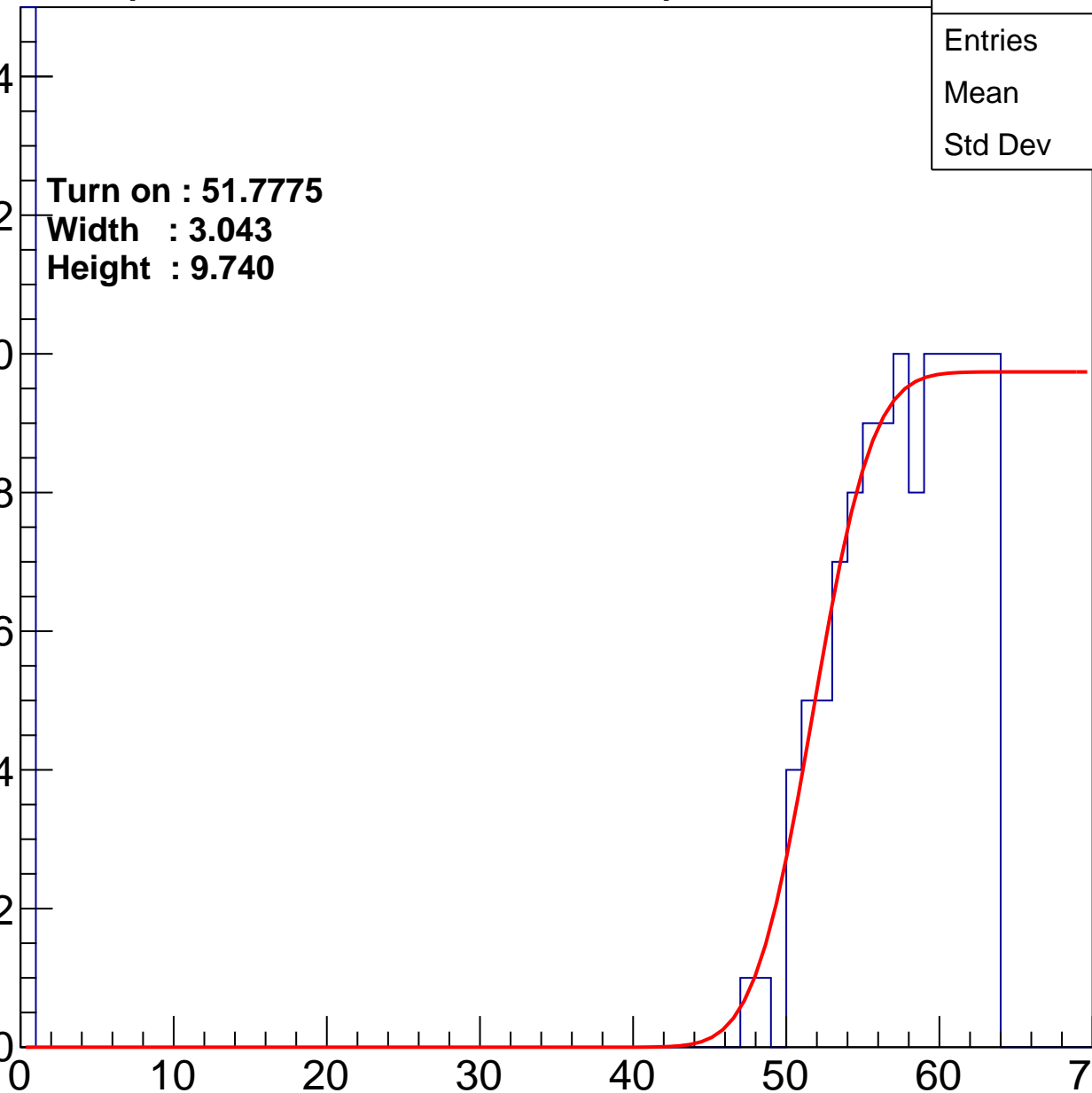
Width : 3.043

Height : 9.740

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch82

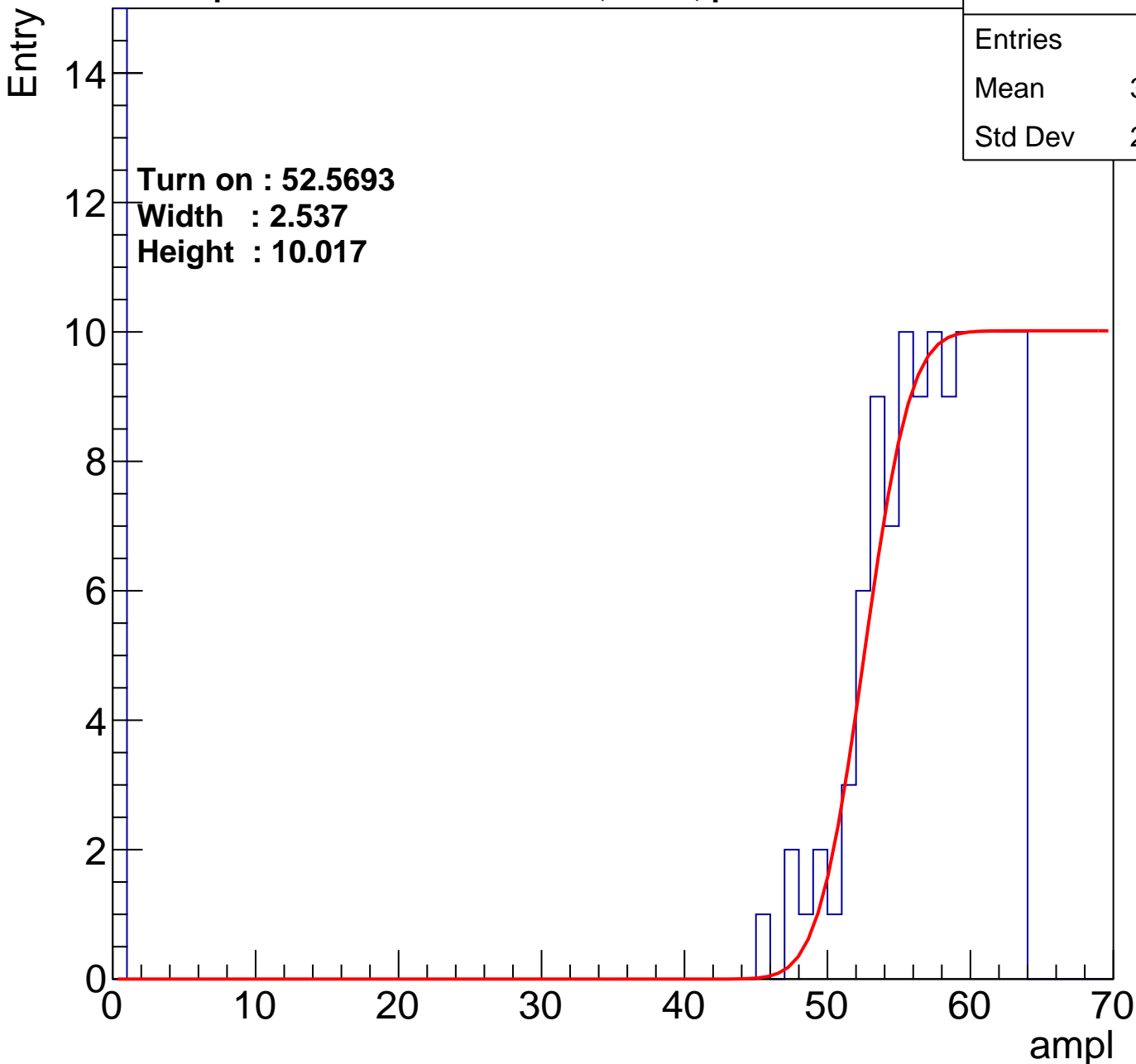
calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	32.62
Std Dev	28.42

Turn on : 52.5693

Width : 2.537

Height : 10.017



B1L104S, U22-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entry

Entries	187
Mean	26.52
Std Dev	29.17

Turn on : 56.8756

Width : 3.969

Height : 10.111

100

80

60

40

20

0

0

10

20

30

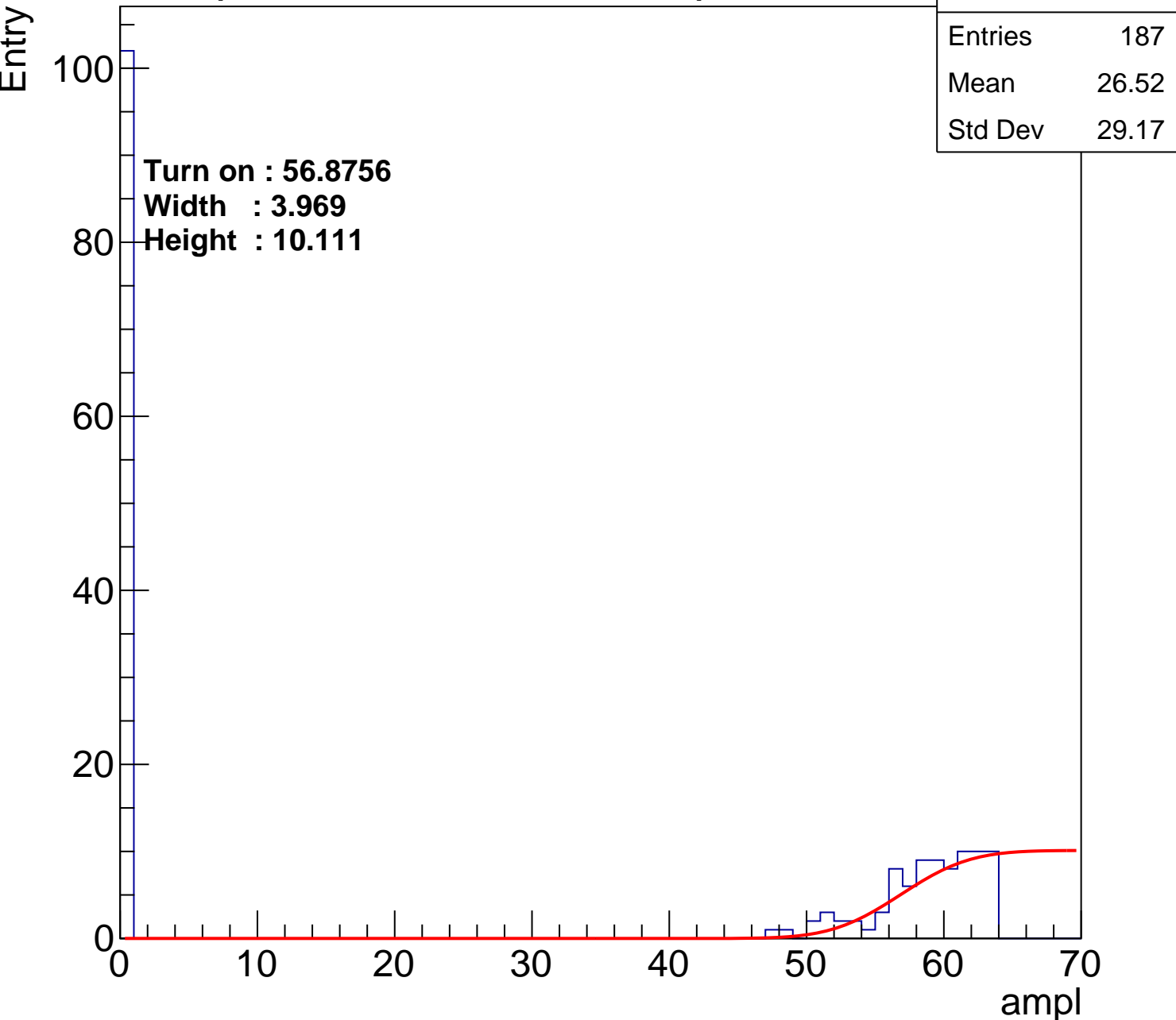
40

50

60

70

ampl



B1L104S, U22-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	28.88
Std Dev	29.12

Turn on : 54.4599

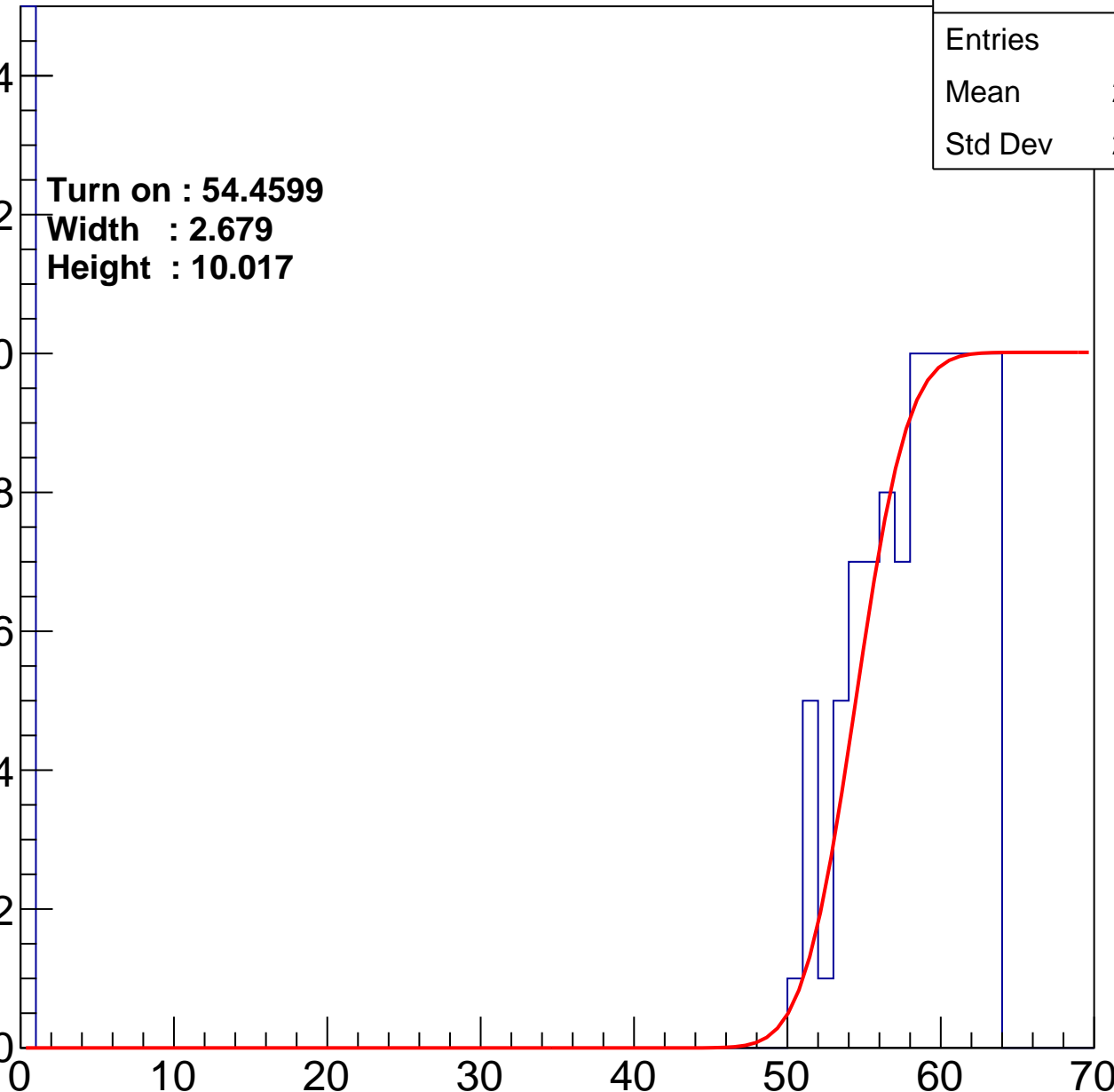
Width : 2.679

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch85

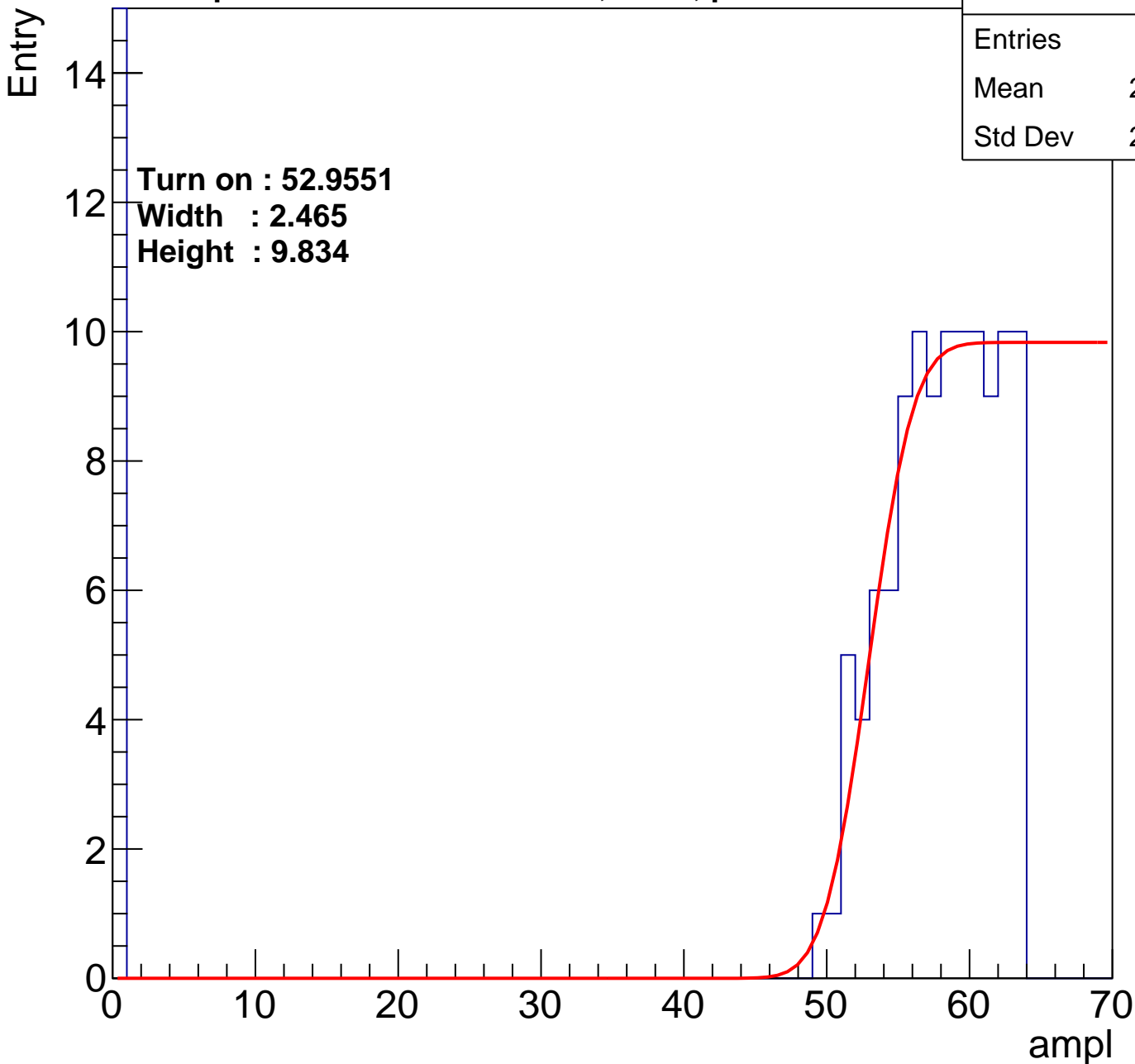
calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	28.82
Std Dev	28.94

Turn on : 52.9551

Width : 2.465

Height : 9.834



B1L104S, U22-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	33.5
Std Dev	28.32

Turn on : 53.1279

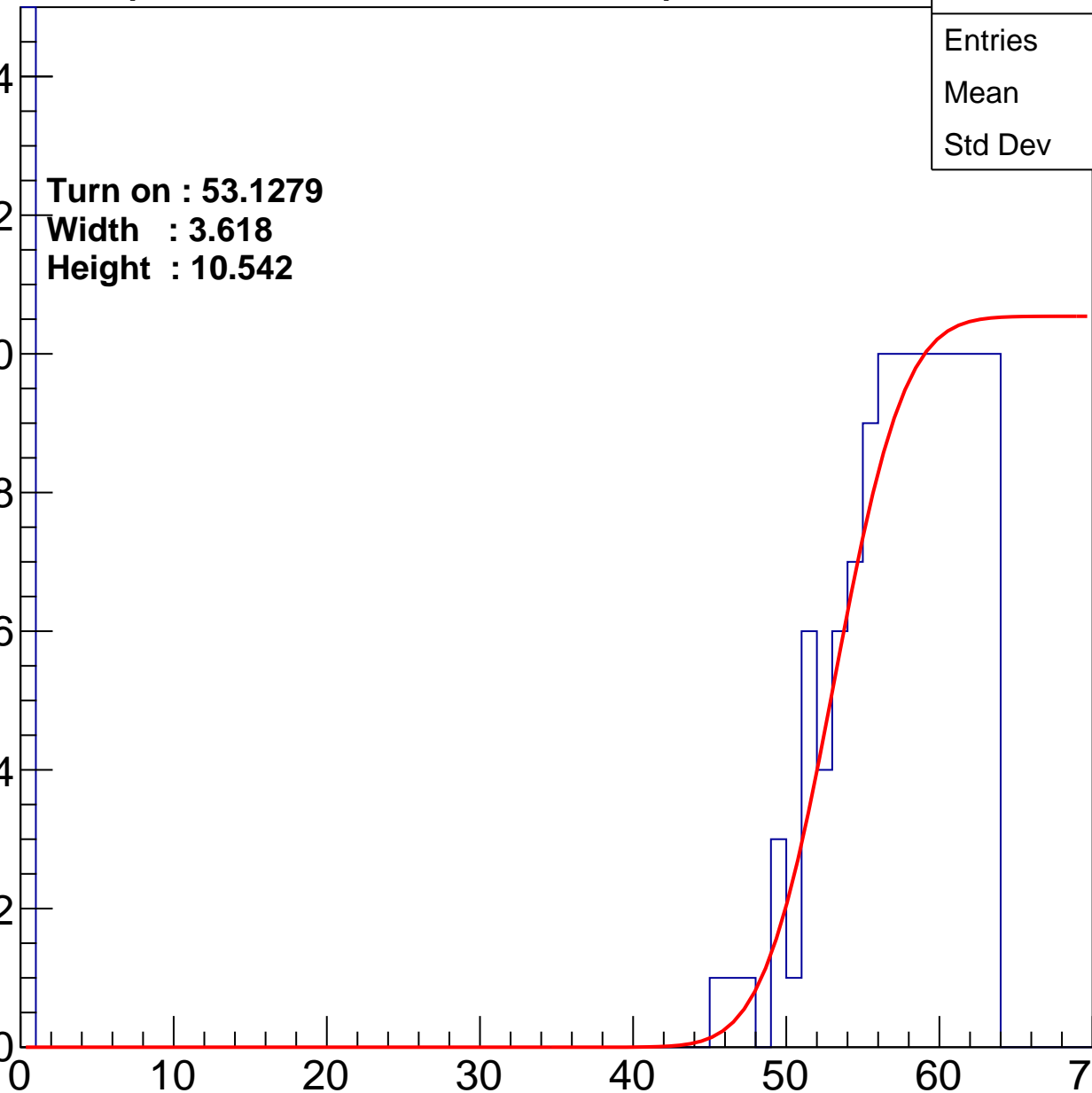
Width : 3.618

Height : 10.542

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch87

calib_packv5_033123_0516.root, FC#4, port A1

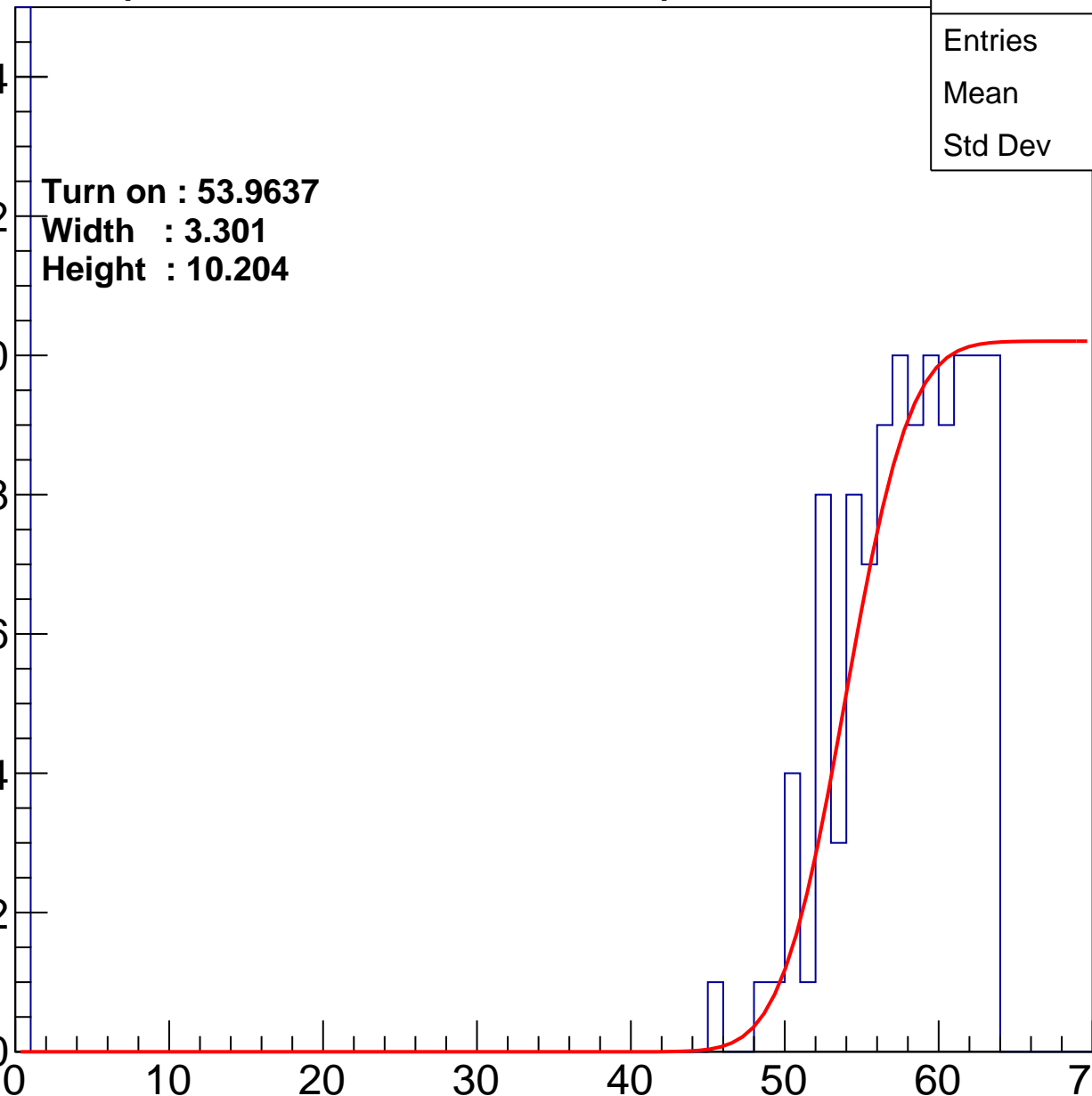
Entry

14
12
10
8
6
4
2
0

Turn on : 53.9637
Width : 3.301
Height : 10.204

Entries	204
Mean	31.23
Std Dev	28.74

ampl



B1L104S, U22-ch88

calib_packv5_033123_0516.root, FC#4, port A1

Entries	221
Mean	32.15
Std Dev	28.35

Turn on : 51.6019

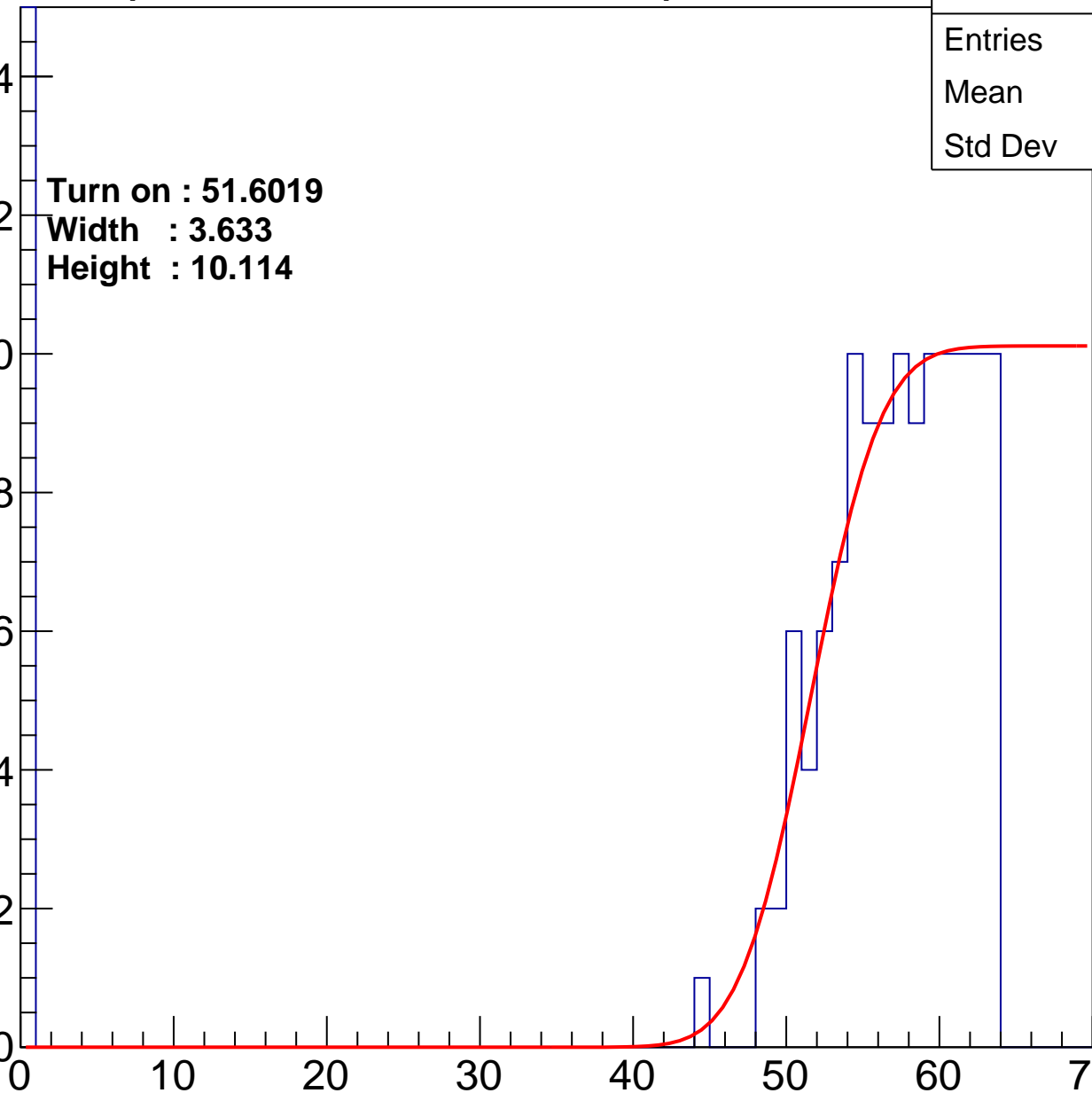
Width : 3.633

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	143
Mean	40.6
Std Dev	26.79

Turn on : 54.2264

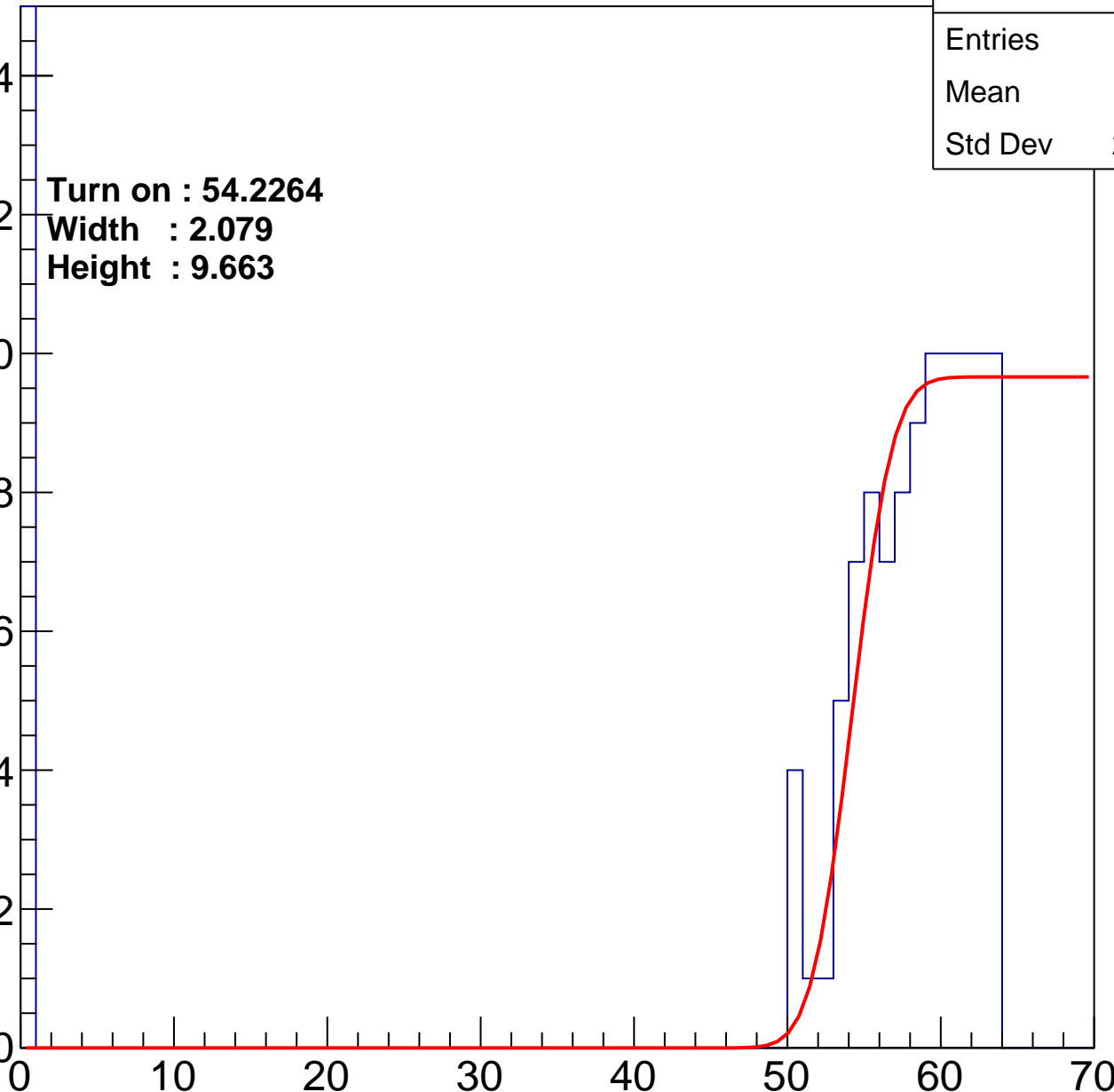
Width : 2.079

Height : 9.663

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch90

calib_packv5_033123_0516.root, FC#4, port A1

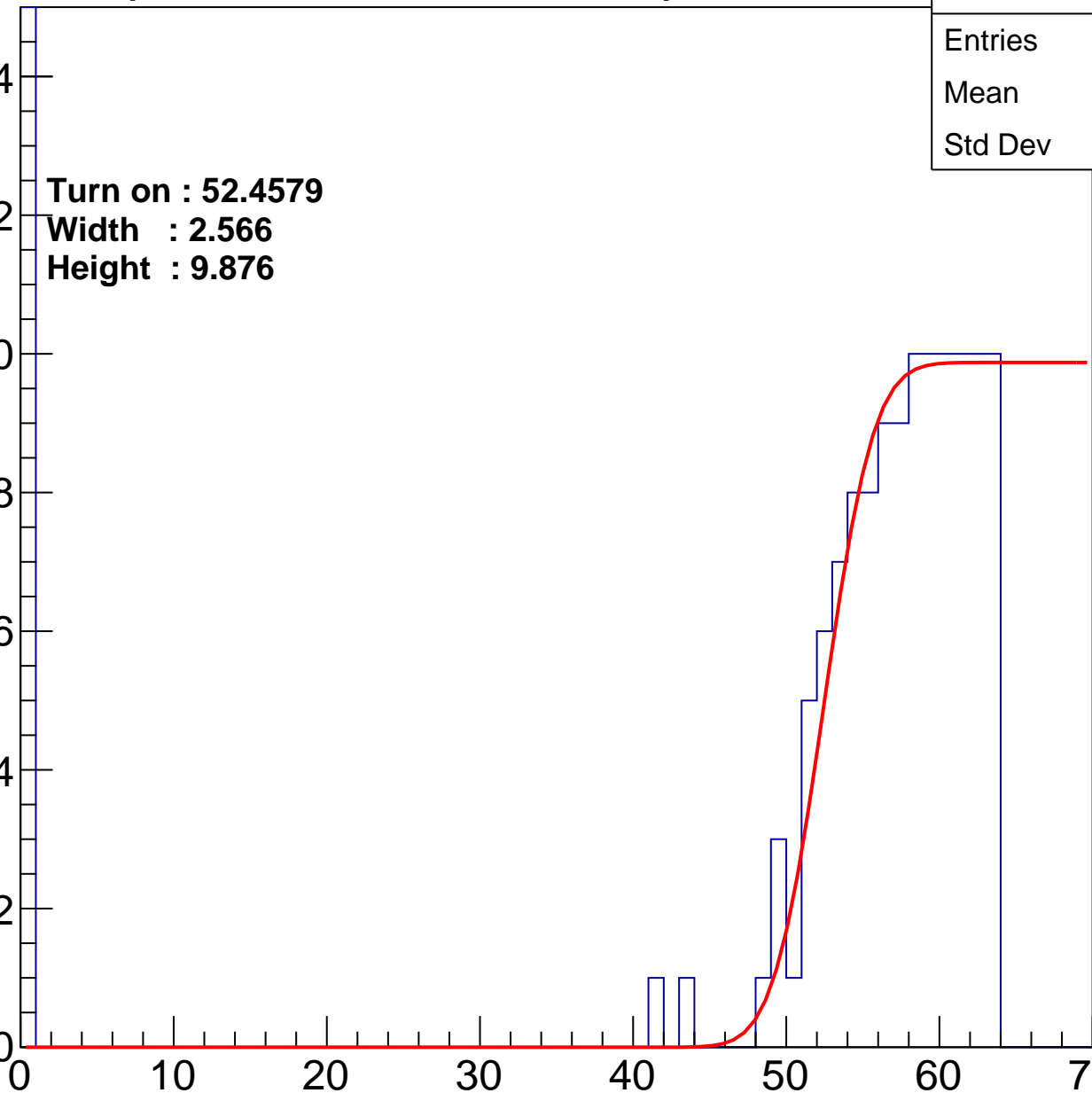
Entry

14
12
10
8
6
4
2
0

Turn on : 52.4579
Width : 2.566
Height : 9.876

Entries	188
Mean	36.1
Std Dev	27.7

ampl



B1L104S, U22-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.57
Std Dev	28.73

Turn on : 53.8318

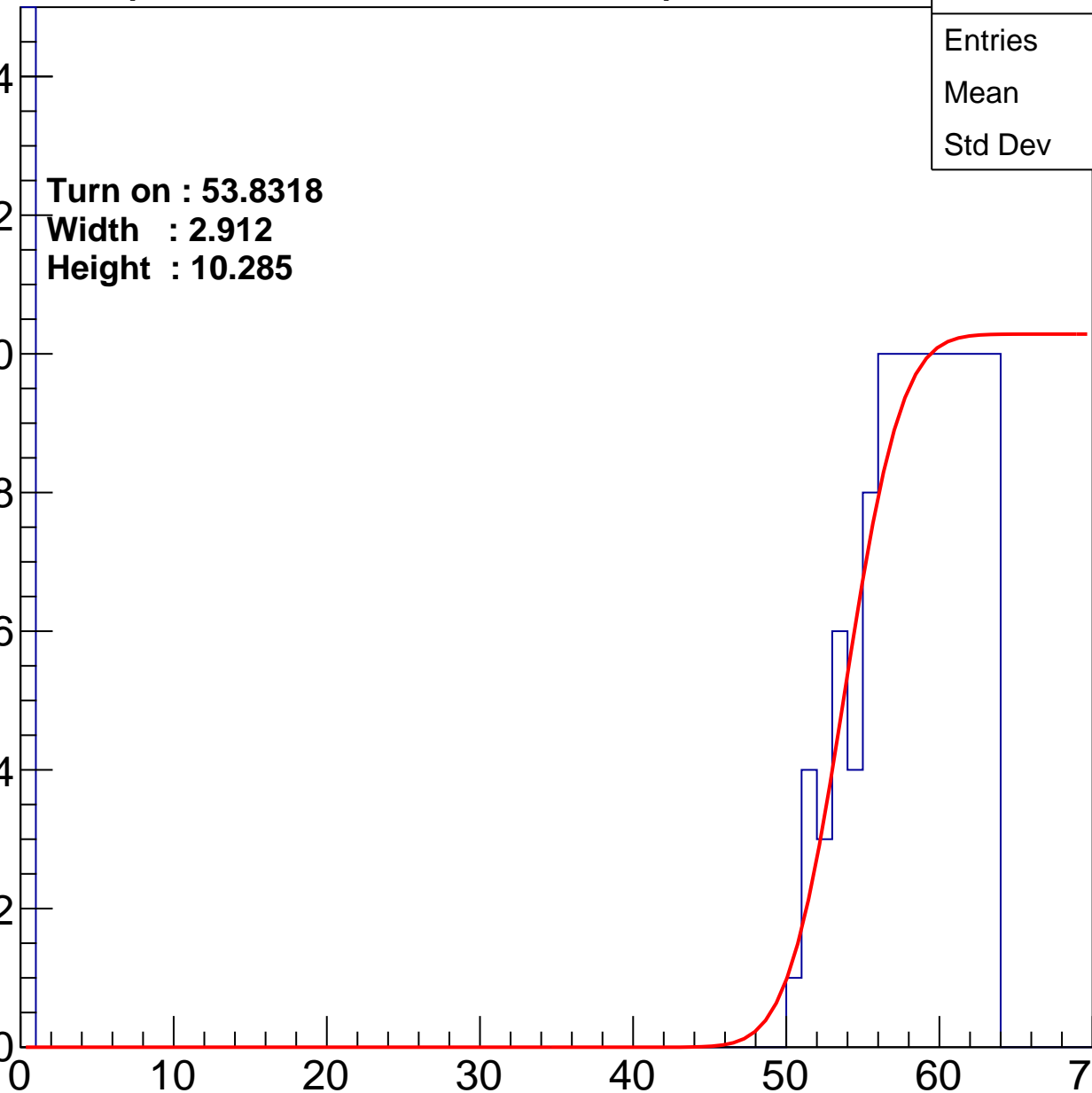
Width : 2.912

Height : 10.285

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch92

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	30.61
Std Dev	28.84

Turn on : 53.5965

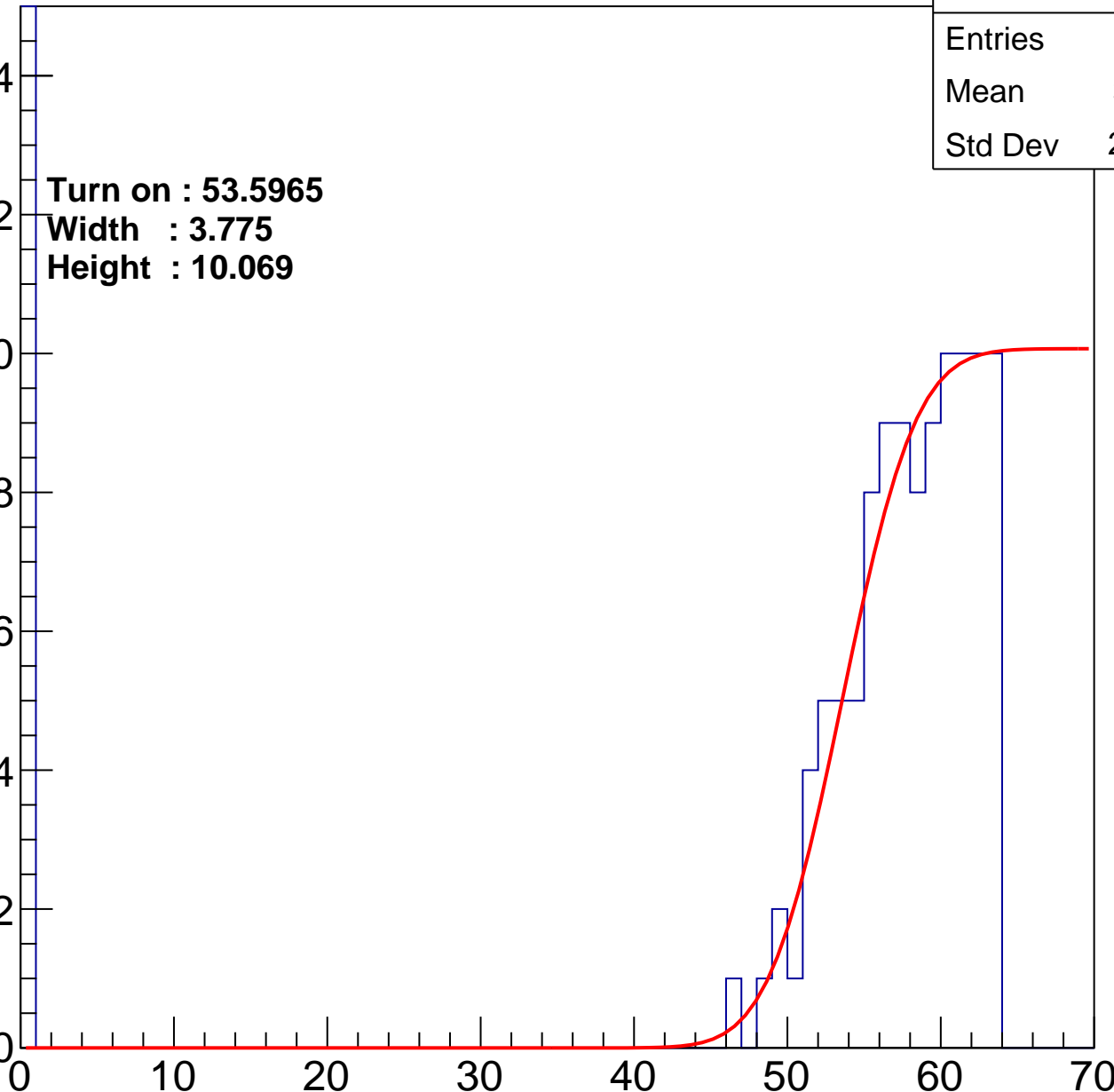
Width : 3.775

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	34.62
Std Dev	28.43

Turn on : 54.5685

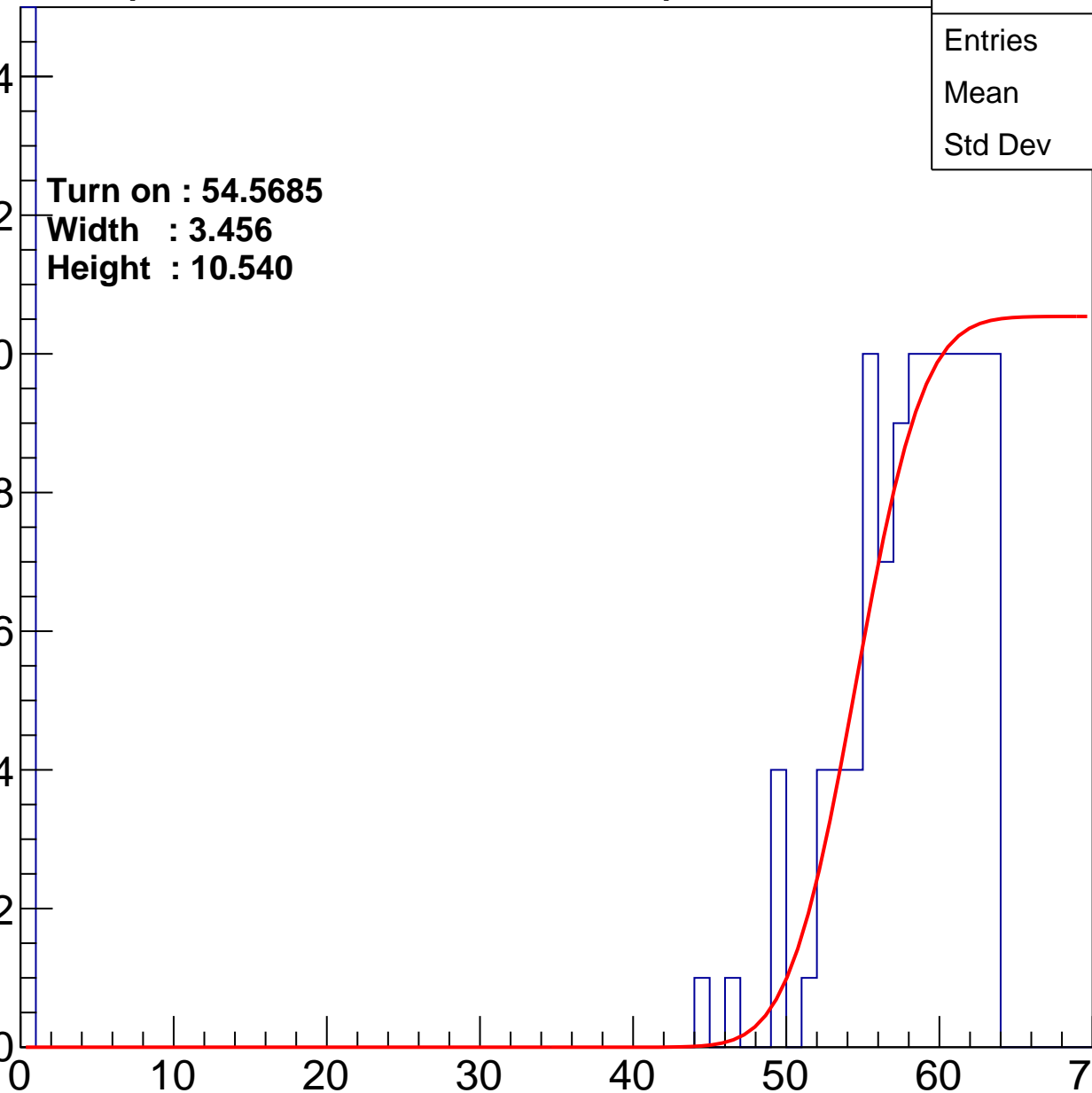
Width : 3.456

Height : 10.540

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	34.85
Std Dev	28.35

Turn on : 54.2123

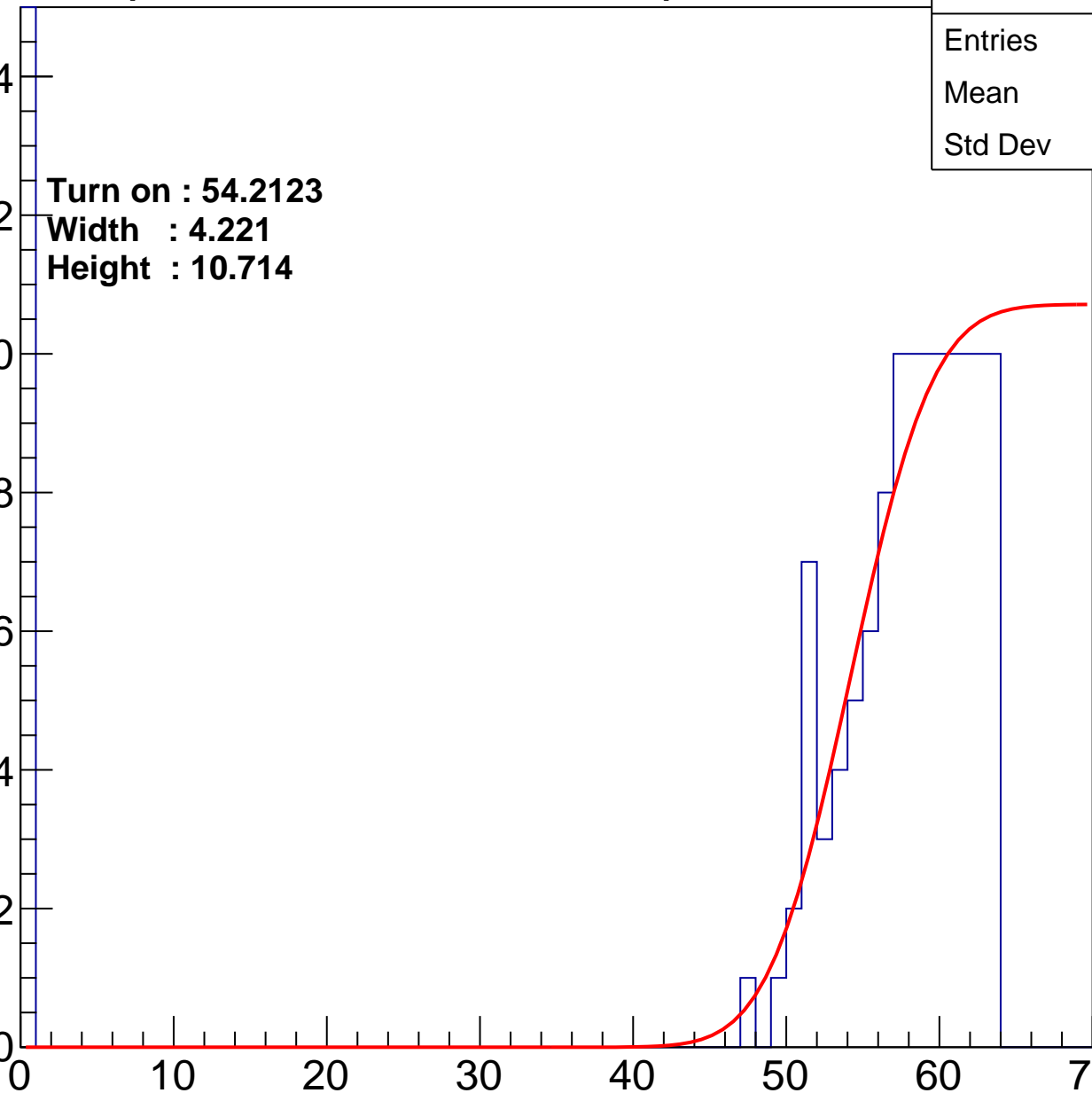
Width : 4.221

Height : 10.714

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch95

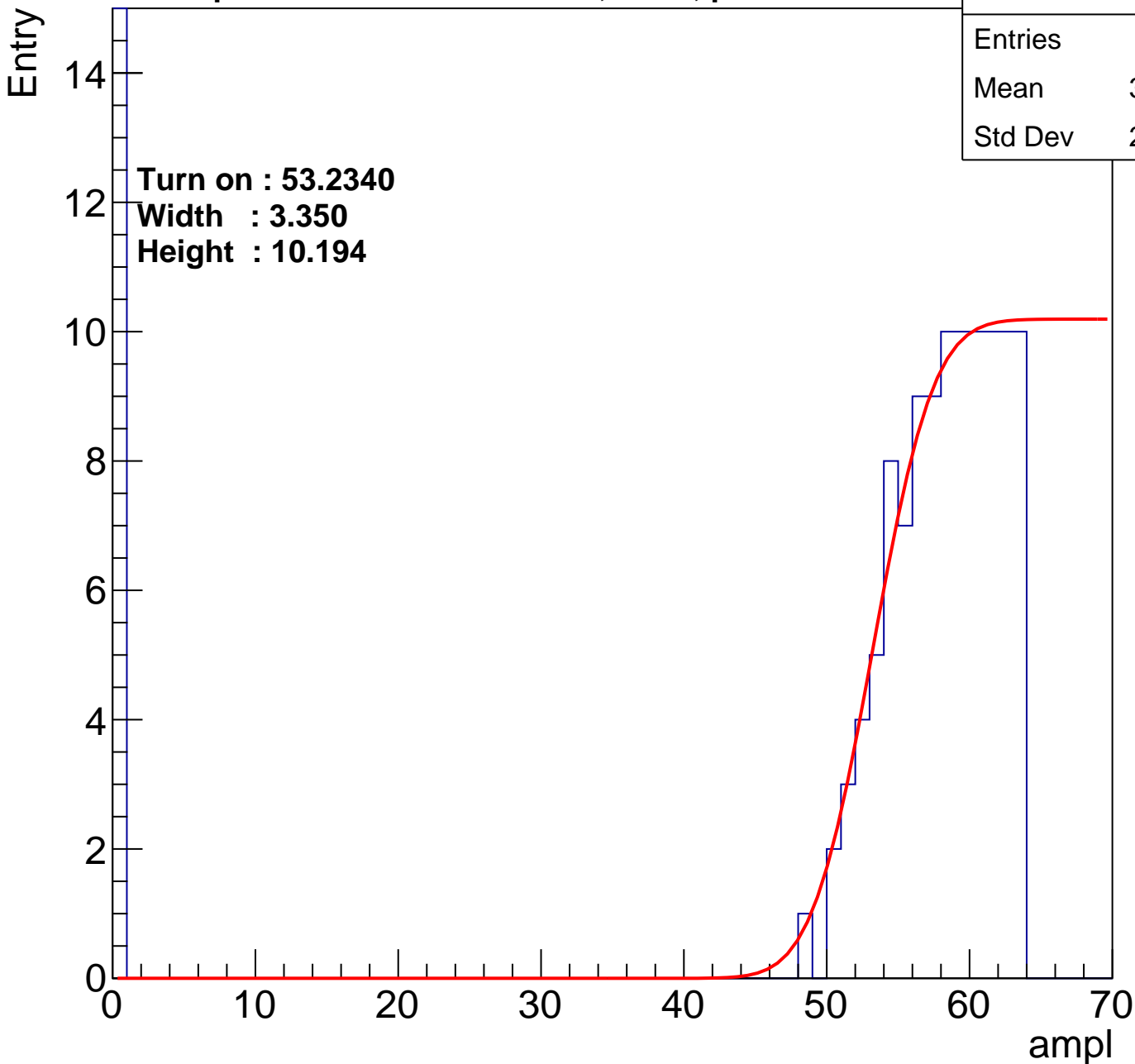
calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	35.65
Std Dev	28.22

Turn on : 53.2340

Width : 3.350

Height : 10.194



B1L104S, U22-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	30.53
Std Dev	29.09

Turn on : 53.4998

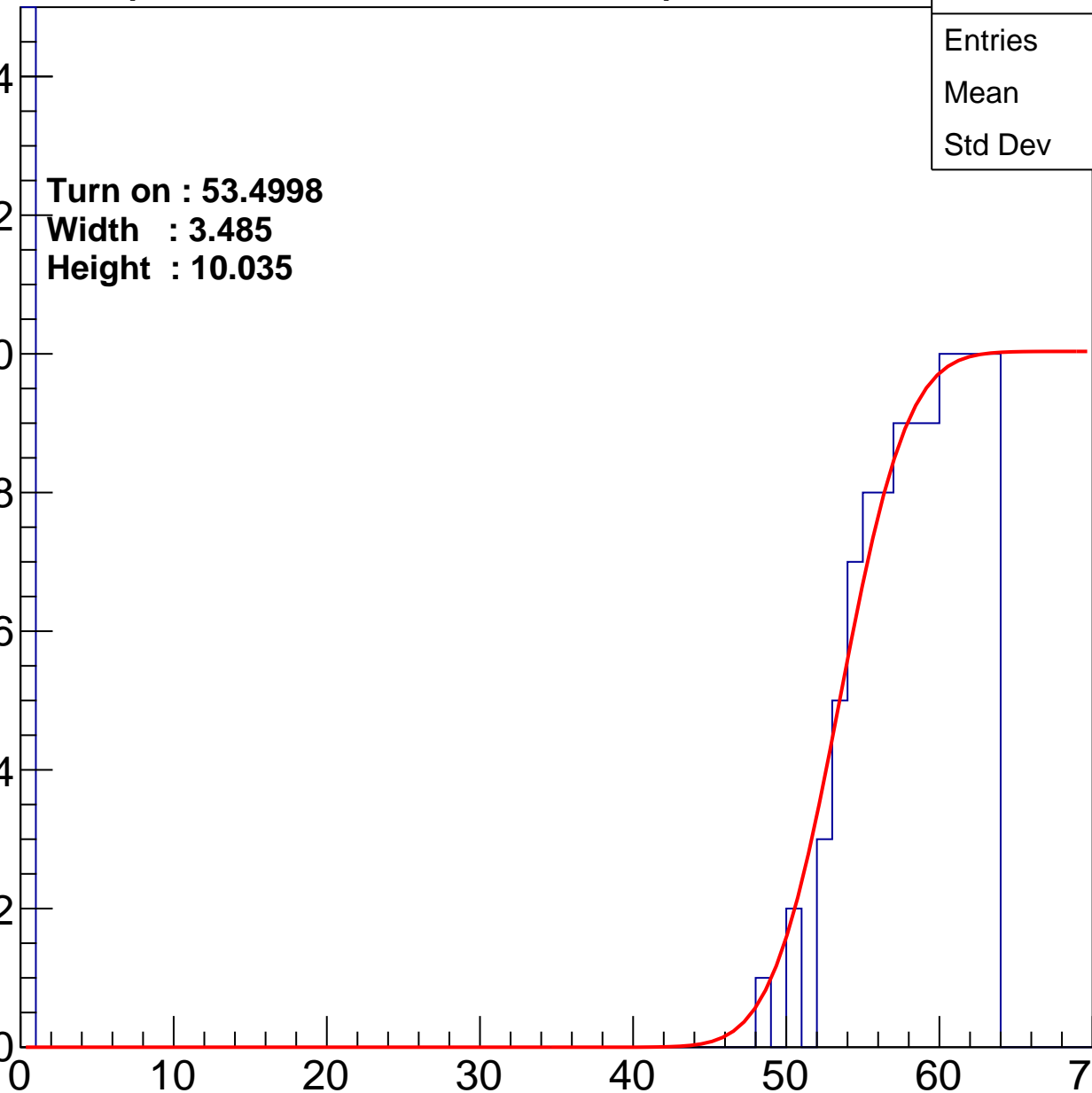
Width : 3.485

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	40.24
Std Dev	26.24

Turn on : 52.6566

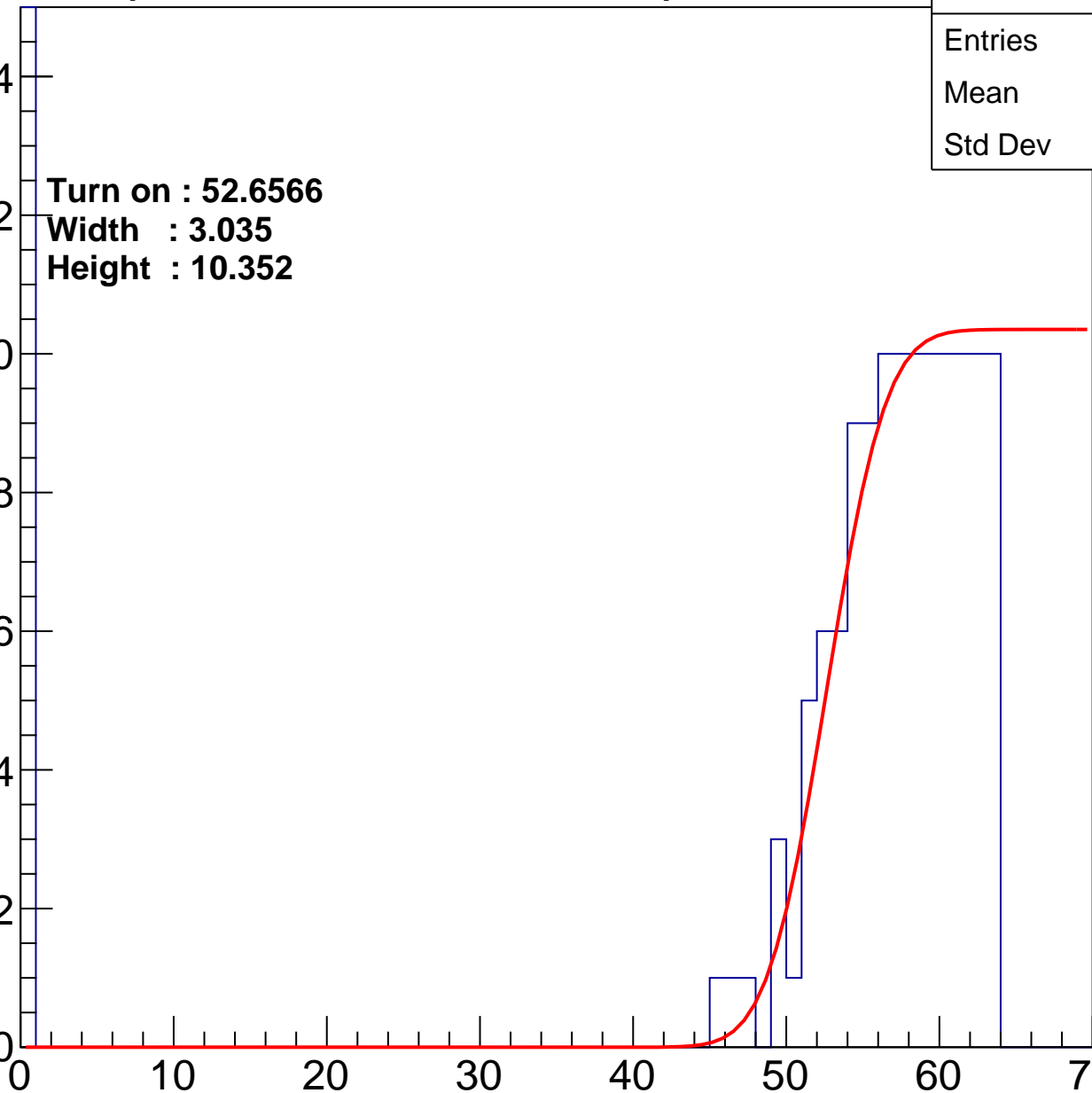
Width : 3.035

Height : 10.352

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch98

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	33.03
Std Dev	28.45

Turn on : 52.2885

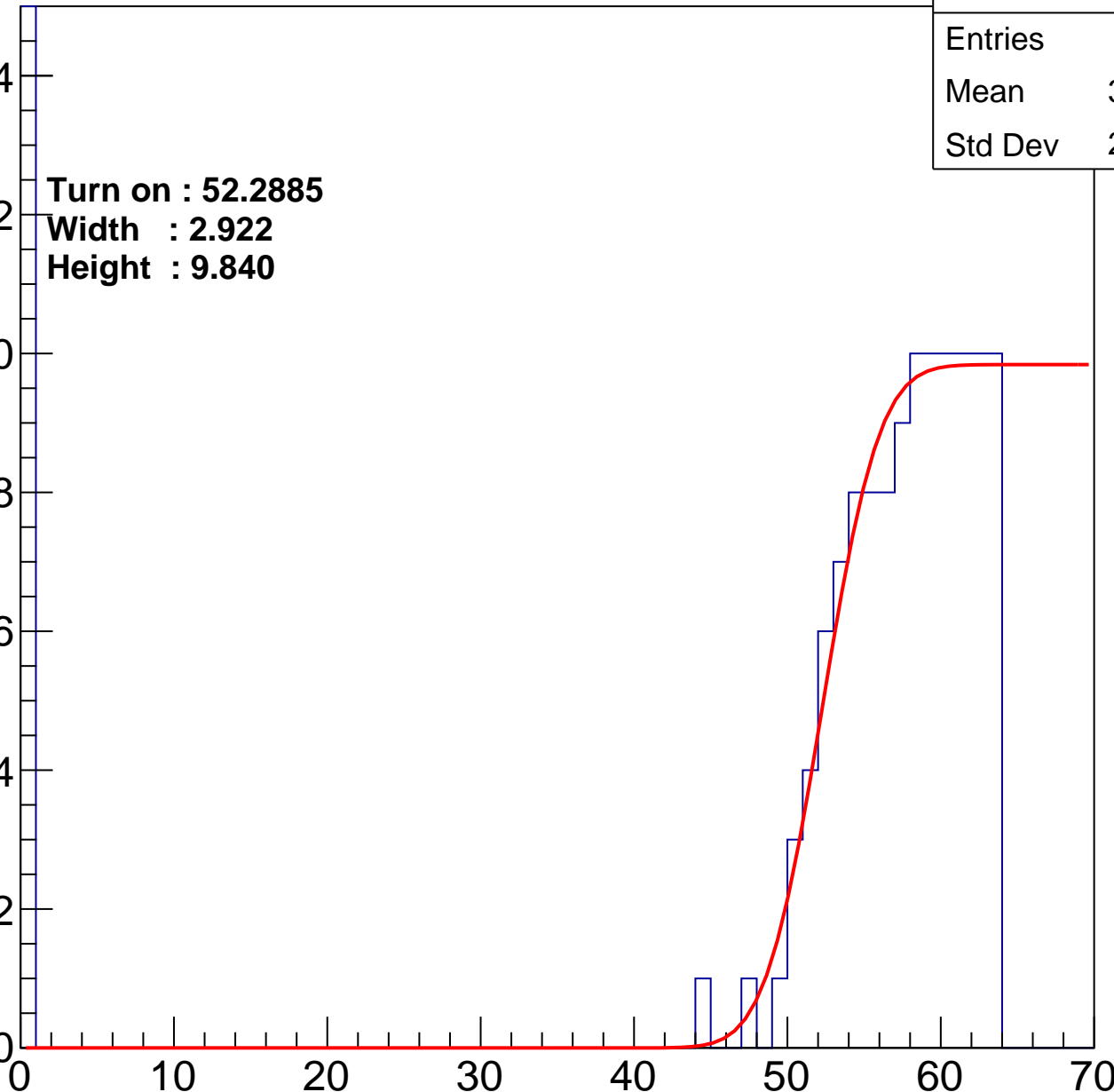
Width : 2.922

Height : 9.840

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	227
Mean	27.87
Std Dev	28.88

Turn on : 53.2113

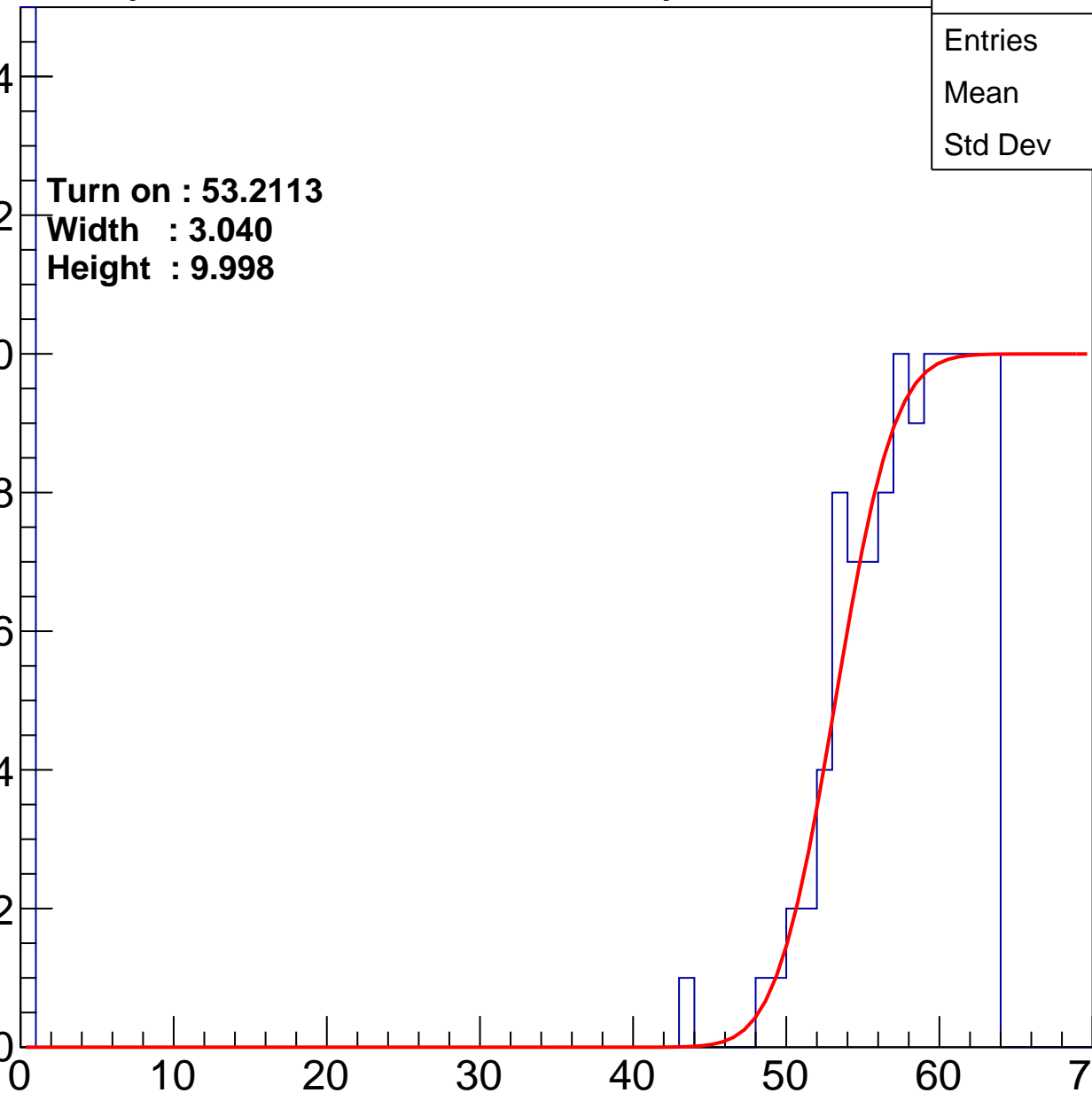
Width : 3.040

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch100

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	33.14
Std Dev	28.35

Turn on : 52.7282

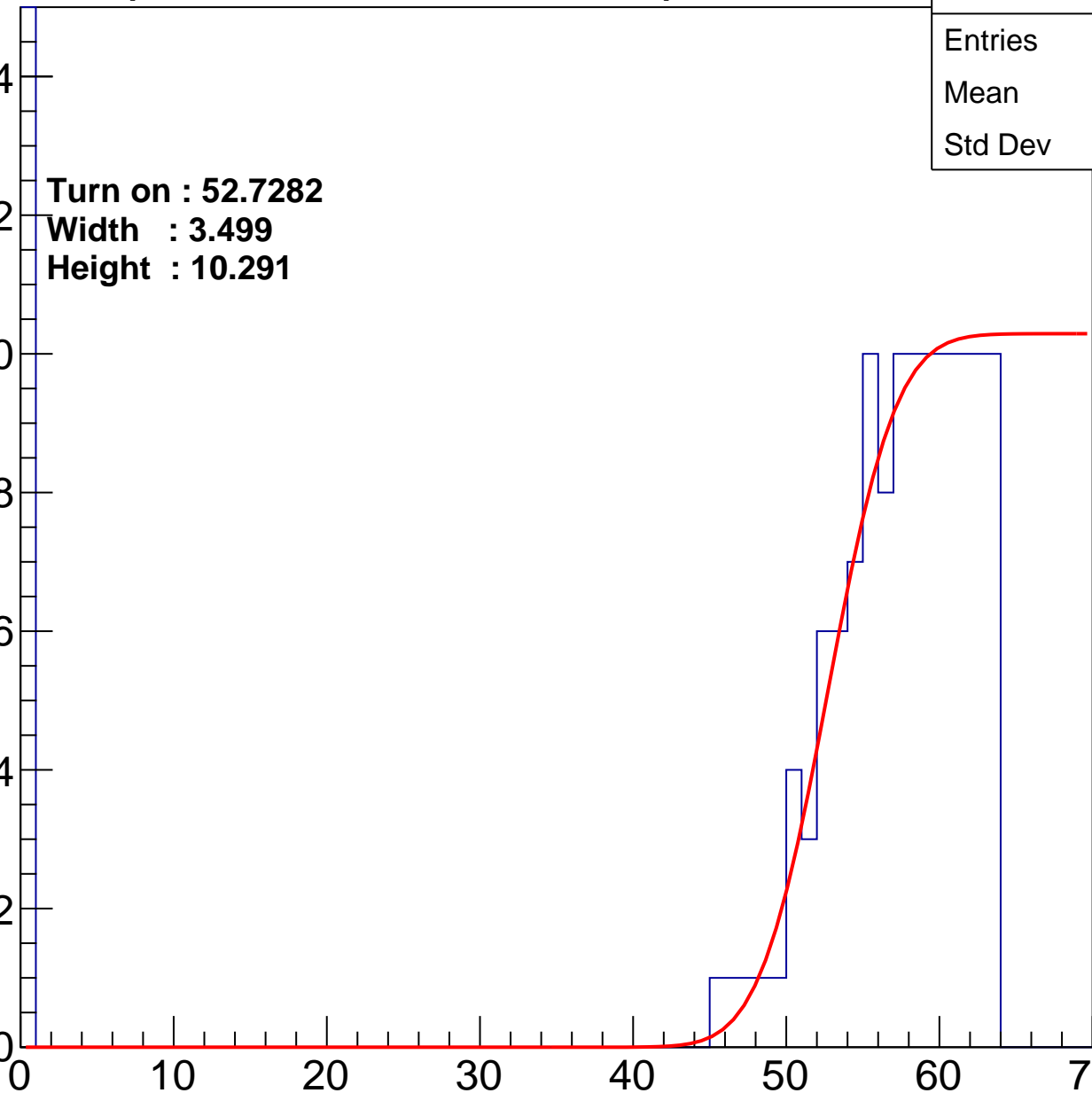
Width : 3.499

Height : 10.291

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	27.86
Std Dev	29.29

Turn on : 55.1127

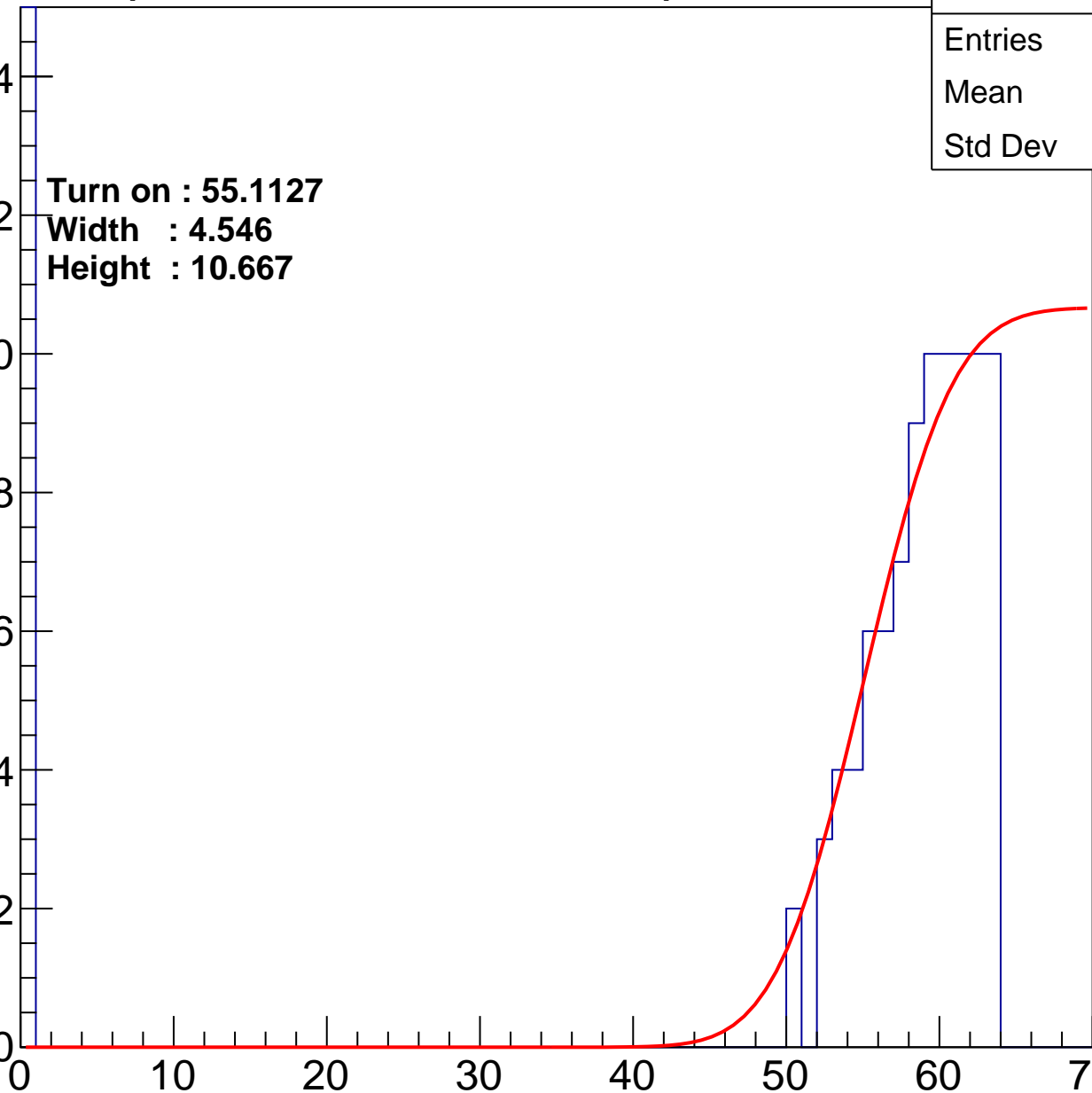
Width : 4.546

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	31.55
Std Dev	28.79

Turn on : 54.2667

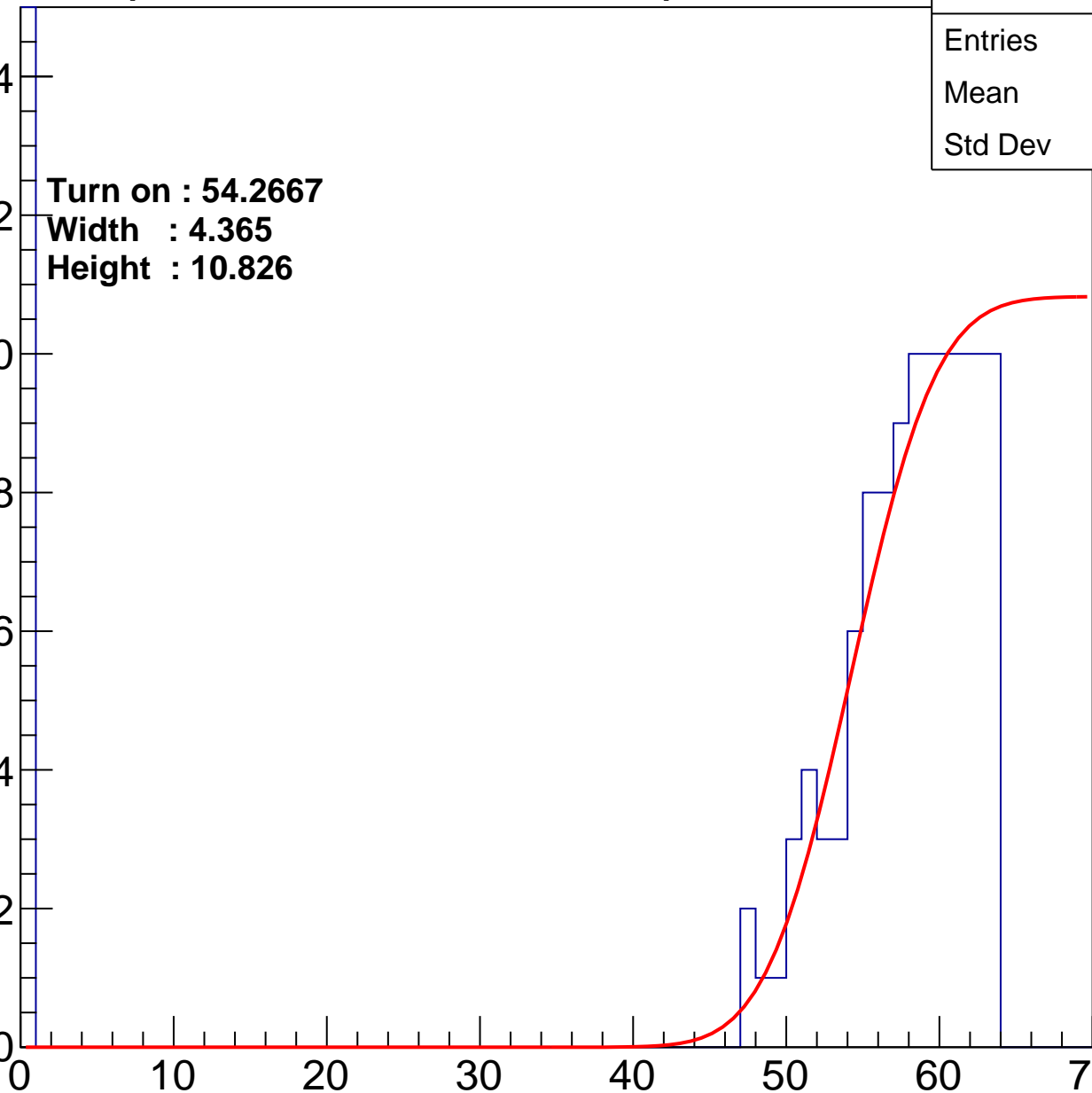
Width : 4.365

Height : 10.826

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch103

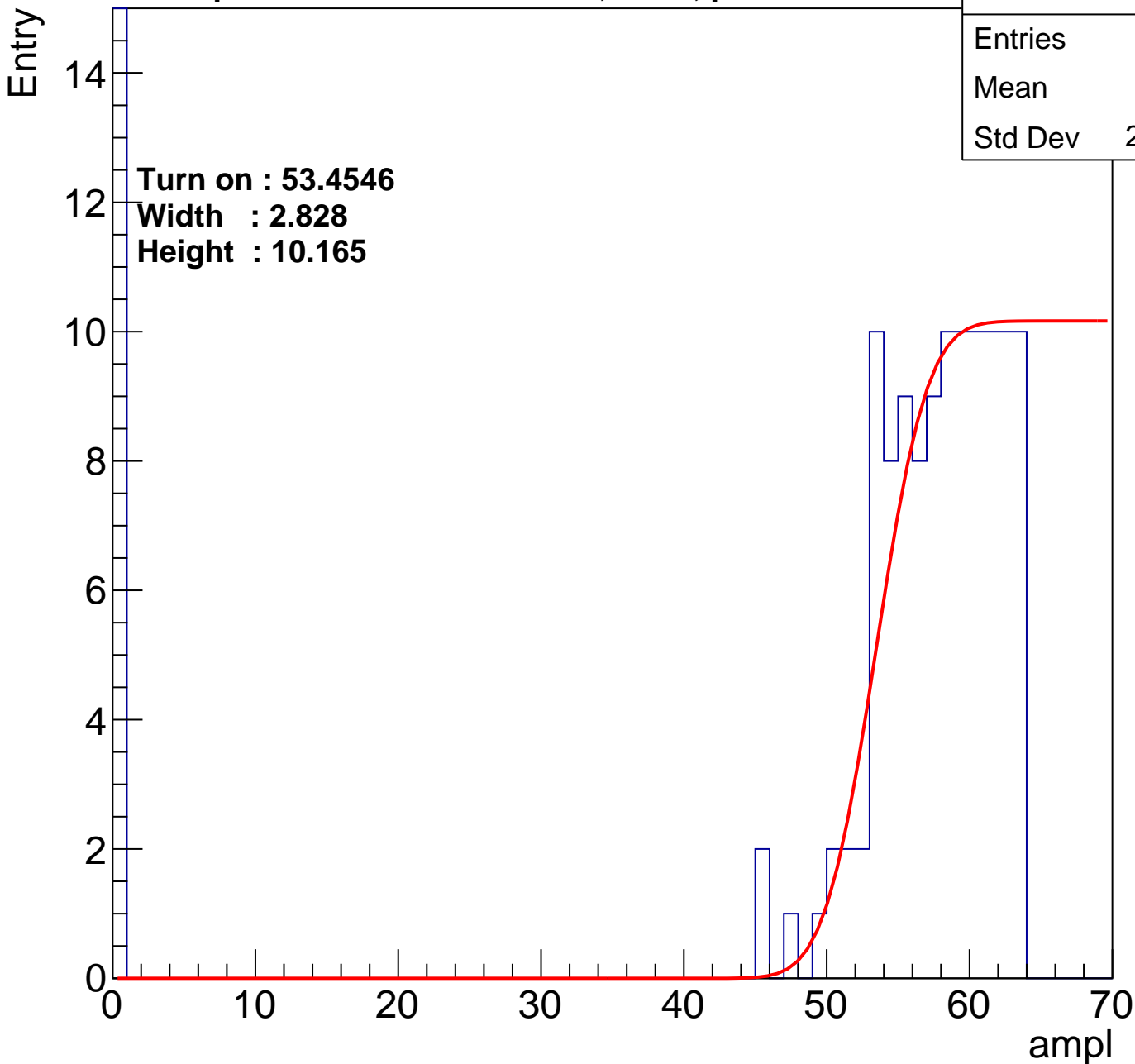
calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	31
Std Dev	28.74

Turn on : 53.4546

Width : 2.828

Height : 10.165



B1L104S, U22-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	28.92
Std Dev	28.92

Turn on : 55.1759

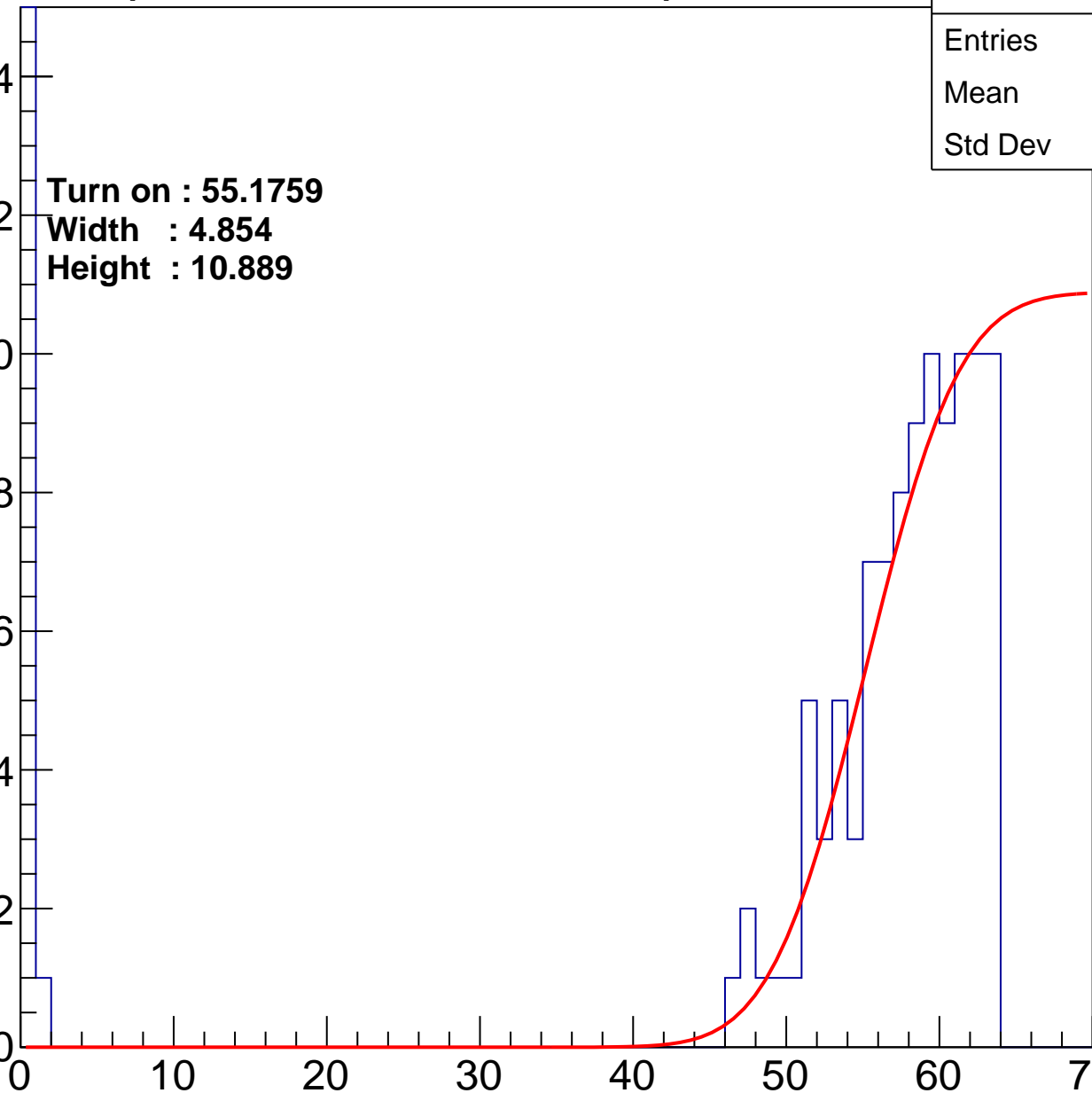
Width : 4.854

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.22
Std Dev	28.94

Turn on : 54.9226

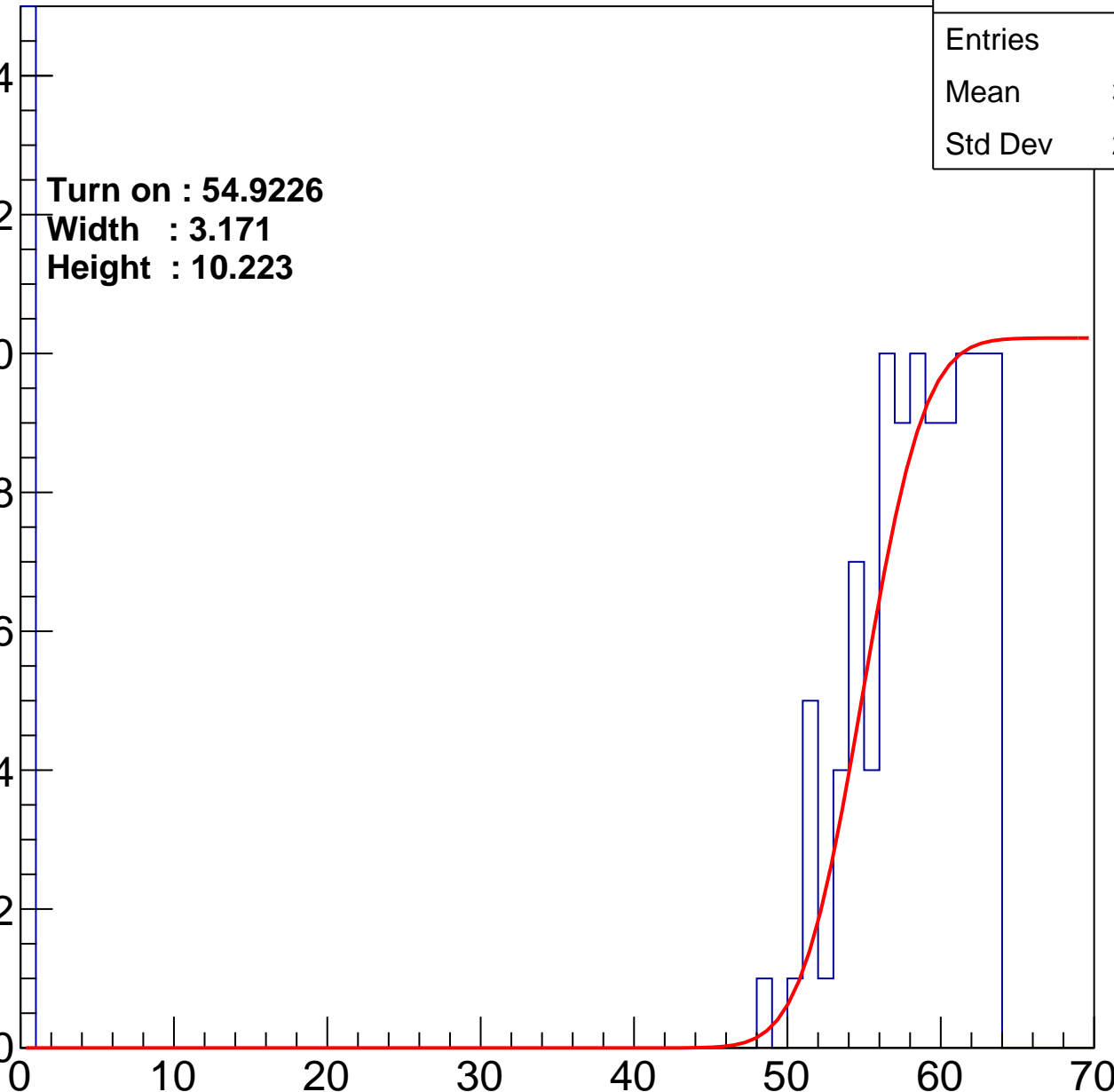
Width : 3.171

Height : 10.223

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	31.38
Std Dev	28.63

Turn on : 52.5756

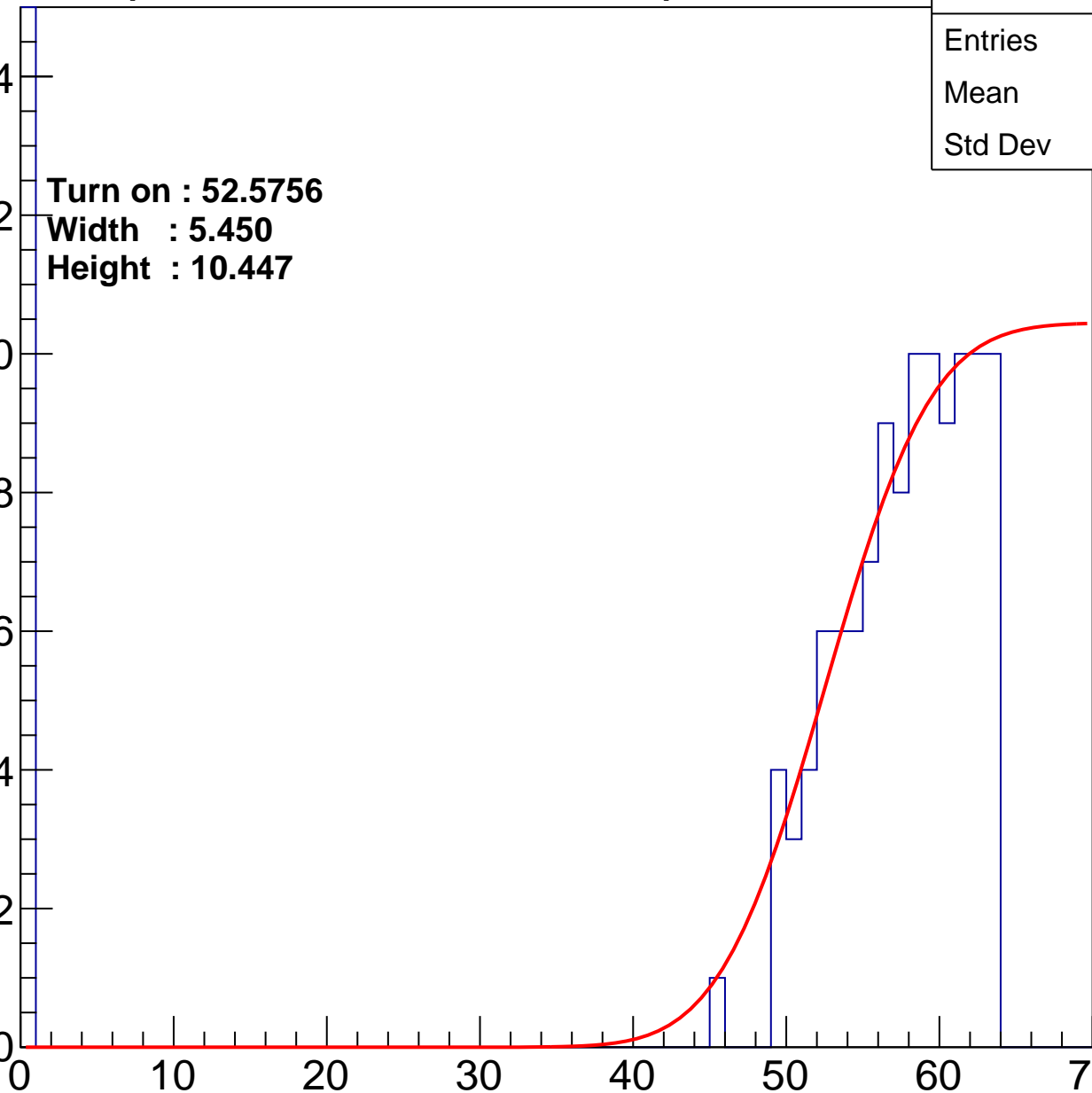
Width : 5.450

Height : 10.447

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch107

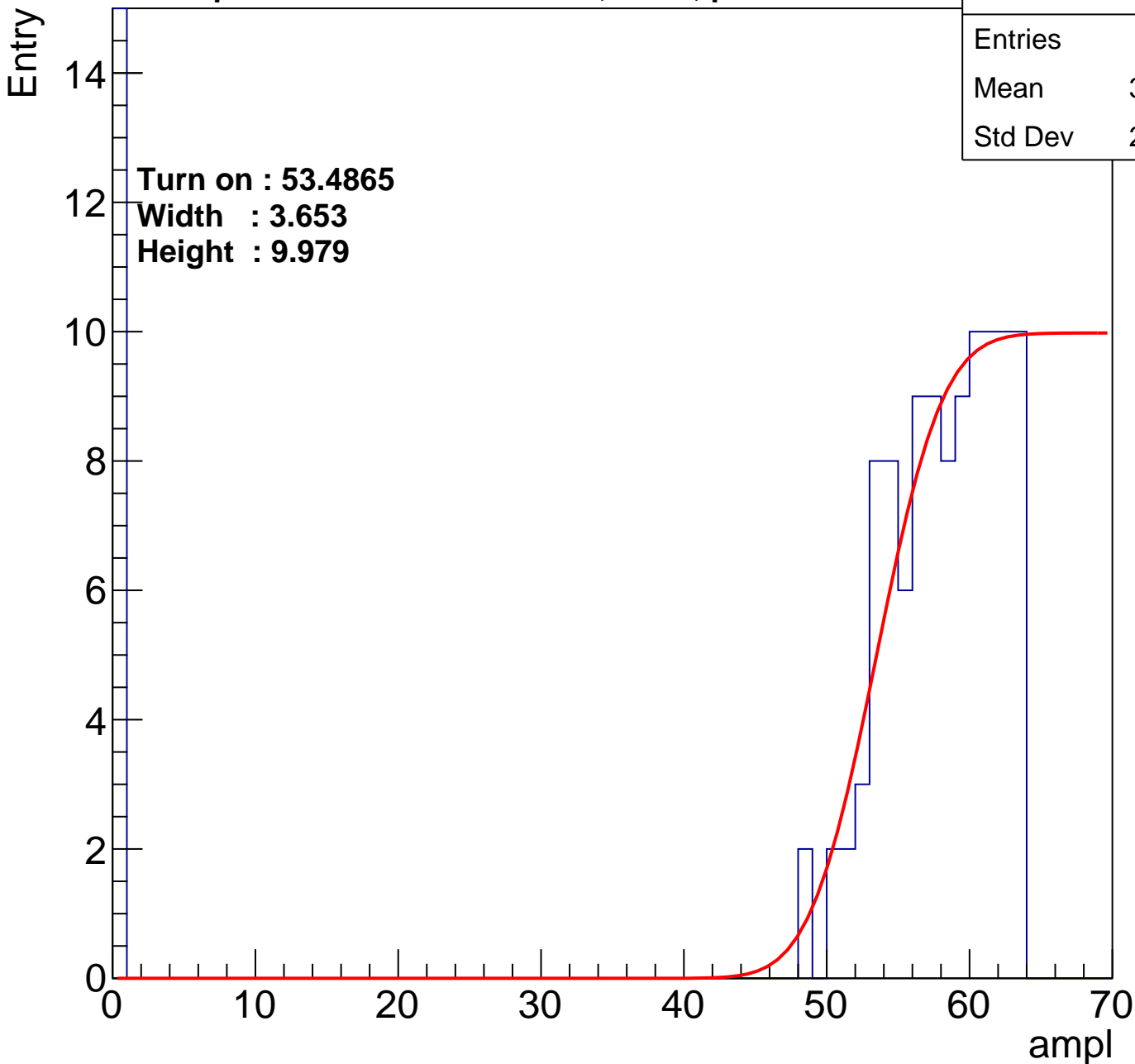
calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	35.33
Std Dev	28.24

Turn on : 53.4865

Width : 3.653

Height : 9.979



B1L104S, U22-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	30.17
Std Dev	29.09

Turn on : 54.5318

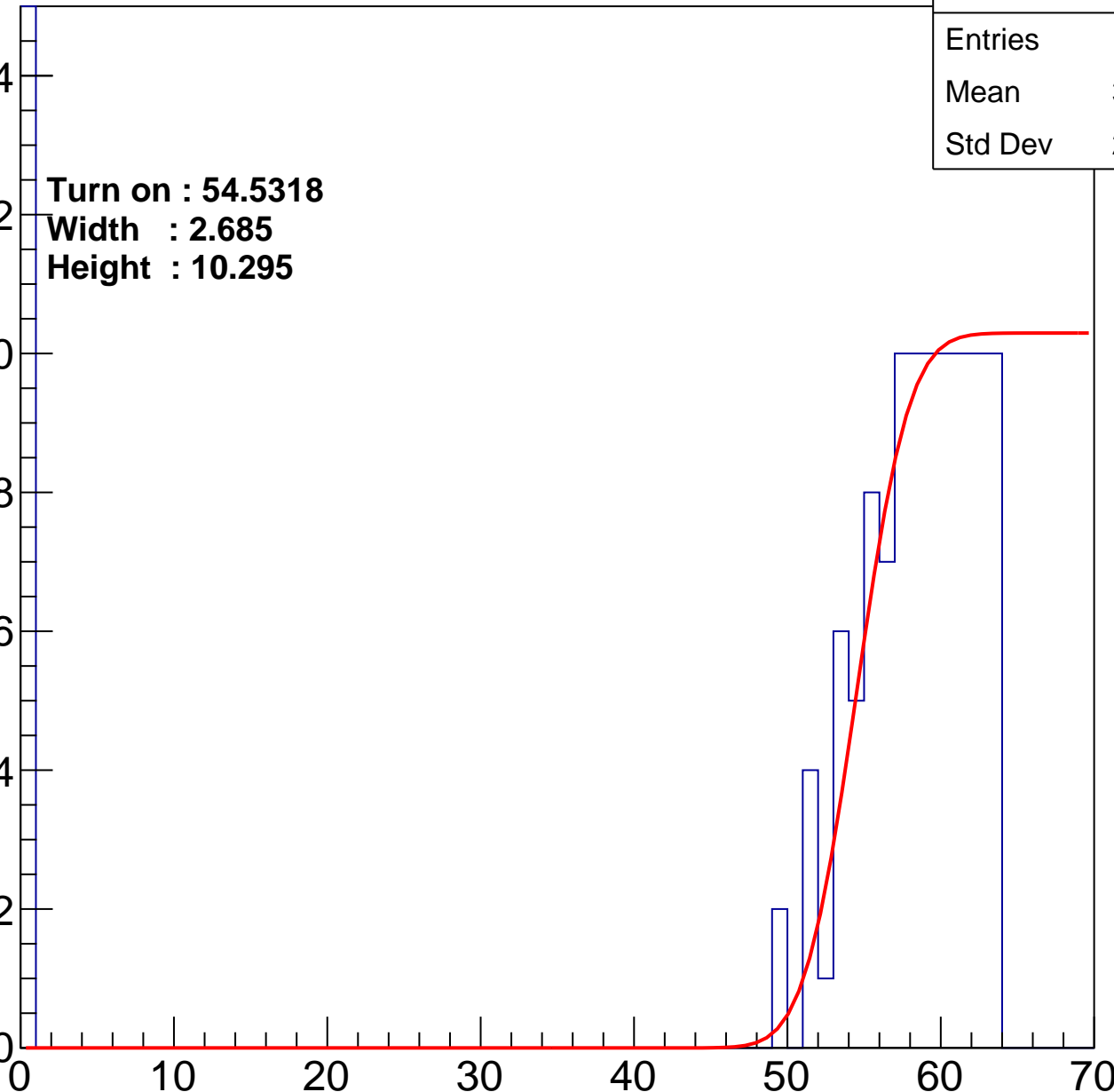
Width : 2.685

Height : 10.295

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch109

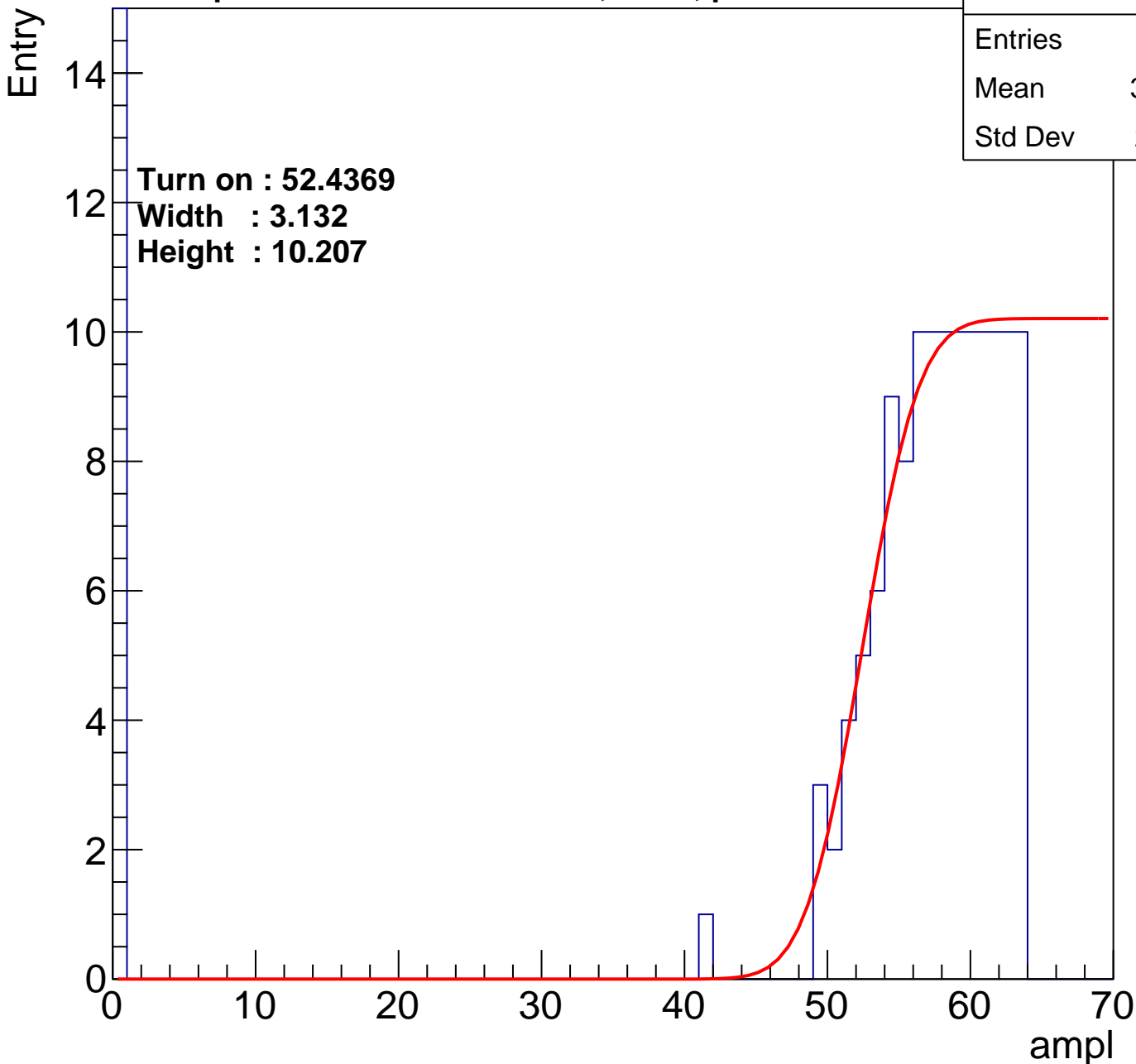
calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	35.75
Std Dev	27.91

Turn on : 52.4369

Width : 3.132

Height : 10.207



B1L104S, U22-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	35.42
Std Dev	27.83

Turn on : 52.4928

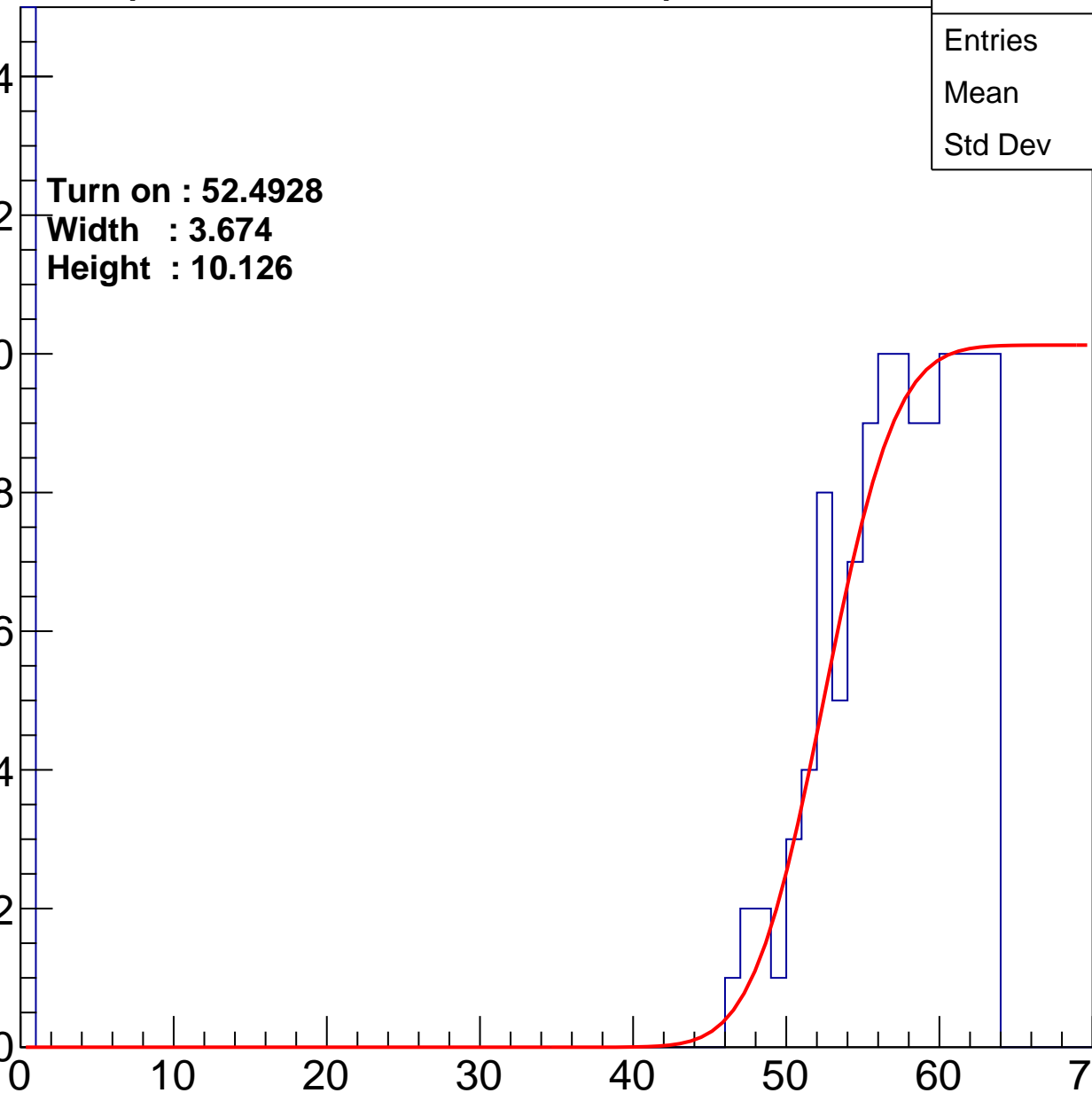
Width : 3.674

Height : 10.126

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch111

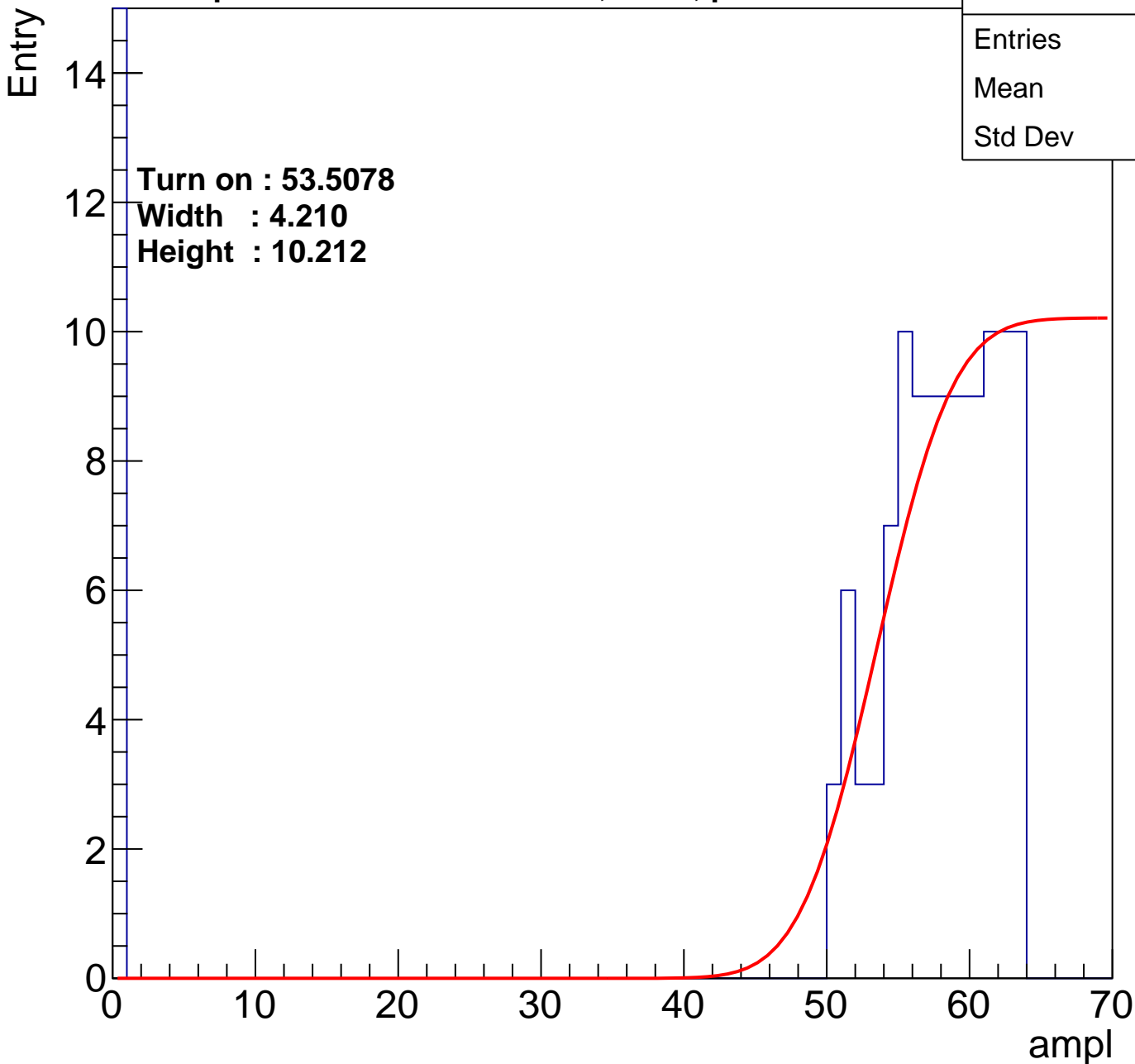
calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.8
Std Dev	28.8

Turn on : 53.5078

Width : 4.210

Height : 10.212



B1L104S, U22-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	31.77
Std Dev	28.56

Turn on : 53.7705

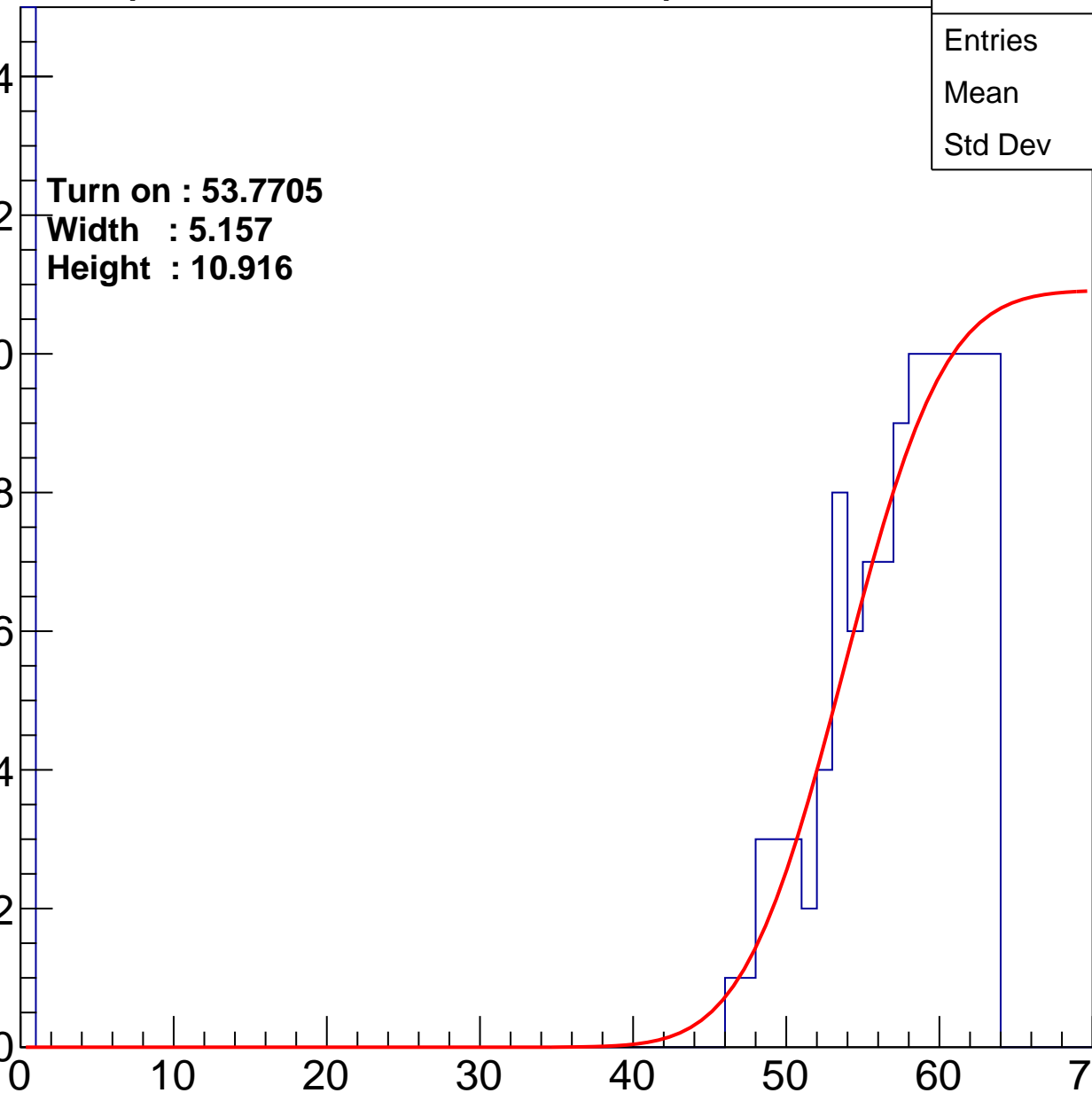
Width : 5.157

Height : 10.916

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	34.57
Std Dev	28.44

Turn on : 53.9607

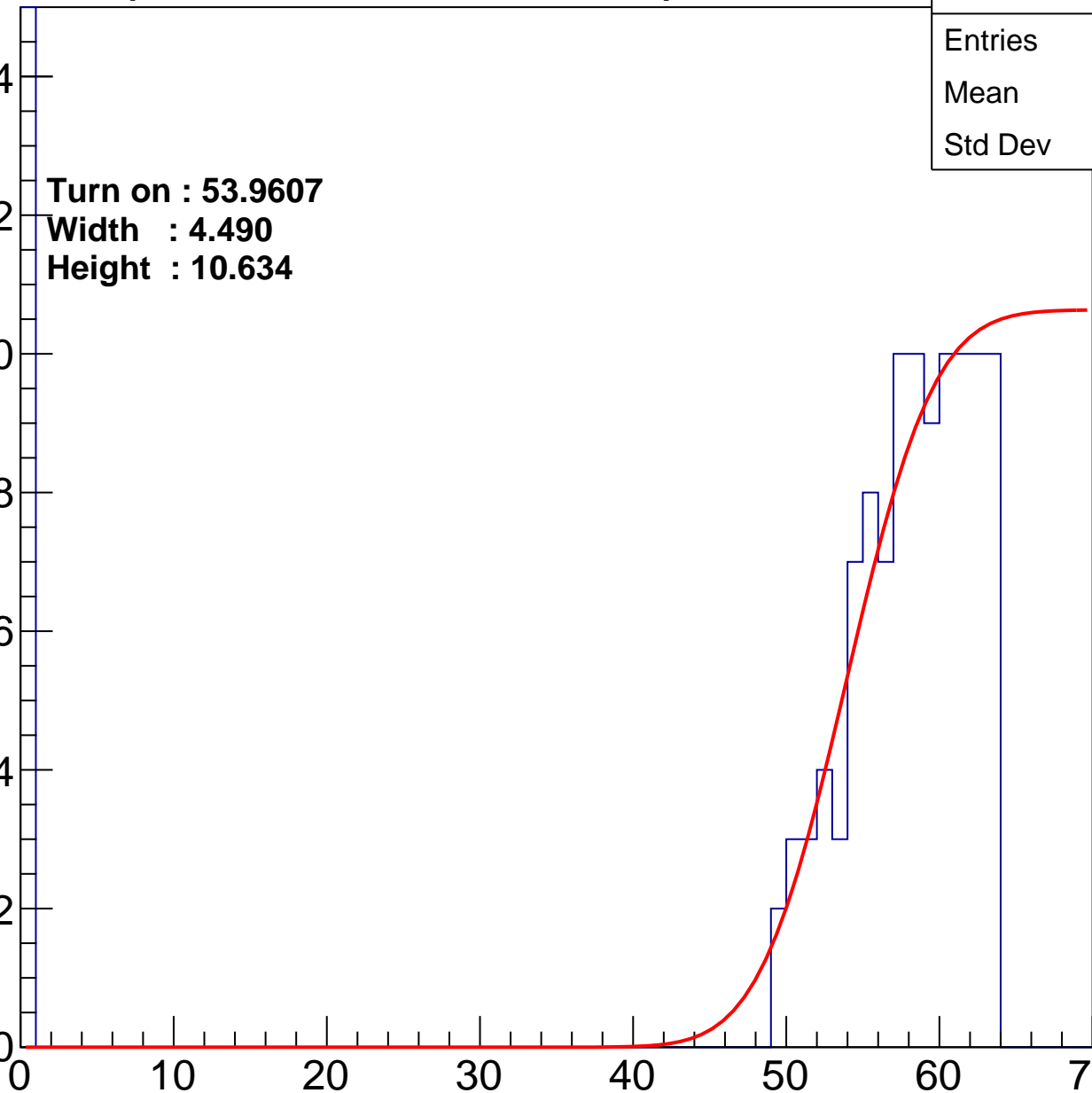
Width : 4.490

Height : 10.634

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	35.1
Std Dev	27.98

Turn on : 52.5352

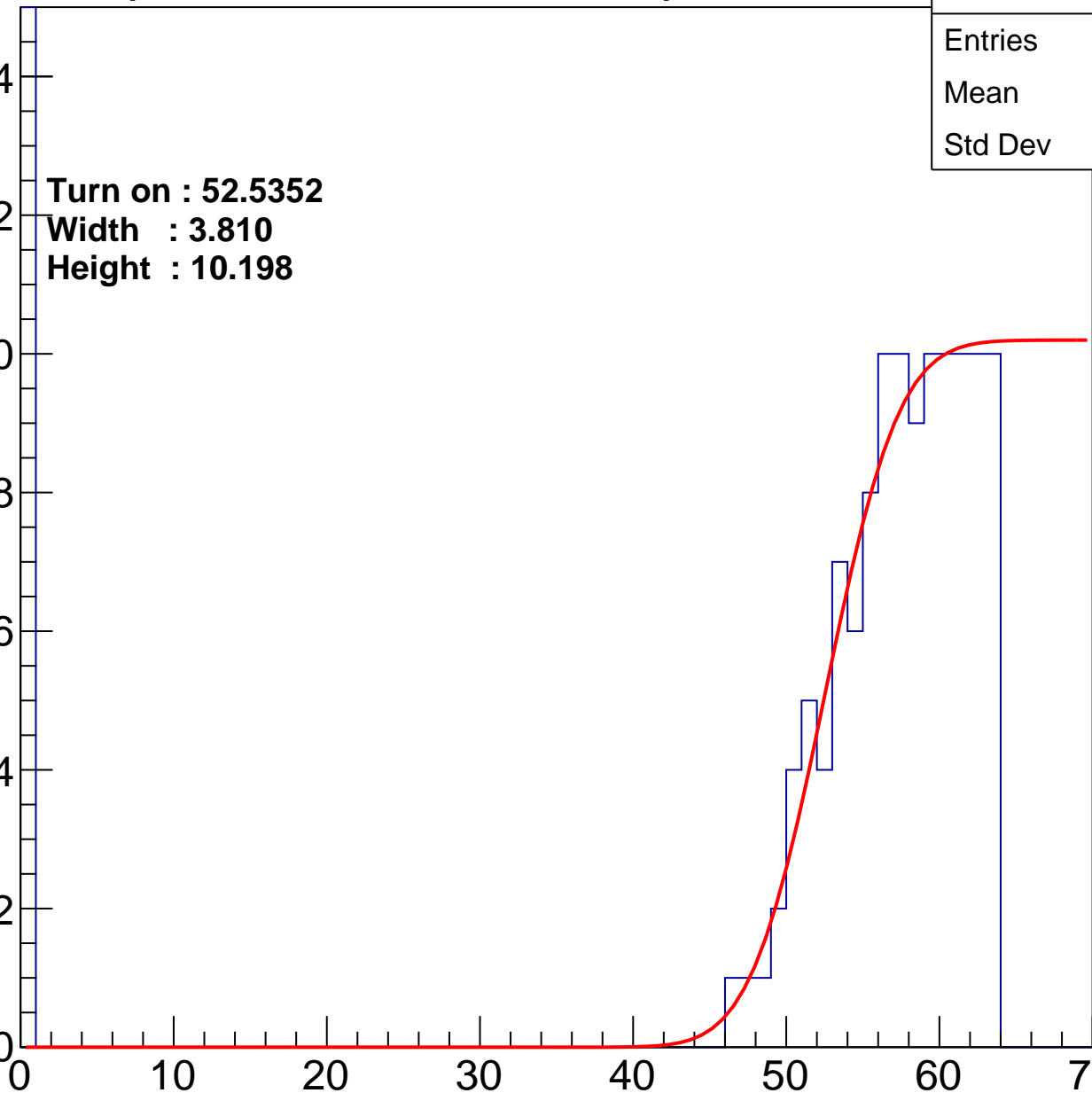
Width : 3.810

Height : 10.198

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	30.62
Std Dev	28.87

Turn on : 52.8561

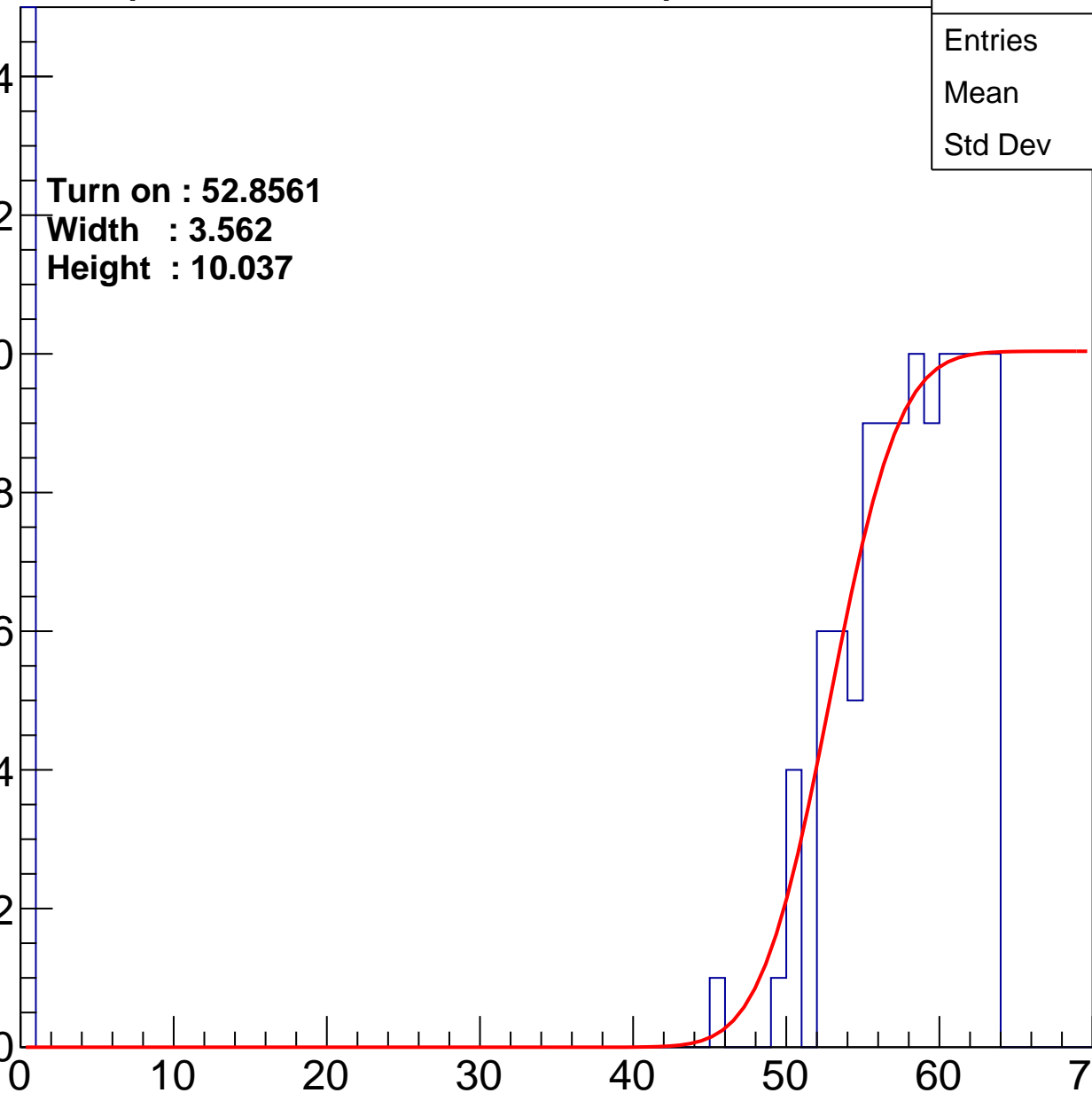
Width : 3.562

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	33.16
Std Dev	28.16

Turn on : 51.8739

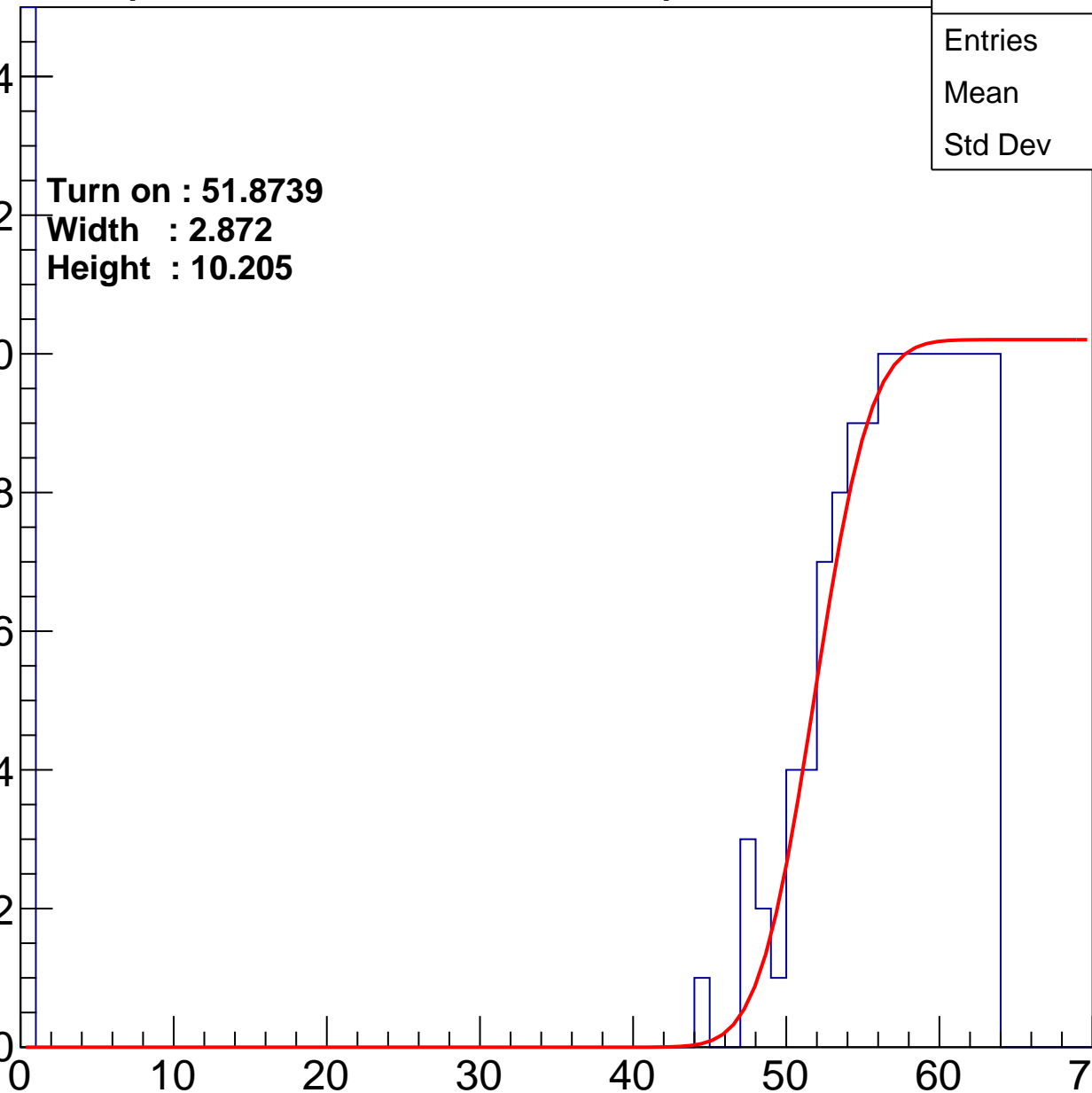
Width : 2.872

Height : 10.205

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	33.79
Std Dev	28.74

Turn on : 54.1191

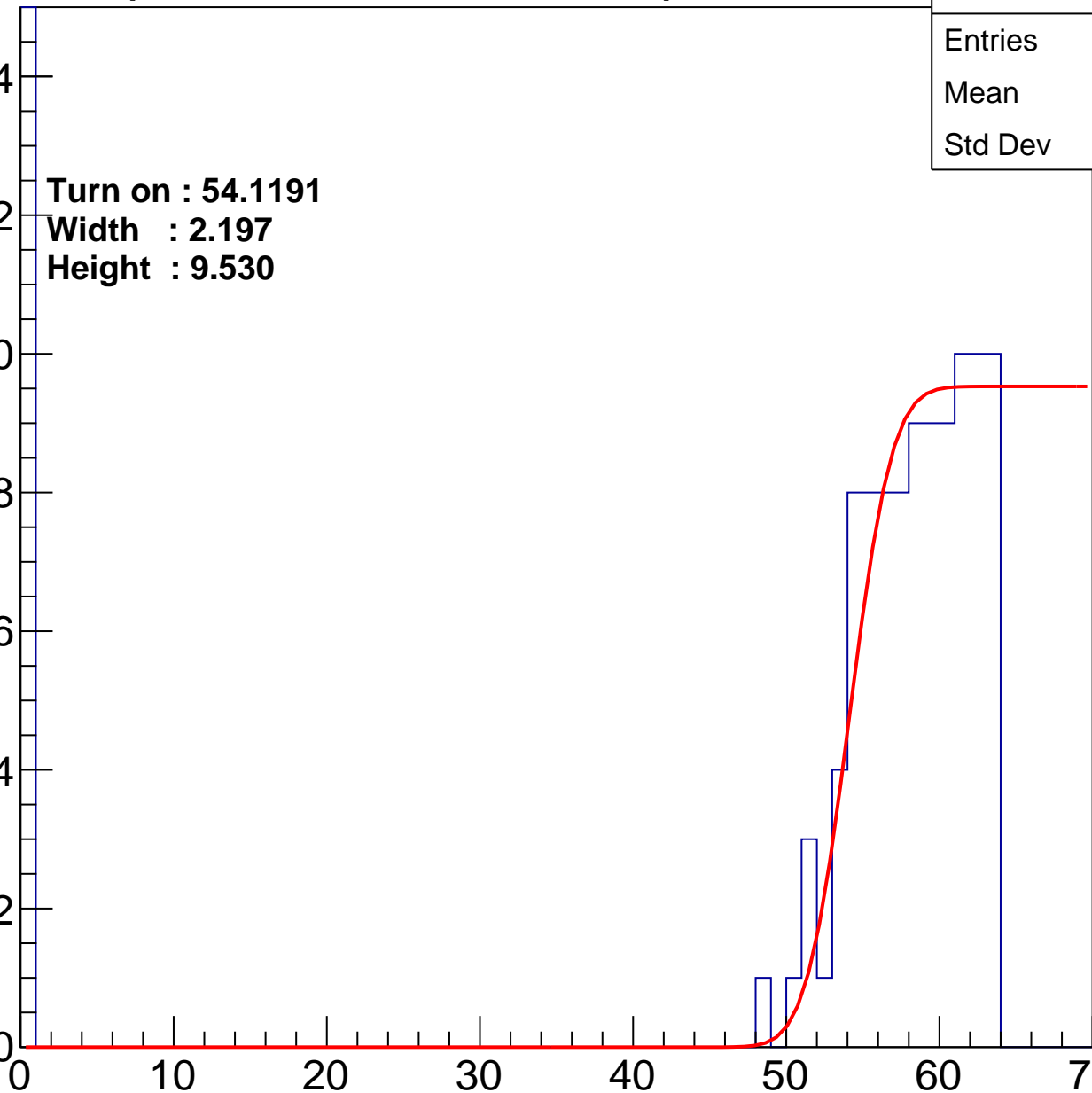
Width : 2.197

Height : 9.530

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch118

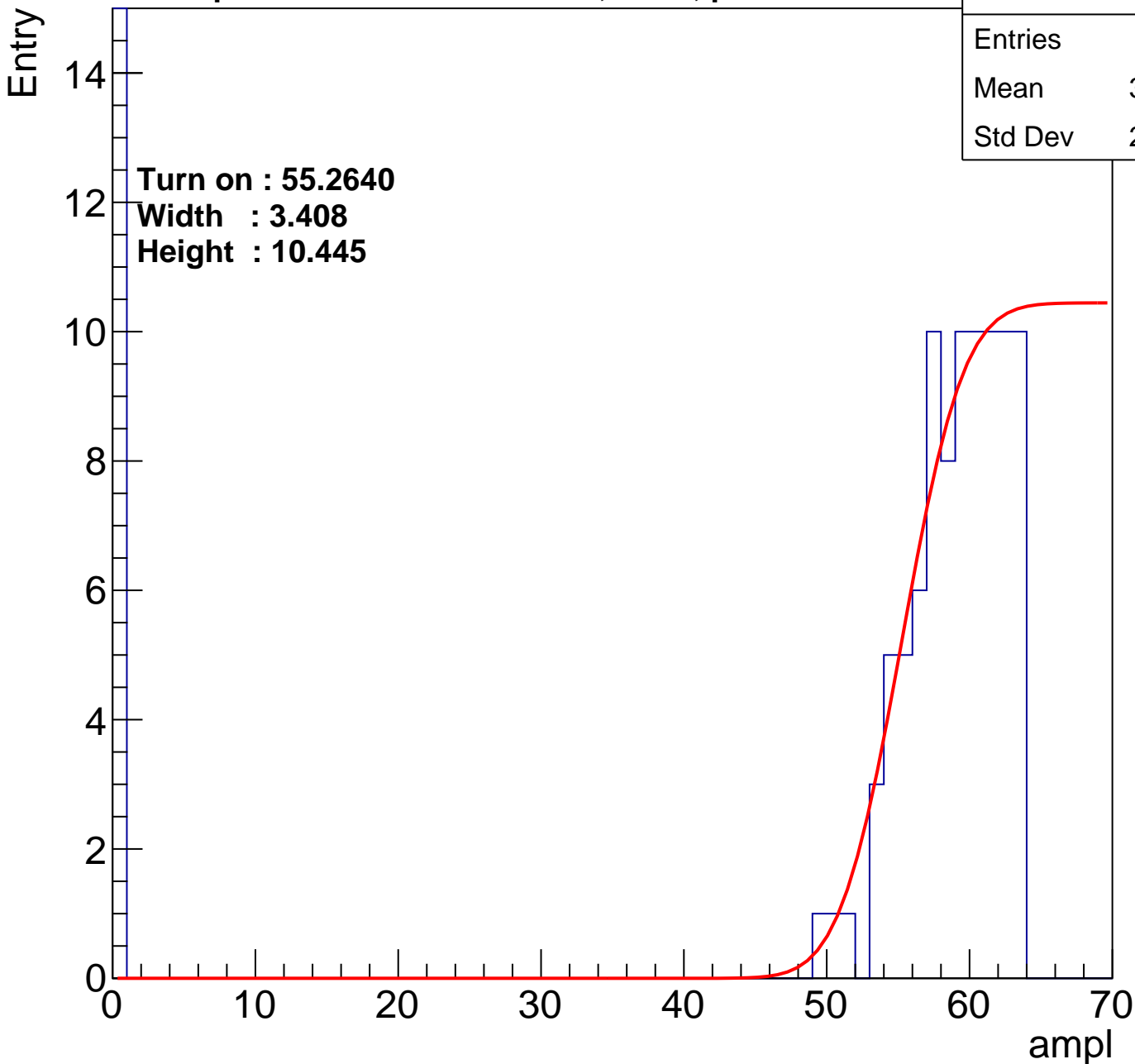
calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	32.36
Std Dev	29.24

Turn on : 55.2640

Width : 3.408

Height : 10.445



B1L104S, U22-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	30.57
Std Dev	29.2

Turn on : 54.6364

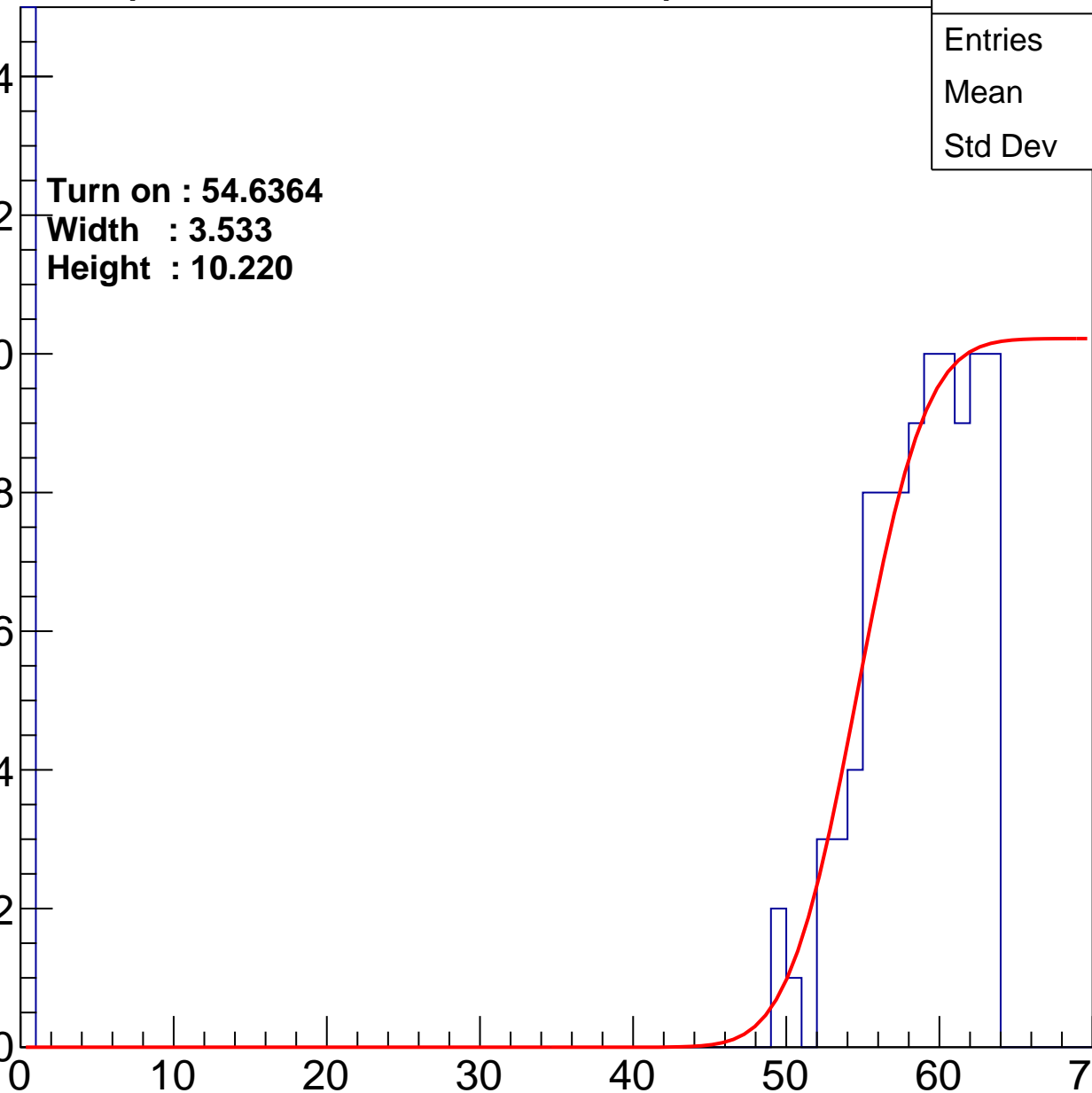
Width : 3.533

Height : 10.220

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	31.89
Std Dev	28.08

Turn on : 53.2967

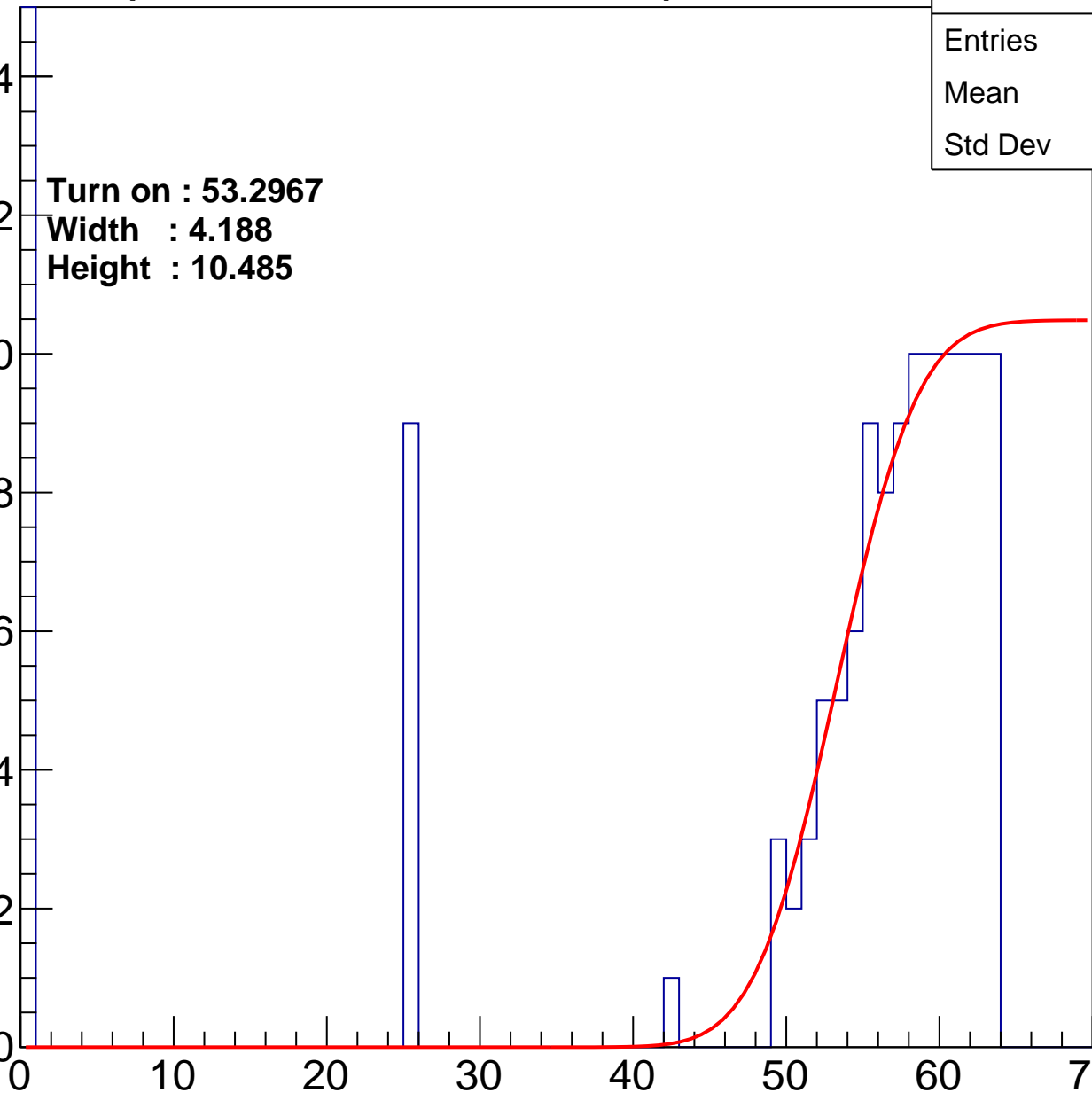
Width : 4.188

Height : 10.485

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch121

calib_packv5_033123_0516.root, FC#4, port A1

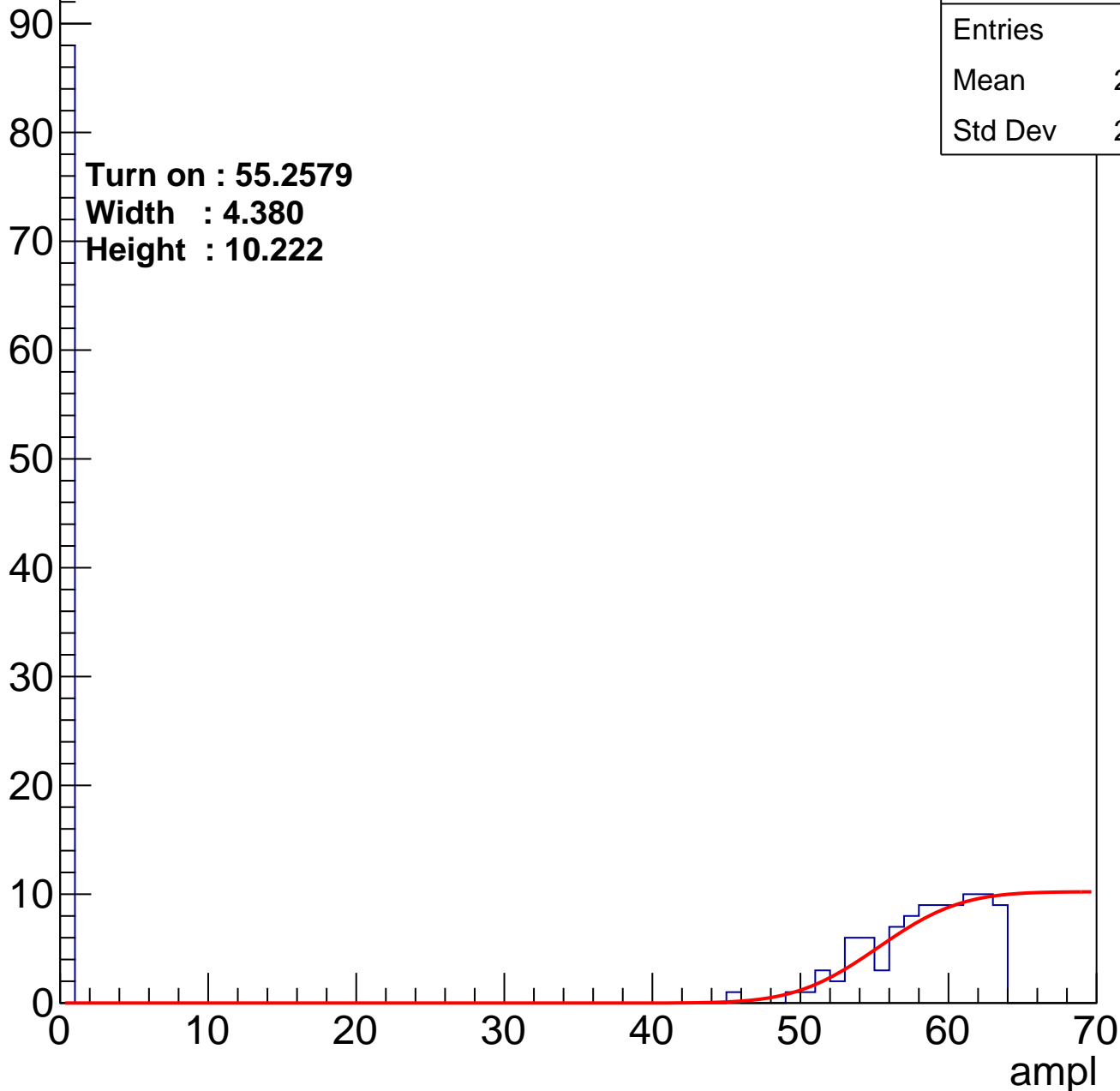
Entries	182
Mean	29.92
Std Dev	29.08

Turn on : 55.2579

Width : 4.380

Height : 10.222

Entry



B1L104S, U22-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	33.12
Std Dev	28.55

Turn on : 53.5474

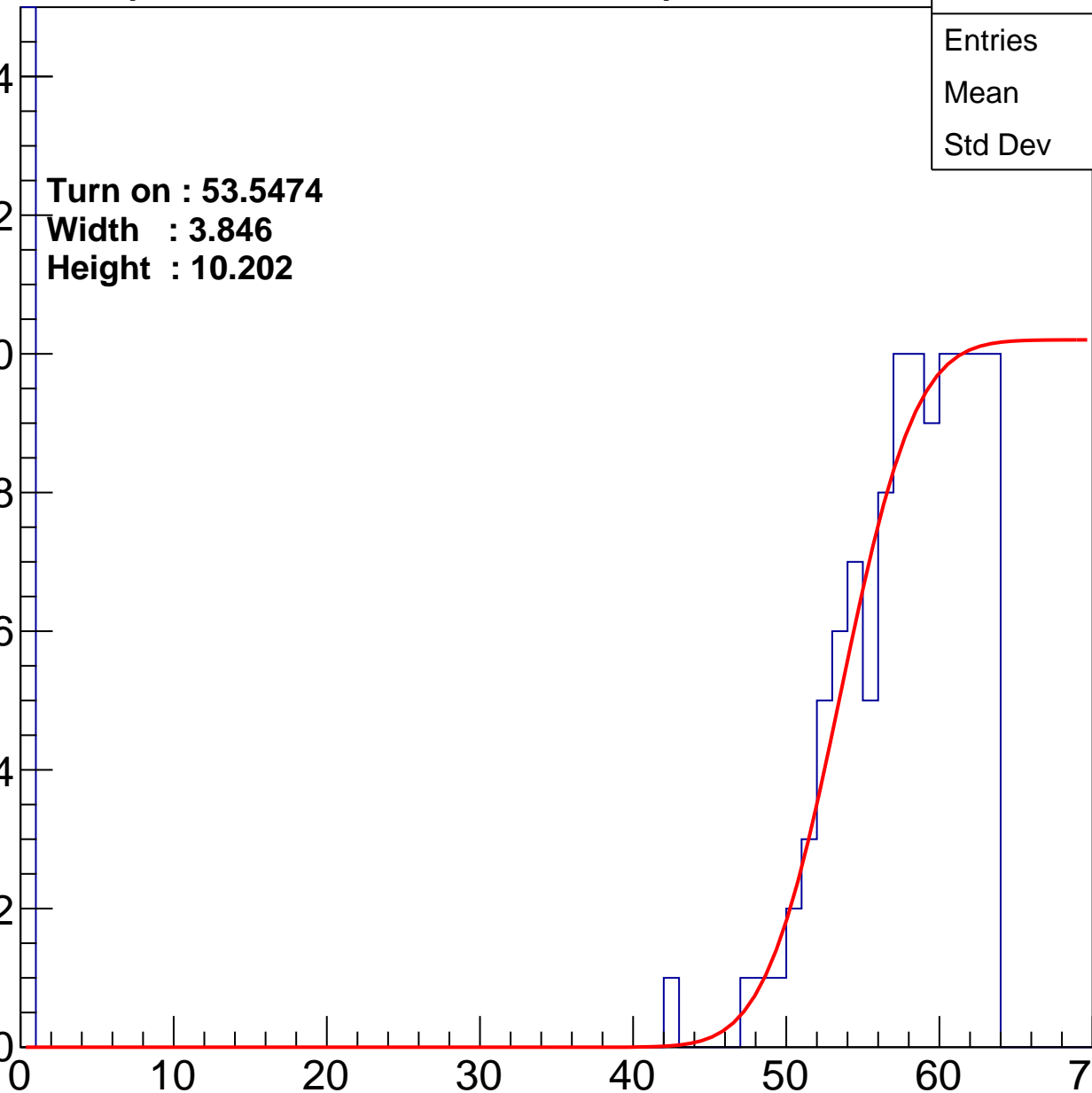
Width : 3.846

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	33.78
Std Dev	28.4

Turn on : 52.2064

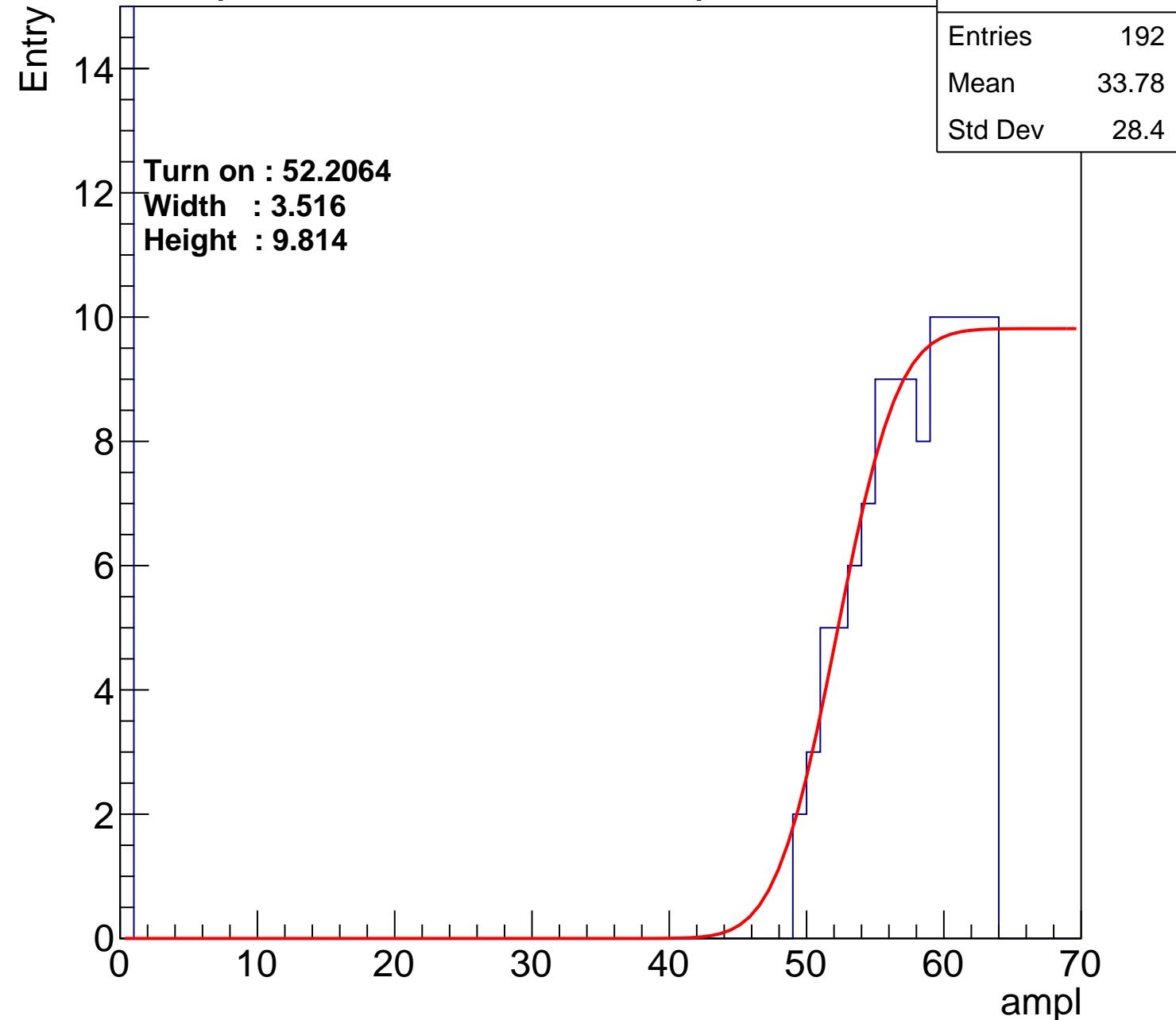
Width : 3.516

Height : 9.814

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	32.42
Std Dev	28.54

Turn on : 53.2945

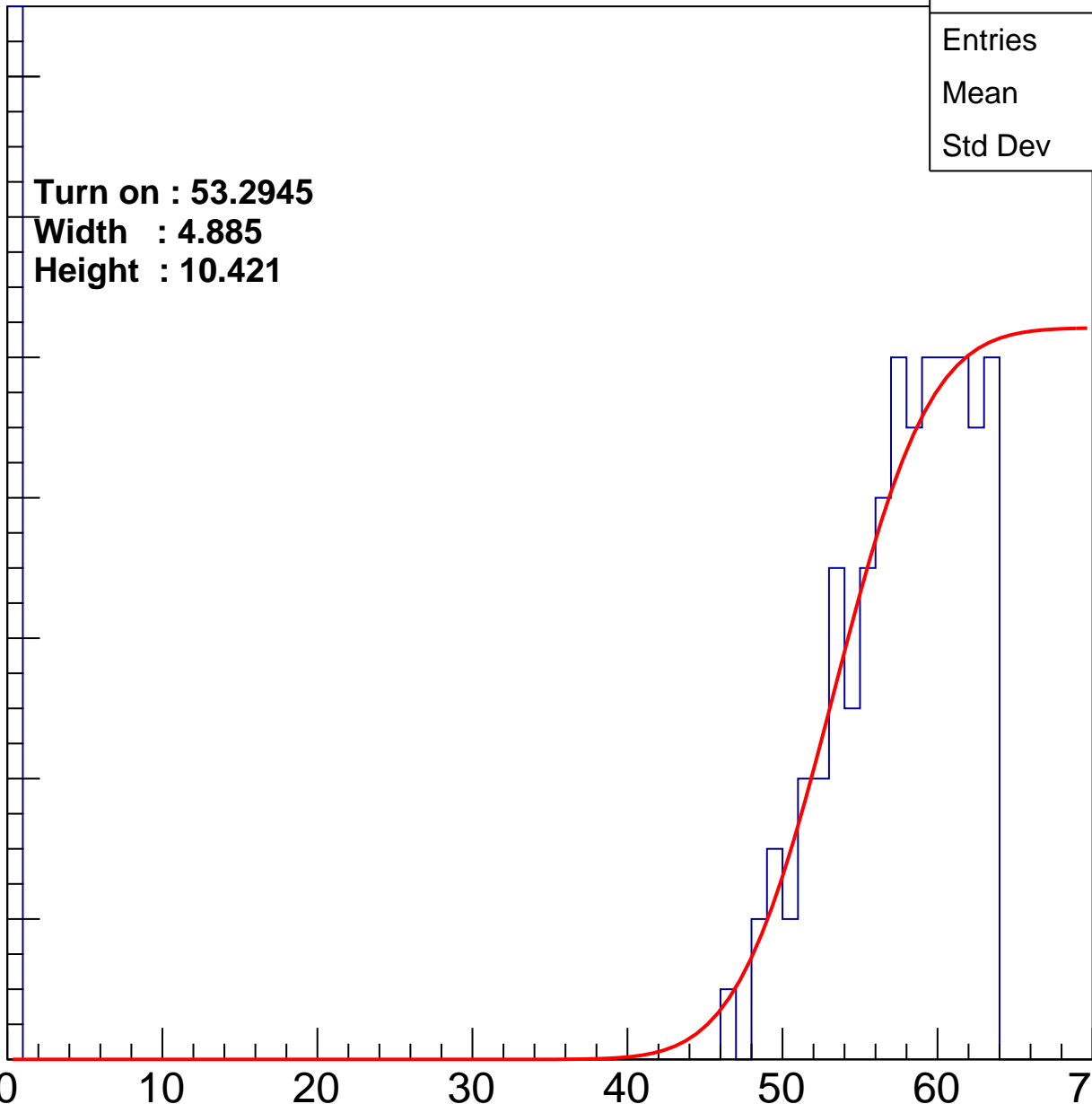
Width : 4.885

Height : 10.421

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	31.04
Std Dev	29.03

Turn on : 54.9509

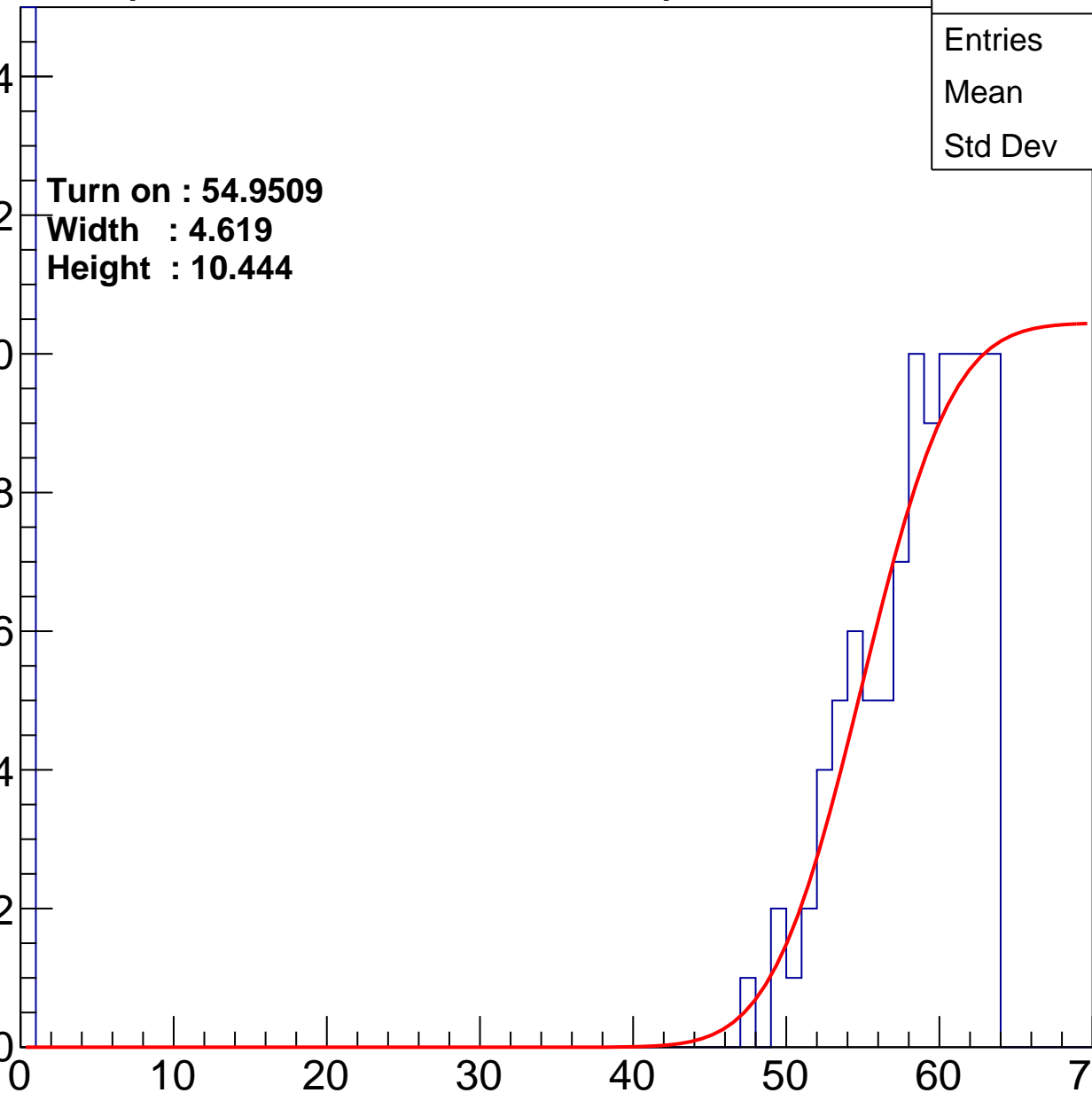
Width : 4.619

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	31.24
Std Dev	29.07

Turn on : 54.6474

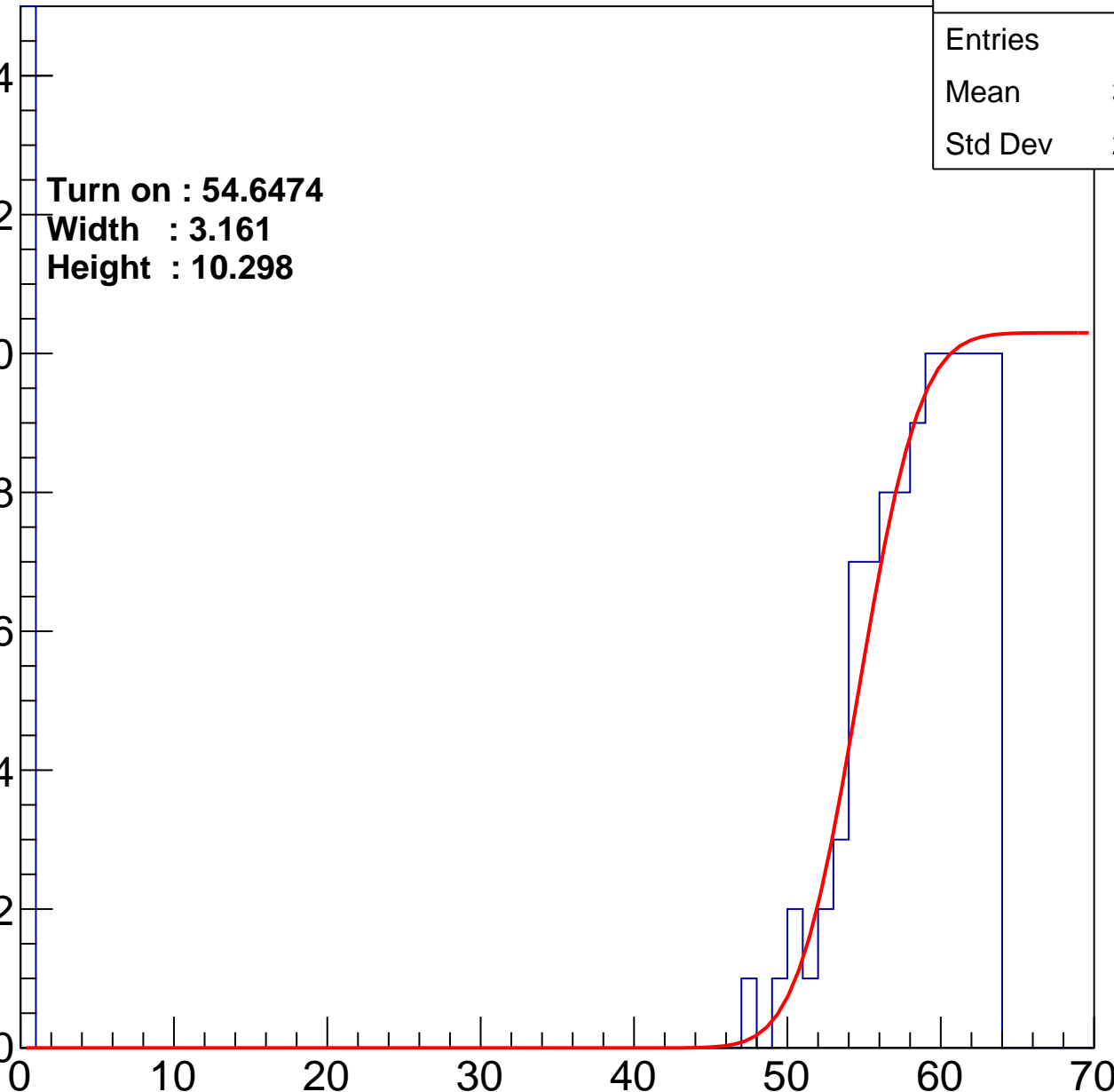
Width : 3.161

Height : 10.298

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	34.3
Std Dev	28.03

Turn on : 52.1992

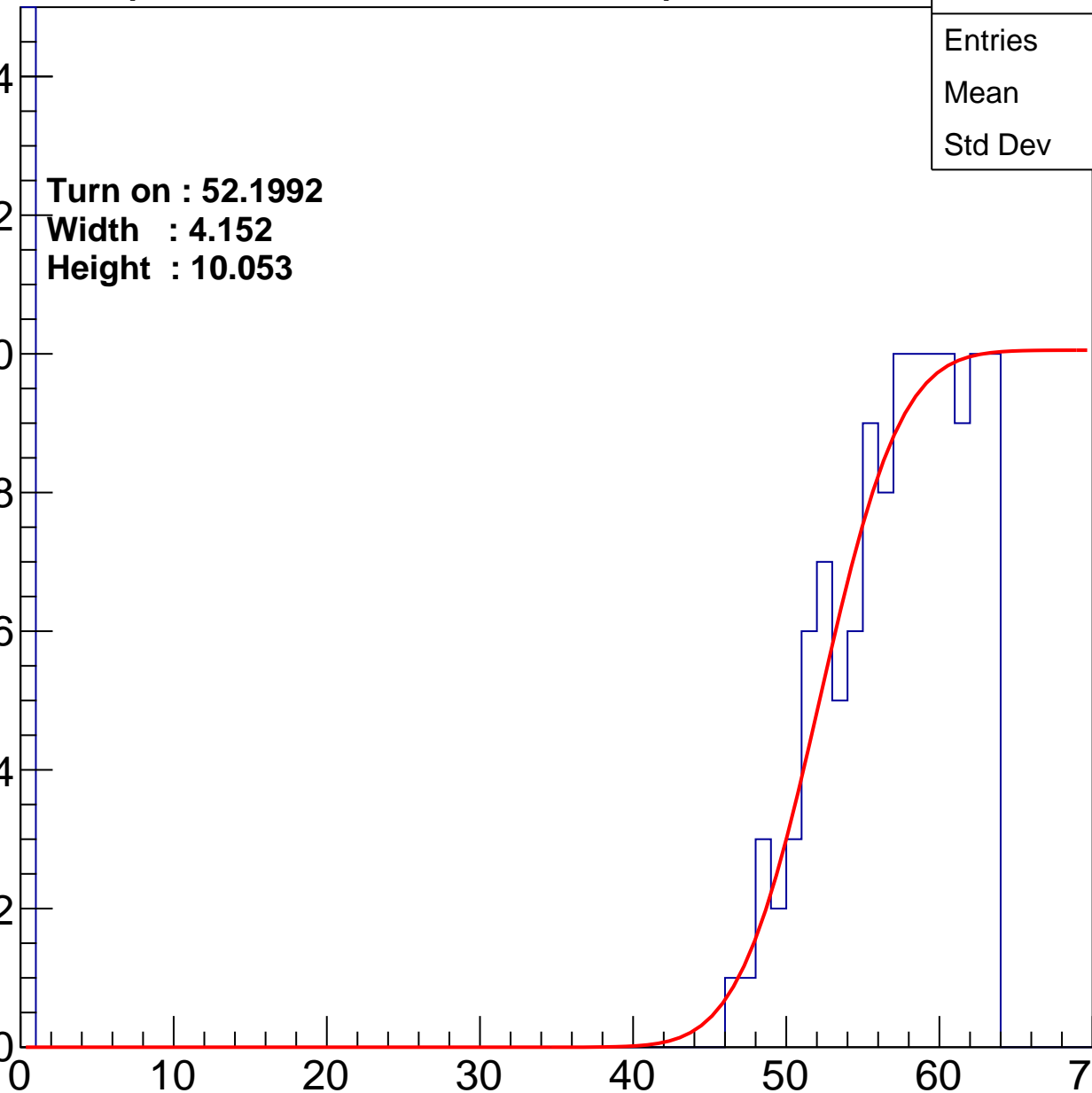
Width : 4.152

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U22-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	34.3
Std Dev	28.03

Turn on : 52.1992

Width : 4.152

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

