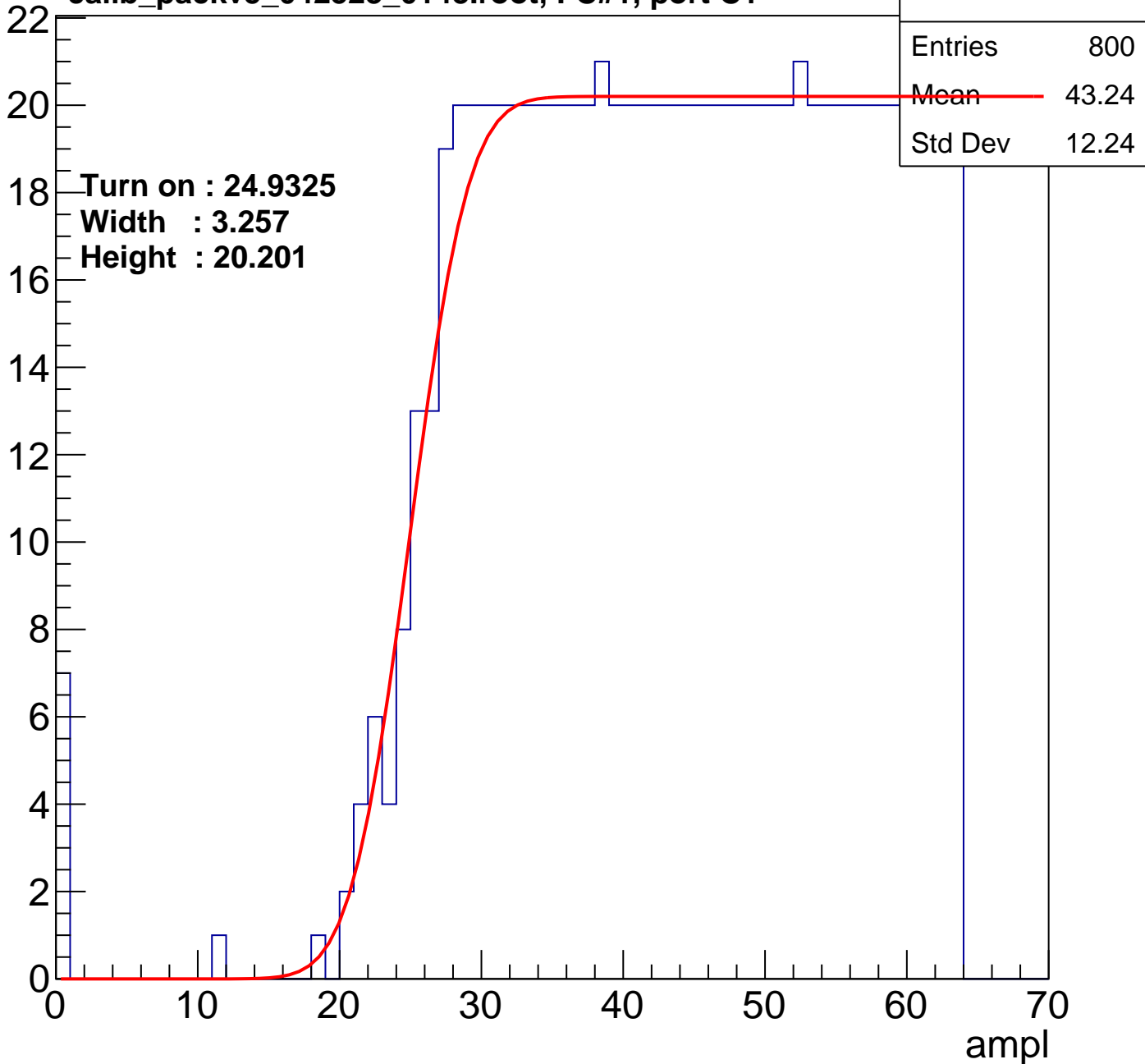


B0L101S, U23-ch0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U23-ch1

calib_packv5_042523_0143.root, FC#1, port C1

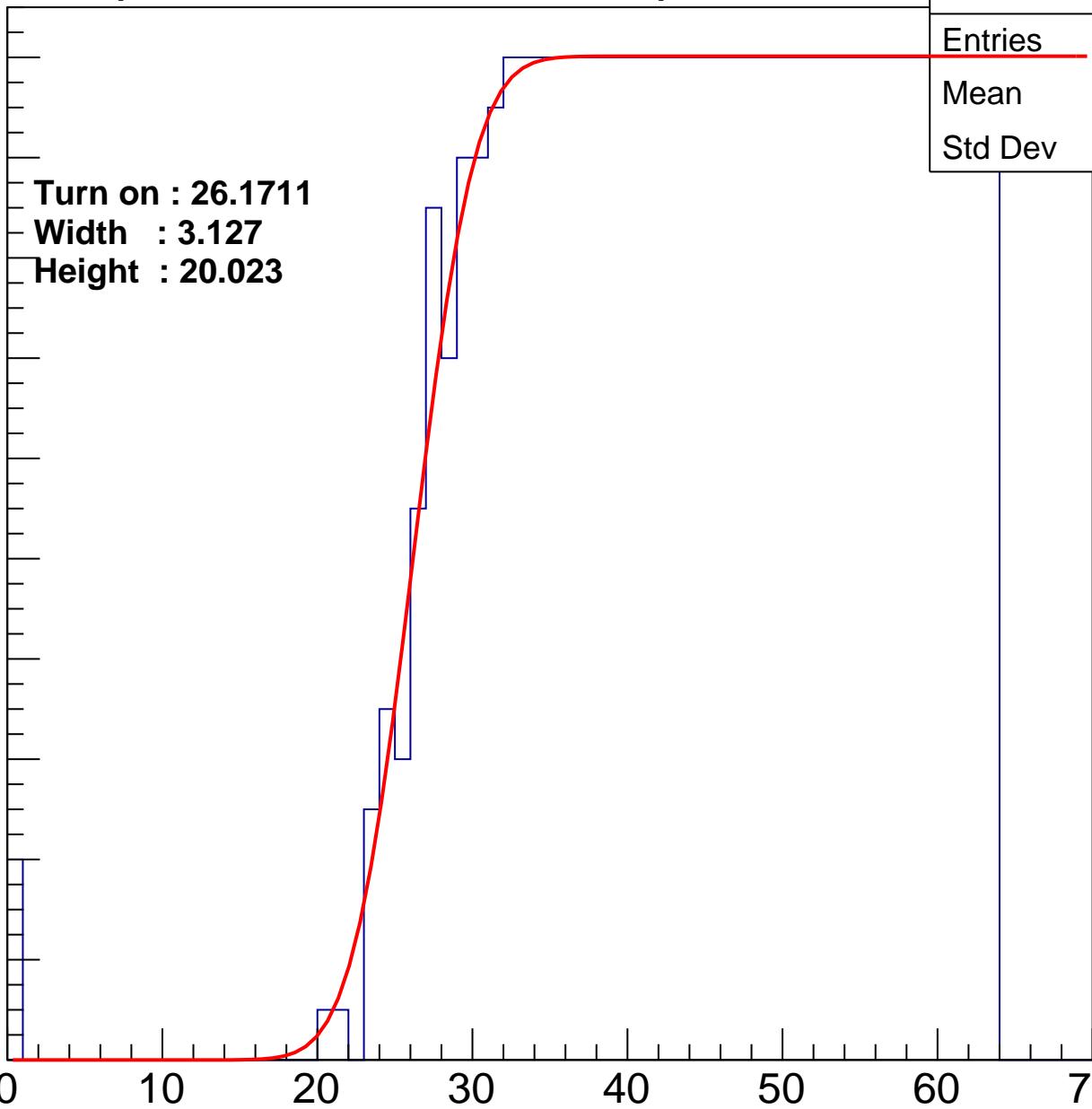
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1711
Width : 3.127
Height : 20.023

Entries	761
Mean	44.23
Std Dev	11.53

ampl



B0L101S, U23-ch2

calib_packv5_042523_0143.root, FC#1, port C1

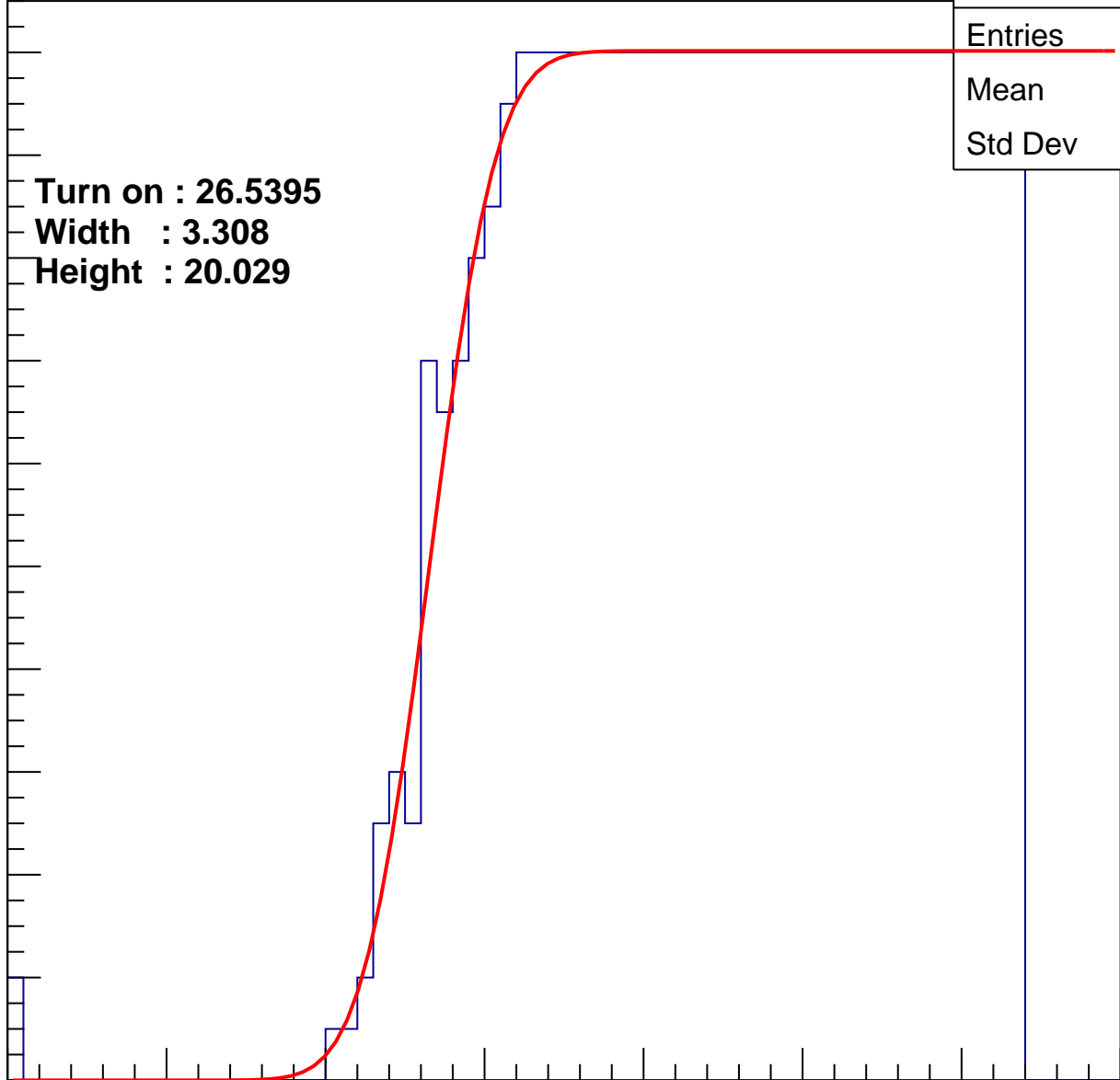
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5395
Width : 3.308
Height : 20.029

Entries	755
Mean	44.42
Std Dev	11.31

ampl



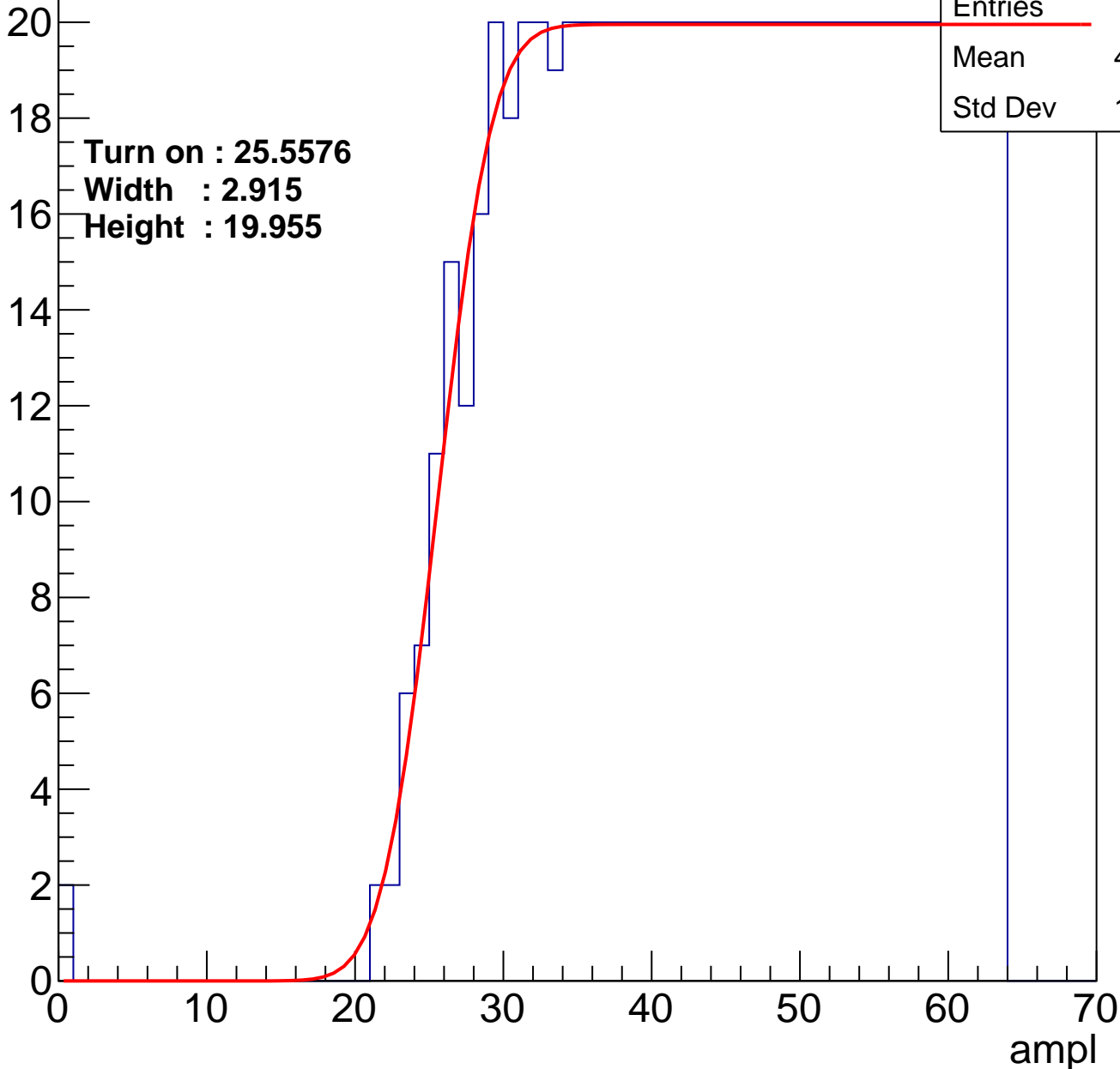
B0L101S, U23-ch3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	770
Mean	44.07
Std Dev	11.48

Turn on : 25.5576
Width : 2.915
Height : 19.955

Entry



B0L101S, U23-ch4

calib_packv5_042523_0143.root, FC#1, port C1

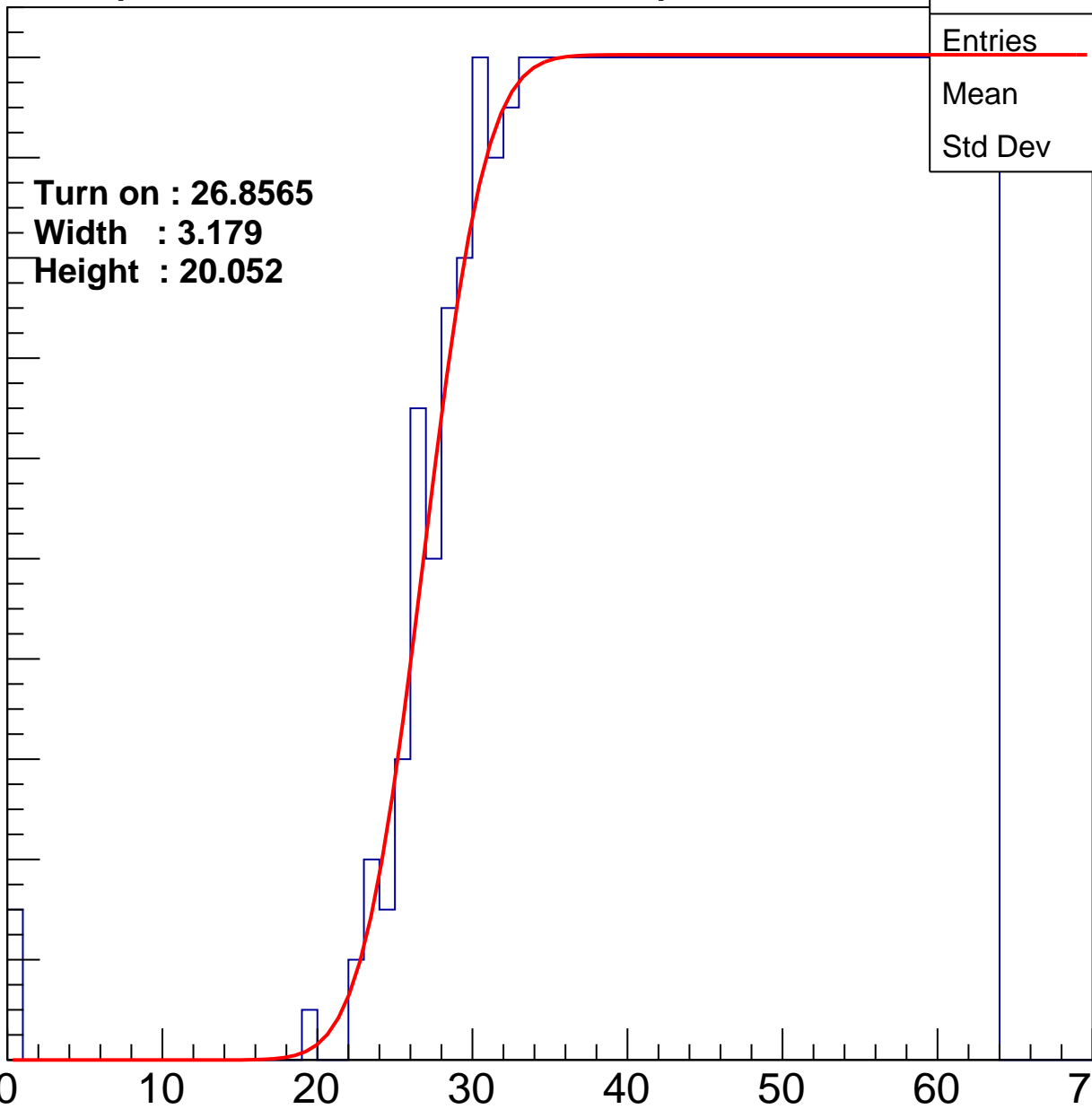
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8565
Width : 3.179
Height : 20.052

Entries	750
Mean	44.53
Std Dev	11.32

ampl



B0L101S, U23-ch5

calib_packv5_042523_0143.root, FC#1, port C1

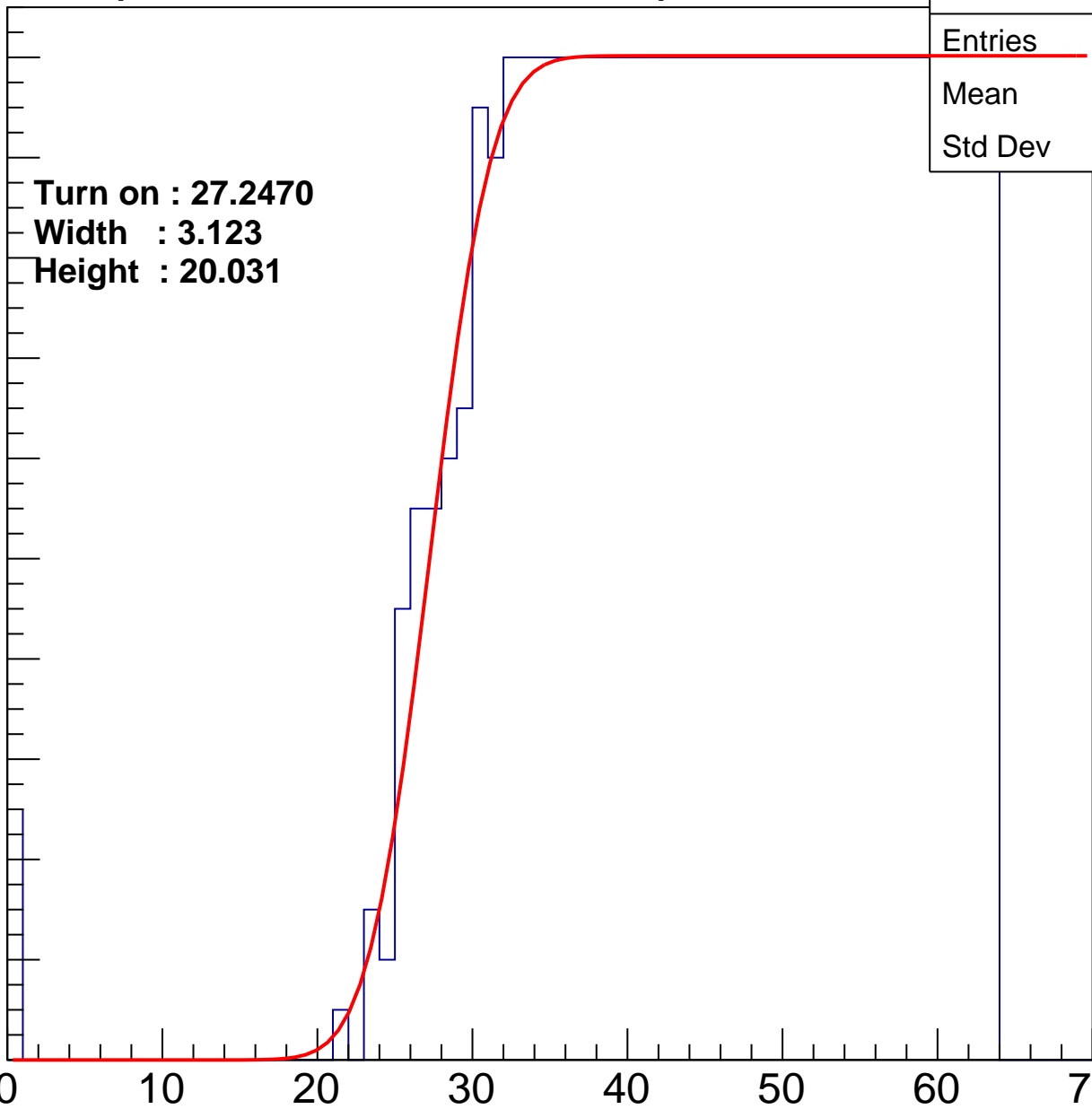
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2470
Width : 3.123
Height : 20.031

Entries	744
Mean	44.61
Std Dev	11.43

ampl



B0L101S, U23-ch6

calib_packv5_042523_0143.root, FC#1, port C1

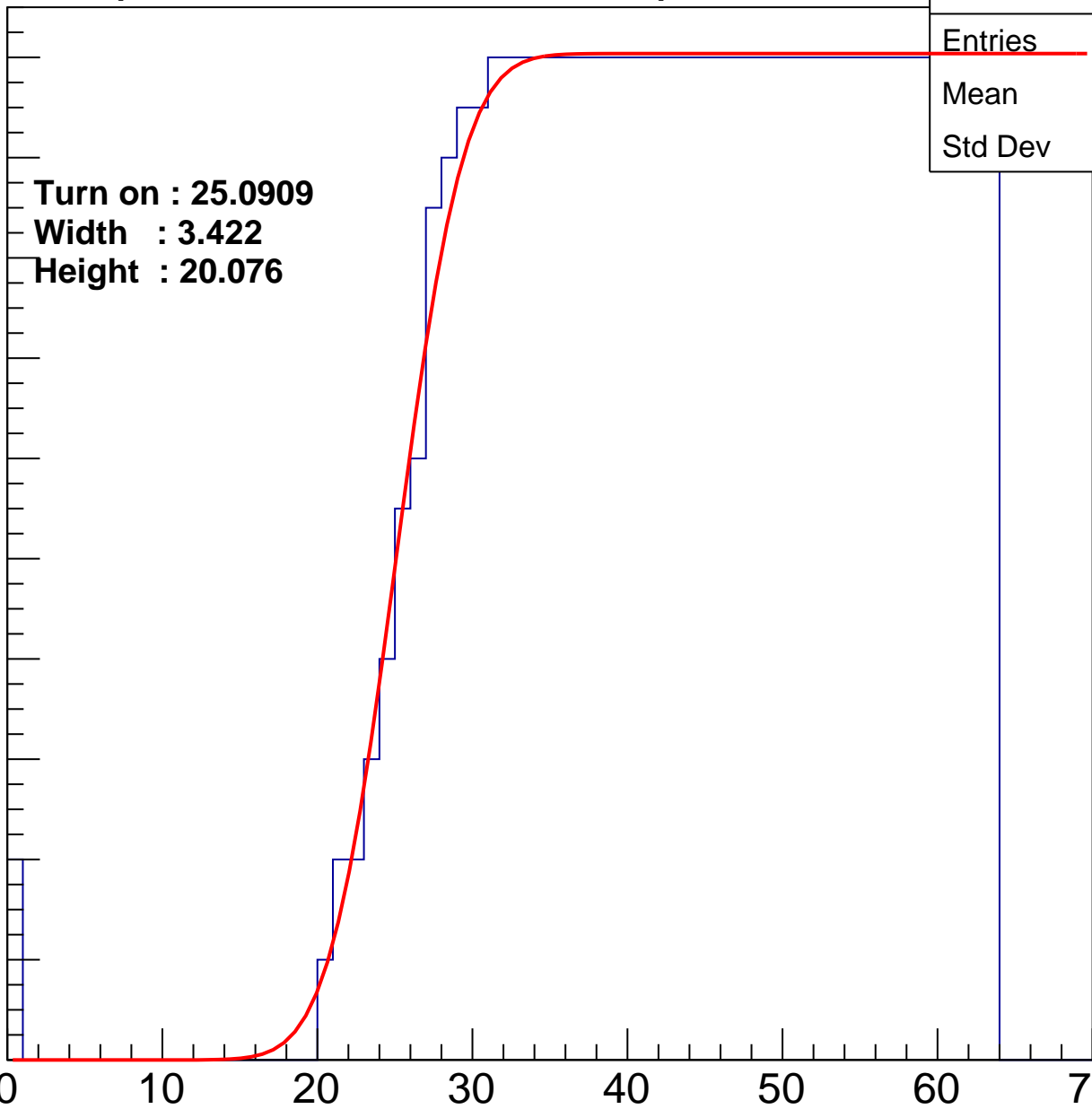
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0909
Width : 3.422
Height : 20.076

Entries	784
Mean	43.66
Std Dev	11.84

ampl



B0L101S, U23-ch7

calib_packv5_042523_0143.root, FC#1, port C1

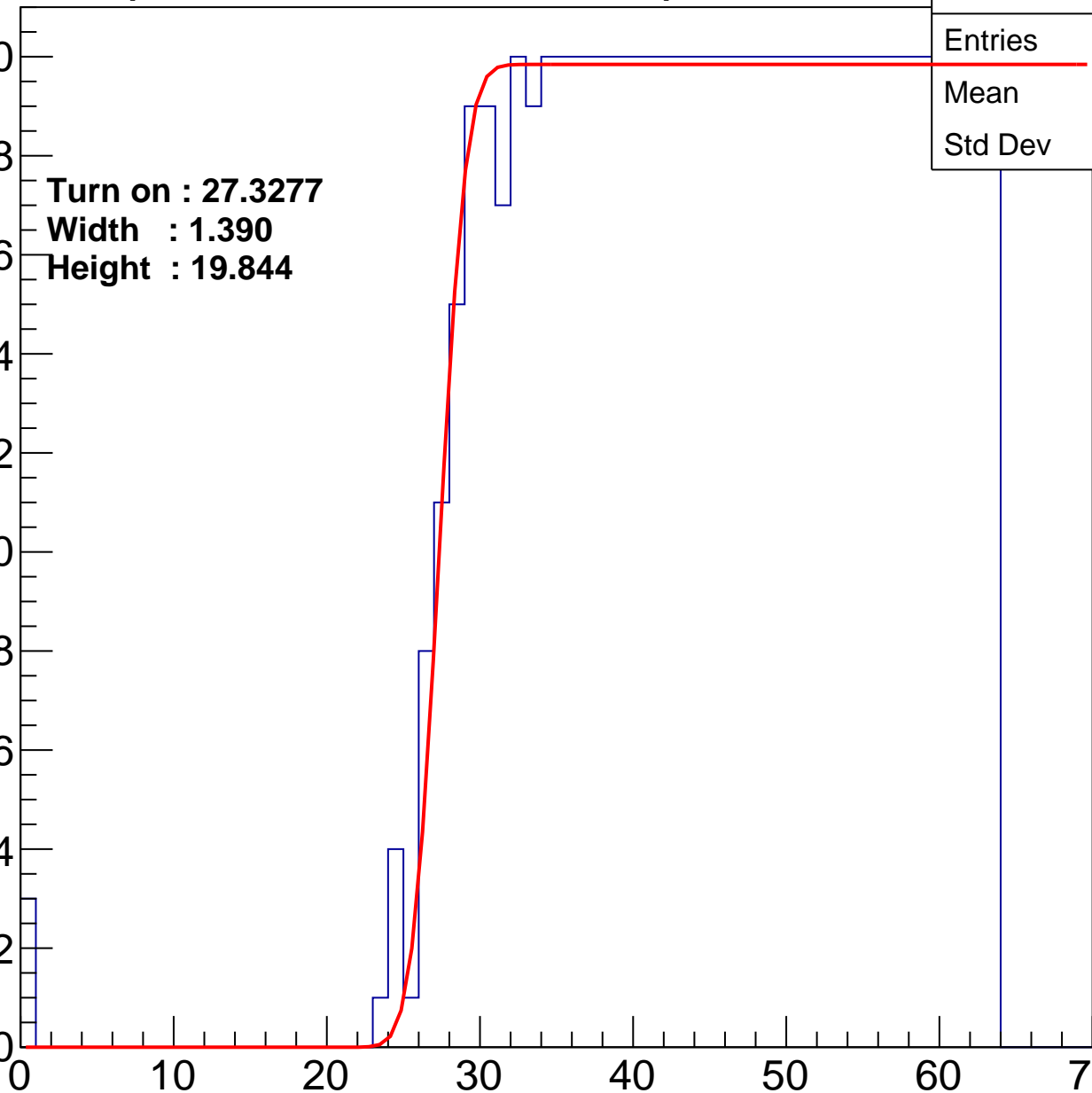
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3277
Width : 1.390
Height : 19.844

Entries	737
Mean	44.89
Std Dev	11.07

ampl



B0L101S, U23-ch8

calib_packv5_042523_0143.root, FC#1, port C1

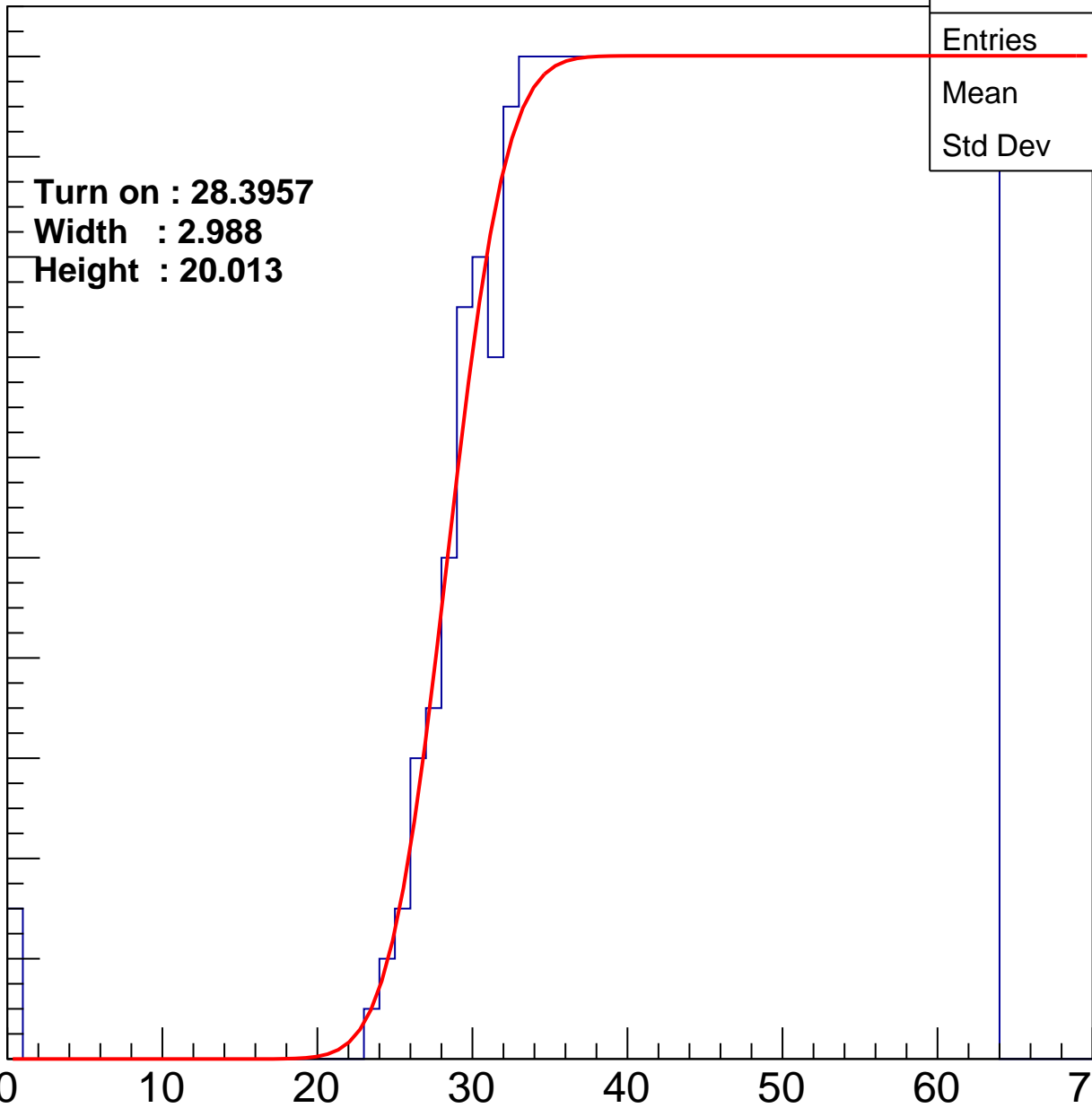
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3957
Width : 2.988
Height : 20.013

Entries	716
Mean	45.37
Std Dev	10.86

ampl



B0L101S, U23-ch9

calib_packv5_042523_0143.root, FC#1, port C1

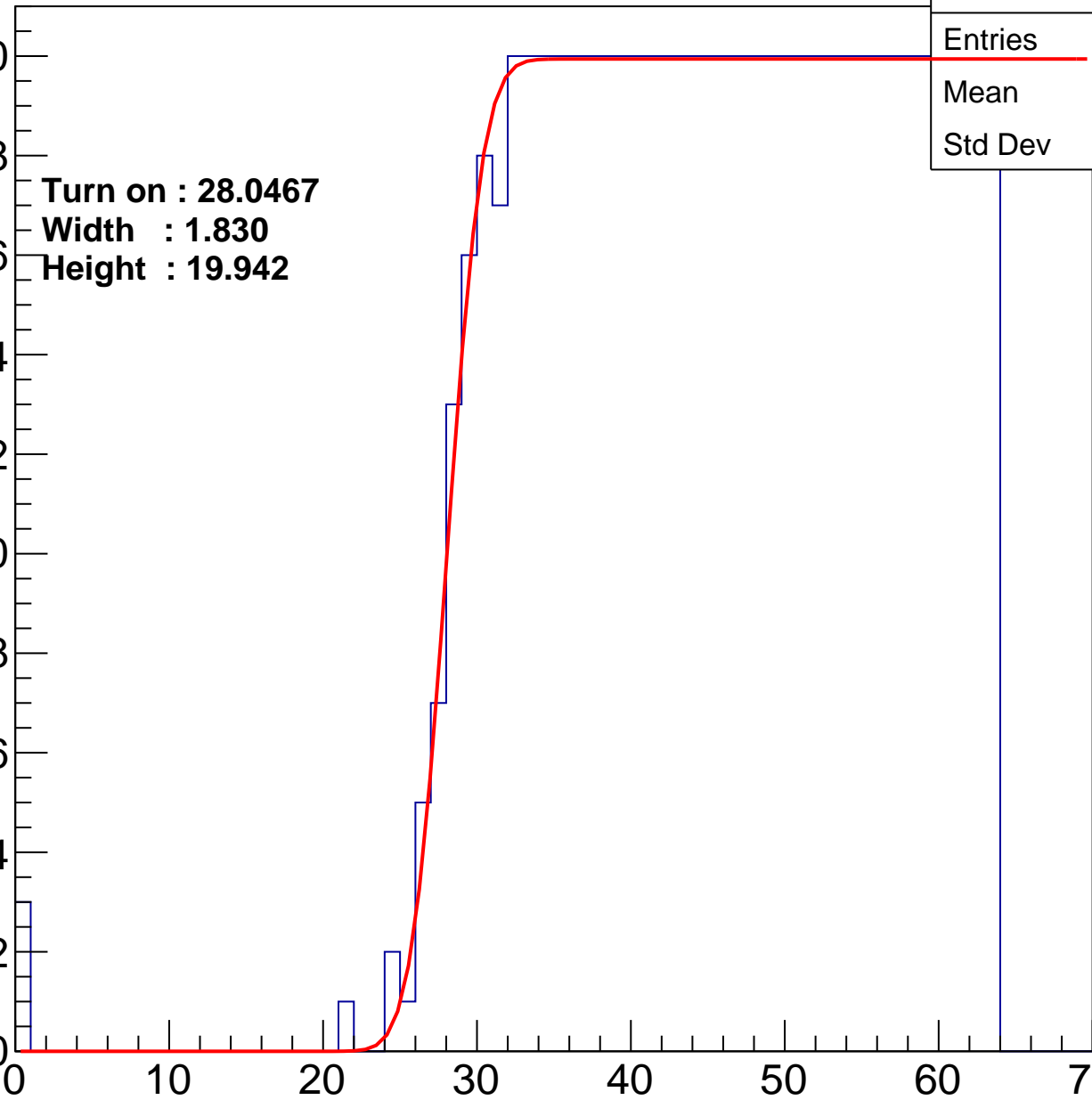
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0467
Width : 1.830
Height : 19.942

Entries	723
Mean	45.24
Std Dev	10.89

ampl



B0L101S, U23-ch10

calib_packv5_042523_0143.root, FC#1, port C1

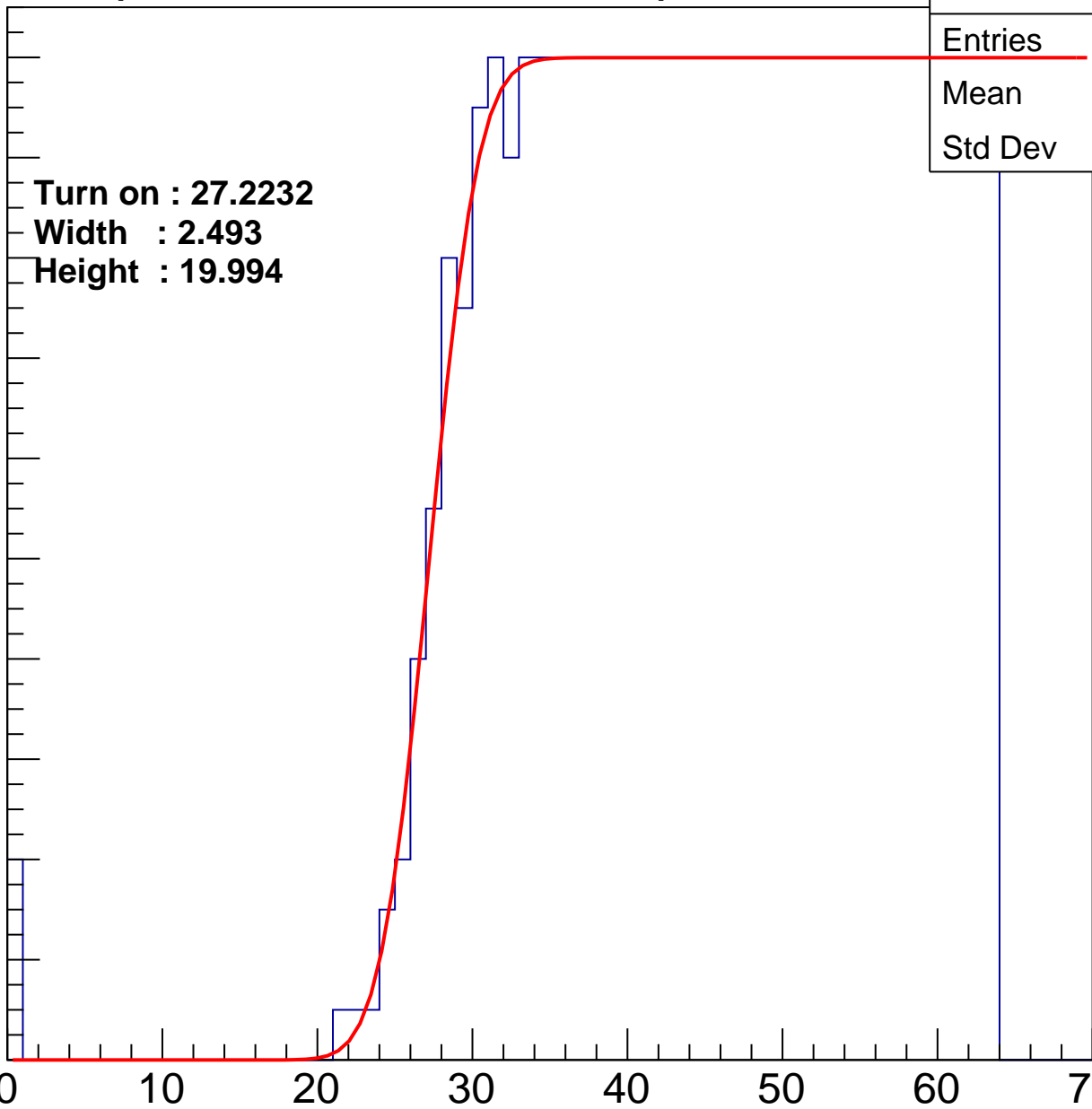
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2232
Width : 2.493
Height : 19.994

Entries	741
Mean	44.74
Std Dev	11.26

ampl



B0L101S, U23-ch11

calib_packv5_042523_0143.root, FC#1, port C1

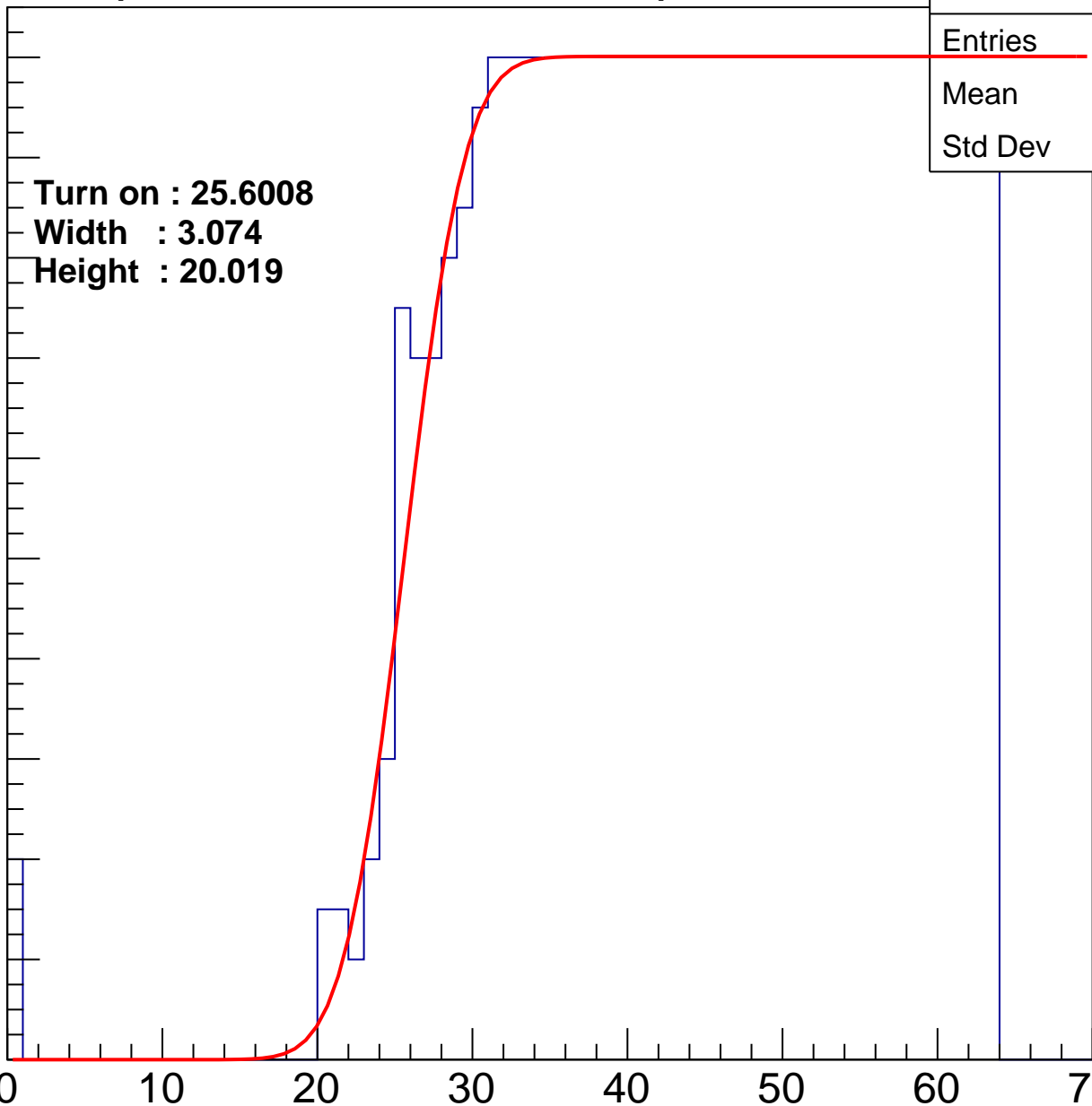
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6008
Width : 3.074
Height : 20.019

Entries	777
Mean	43.82
Std Dev	11.77

ampl



B0L101S, U23-ch12

calib_packv5_042523_0143.root, FC#1, port C1

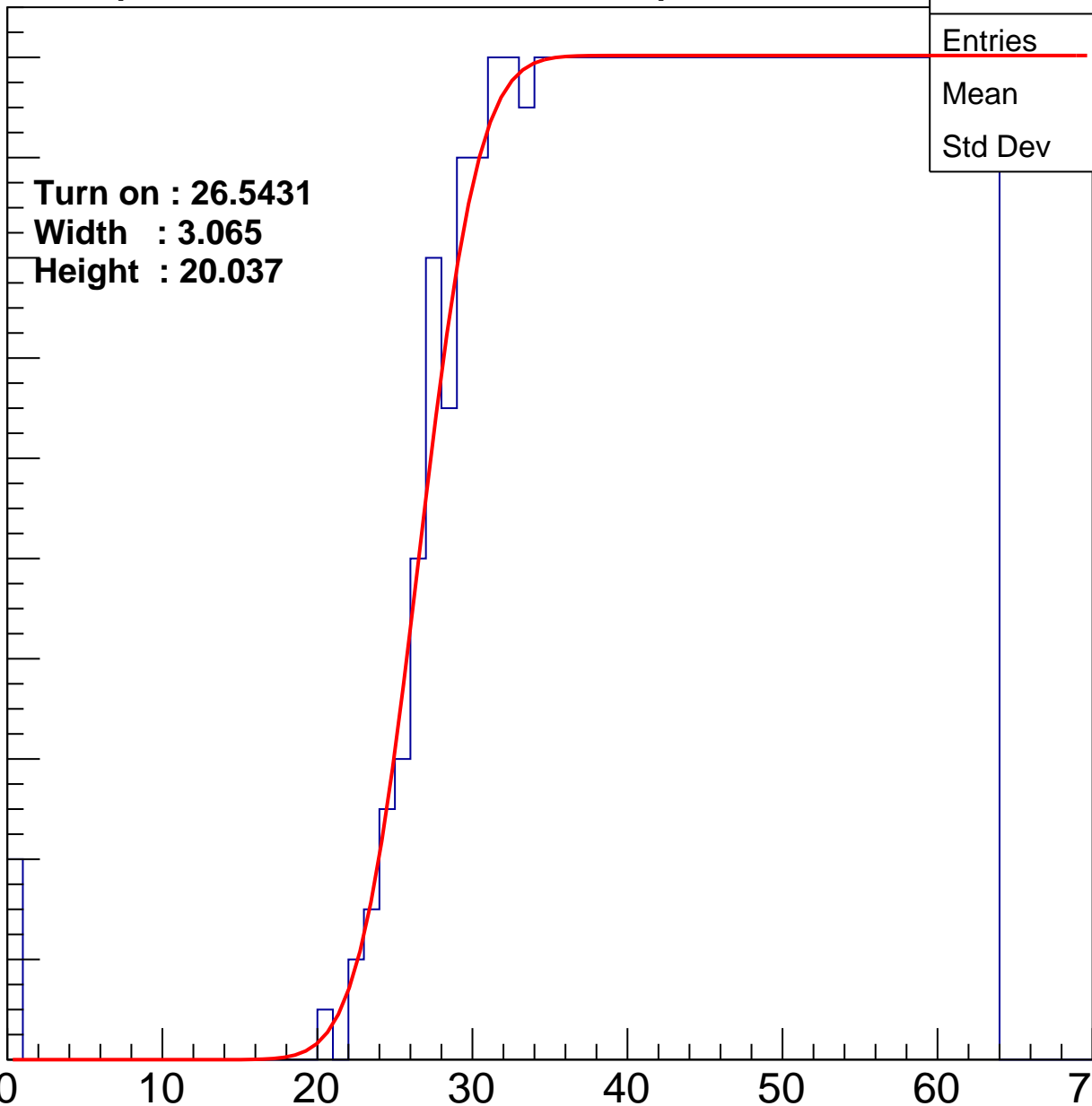
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5431
Width : 3.065
Height : 20.037

Entries	755
Mean	44.38
Std Dev	11.46

ampl



B0L101S, U23-ch13

calib_packv5_042523_0143.root, FC#1, port C1

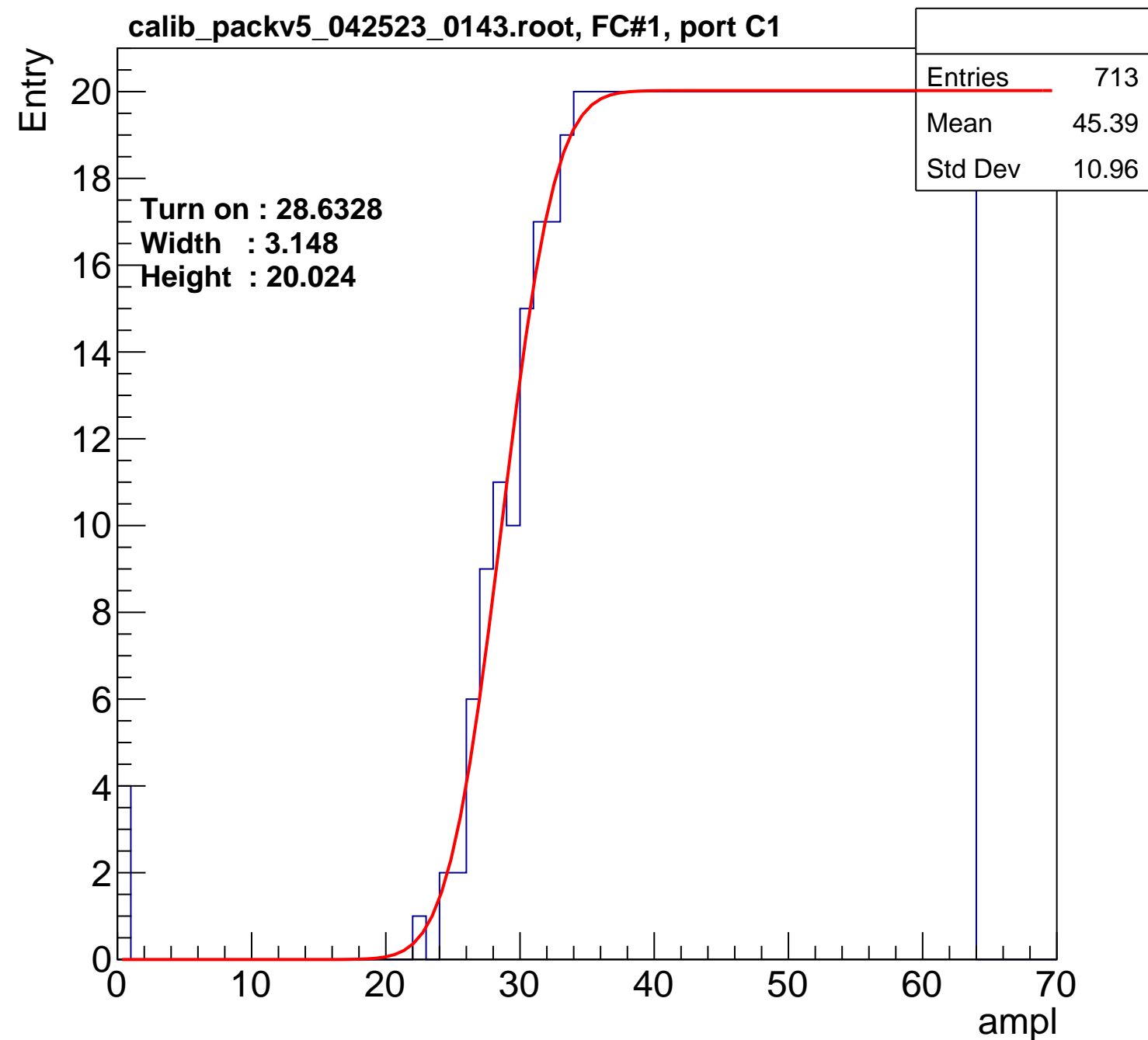
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6328
Width : 3.148
Height : 20.024

Entries	713
Mean	45.39
Std Dev	10.96

ampl



B0L101S, U23-ch14

calib_packv5_042523_0143.root, FC#1, port C1

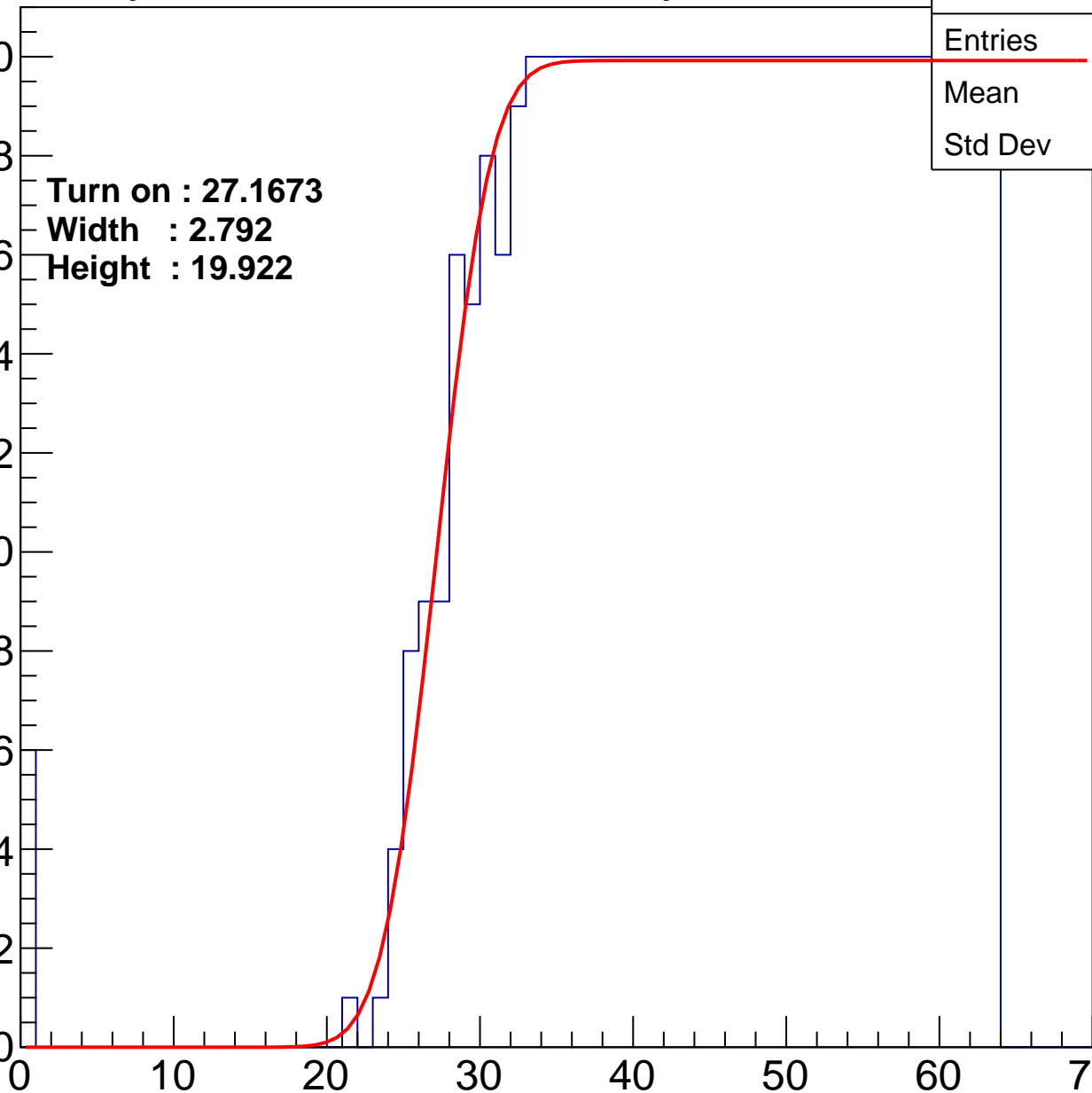
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1673
Width : 2.792
Height : 19.922

Entries	742
Mean	44.61
Std Dev	11.51

ampl



B0L101S, U23-ch15

calib_packv5_042523_0143.root, FC#1, port C1

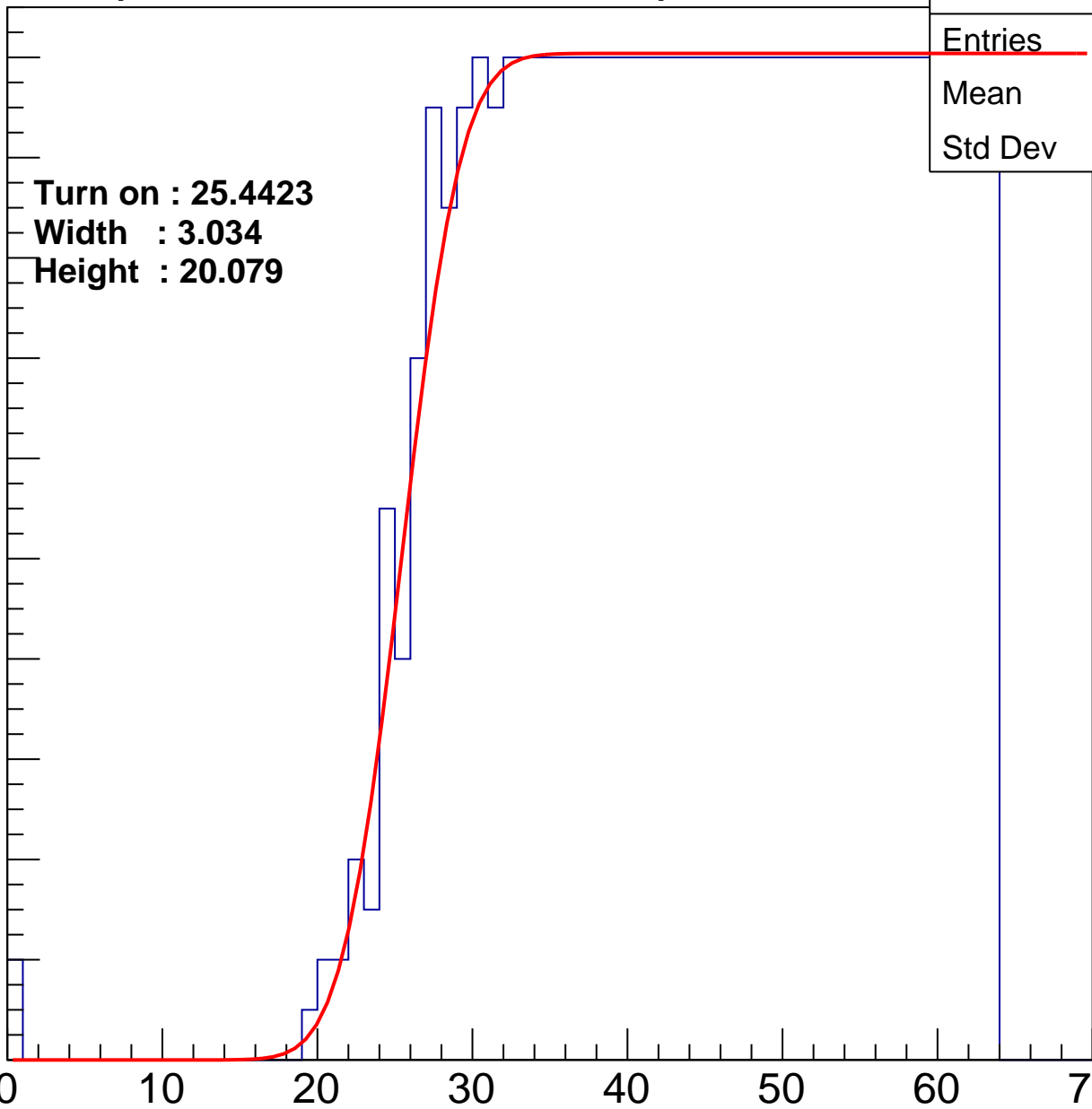
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4423
Width : 3.034
Height : 20.079

Entries	781
Mean	43.81
Std Dev	11.62

ampl



B0L101S, U23-ch16

calib_packv5_042523_0143.root, FC#1, port C1

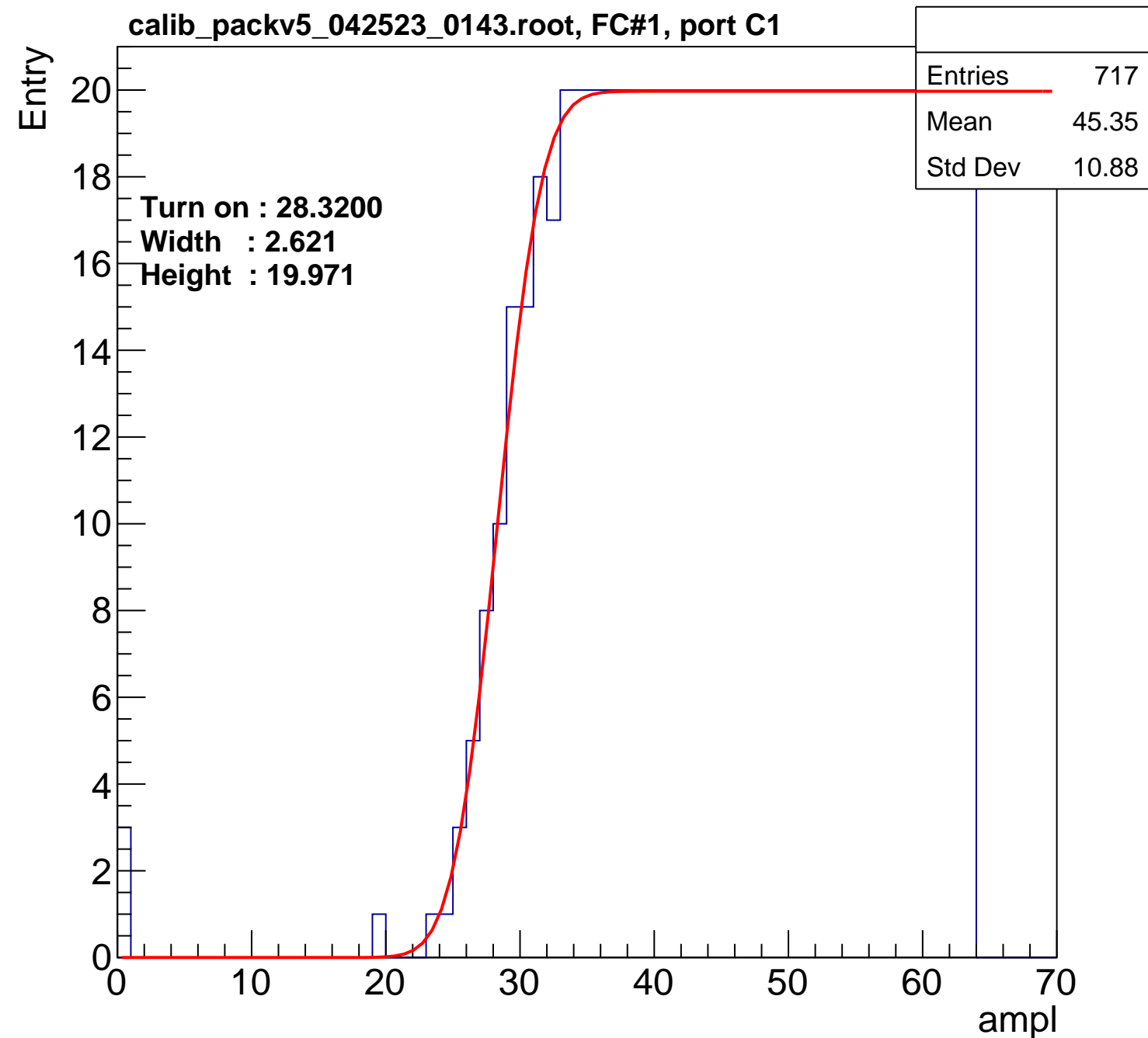
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3200
Width : 2.621
Height : 19.971

Entries	717
Mean	45.35
Std Dev	10.88

ampl



B0L101S, U23-ch17

calib_packv5_042523_0143.root, FC#1, port C1

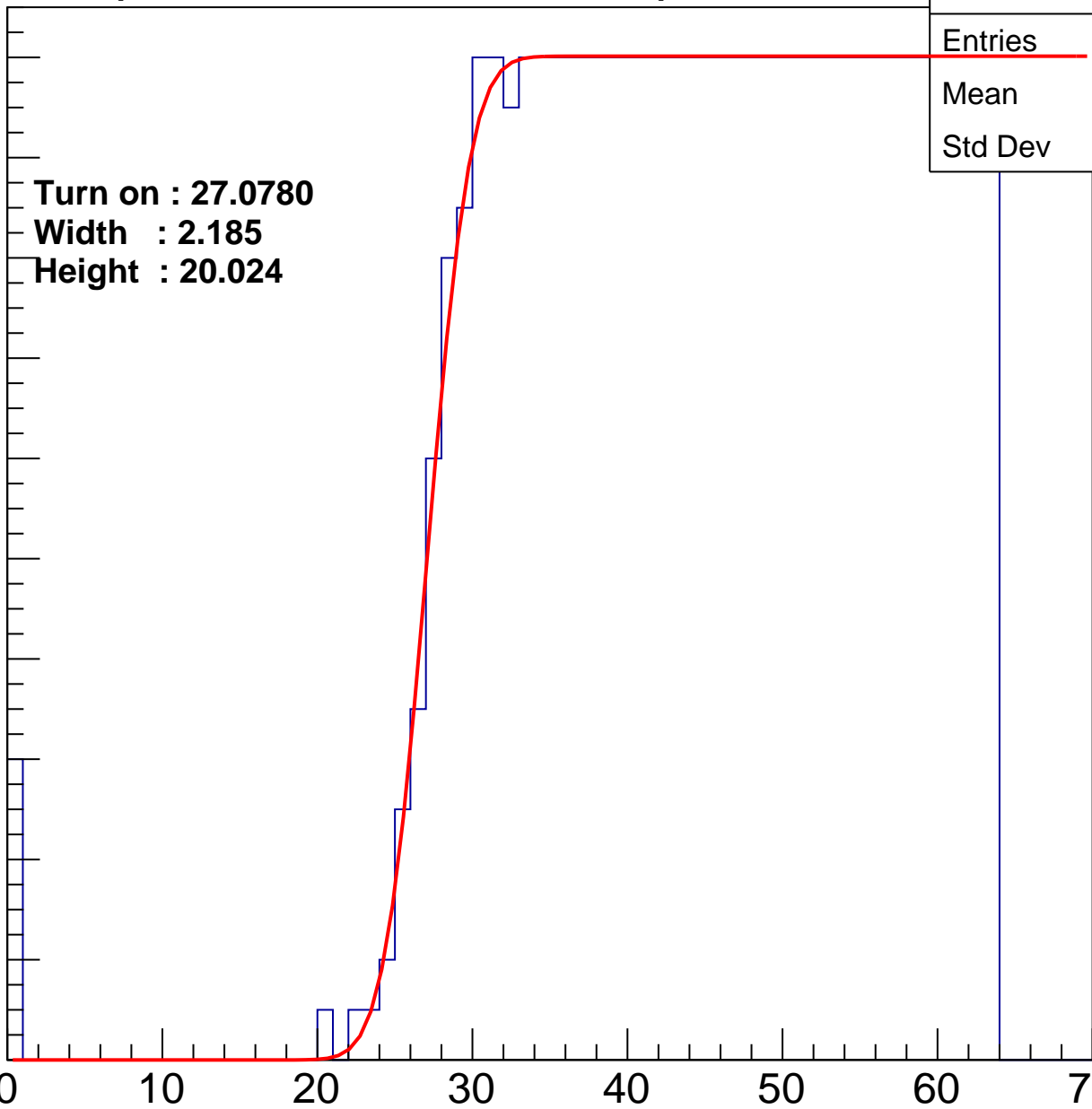
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0780
Width : 2.185
Height : 20.024

Entries	747
Mean	44.54
Std Dev	11.5

ampl



B0L101S, U23-ch18

calib_packv5_042523_0143.root, FC#1, port C1

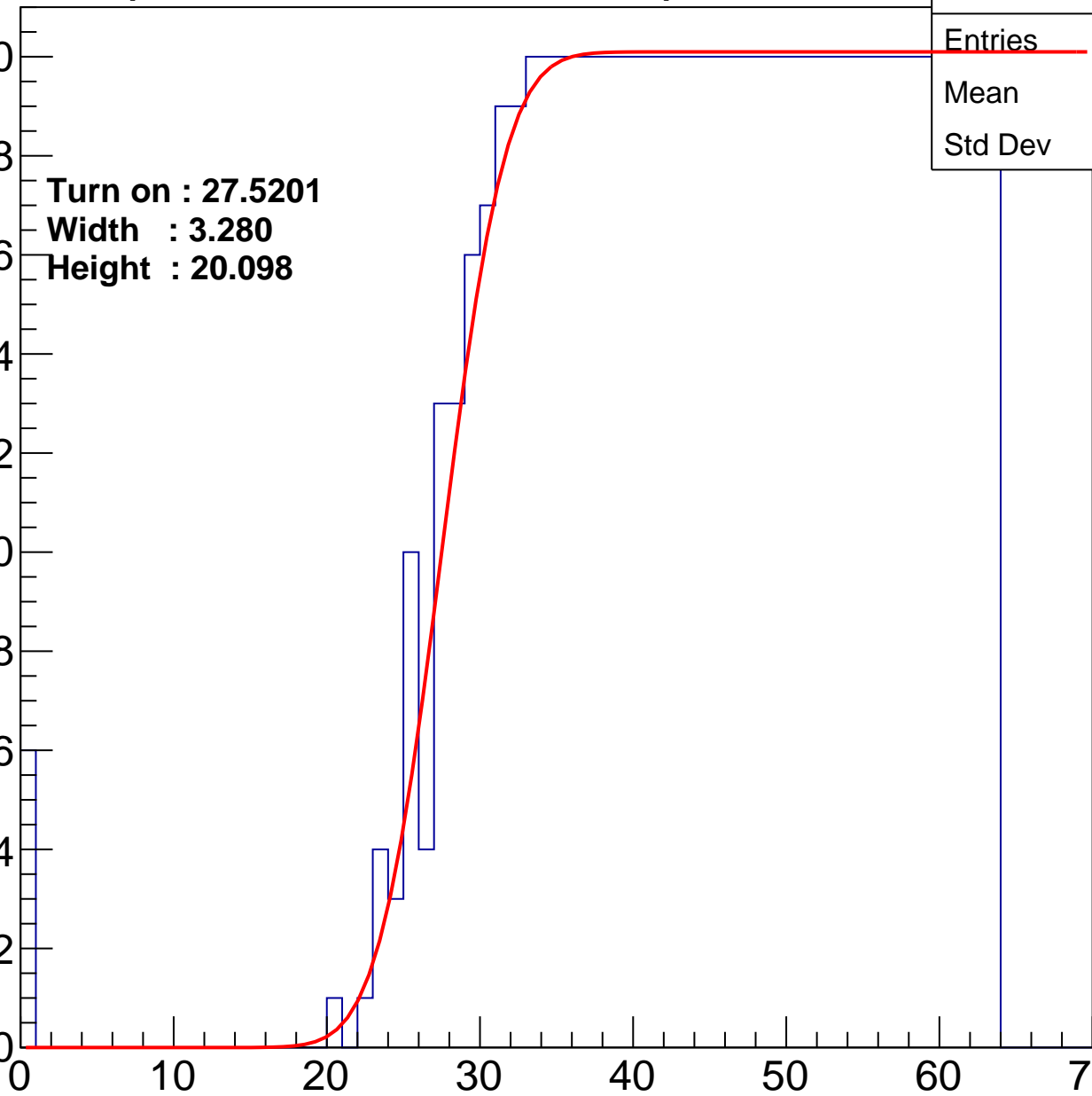
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5201
Width : 3.280
Height : 20.098

Entries	746
Mean	44.51
Std Dev	11.57

ampl



B0L101S, U23-ch19

calib_packv5_042523_0143.root, FC#1, port C1

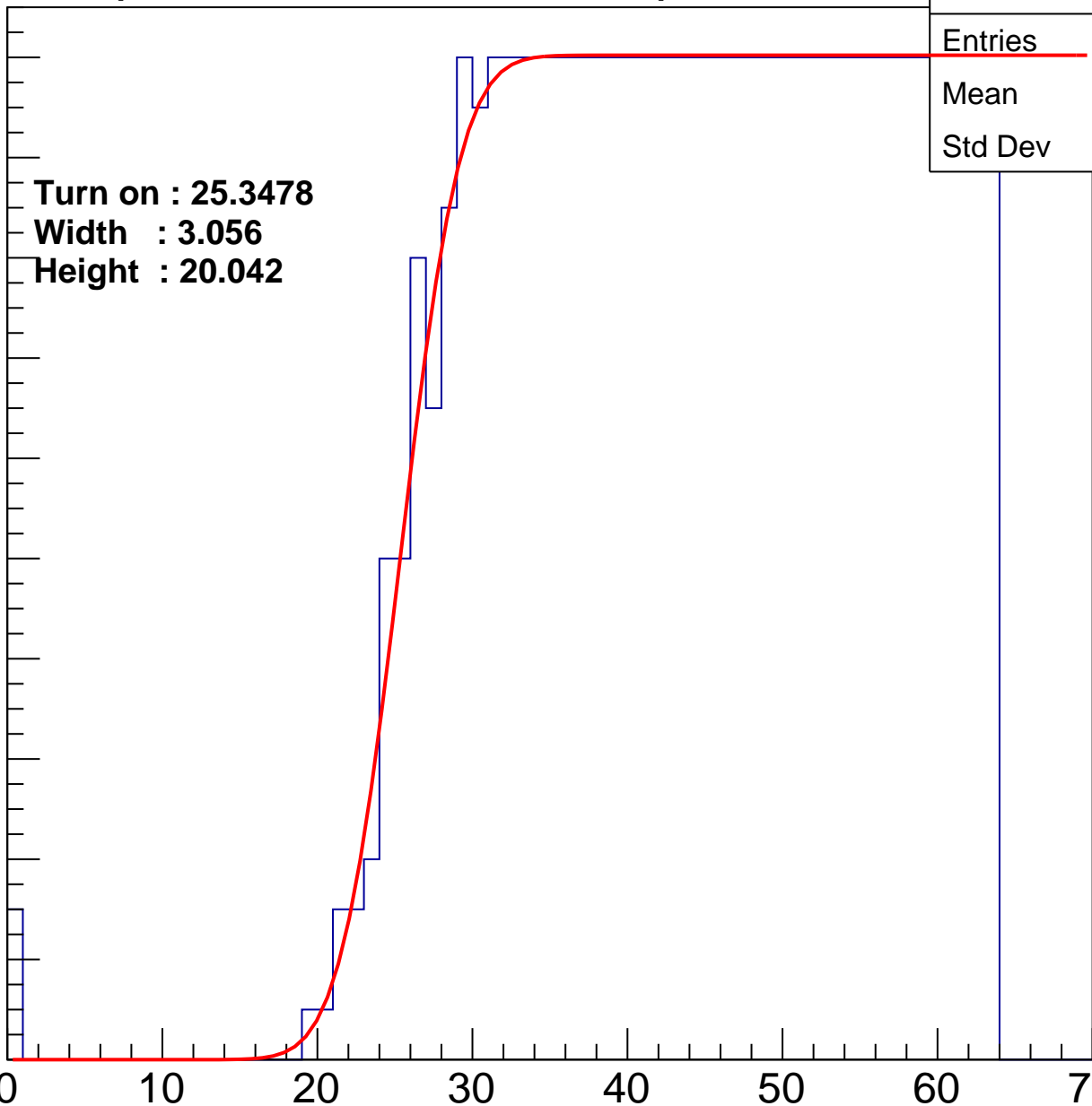
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3478
Width : 3.056
Height : 20.042

Entries	780
Mean	43.8
Std Dev	11.7

ampl



B0L101S, U23-ch20

calib_packv5_042523_0143.root, FC#1, port C1

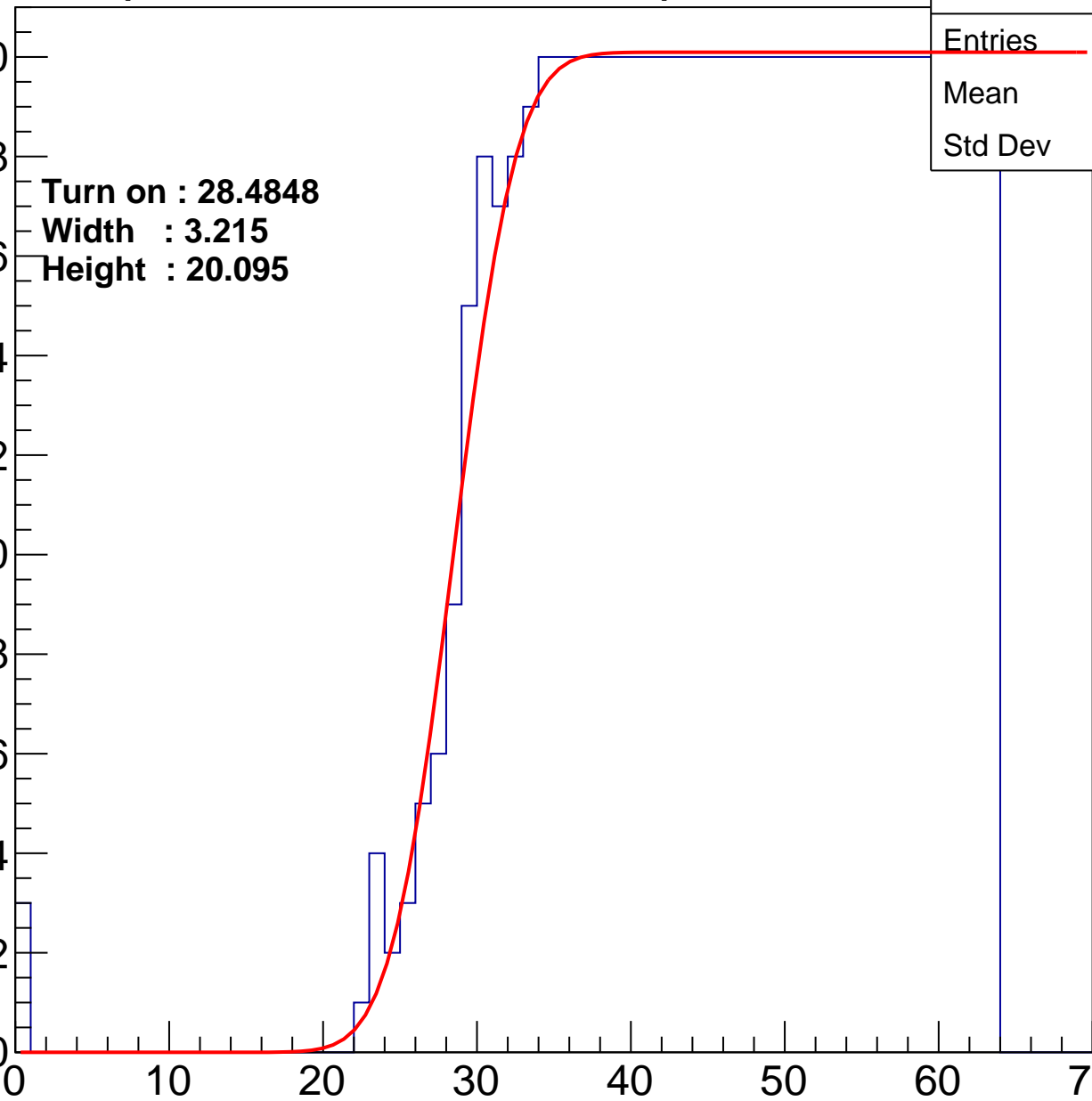
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4848
Width : 3.215
Height : 20.095

Entries	720
Mean	45.26
Std Dev	10.94

ampl



B0L101S, U23-ch21

calib_packv5_042523_0143.root, FC#1, port C1

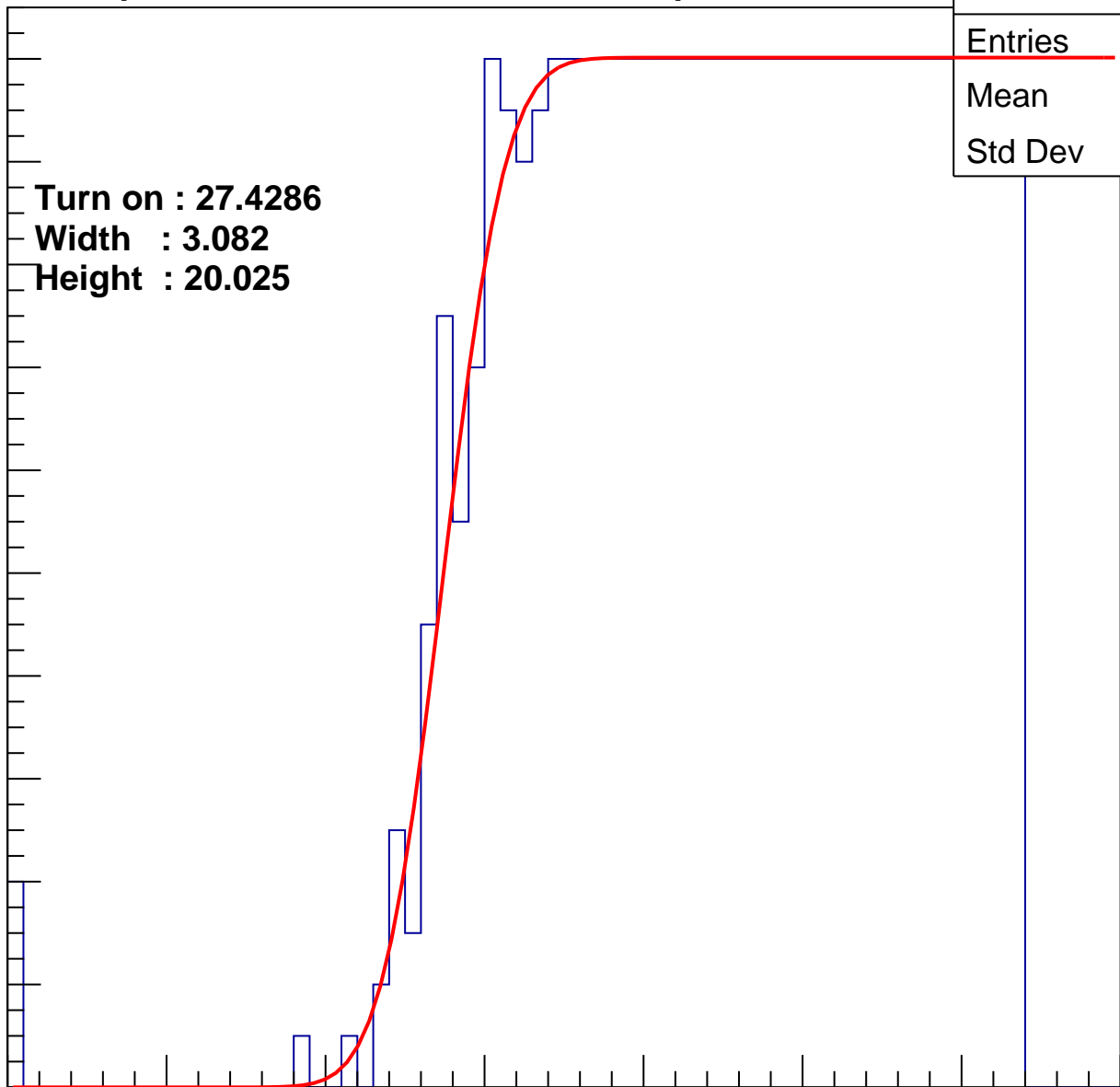
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4286
Width : 3.082
Height : 20.025

Entries	741
Mean	44.7
Std Dev	11.32

ampl



B0L101S, U23-ch22

calib_packv5_042523_0143.root, FC#1, port C1

Entry

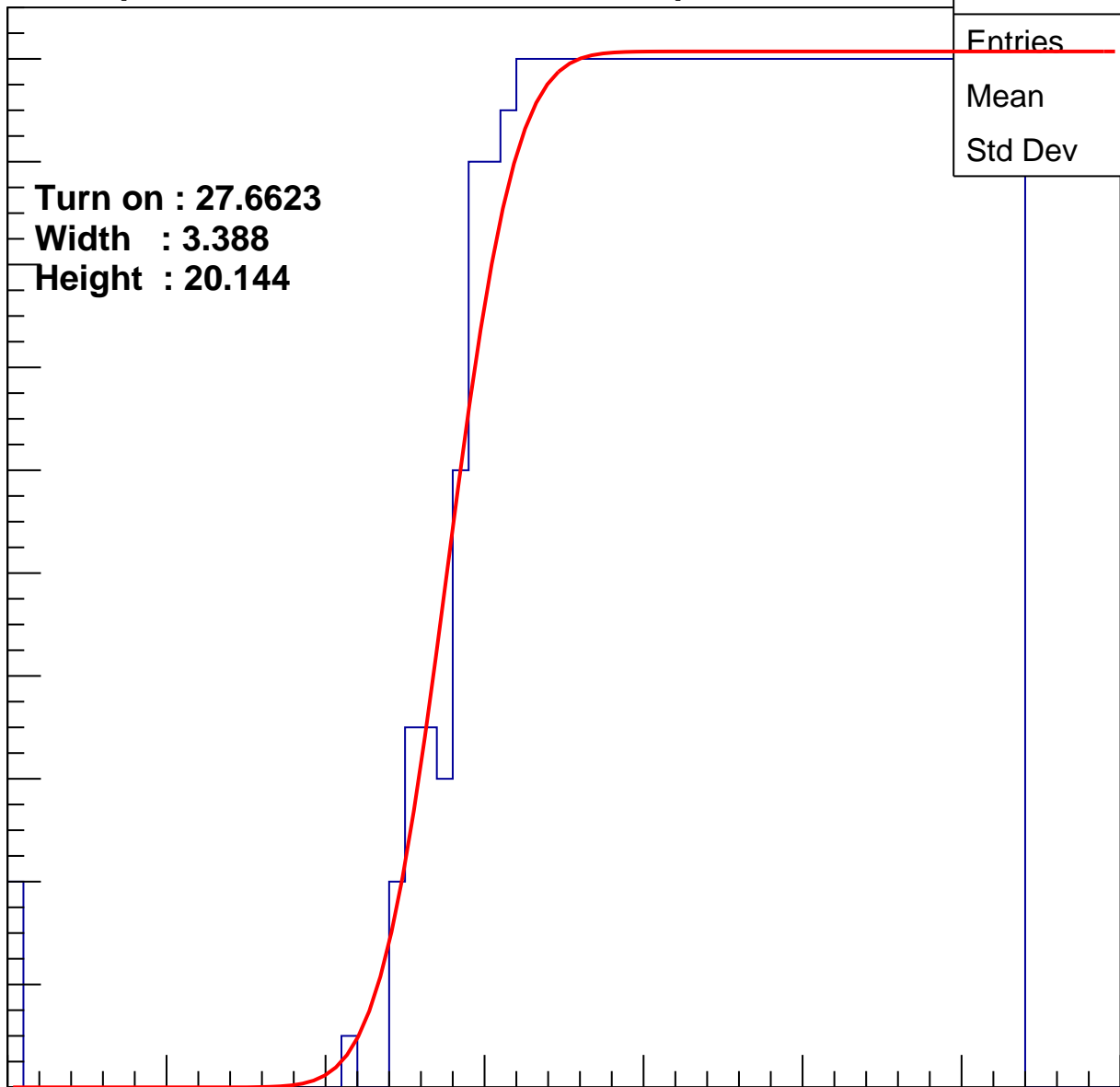
20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6623
Width : 3.388
Height : 20.144

Entries	736
Mean	44.87
Std Dev	11.19

ampl

0 10 20 30 40 50 60 70



B0L101S, U23-ch23

calib_packv5_042523_0143.root, FC#1, port C1

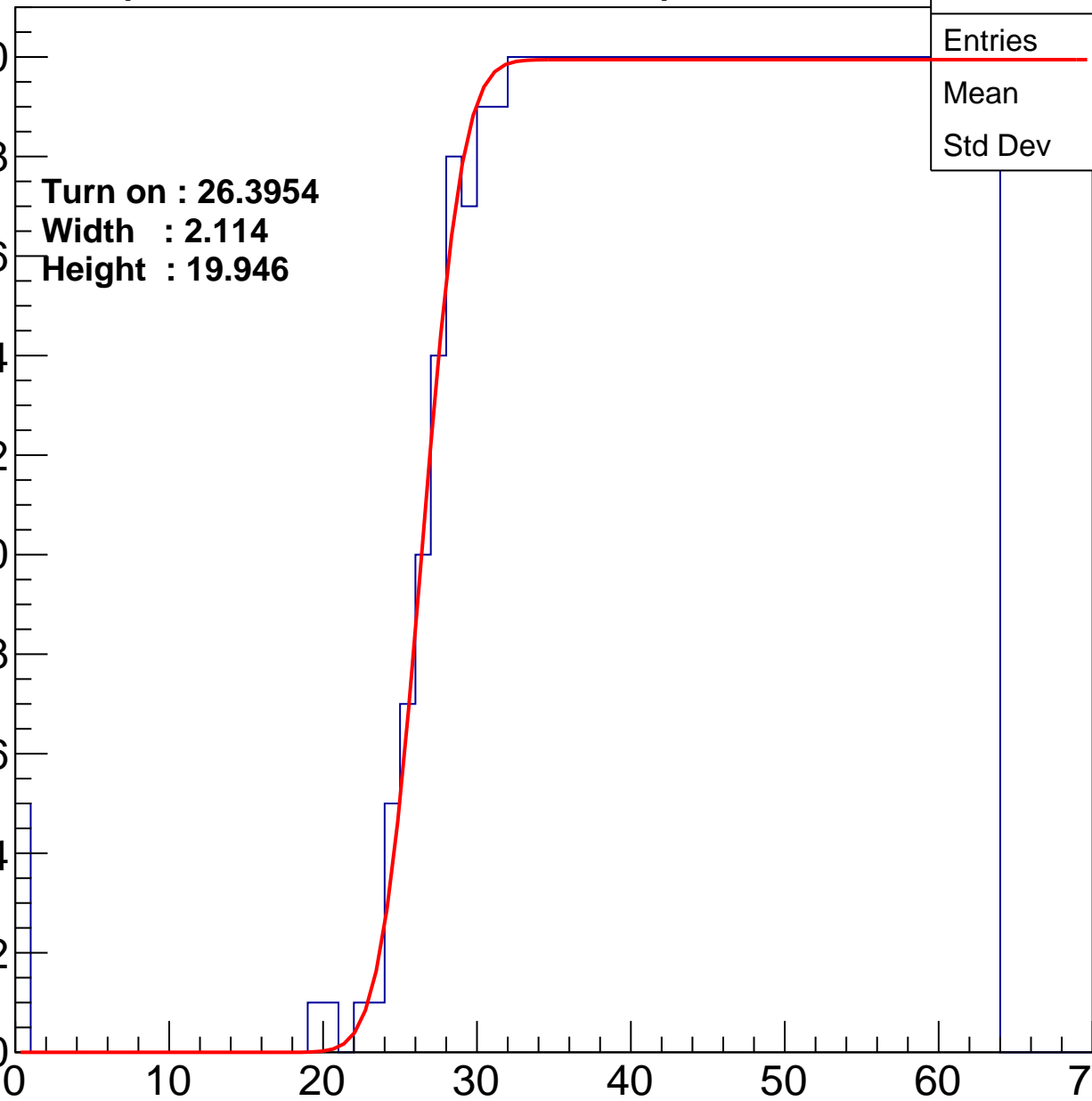
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3954
Width : 2.114
Height : 19.946

Entries	758
Mean	44.29
Std Dev	11.56

ampl



B0L101S, U23-ch24

calib_packv5_042523_0143.root, FC#1, port C1

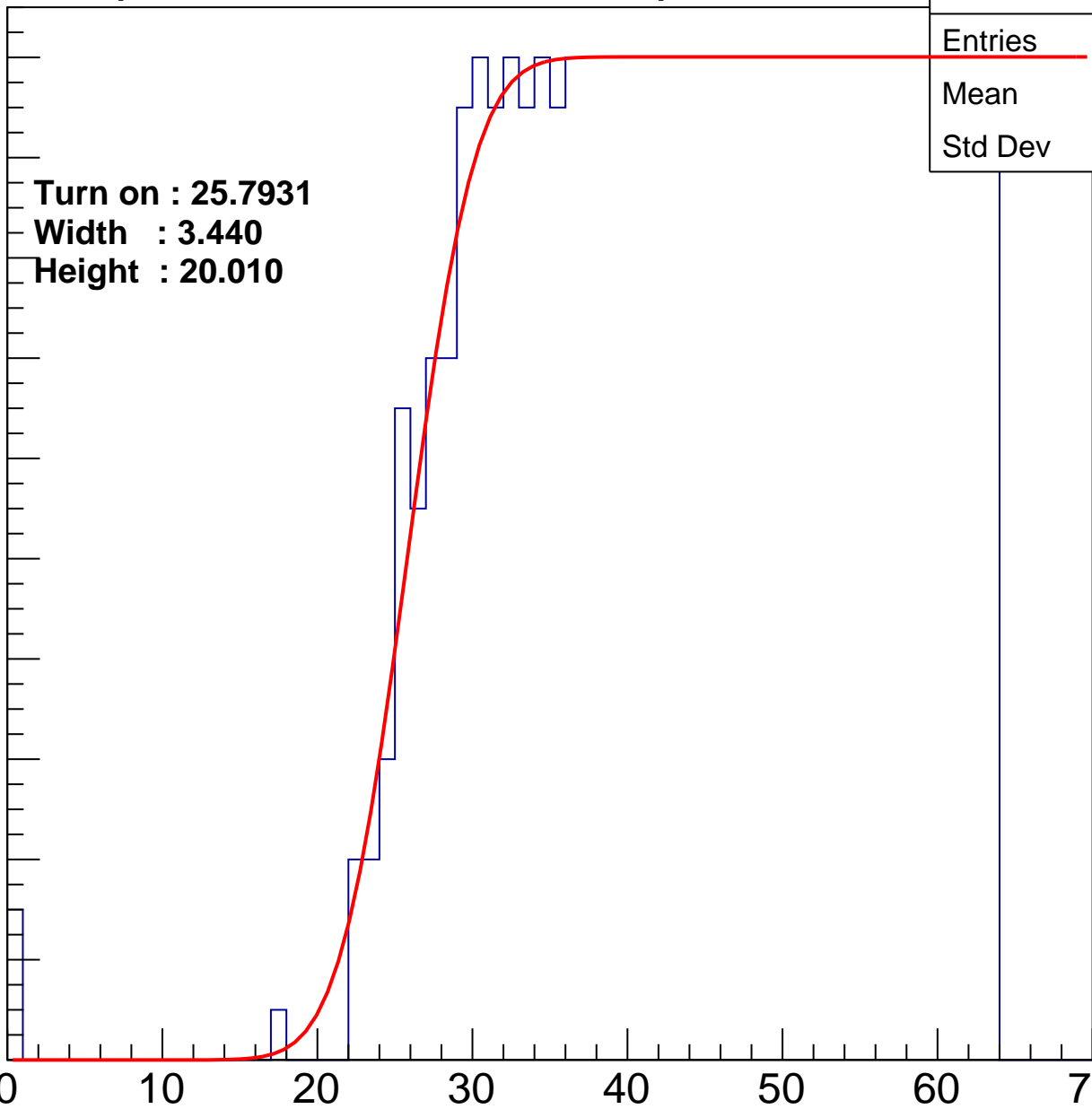
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7931
Width : 3.440
Height : 20.010

Entries	766
Mean	44.12
Std Dev	11.55

ampl



B0L101S, U23-ch25

calib_packv5_042523_0143.root, FC#1, port C1

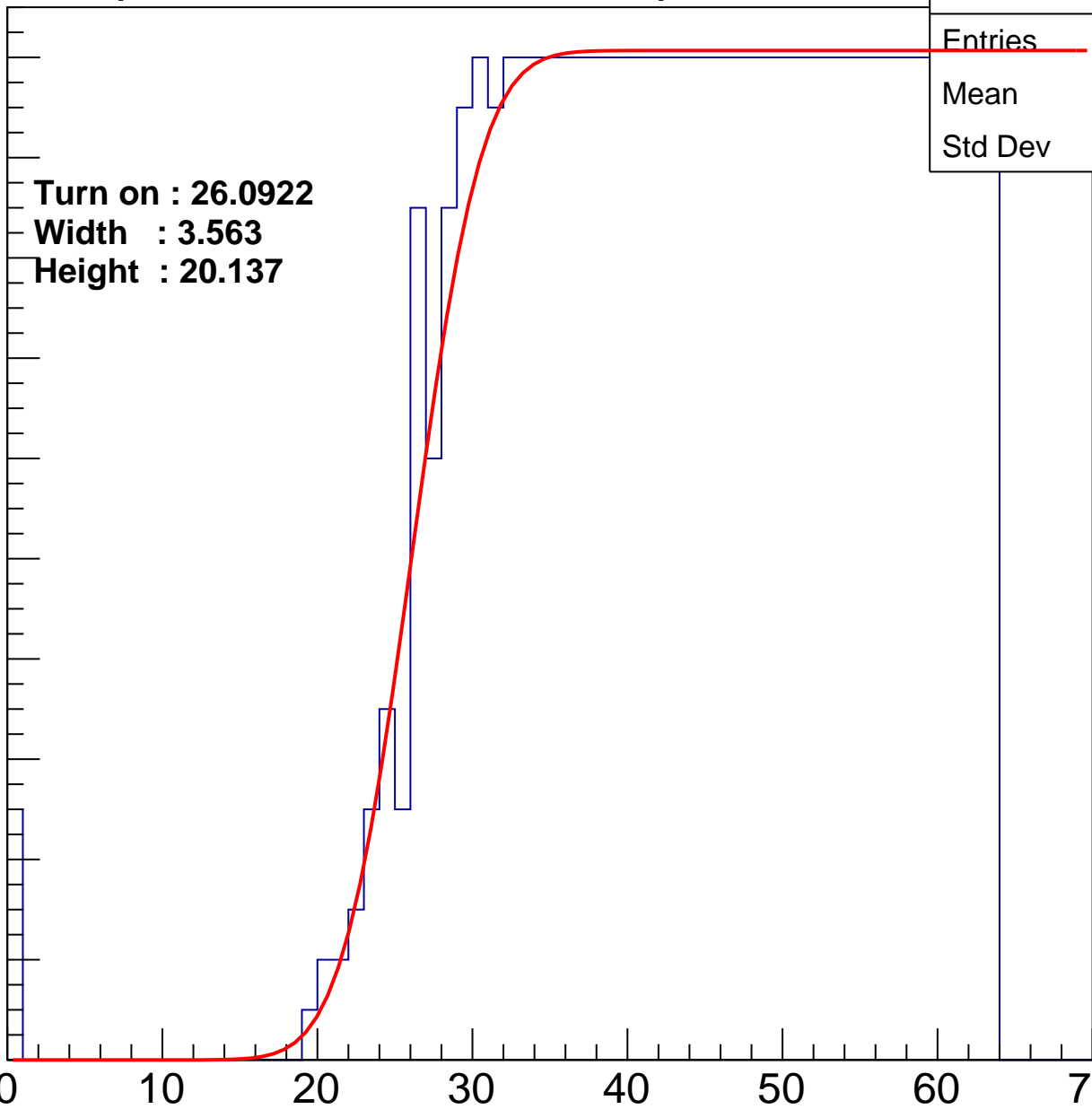
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0922
Width : 3.563
Height : 20.137

Entries	774
Mean	43.87
Std Dev	11.81

ampl



B0L101S, U23-ch26

calib_packv5_042523_0143.root, FC#1, port C1

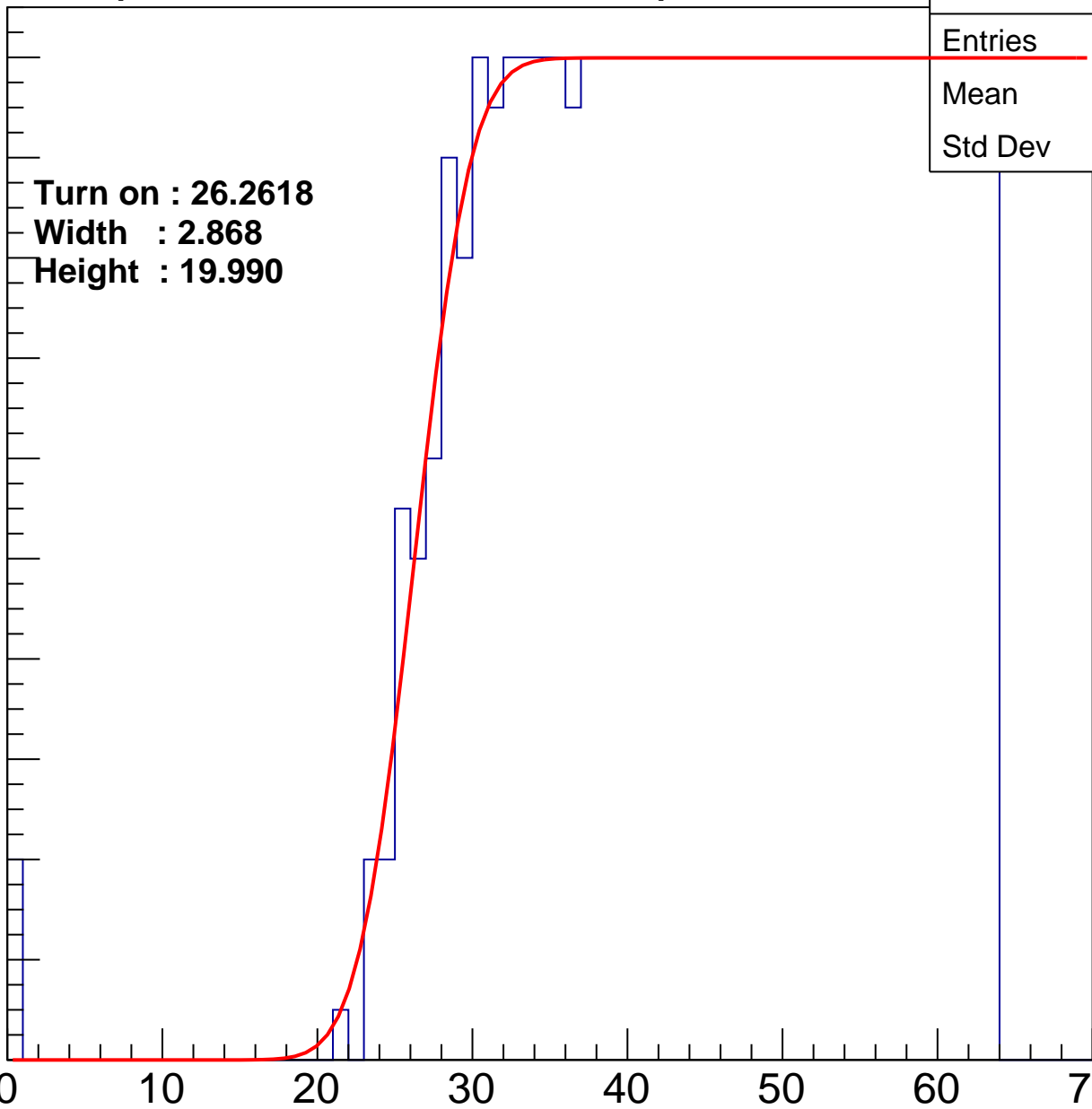
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2618
Width : 2.868
Height : 19.990

Entries	758
Mean	44.31
Std Dev	11.48

ampl



B0L101S, U23-ch27

calib_packv5_042523_0143.root, FC#1, port C1

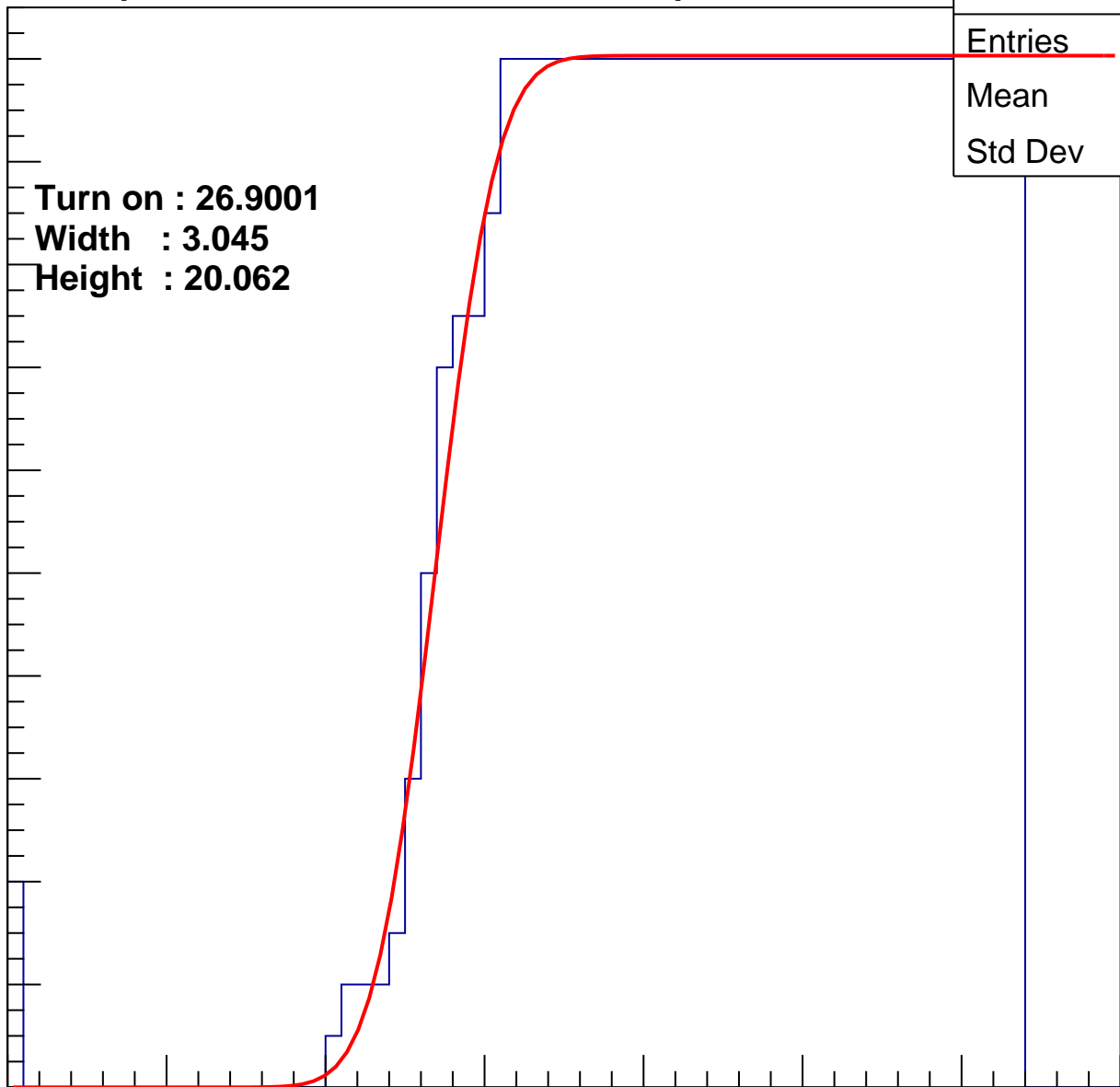
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9001
Width : 3.045
Height : 20.062

Entries	751
Mean	44.47
Std Dev	11.43

ampl



B0L101S, U23-ch28

calib_packv5_042523_0143.root, FC#1, port C1

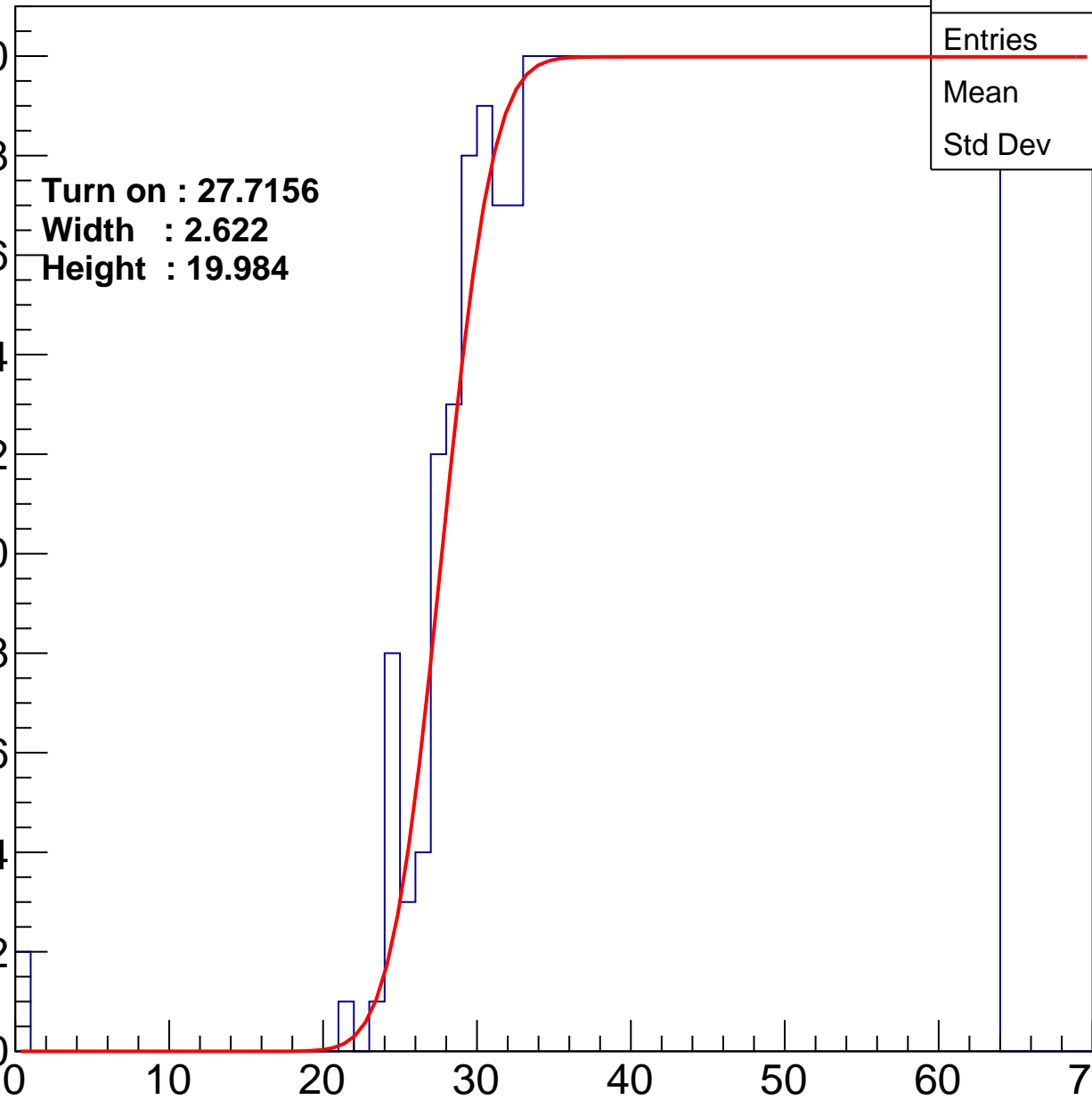
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7156
Width : 2.622
Height : 19.984

Entries	735
Mean	44.93
Std Dev	11.02

ampl



B0L101S, U23-ch29

calib_packv5_042523_0143.root, FC#1, port C1

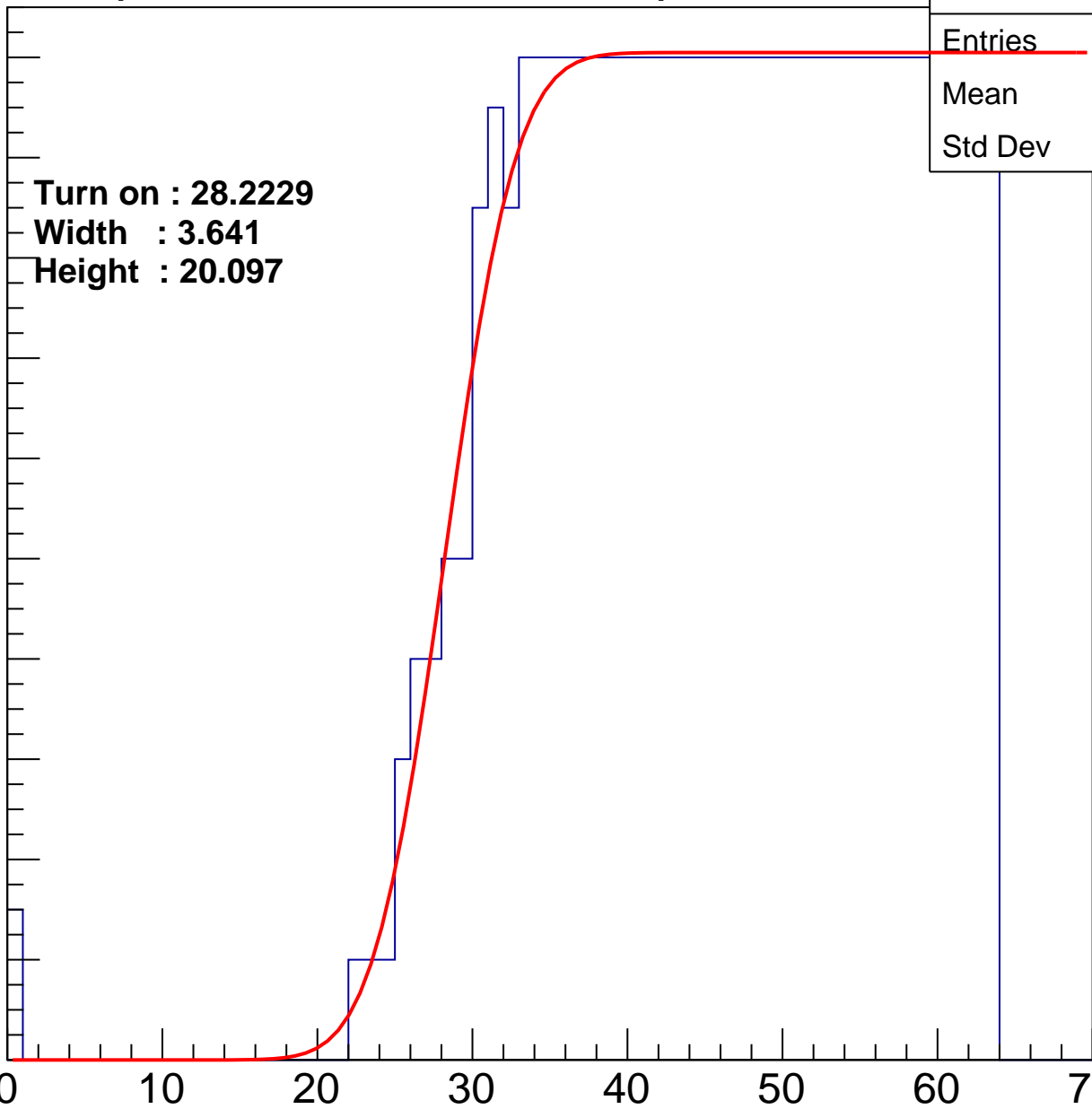
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.2229
Width : 3.641
Height : 20.097

Entries	724
Mean	45.15
Std Dev	11.02

ampl



B0L101S, U23-ch30

calib_packv5_042523_0143.root, FC#1, port C1

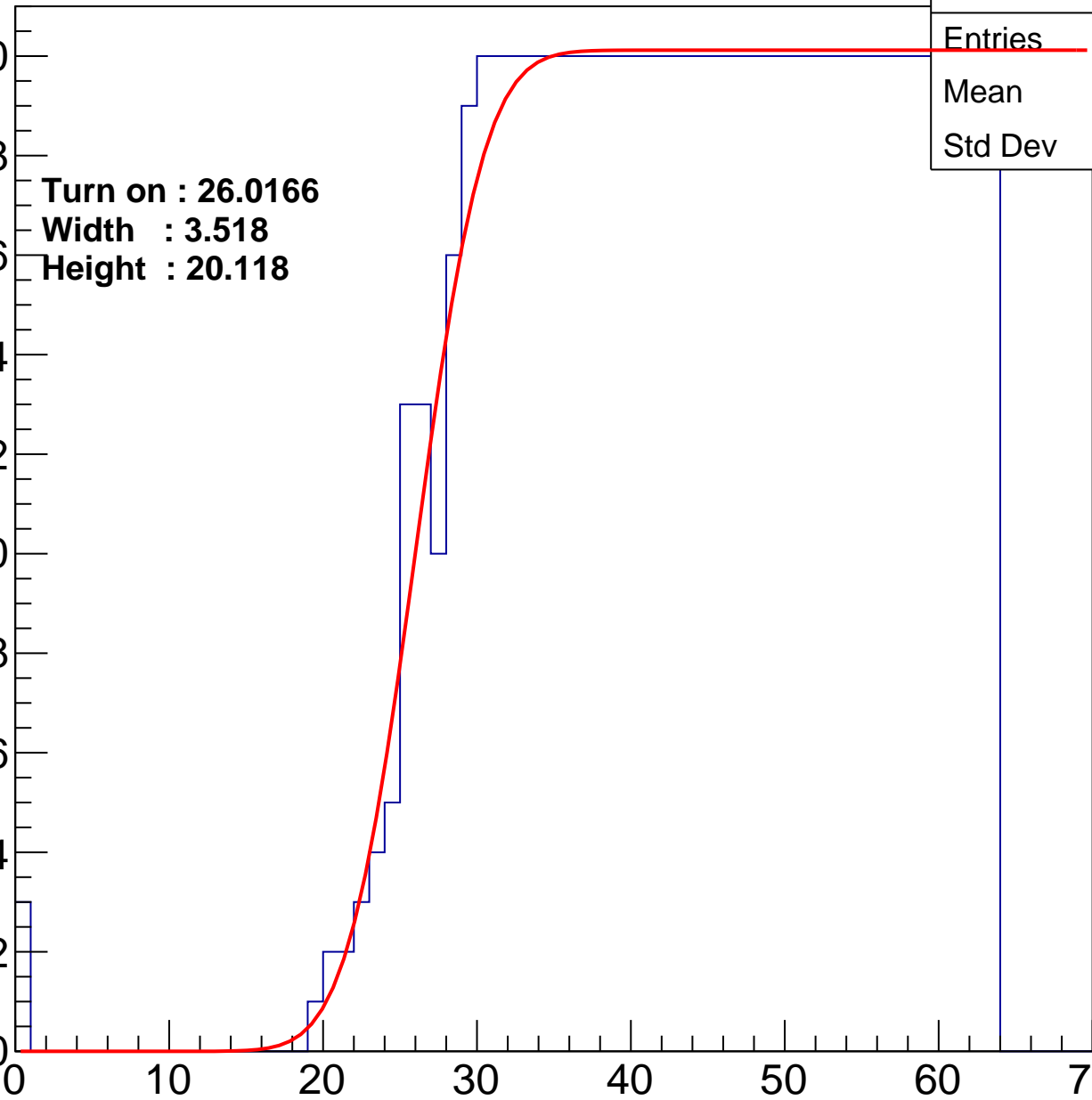
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0166
Width : 3.518
Height : 20.118

Entries	771
Mean	44.01
Std Dev	11.6

ampl



B0L101S, U23-ch31

calib_packv5_042523_0143.root, FC#1, port C1

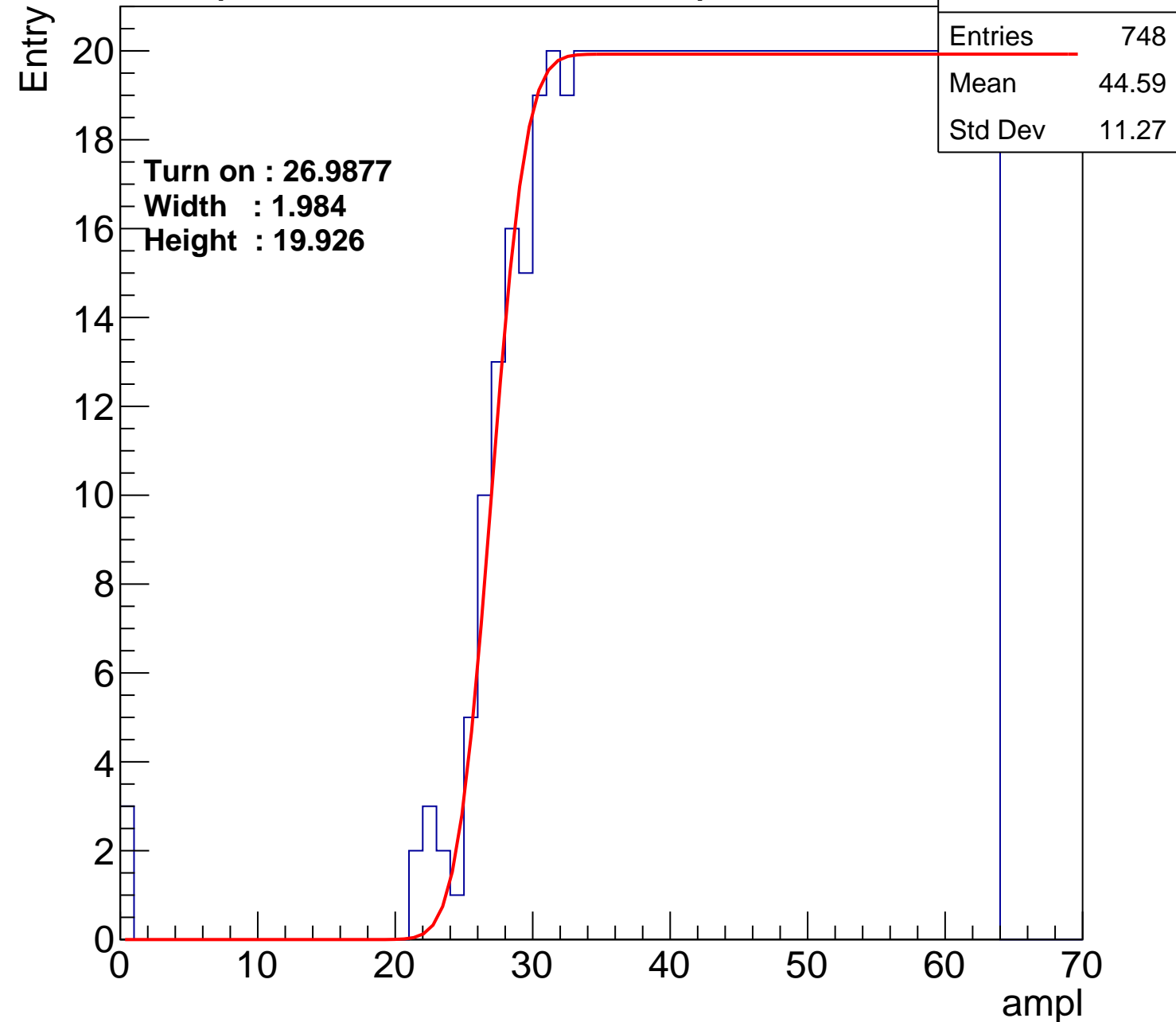
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9877
Width : 1.984
Height : 19.926

Entries	748
Mean	44.59
Std Dev	11.27

ampl



B0L101S, U23-ch32

calib_packv5_042523_0143.root, FC#1, port C1

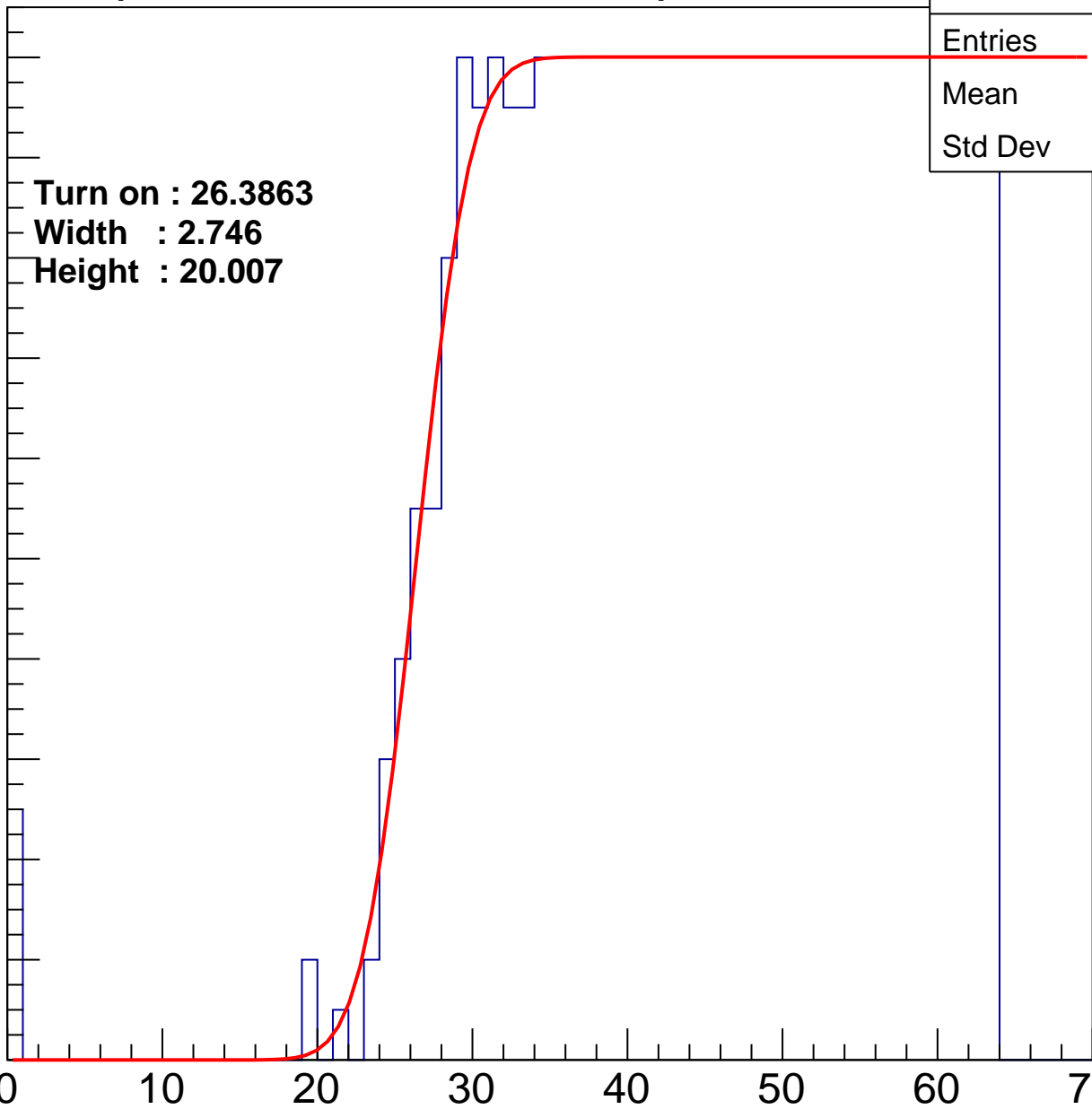
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3863
Width : 2.746
Height : 20.007

Entries	759
Mean	44.25
Std Dev	11.6

ampl



B0L101S, U23-ch33

calib_packv5_042523_0143.root, FC#1, port C1

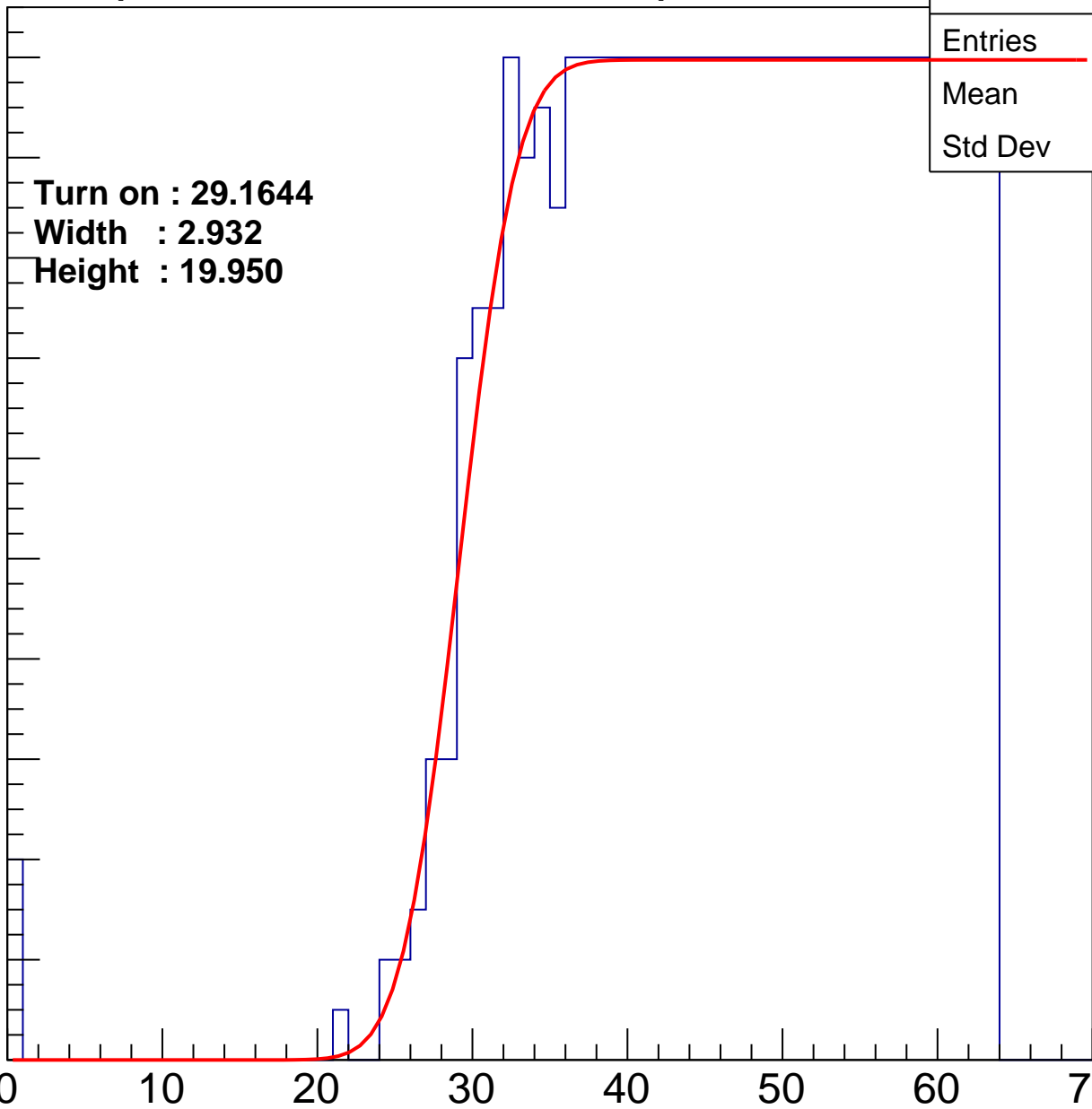
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.1644
Width : 2.932
Height : 19.950

Entries	702
Mean	45.65
Std Dev	10.85

ampl



B0L101S, U23-ch34

calib_packv5_042523_0143.root, FC#1, port C1

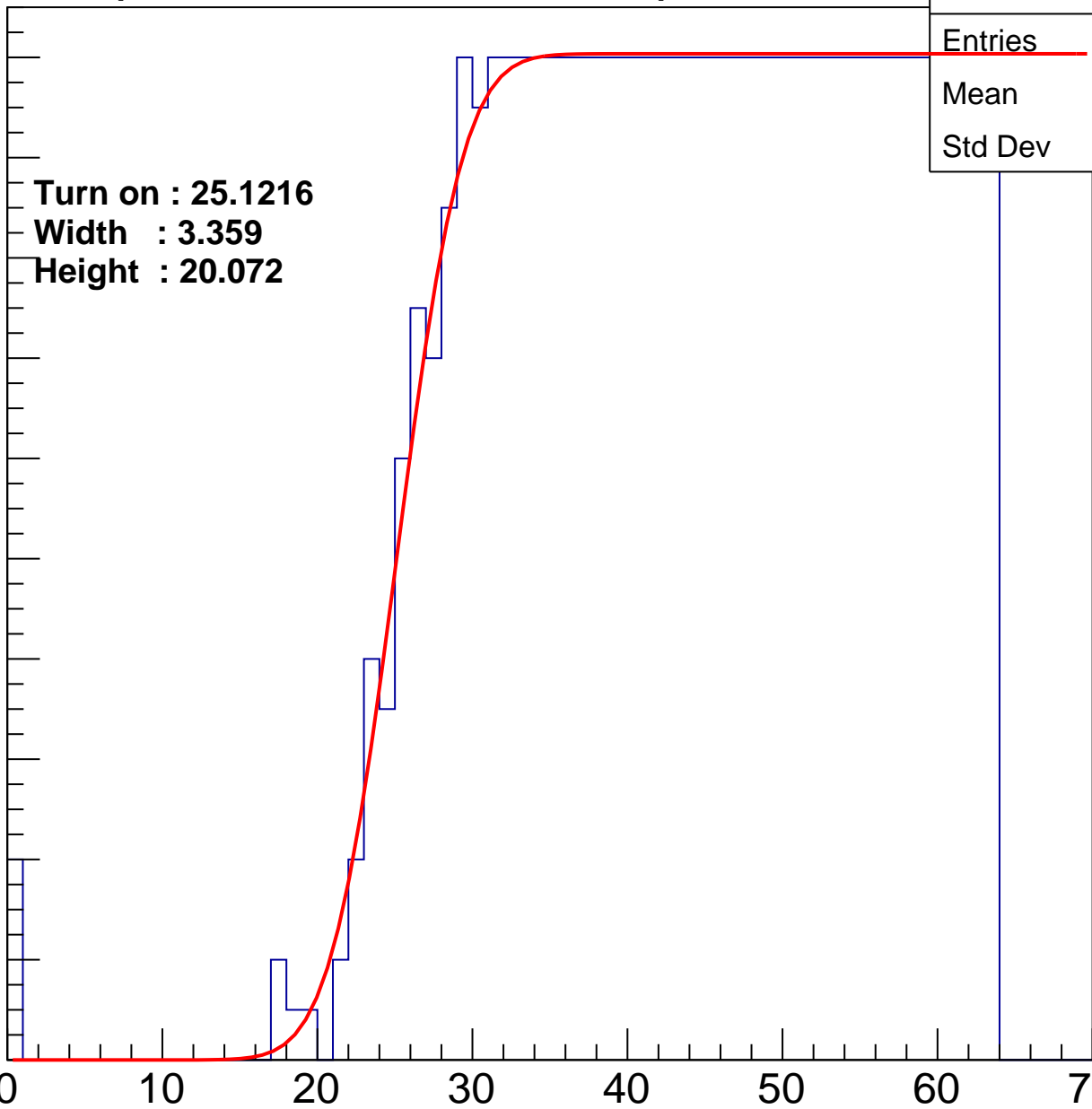
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1216
Width : 3.359
Height : 20.072

Entries	786
Mean	43.6
Std Dev	11.9

ampl



B0L101S, U23-ch35

calib_packv5_042523_0143.root, FC#1, port C1

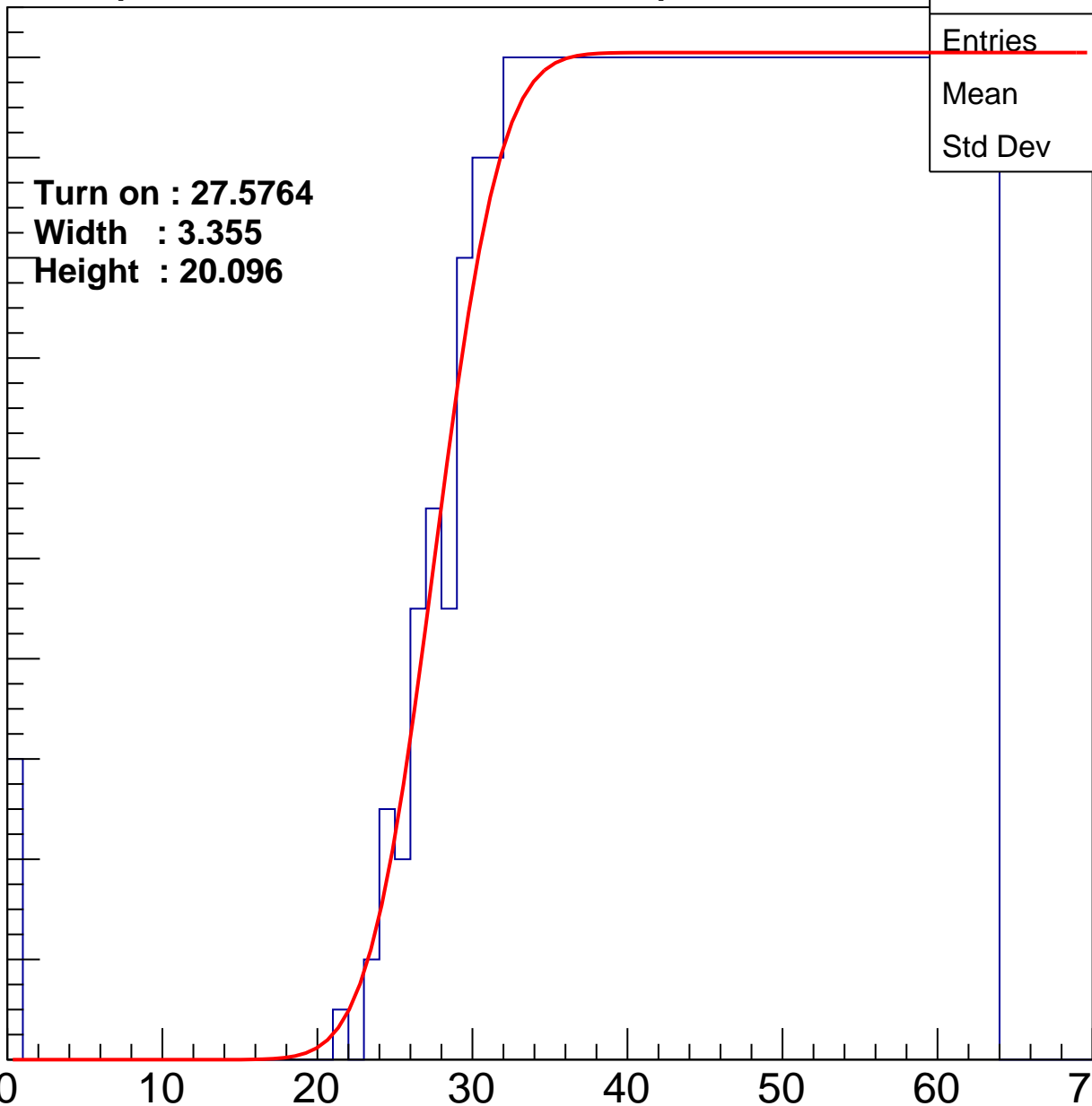
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5764
Width : 3.355
Height : 20.096

Entries	739
Mean	44.7
Std Dev	11.46

ampl



B0L101S, U23-ch36

calib_packv5_042523_0143.root, FC#1, port C1

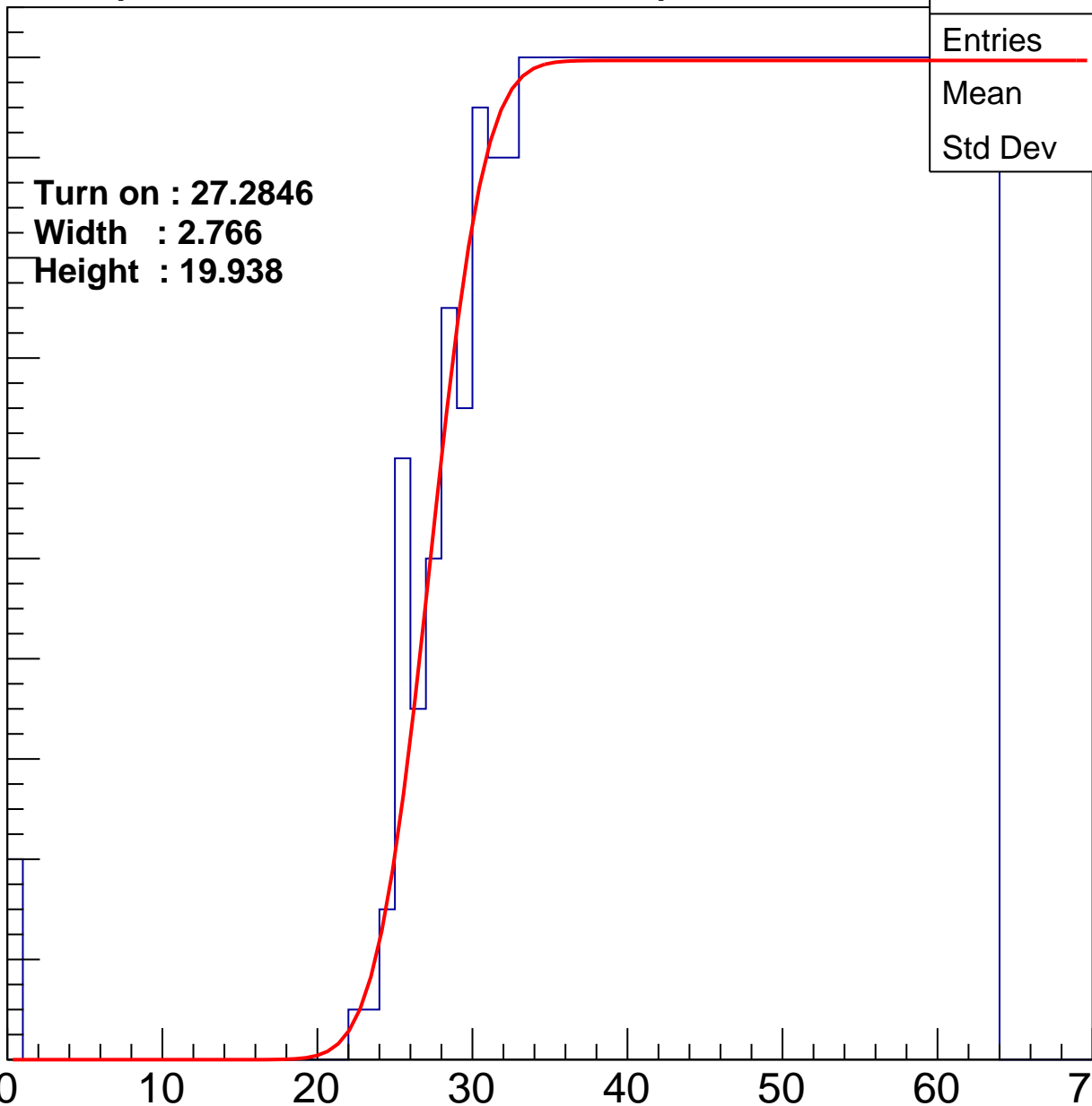
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2846
Width : 2.766
Height : 19.938

Entries	741
Mean	44.71
Std Dev	11.3

ampl



B0L101S, U23-ch37

calib_packv5_042523_0143.root, FC#1, port C1

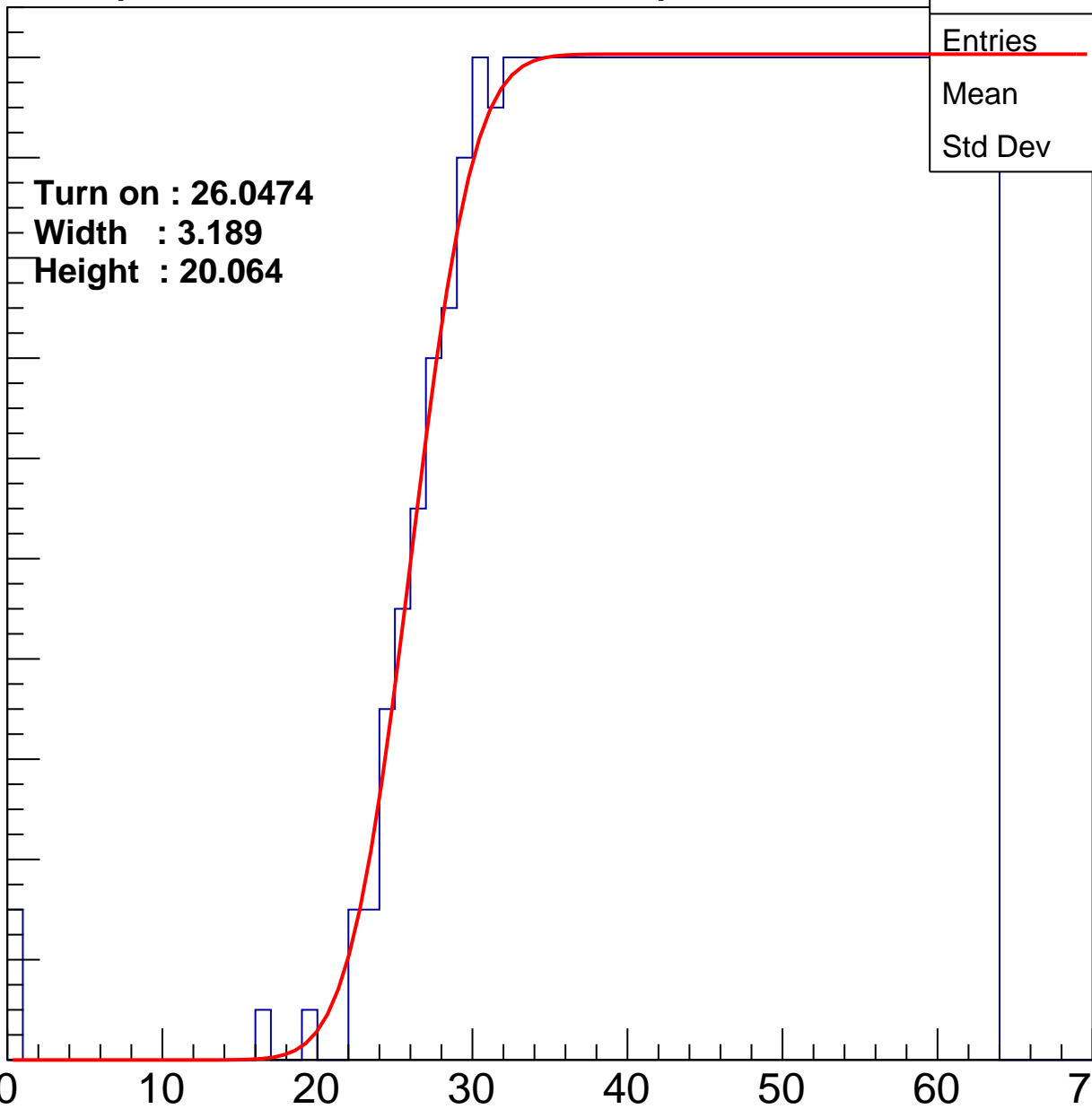
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0474
Width : 3.189
Height : 20.064

Entries	764
Mean	44.19
Std Dev	11.5

ampl



B0L101S, U23-ch38

calib_packv5_042523_0143.root, FC#1, port C1

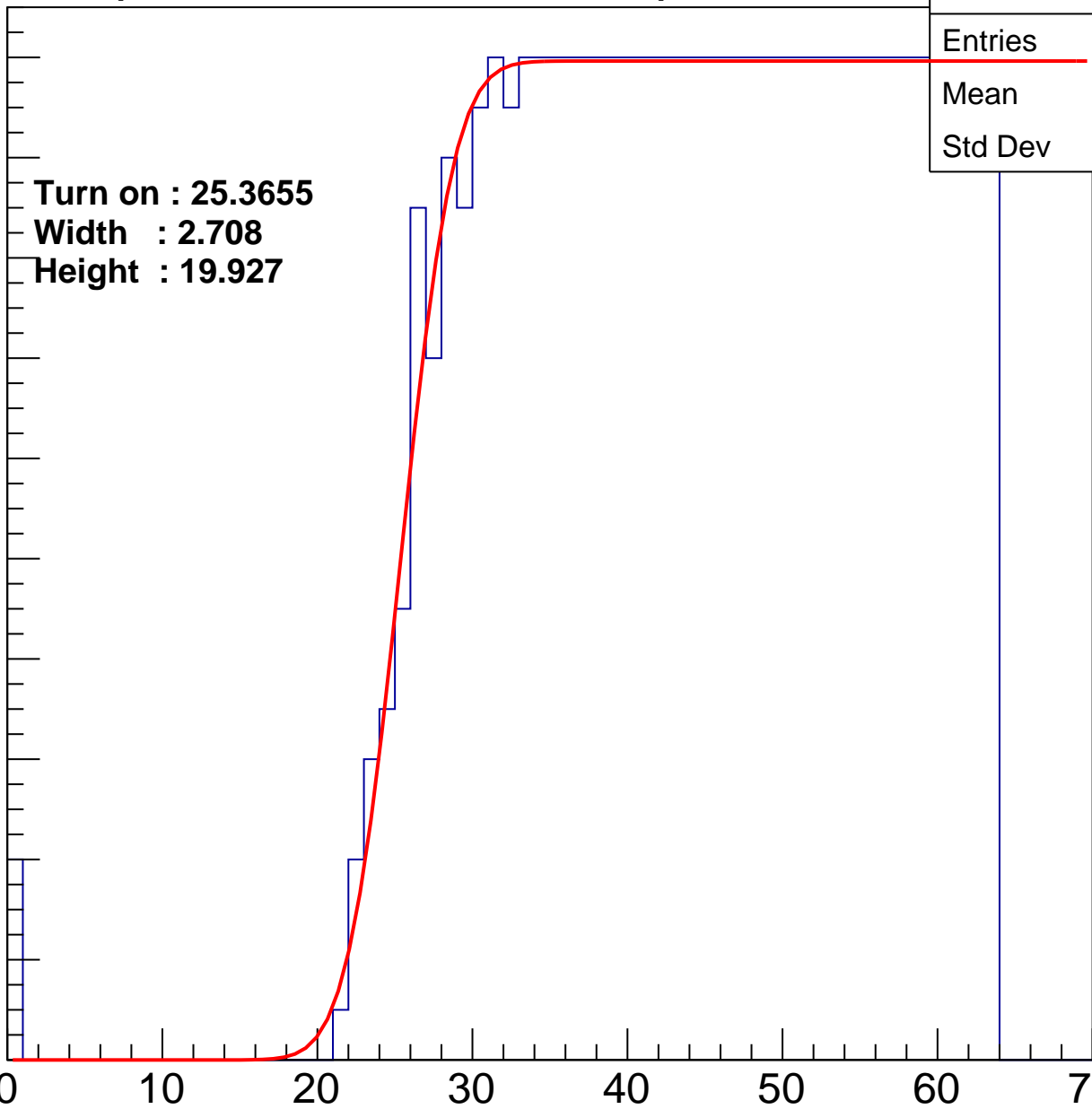
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3655
Width : 2.708
Height : 19.927

Entries	775
Mean	43.89
Std Dev	11.71

ampl



B0L101S, U23-ch39

calib_packv5_042523_0143.root, FC#1, port C1

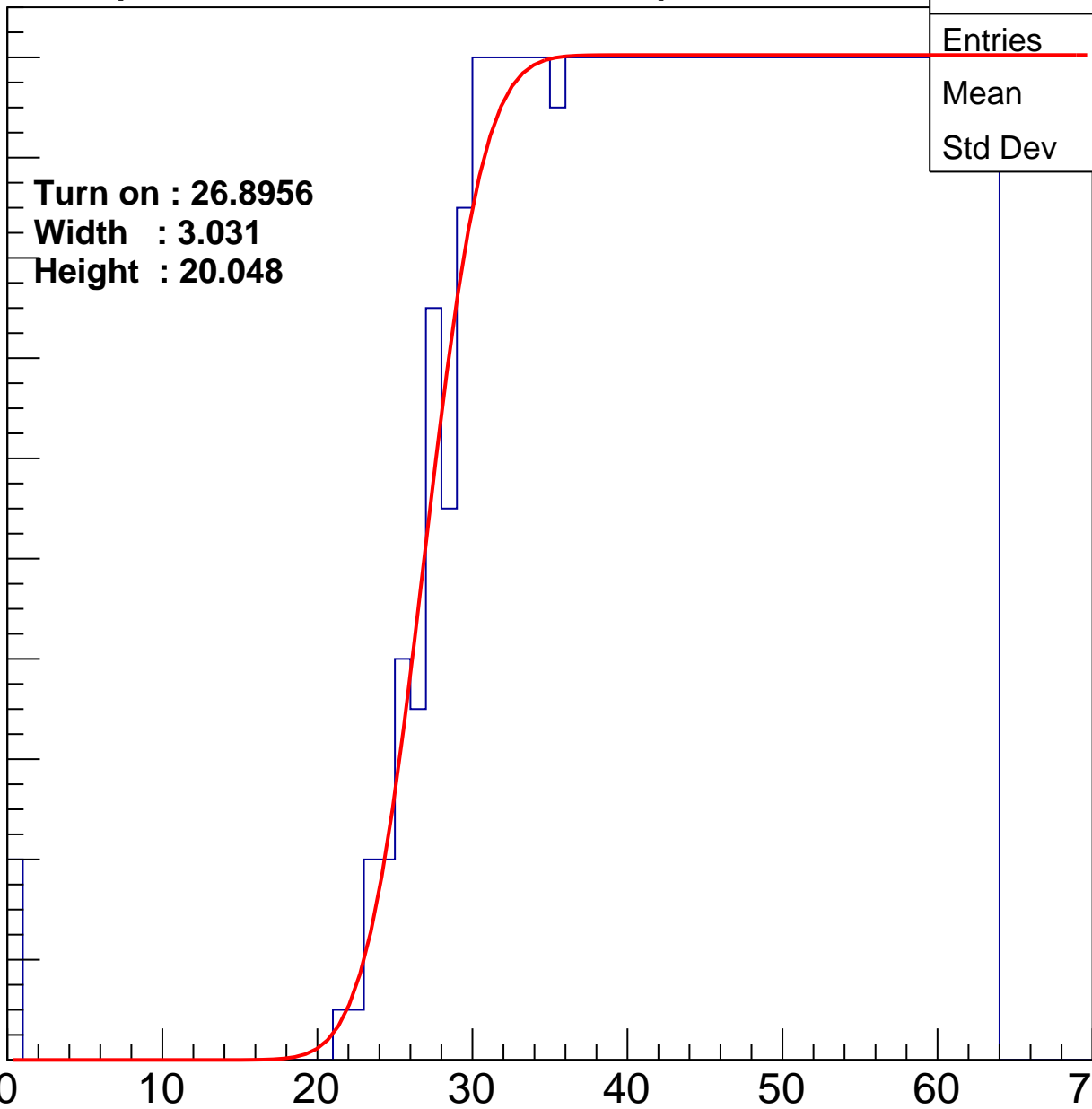
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8956
Width : 3.031
Height : 20.048

Entries	751
Mean	44.48
Std Dev	11.41

ampl



B0L101S, U23-ch40

calib_packv5_042523_0143.root, FC#1, port C1

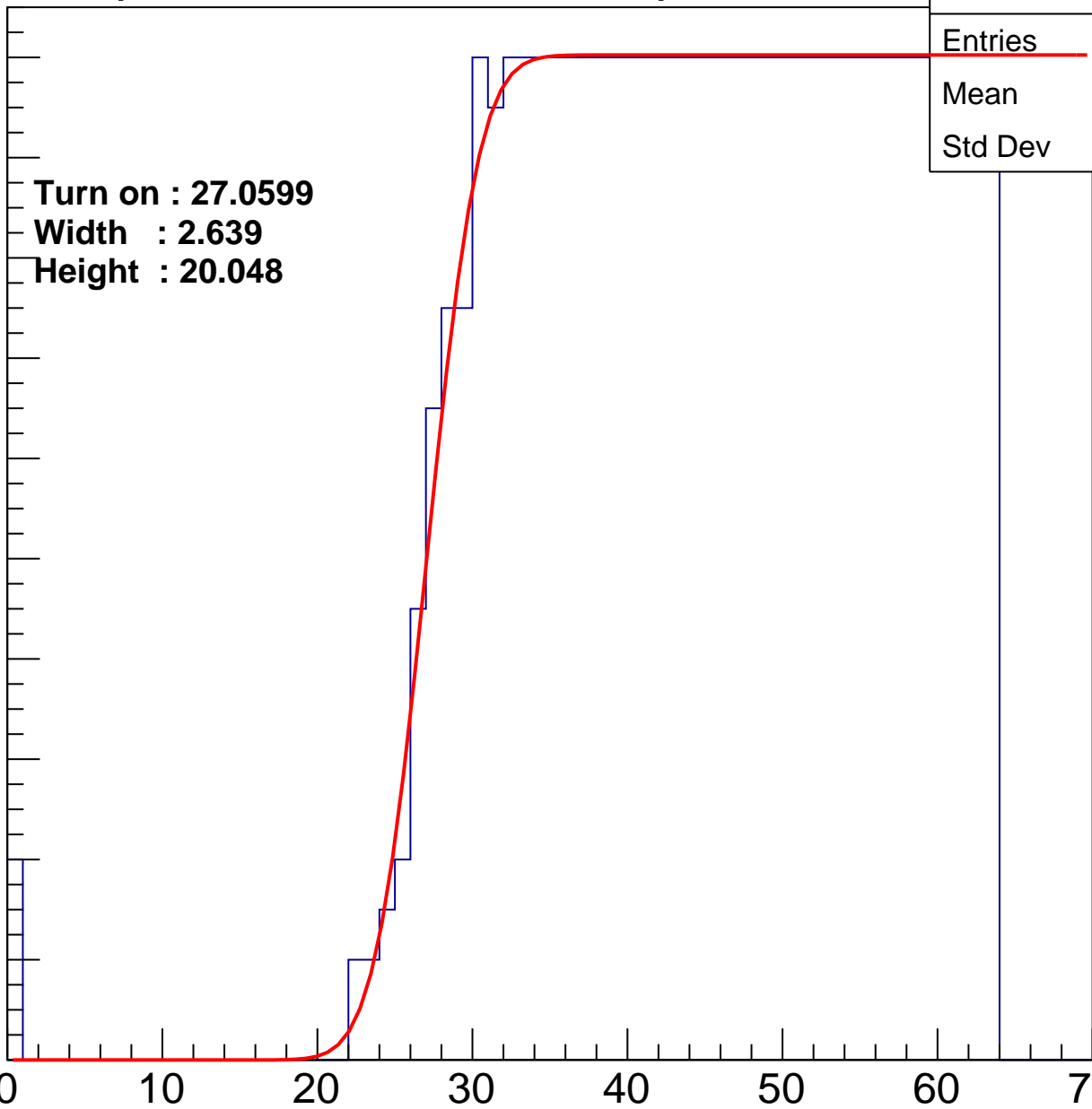
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0599
Width : 2.639
Height : 20.048

Entries	746
Mean	44.63
Std Dev	11.31

ampl



B0L101S, U23-ch41

calib_packv5_042523_0143.root, FC#1, port C1

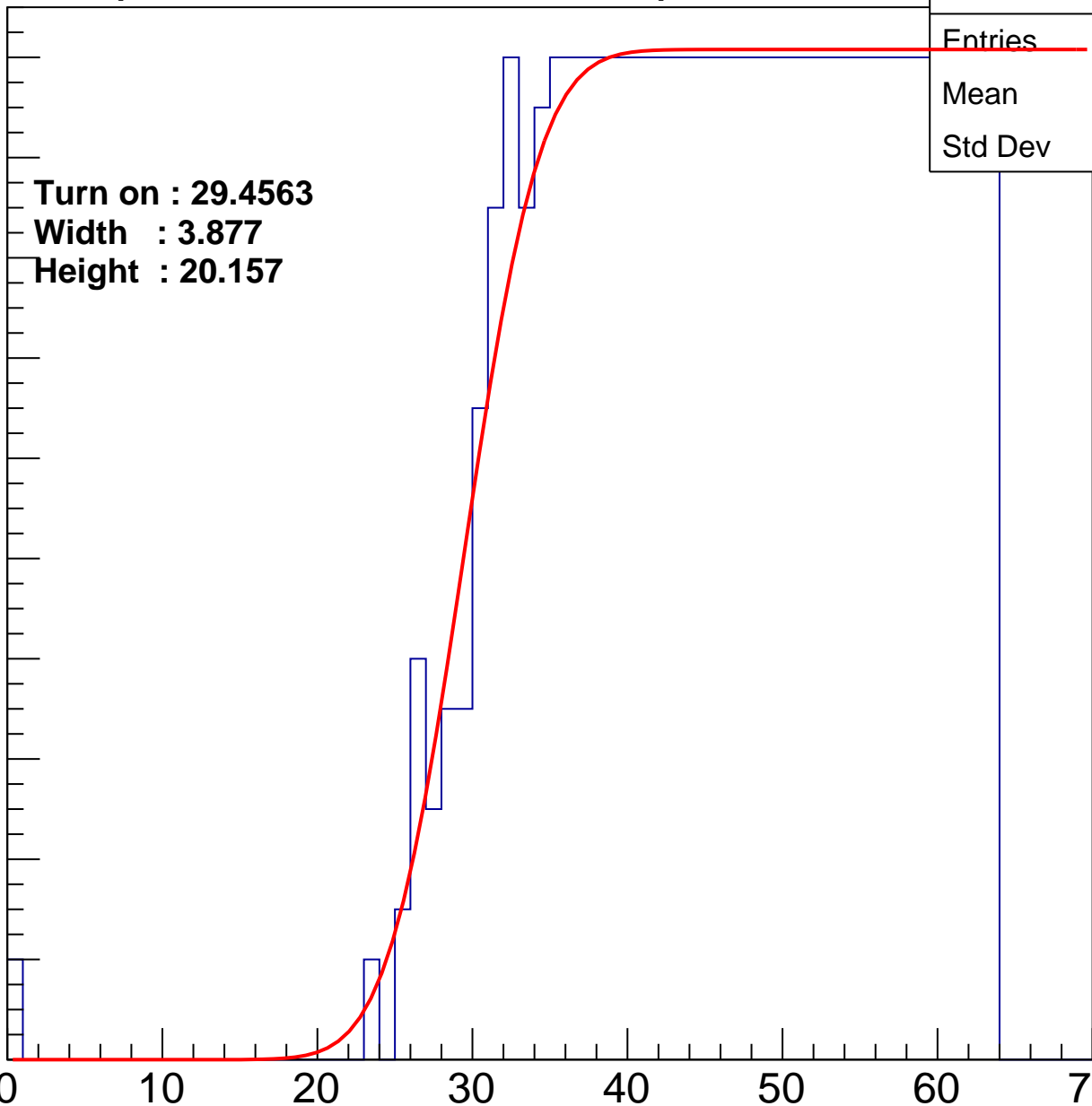
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.4563
Width : 3.877
Height : 20.157

Entries	700
Mean	45.78
Std Dev	10.58

ampl



B0L101S, U23-ch42

calib_packv5_042523_0143.root, FC#1, port C1

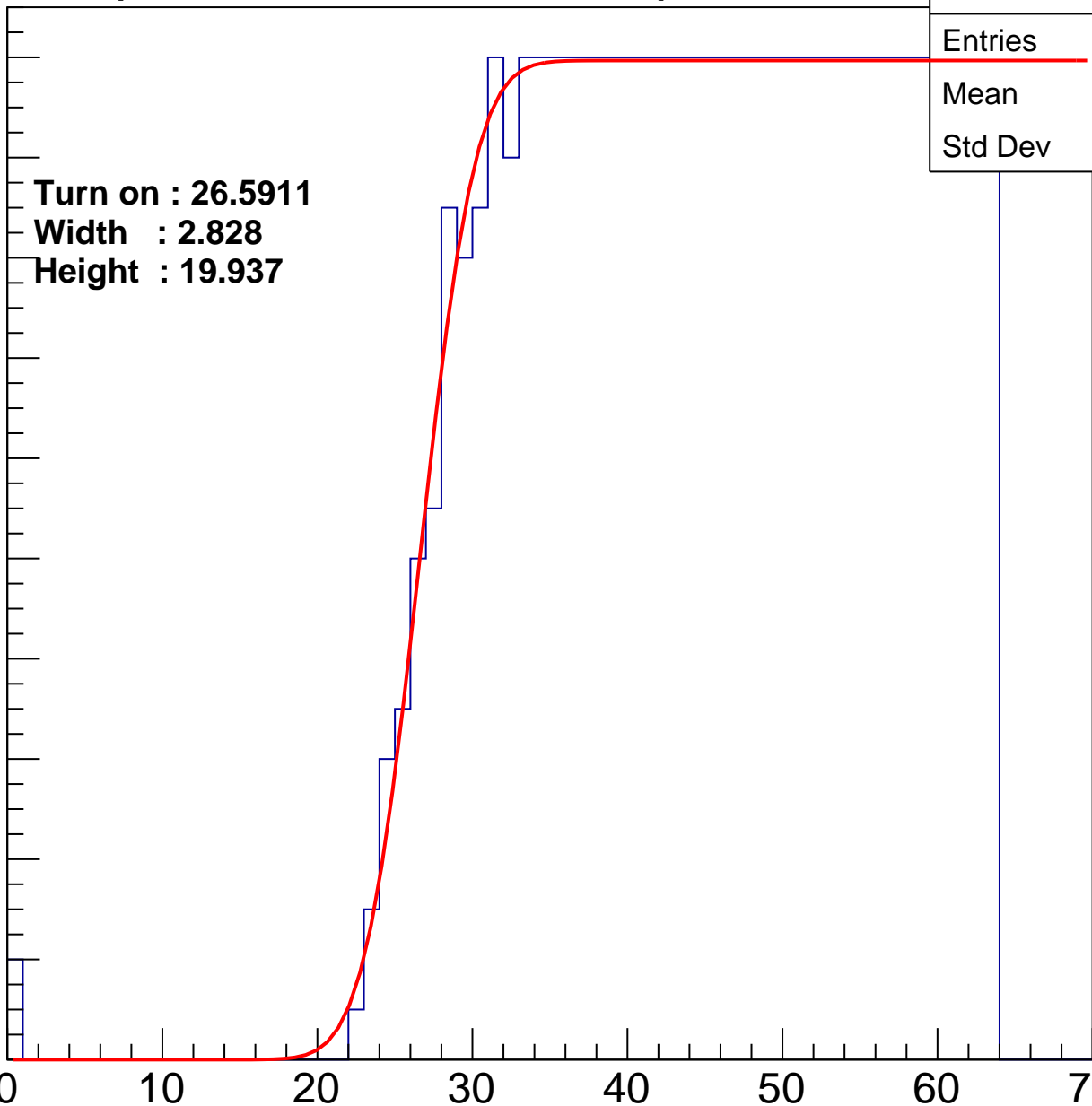
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5911
Width : 2.828
Height : 19.937

Entries	748
Mean	44.62
Std Dev	11.18

ampl



B0L101S, U23-ch43

calib_packv5_042523_0143.root, FC#1, port C1

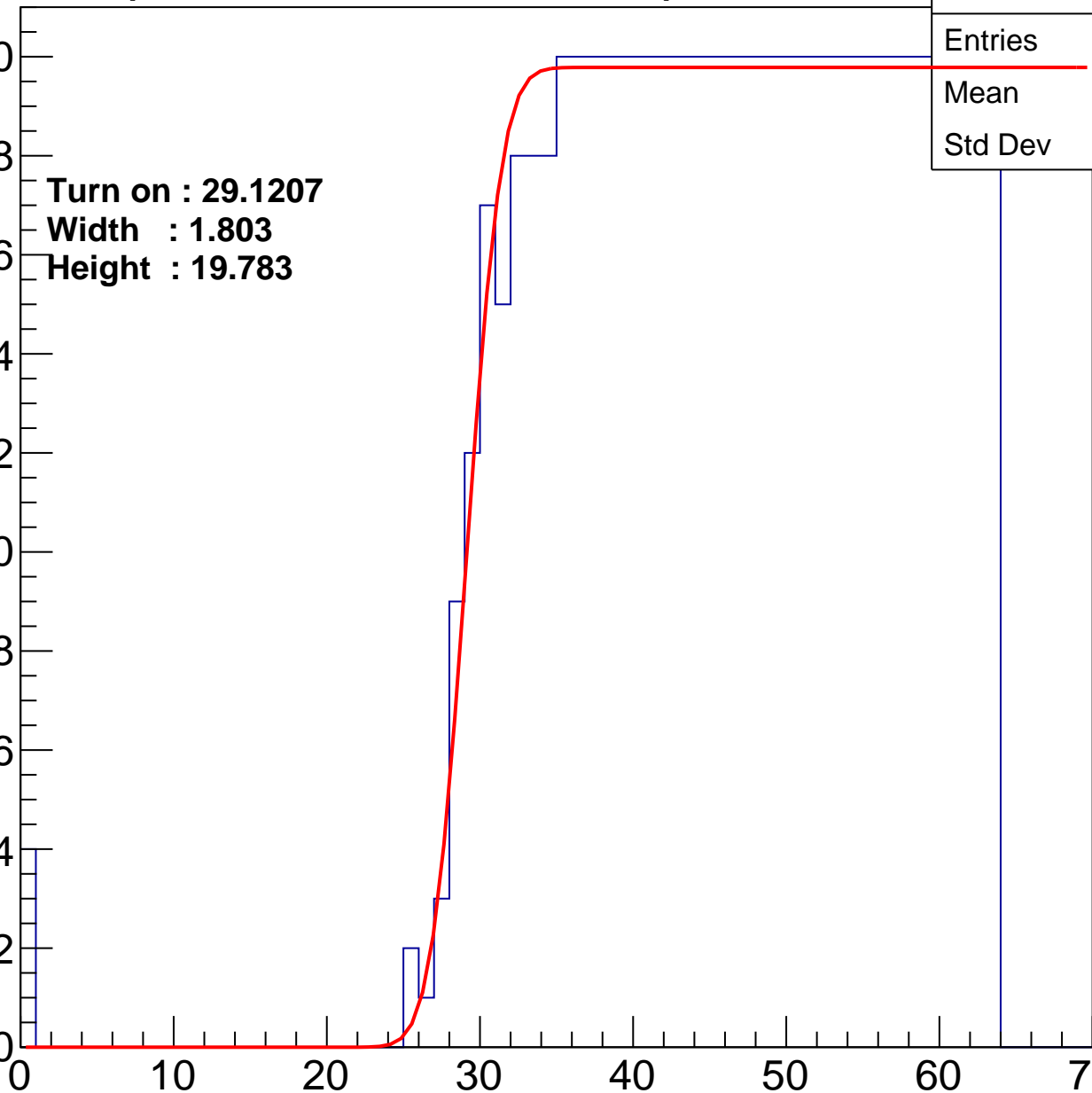
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.1207
Width : 1.803
Height : 19.783

Entries	697
Mean	45.82
Std Dev	10.71

ampl



B0L101S, U23-ch44

calib_packv5_042523_0143.root, FC#1, port C1

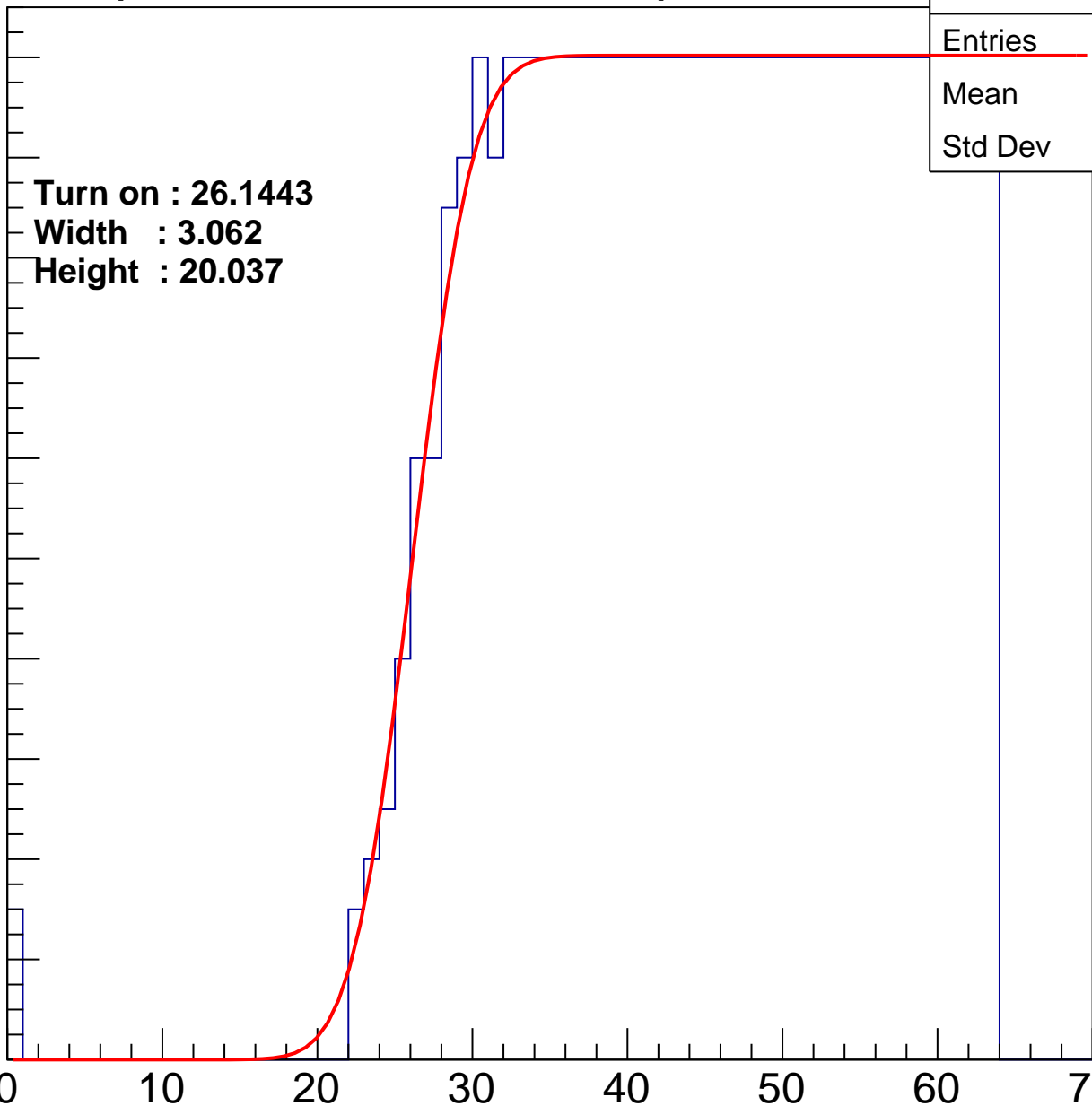
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1443
Width : 3.062
Height : 20.037

Entries	760
Mean	44.3
Std Dev	11.41

ampl



B0L101S, U23-ch45

calib_packv5_042523_0143.root, FC#1, port C1

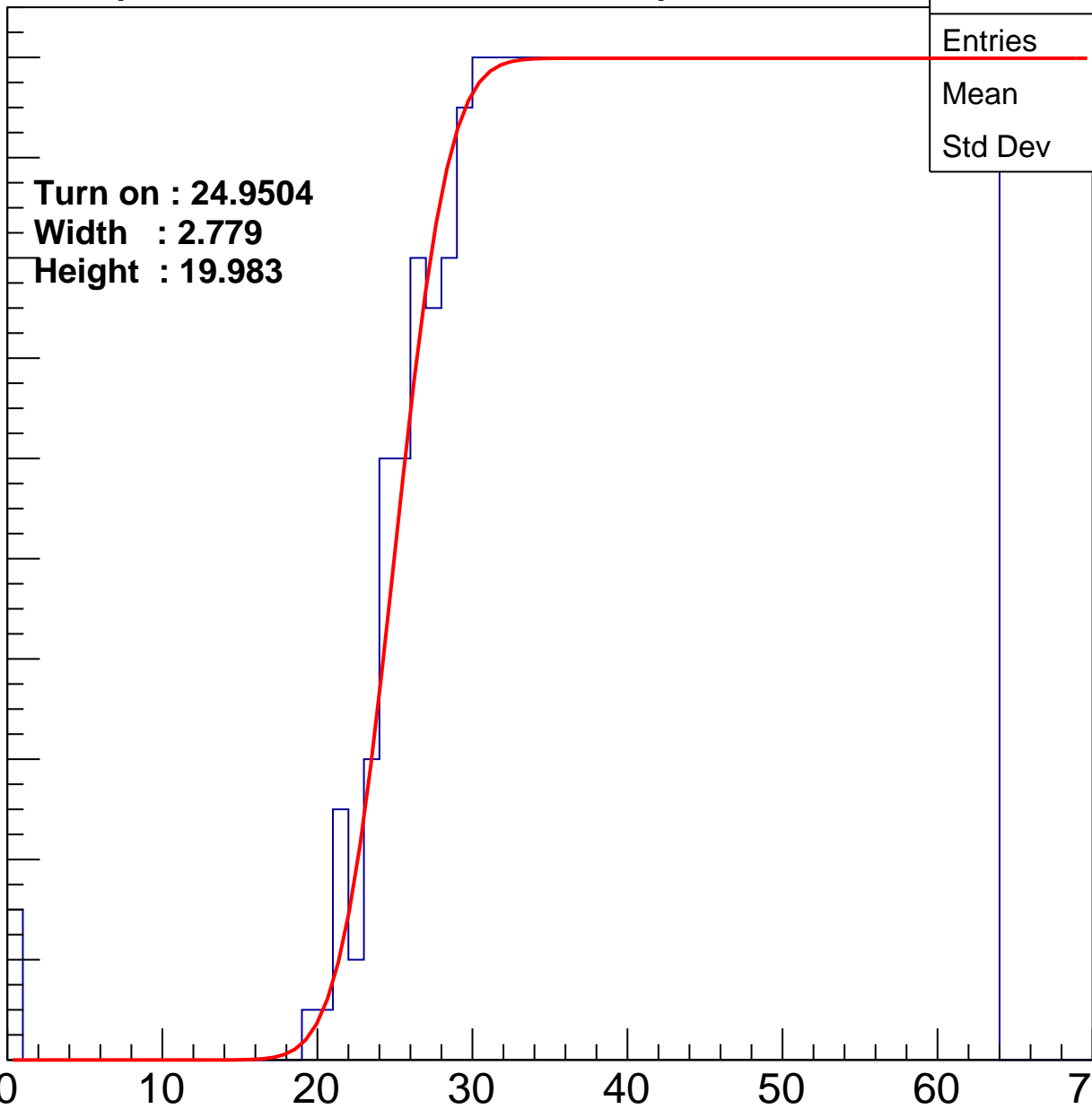
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.9504
Width : 2.779
Height : 19.983

Entries	788
Mean	43.6
Std Dev	11.81

ampl



B0L101S, U23-ch46

calib_packv5_042523_0143.root, FC#1, port C1

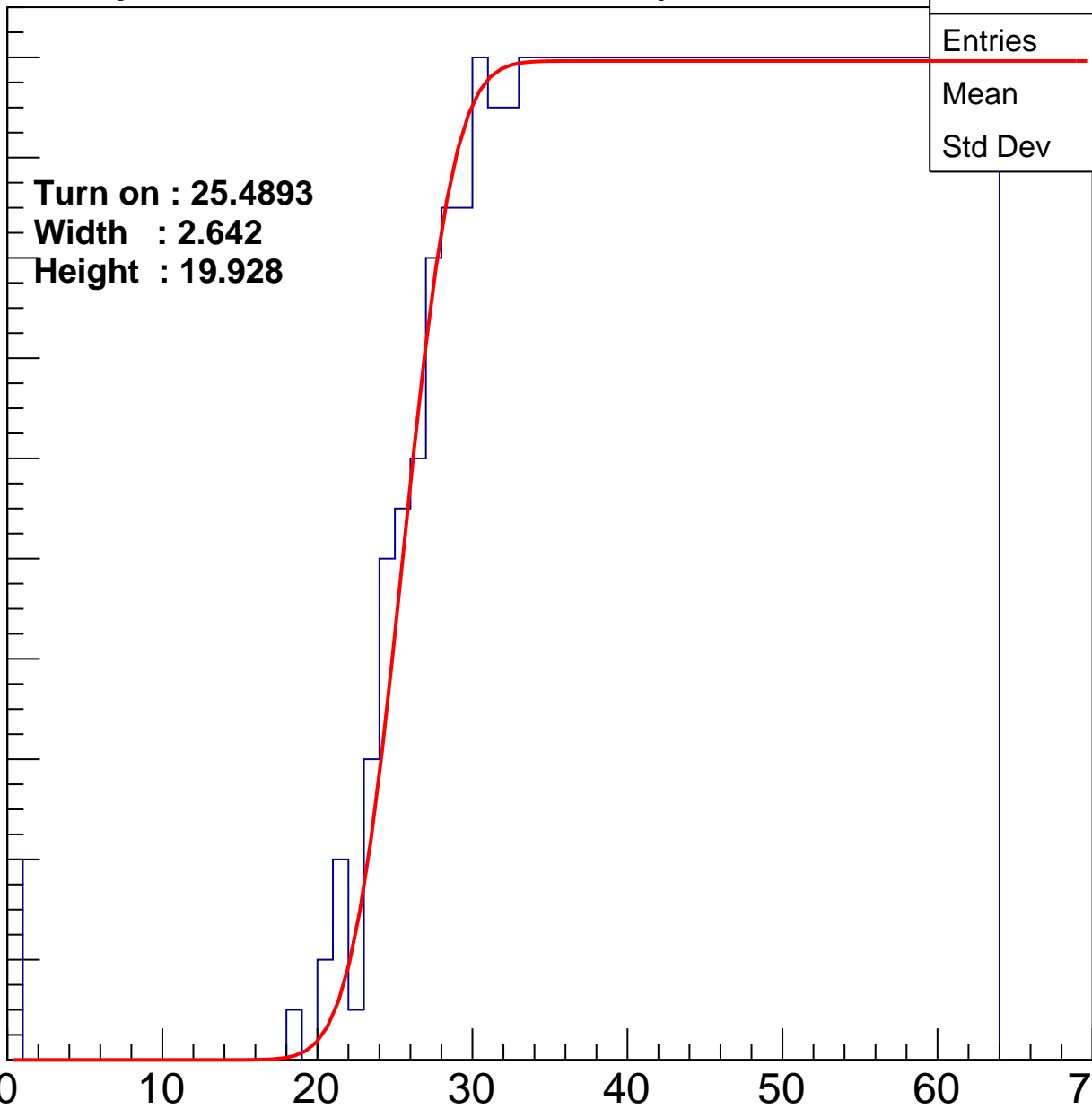
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4893
Width : 2.642
Height : 19.928

Entries	779
Mean	43.76
Std Dev	11.82

ampl



B0L101S, U23-ch47

calib_packv5_042523_0143.root, FC#1, port C1

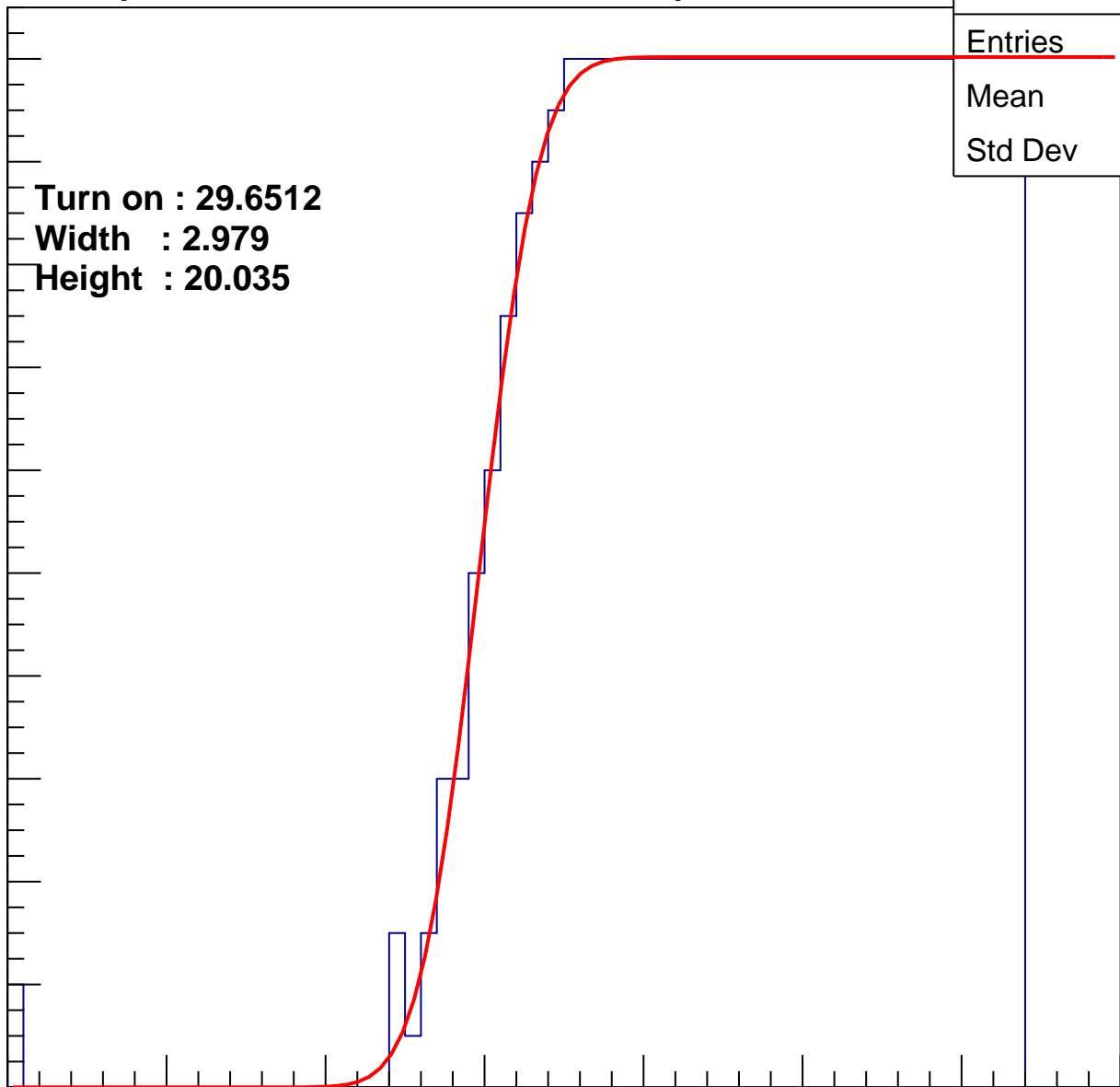
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.6512
Width : 2.979
Height : 20.035

Entries	692
Mean	45.99
Std Dev	10.46

ampl



B0L101S, U23-ch48

calib_packv5_042523_0143.root, FC#1, port C1

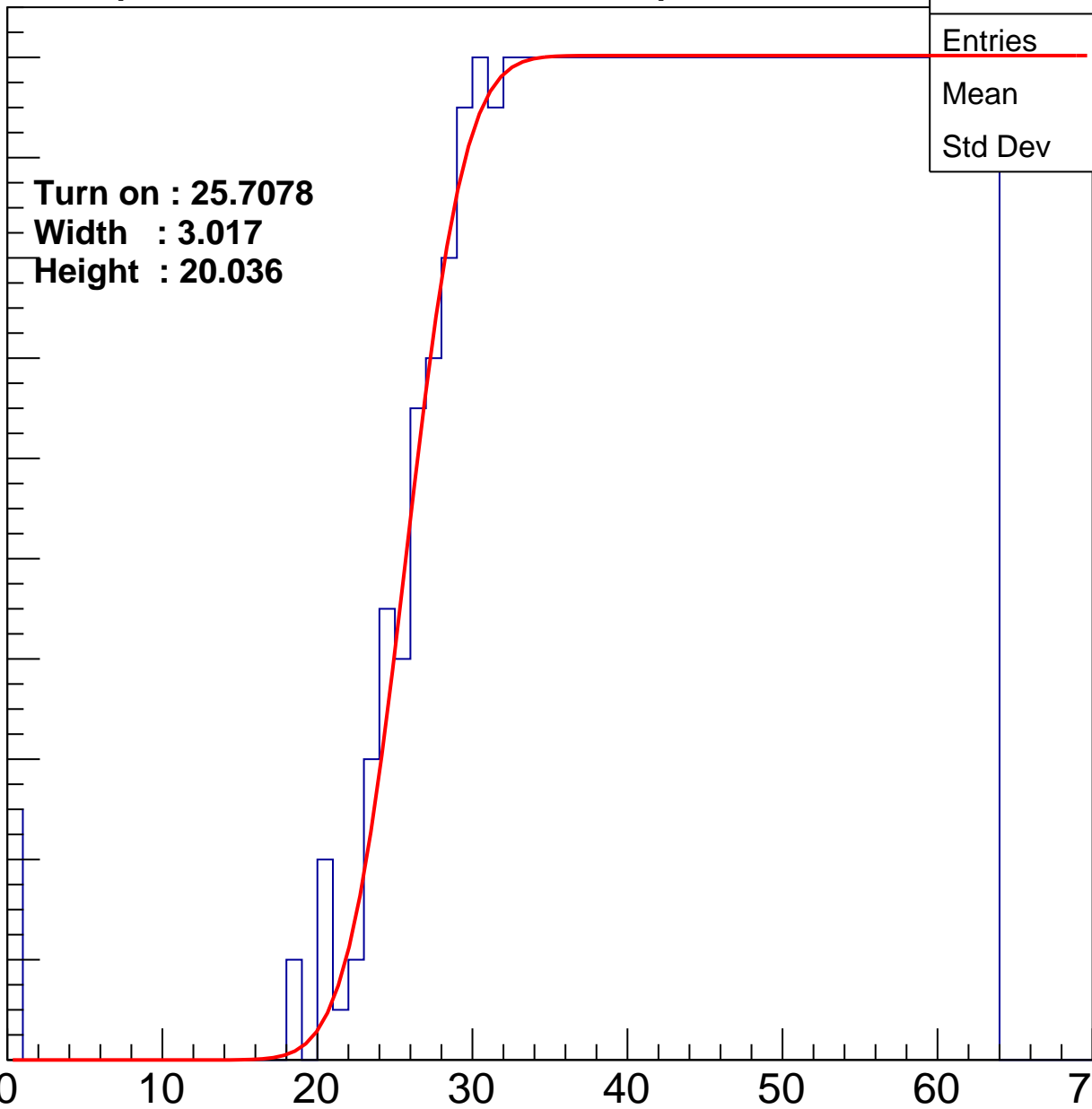
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7078
Width : 3.017
Height : 20.036

Entries	778
Mean	43.75
Std Dev	11.89

ampl



B0L101S, U23-ch49

calib_packv5_042523_0143.root, FC#1, port C1

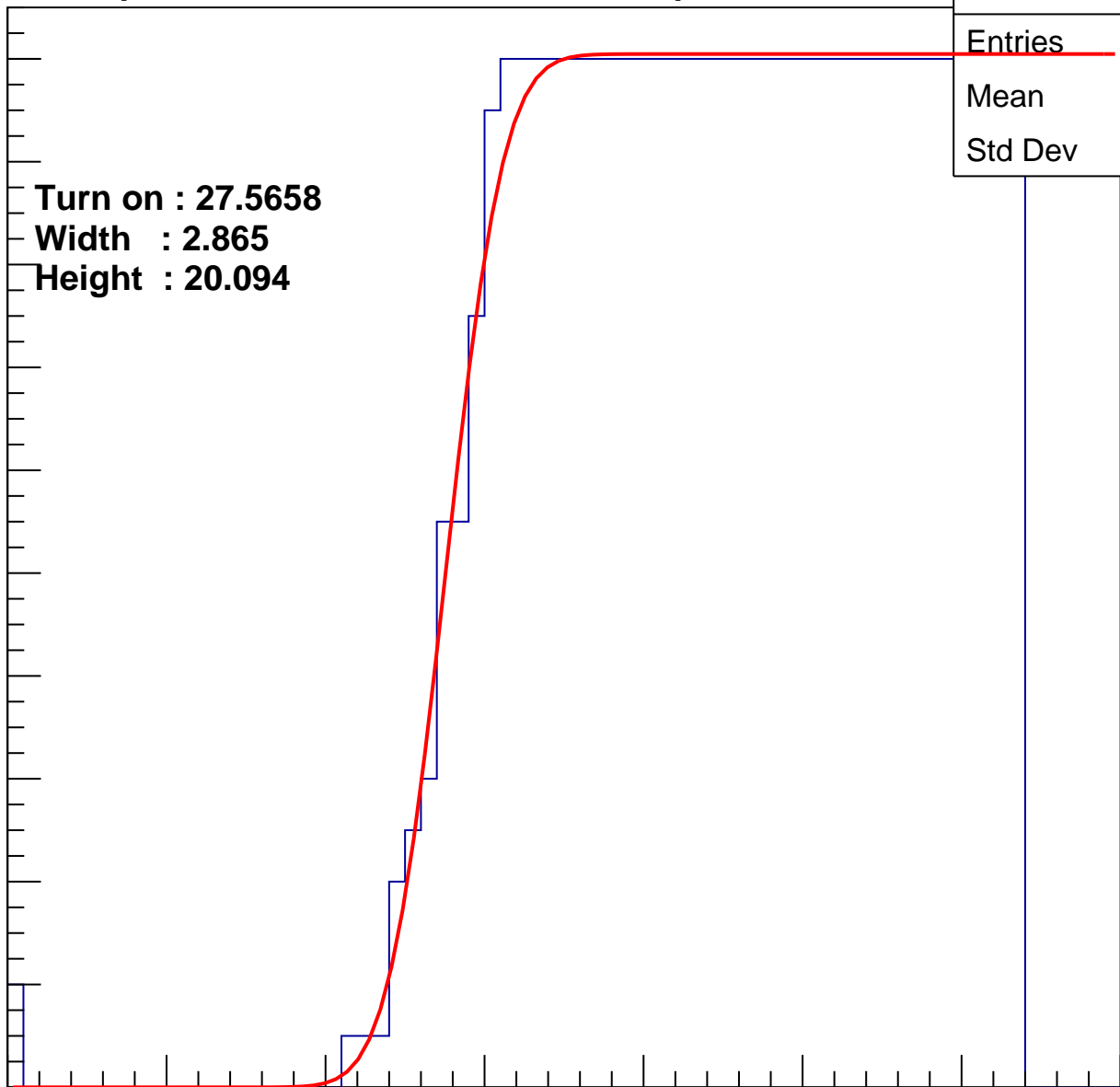
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5658
Width : 2.865
Height : 20.094

Entries	736
Mean	44.94
Std Dev	10.99

ampl



B0L101S, U23-ch50

calib_packv5_042523_0143.root, FC#1, port C1

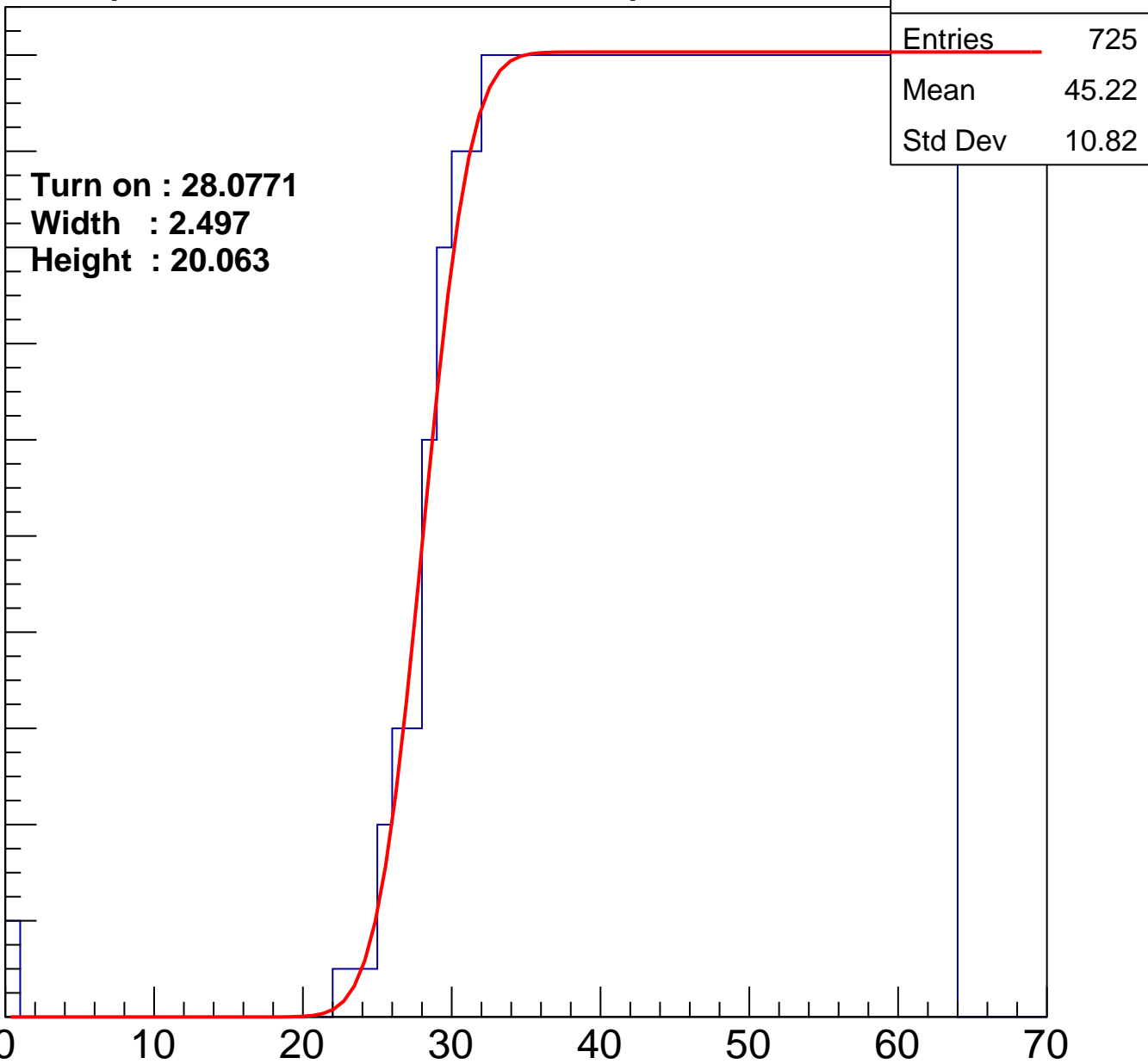
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0771
Width : 2.497
Height : 20.063

Entries	725
Mean	45.22
Std Dev	10.82

ampl



B0L101S, U23-ch51

calib_packv5_042523_0143.root, FC#1, port C1

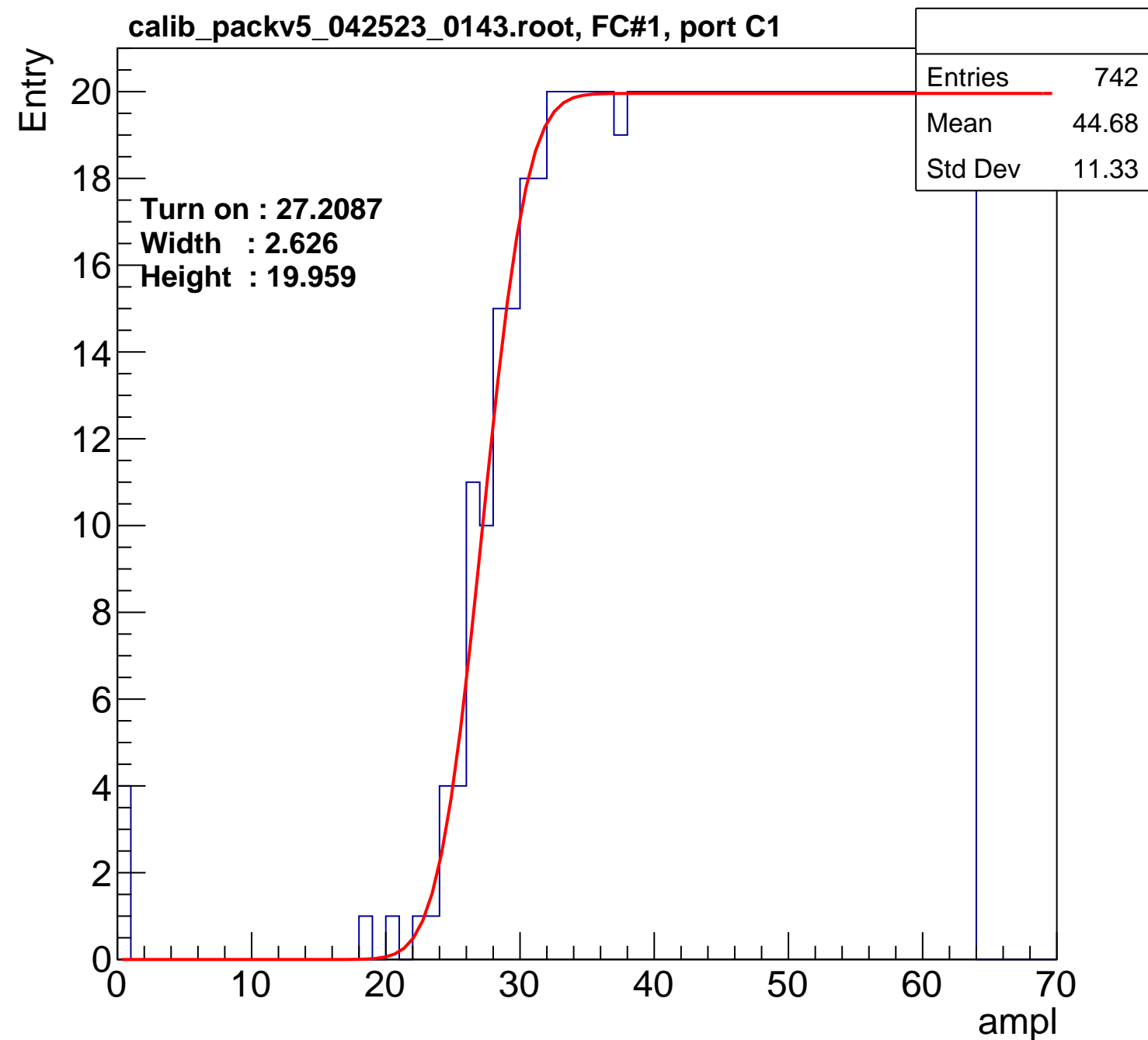
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2087
Width : 2.626
Height : 19.959

Entries	742
Mean	44.68
Std Dev	11.33

ampl



B0L101S, U23-ch52

calib_packv5_042523_0143.root, FC#1, port C1

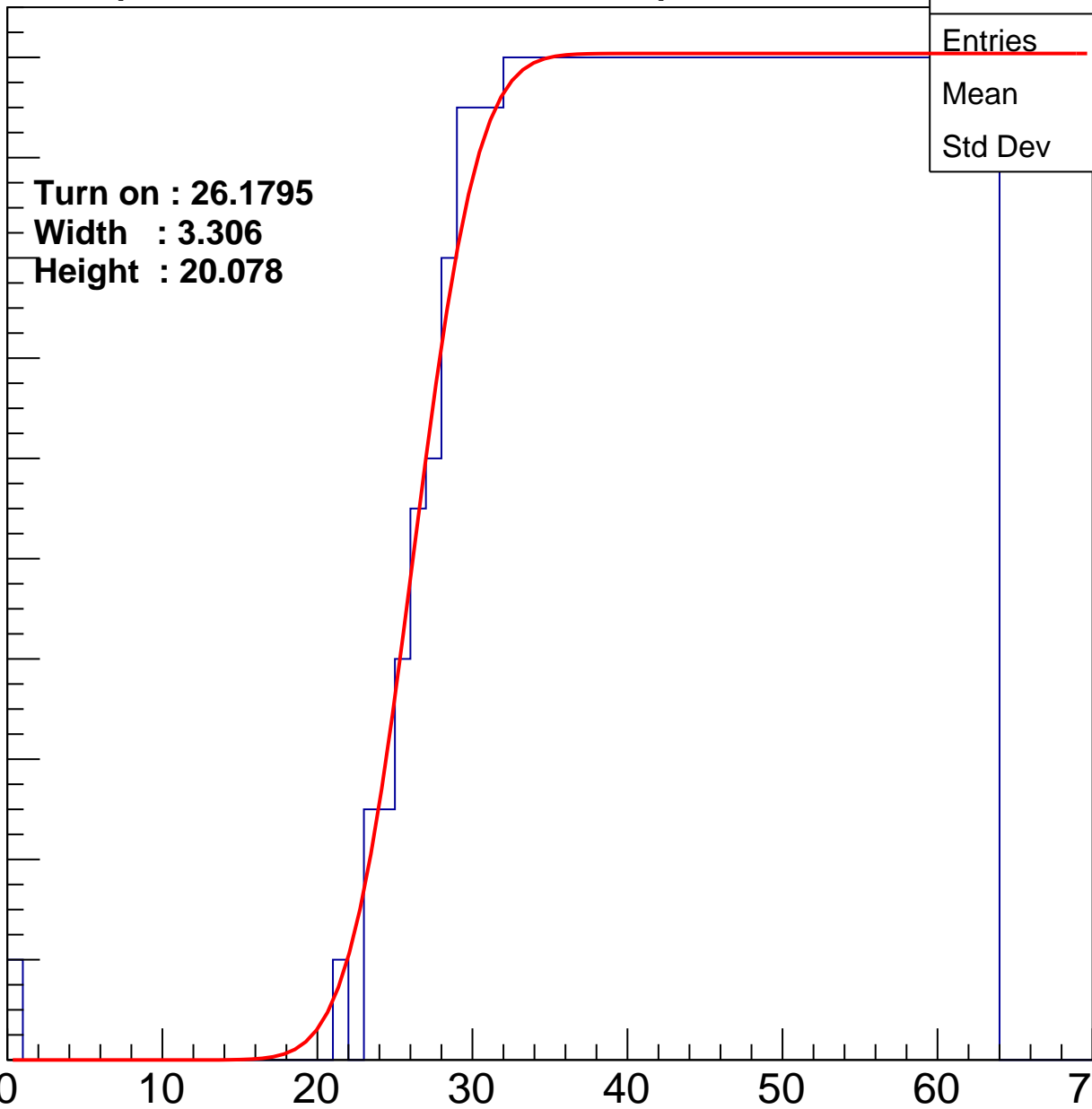
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1795
Width : 3.306
Height : 20.078

Entries	758
Mean	44.39
Std Dev	11.29

ampl



B0L101S, U23-ch53

calib_packv5_042523_0143.root, FC#1, port C1

Entry

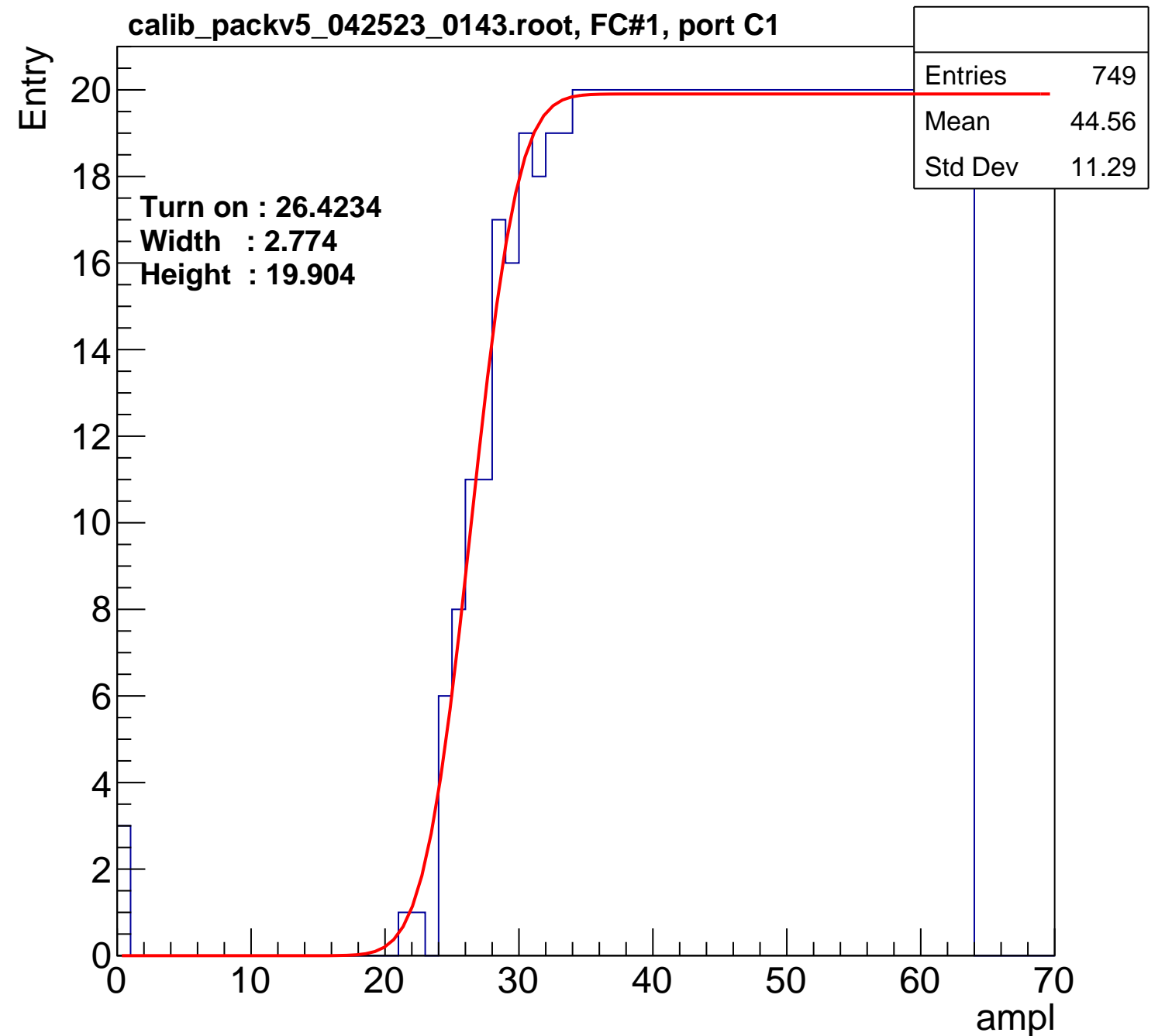
20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4234
Width : 2.774
Height : 19.904

Entries	749
Mean	44.56
Std Dev	11.29

ampl

0 10 20 30 40 50 60 70



B0L101S, U23-ch54

calib_packv5_042523_0143.root, FC#1, port C1

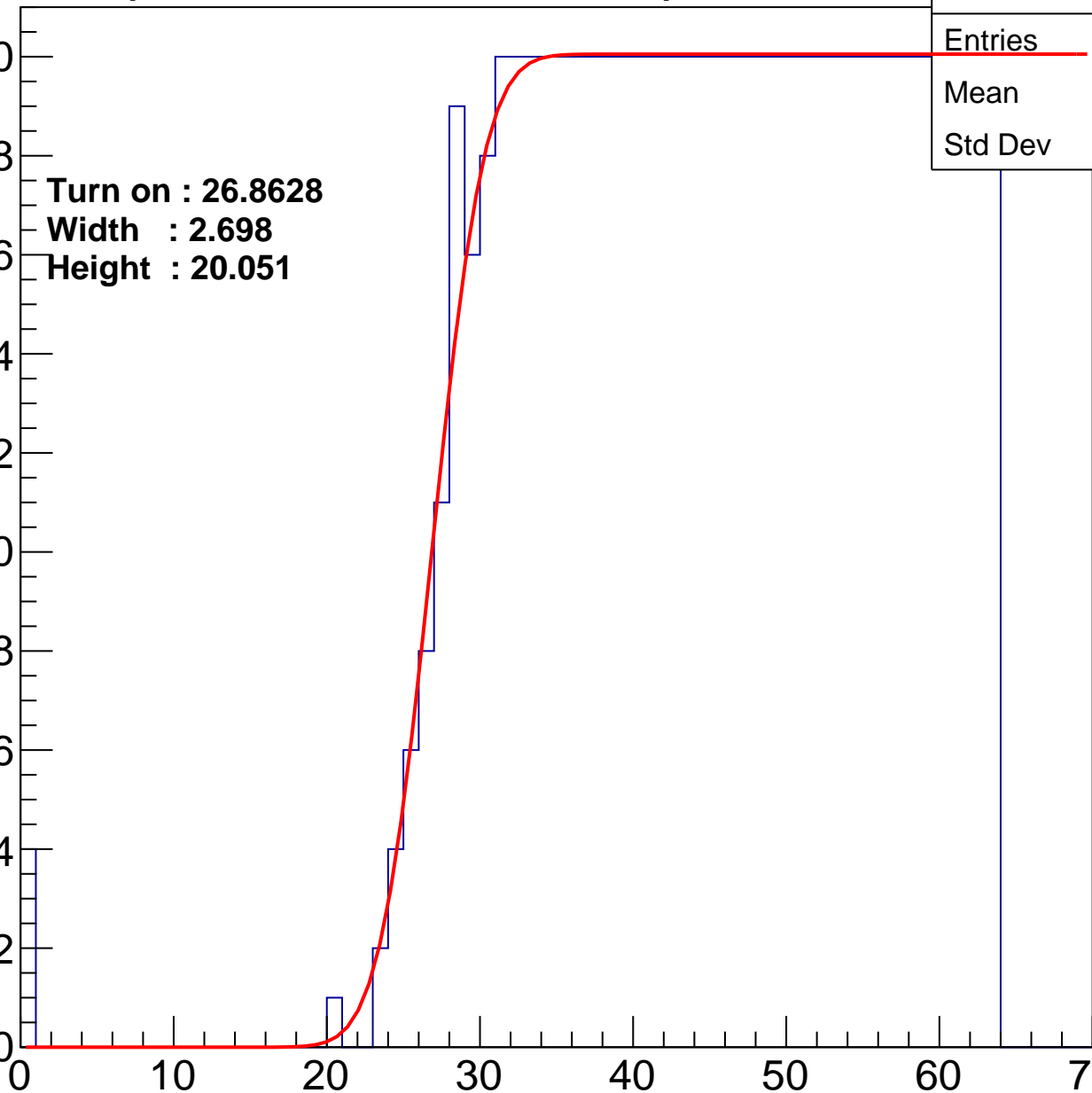
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8628
Width : 2.698
Height : 20.051

Entries	749
Mean	44.56
Std Dev	11.34

ampl



B0L101S, U23-ch55

calib_packv5_042523_0143.root, FC#1, port C1

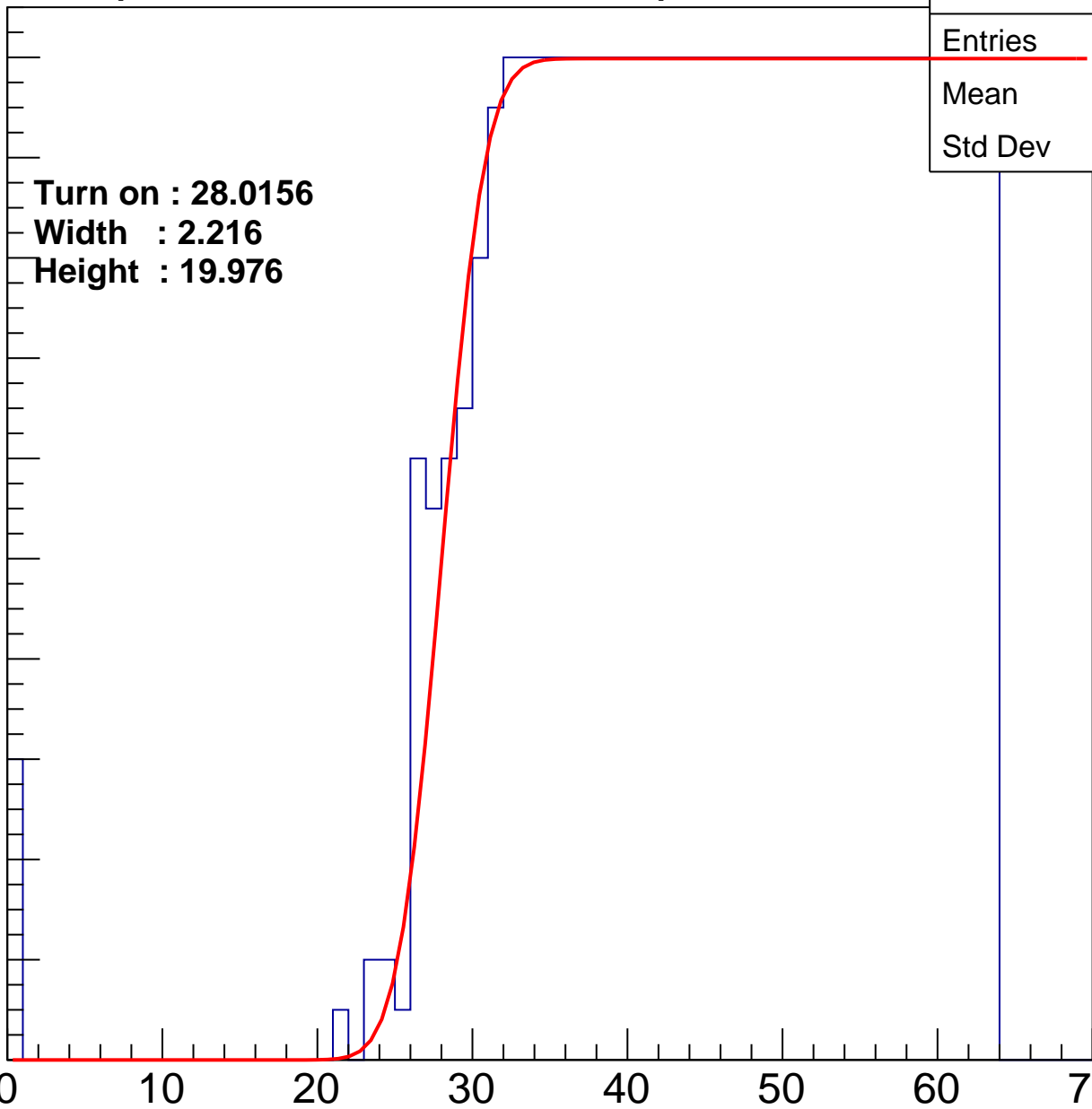
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0156
Width : 2.216
Height : 19.976

Entries	735
Mean	44.8
Std Dev	11.4

ampl



B0L101S, U23-ch56

calib_packv5_042523_0143.root, FC#1, port C1

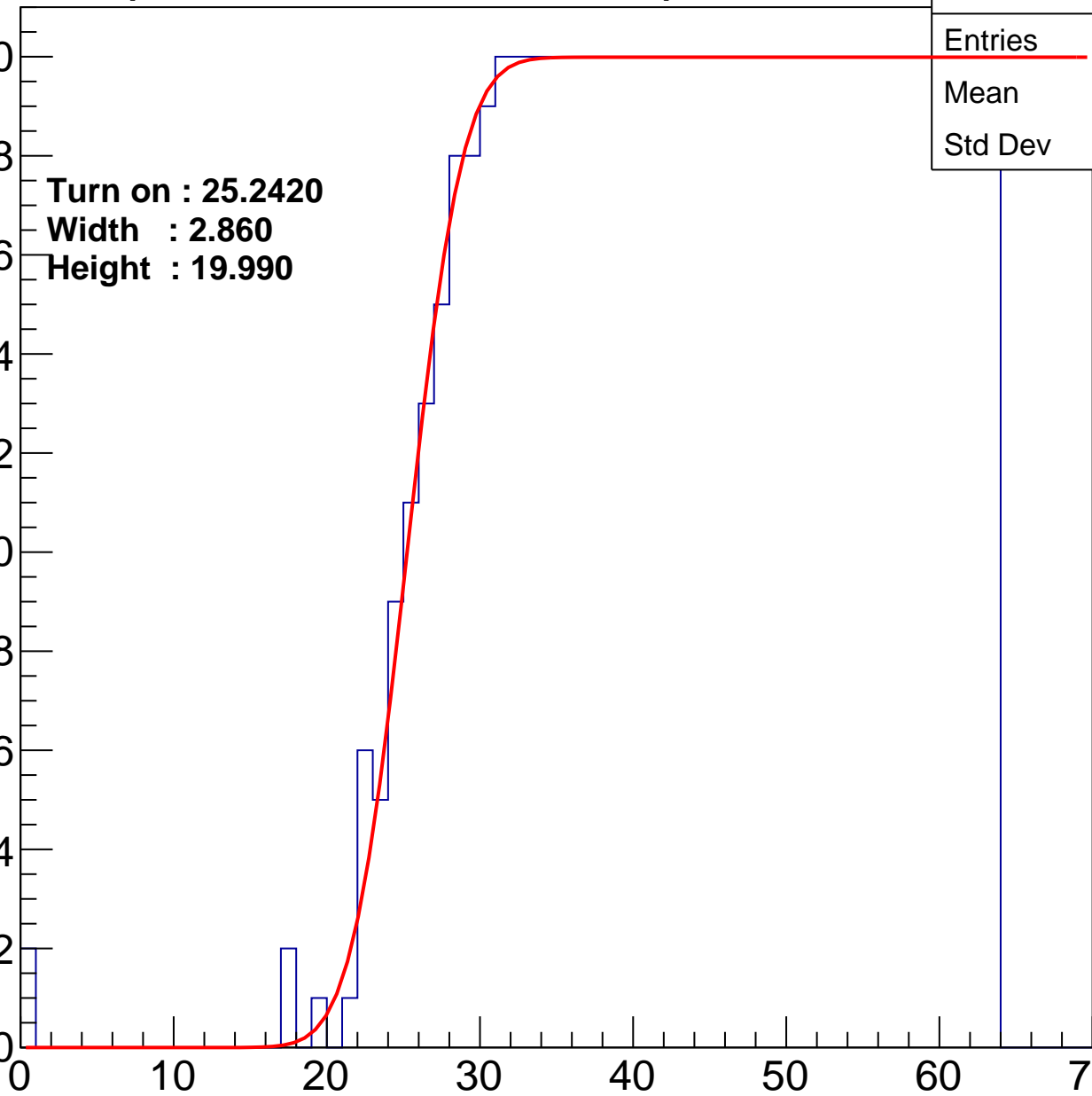
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2420
Width : 2.860
Height : 19.990

Entries	780
Mean	43.81
Std Dev	11.65

ampl



B0L101S, U23-ch57

calib_packv5_042523_0143.root, FC#1, port C1

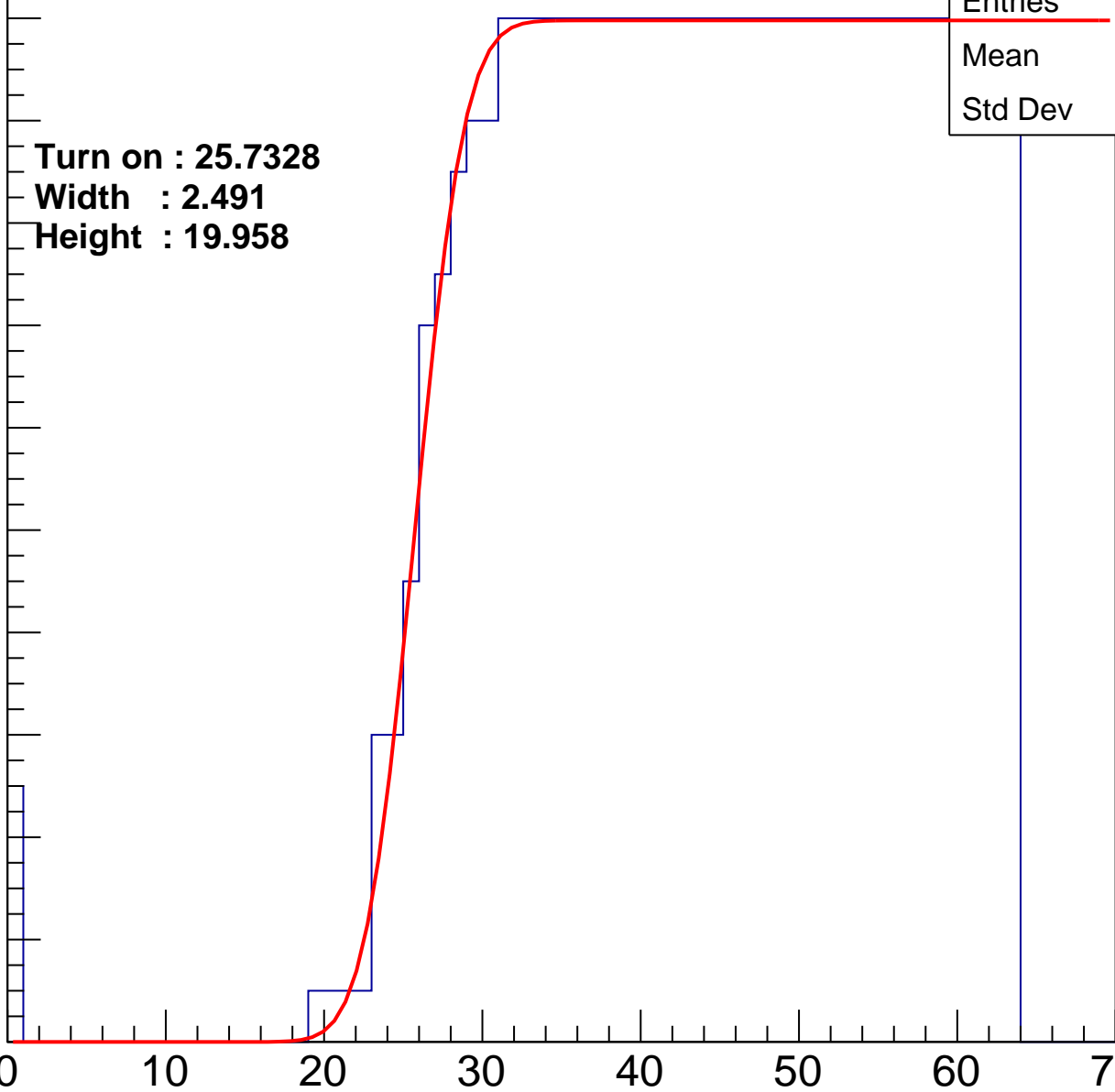
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7328
Width : 2.491
Height : 19.958

Entries	772
Mean	43.93
Std Dev	11.76

ampl



B0L101S, U23-ch58

calib_packv5_042523_0143.root, FC#1, port C1

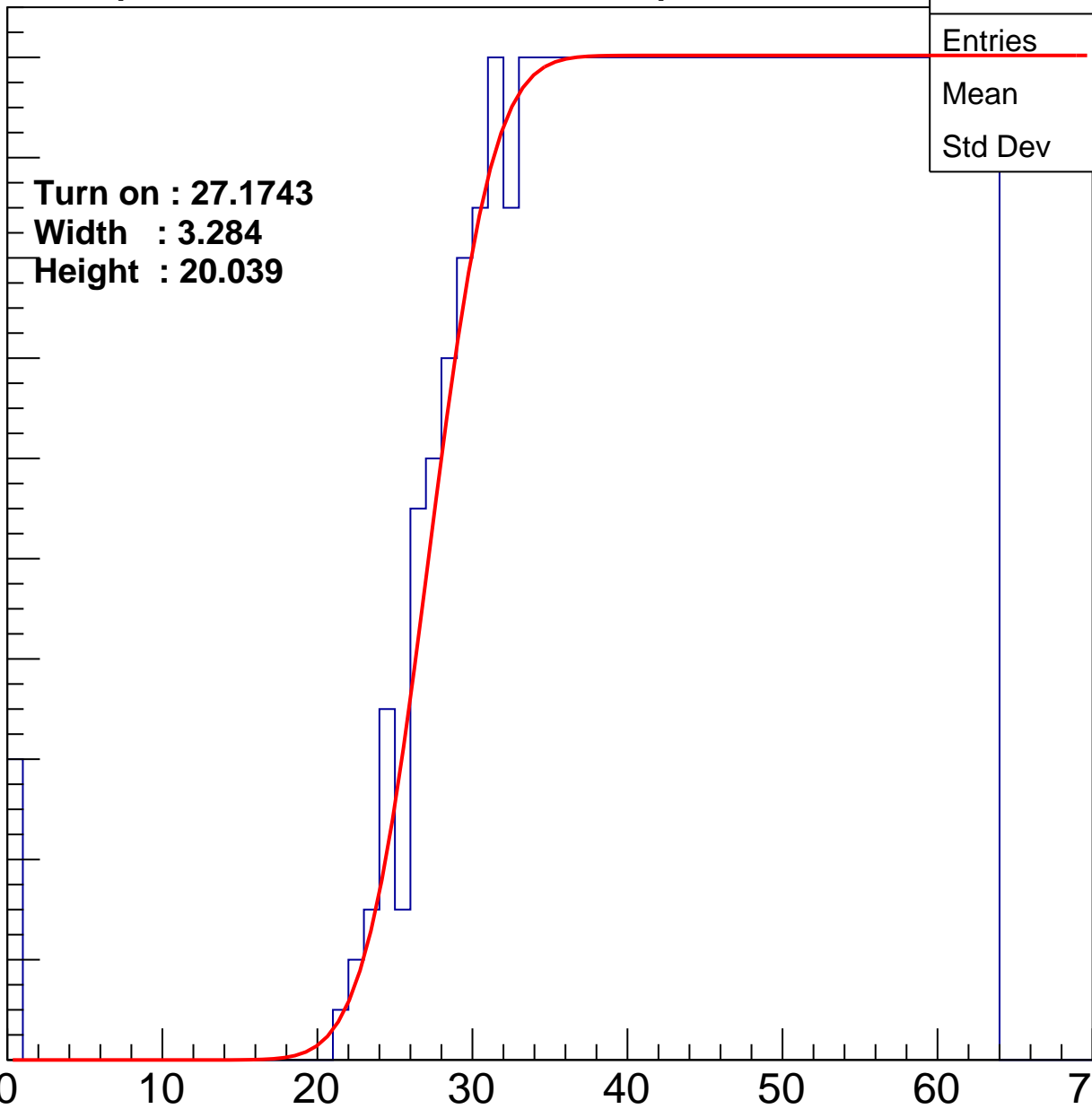
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1743
Width : 3.284
Height : 20.039

Entries	749
Mean	44.43
Std Dev	11.62

ampl



B0L101S, U23-ch59

calib_packv5_042523_0143.root, FC#1, port C1

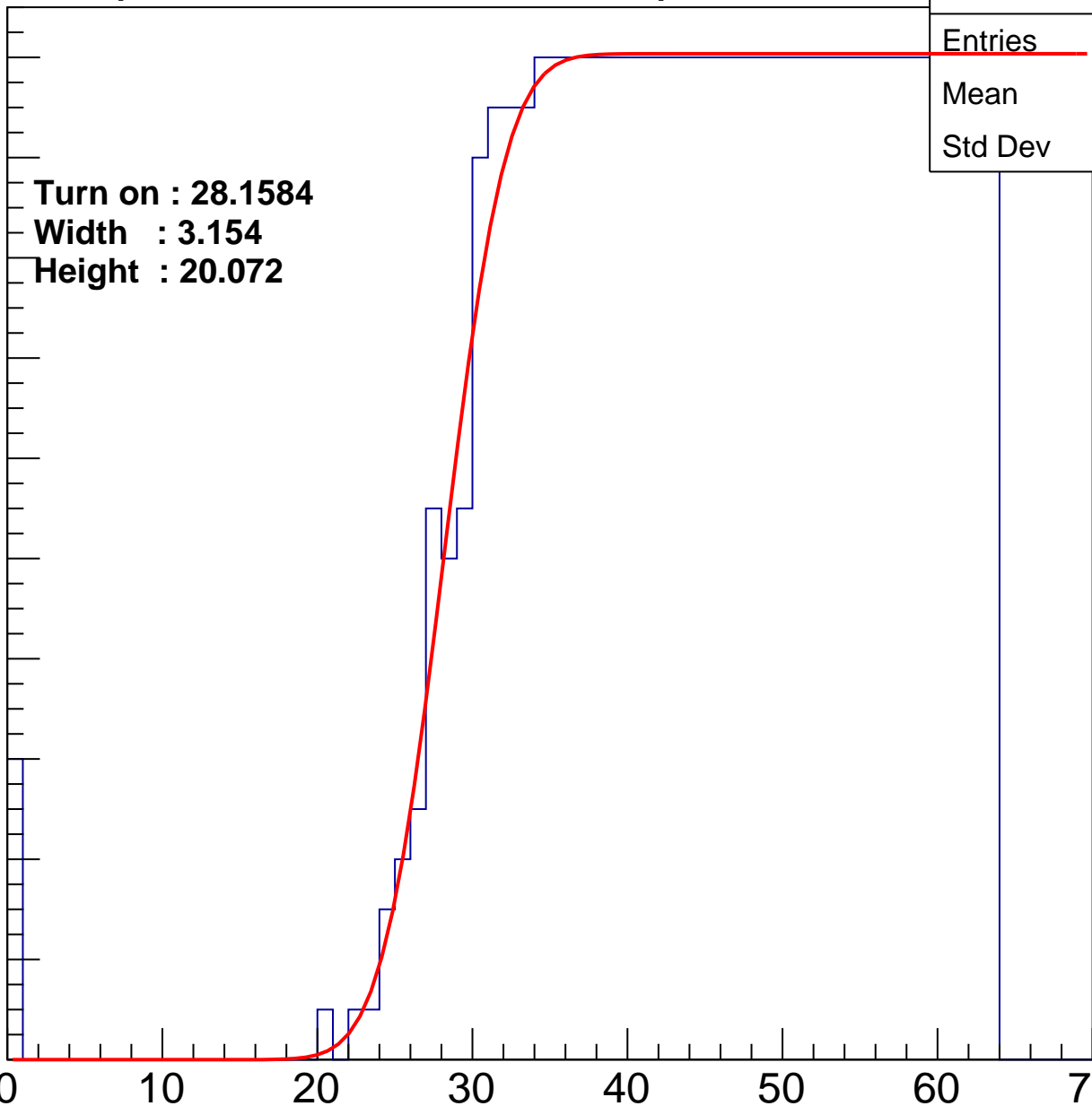
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1584
Width : 3.154
Height : 20.072

Entries	728
Mean	44.95
Std Dev	11.35

ampl



B0L101S, U23-ch60

calib_packv5_042523_0143.root, FC#1, port C1

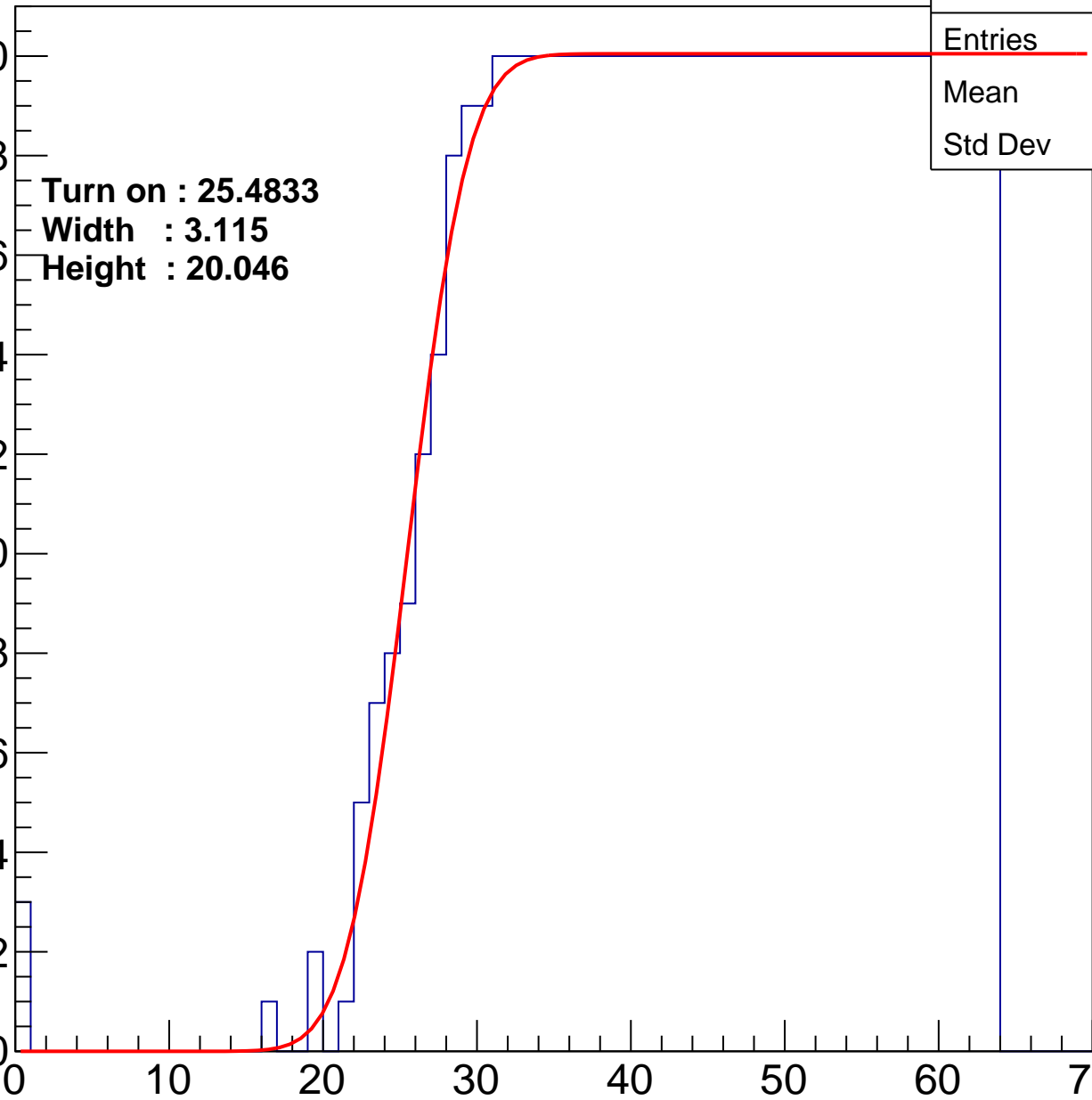
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4833
Width : 3.115
Height : 20.046

Entries	778
Mean	43.83
Std Dev	11.71

ampl



B0L101S, U23-ch61

calib_packv5_042523_0143.root, FC#1, port C1

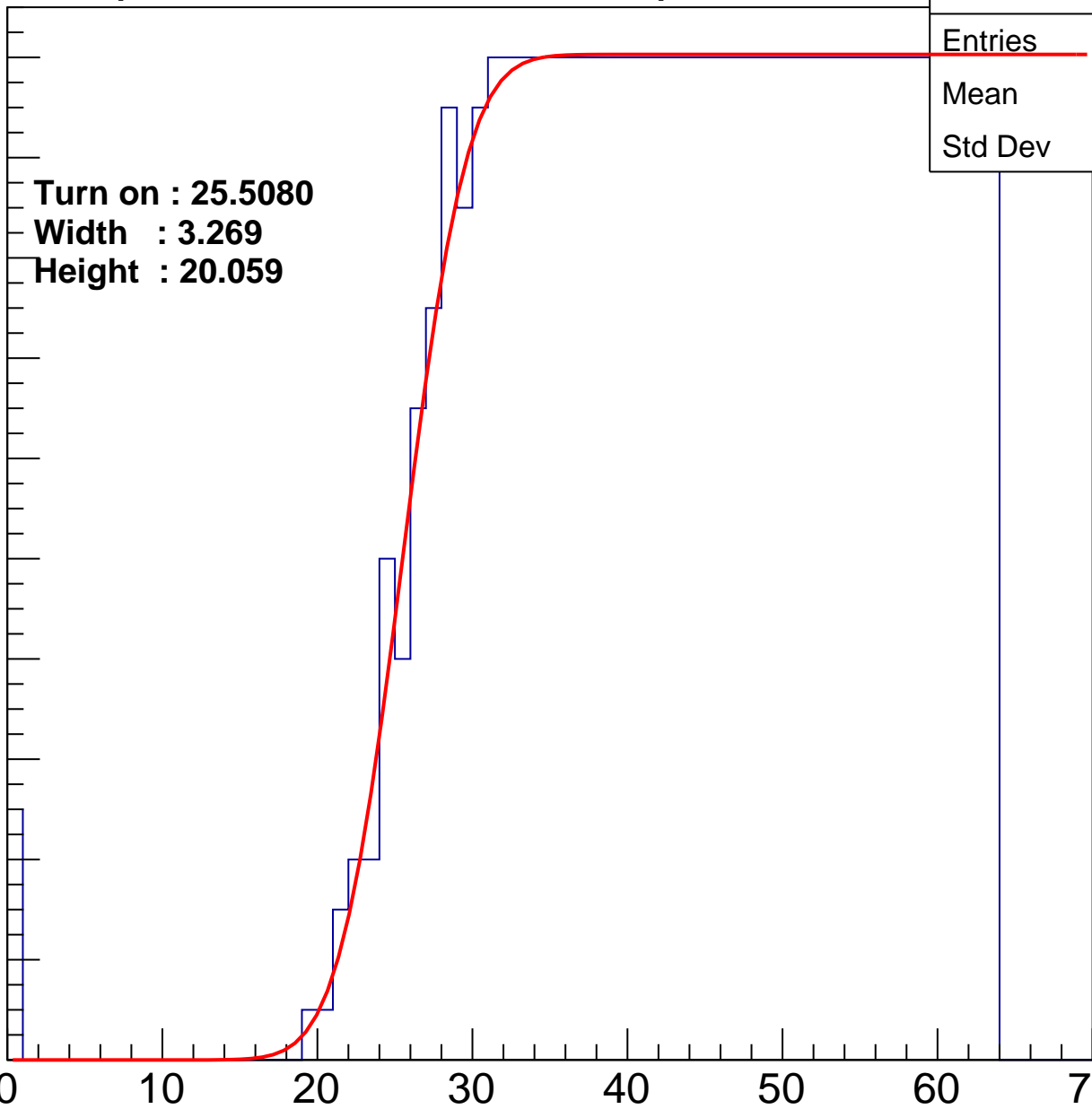
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5080
Width : 3.269
Height : 20.059

Entries	779
Mean	43.75
Std Dev	11.87

ampl



B0L101S, U23-ch62

calib_packv5_042523_0143.root, FC#1, port C1

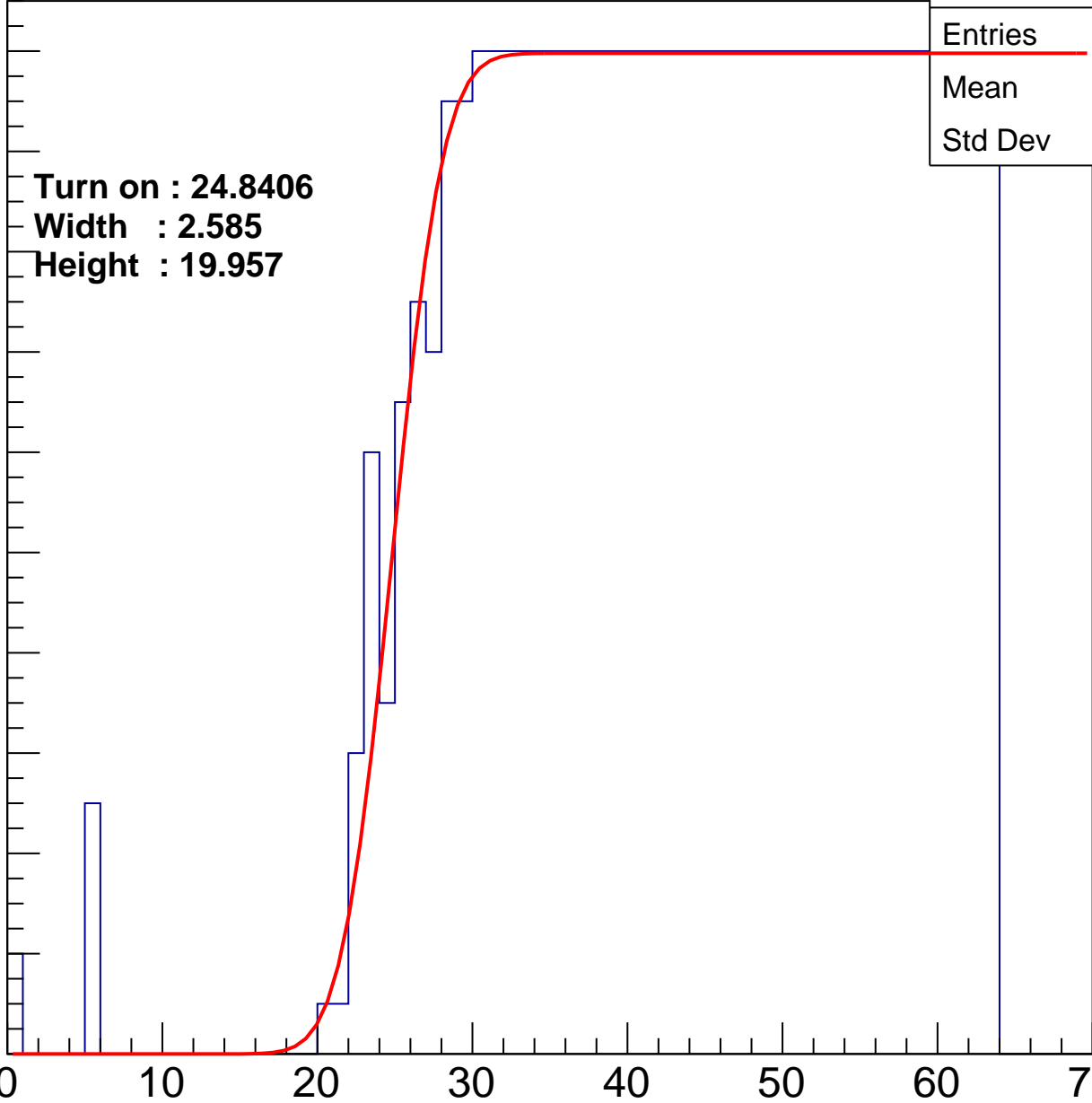
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8406
Width : 2.585
Height : 19.957

Entries	794
Mean	43.37
Std Dev	12.07

ampl



B0L101S, U23-ch63

calib_packv5_042523_0143.root, FC#1, port C1

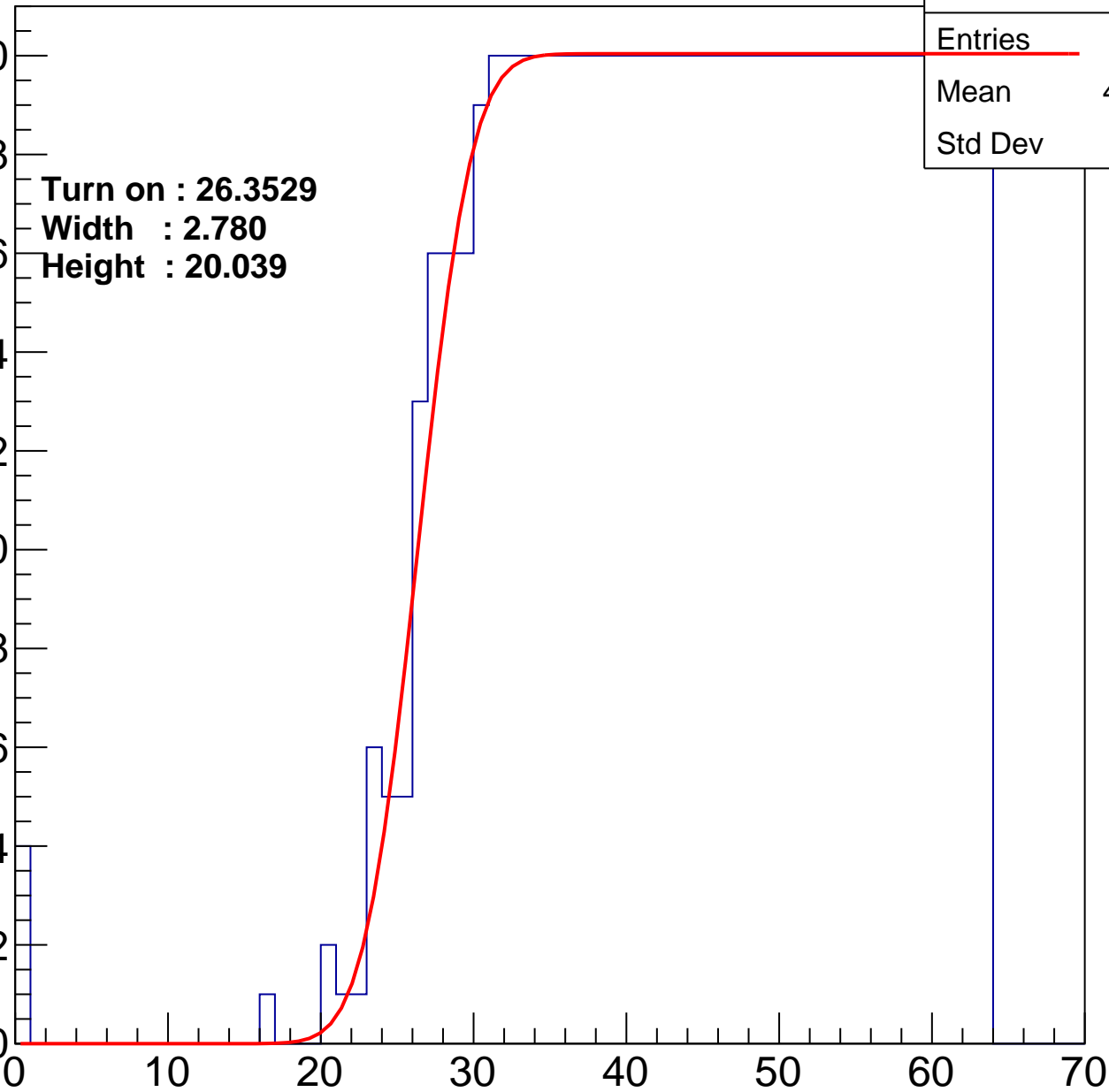
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3529
Width : 2.780
Height : 20.039

Entries	765
Mean	44.12
Std Dev	11.61

ampl



B0L101S, U23-ch64

calib_packv5_042523_0143.root, FC#1, port C1

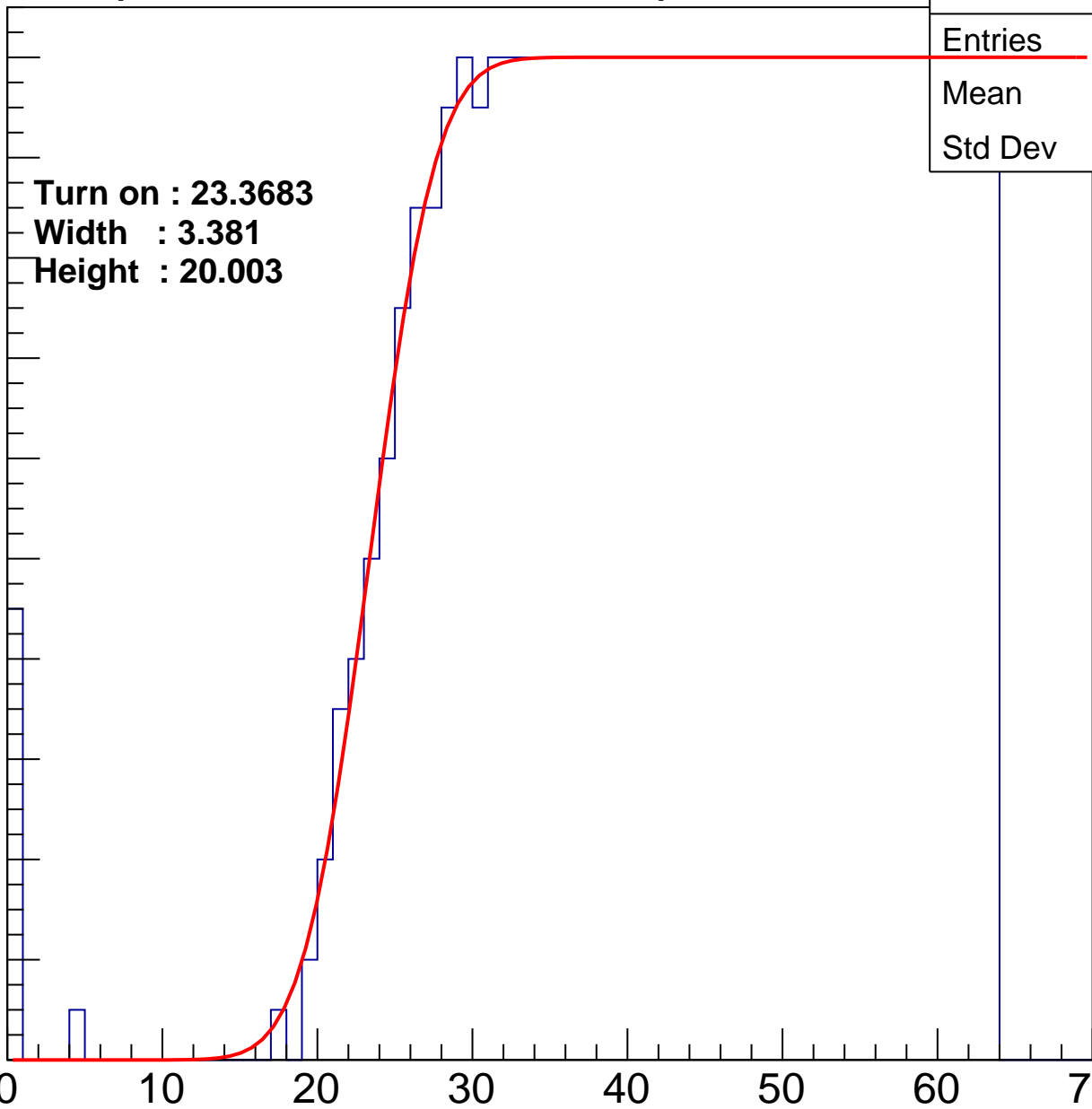
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 23.3683
Width : 3.381
Height : 20.003

Entries	821
Mean	42.58
Std Dev	12.73

ampl



B0L101S, U23-ch65

calib_packv5_042523_0143.root, FC#1, port C1

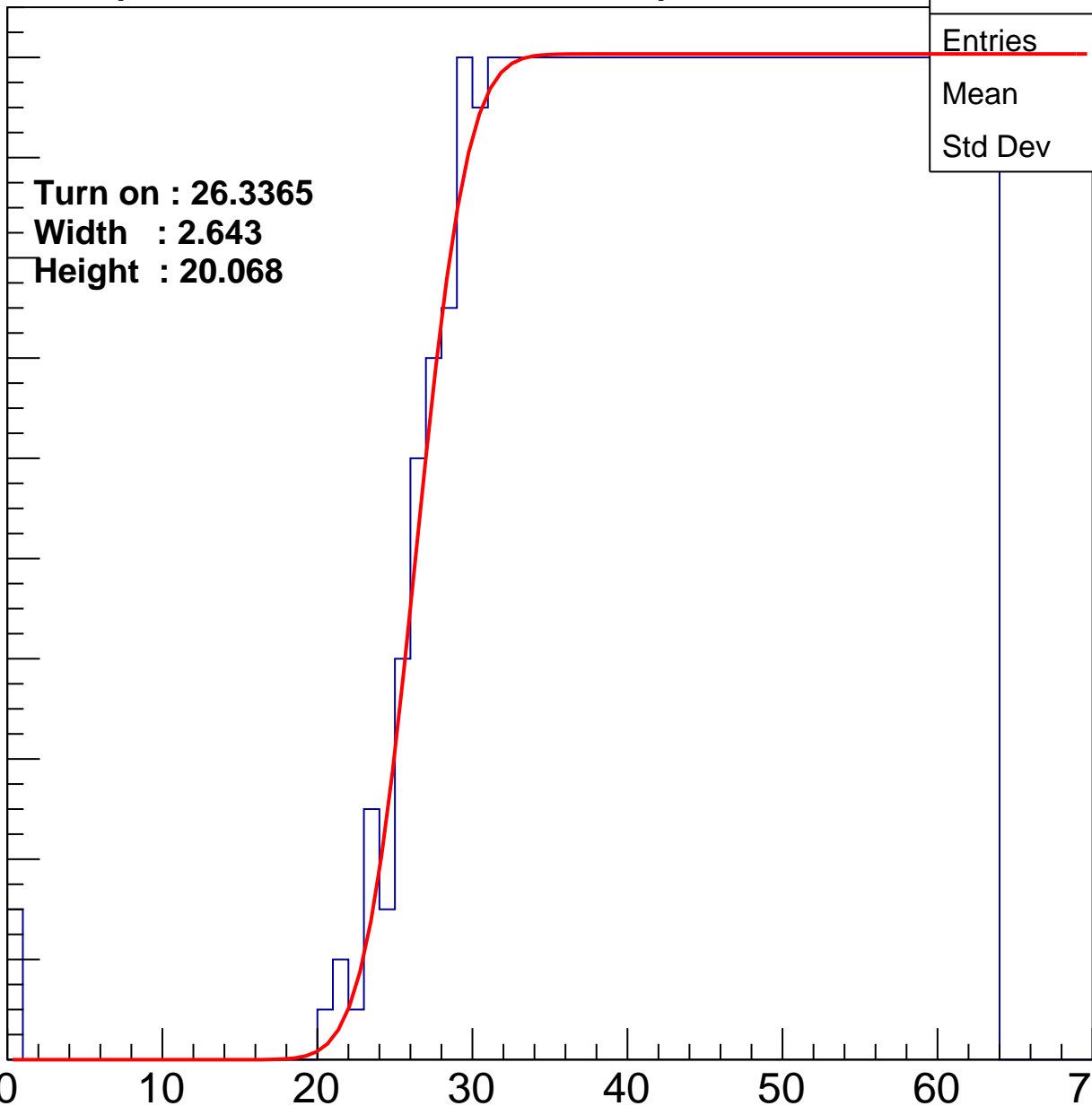
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3365
Width : 2.643
Height : 20.068

Entries	763
Mean	44.23
Std Dev	11.45

ampl



B0L101S, U23-ch66

calib_packv5_042523_0143.root, FC#1, port C1

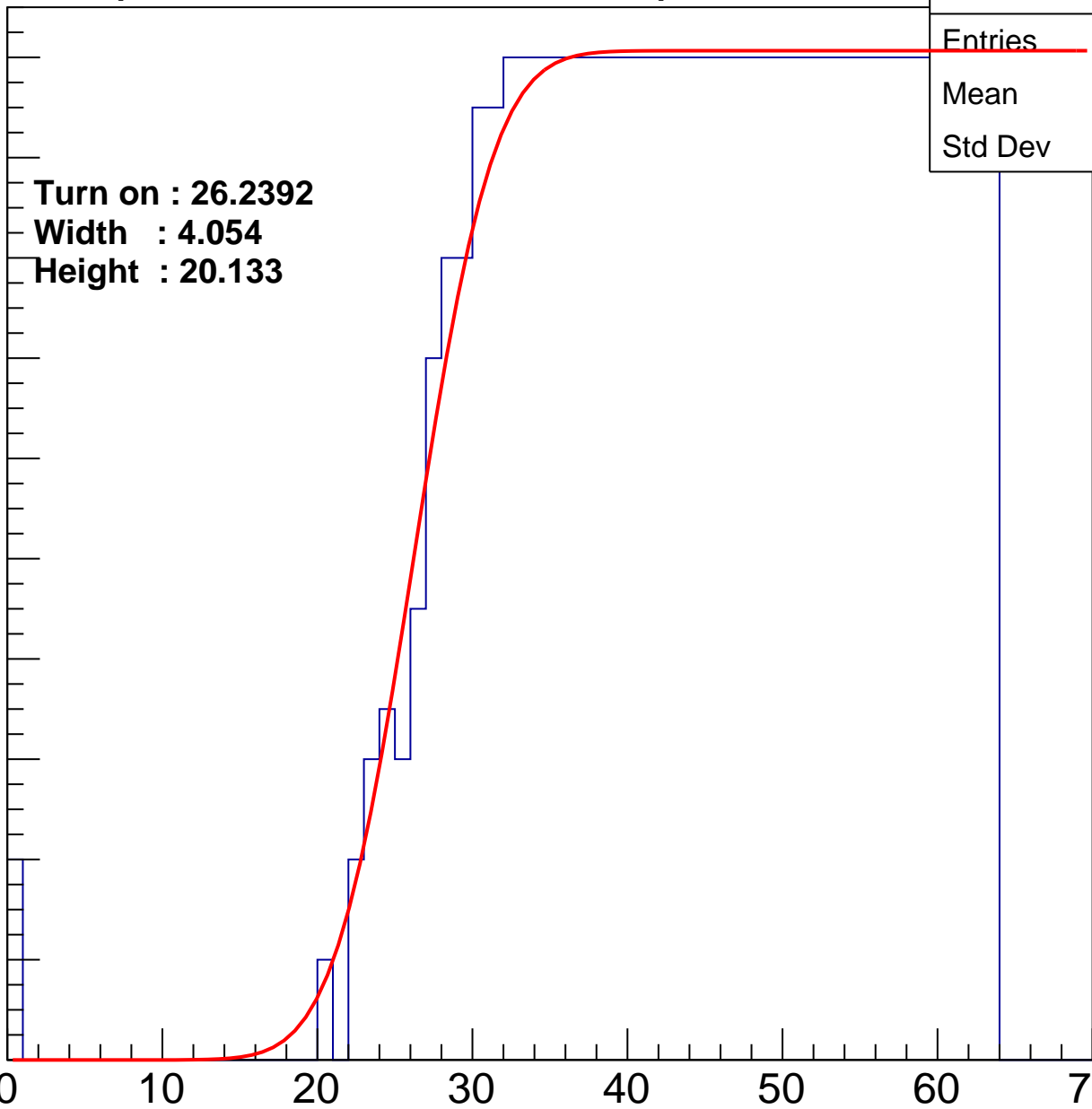
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2392
Width : 4.054
Height : 20.133

Entries	762
Mean	44.18
Std Dev	11.59

ampl



B0L101S, U23-ch67

calib_packv5_042523_0143.root, FC#1, port C1

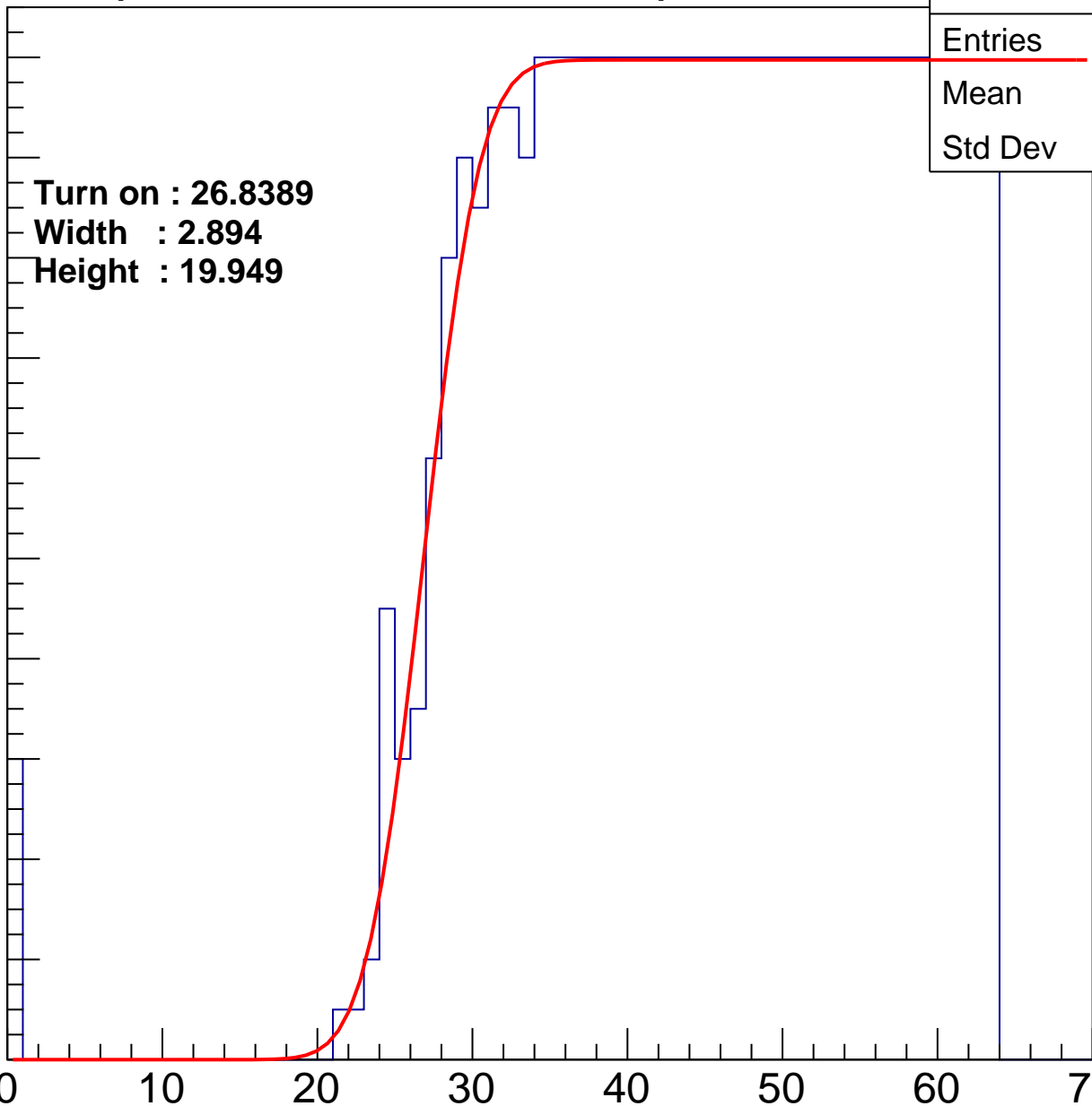
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8389
Width : 2.894
Height : 19.949

Entries	751
Mean	44.38
Std Dev	11.63

ampl



B0L101S, U23-ch68

calib_packv5_042523_0143.root, FC#1, port C1

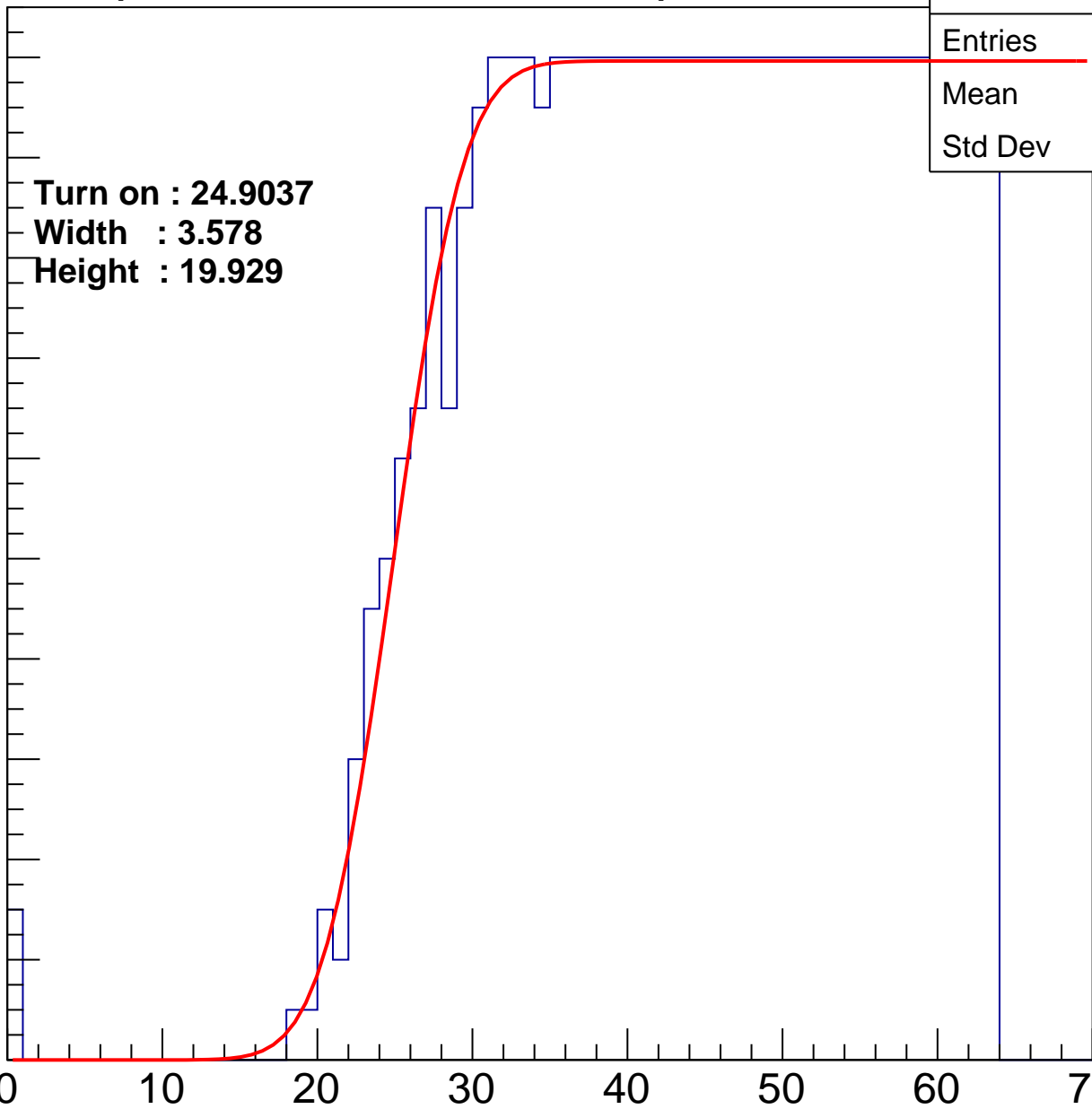
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.9037
Width : 3.578
Height : 19.929

Entries	785
Mean	43.6
Std Dev	11.87

ampl



B0L101S, U23-ch69

calib_packv5_042523_0143.root, FC#1, port C1

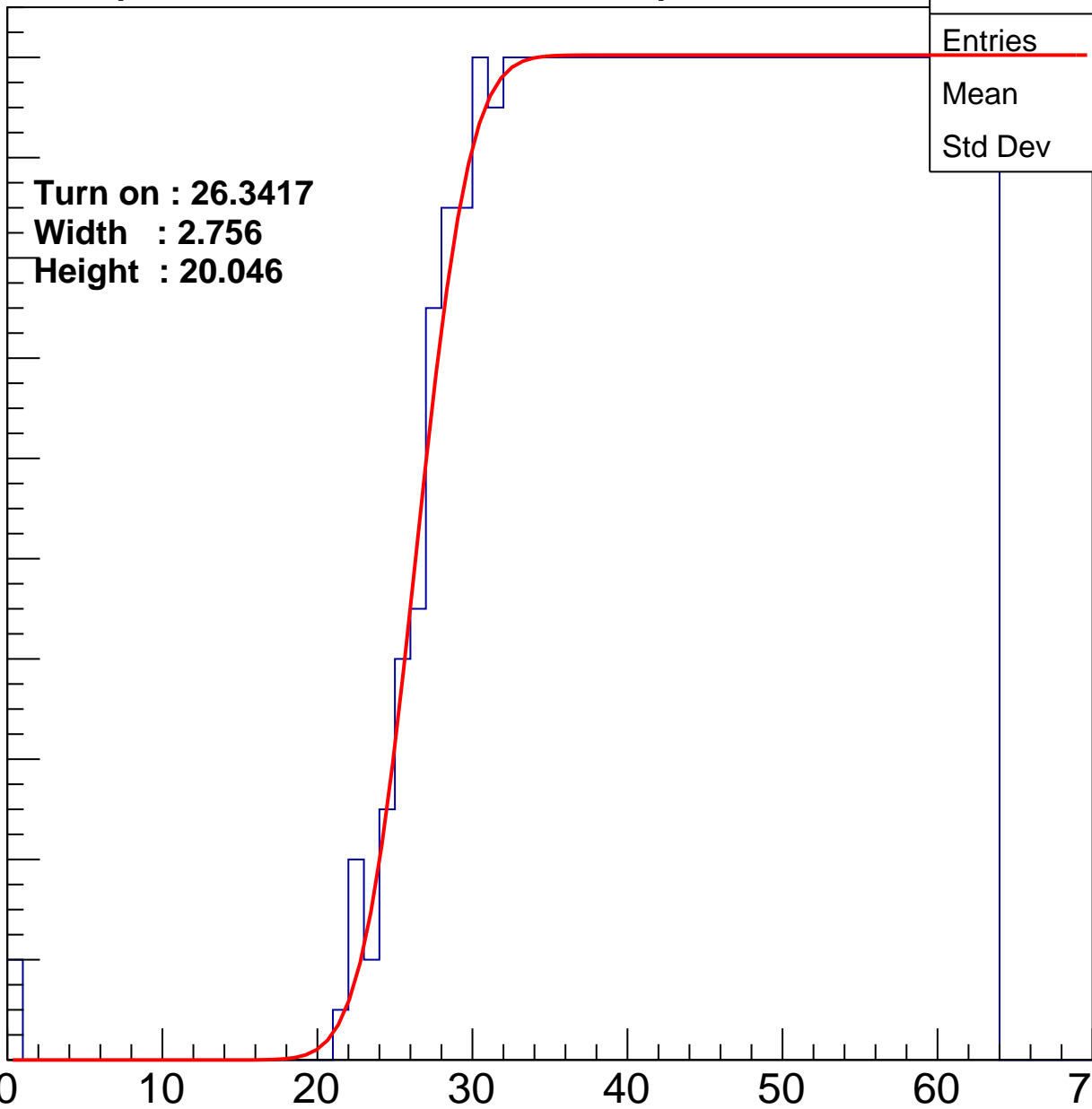
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3417
Width : 2.756
Height : 20.046

Entries	759
Mean	44.36
Std Dev	11.3

ampl



B0L101S, U23-ch70

calib_packv5_042523_0143.root, FC#1, port C1

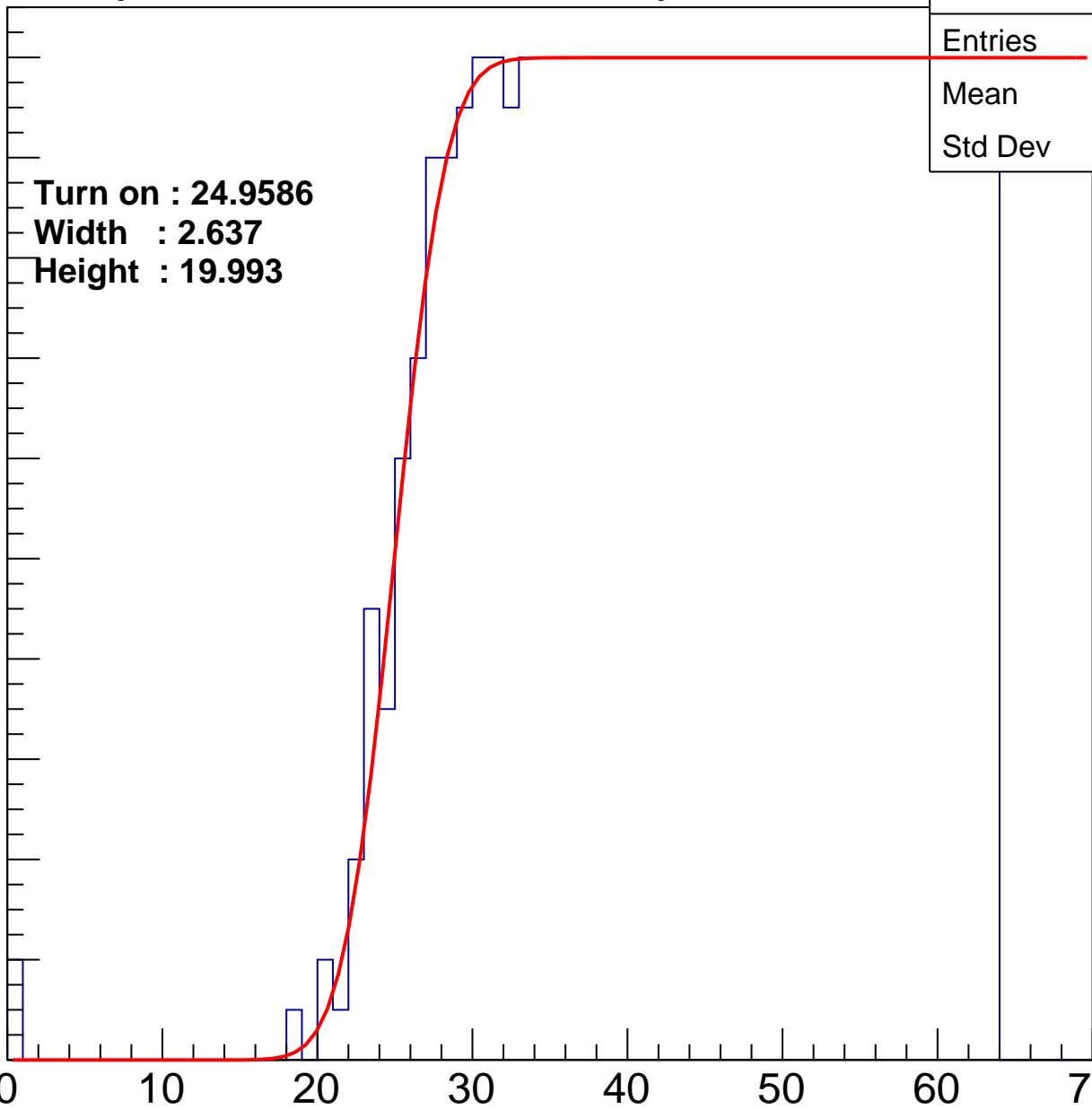
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.9586
Width : 2.637
Height : 19.993

Entries	786
Mean	43.68
Std Dev	11.69

ampl



B0L101S, U23-ch71

calib_packv5_042523_0143.root, FC#1, port C1

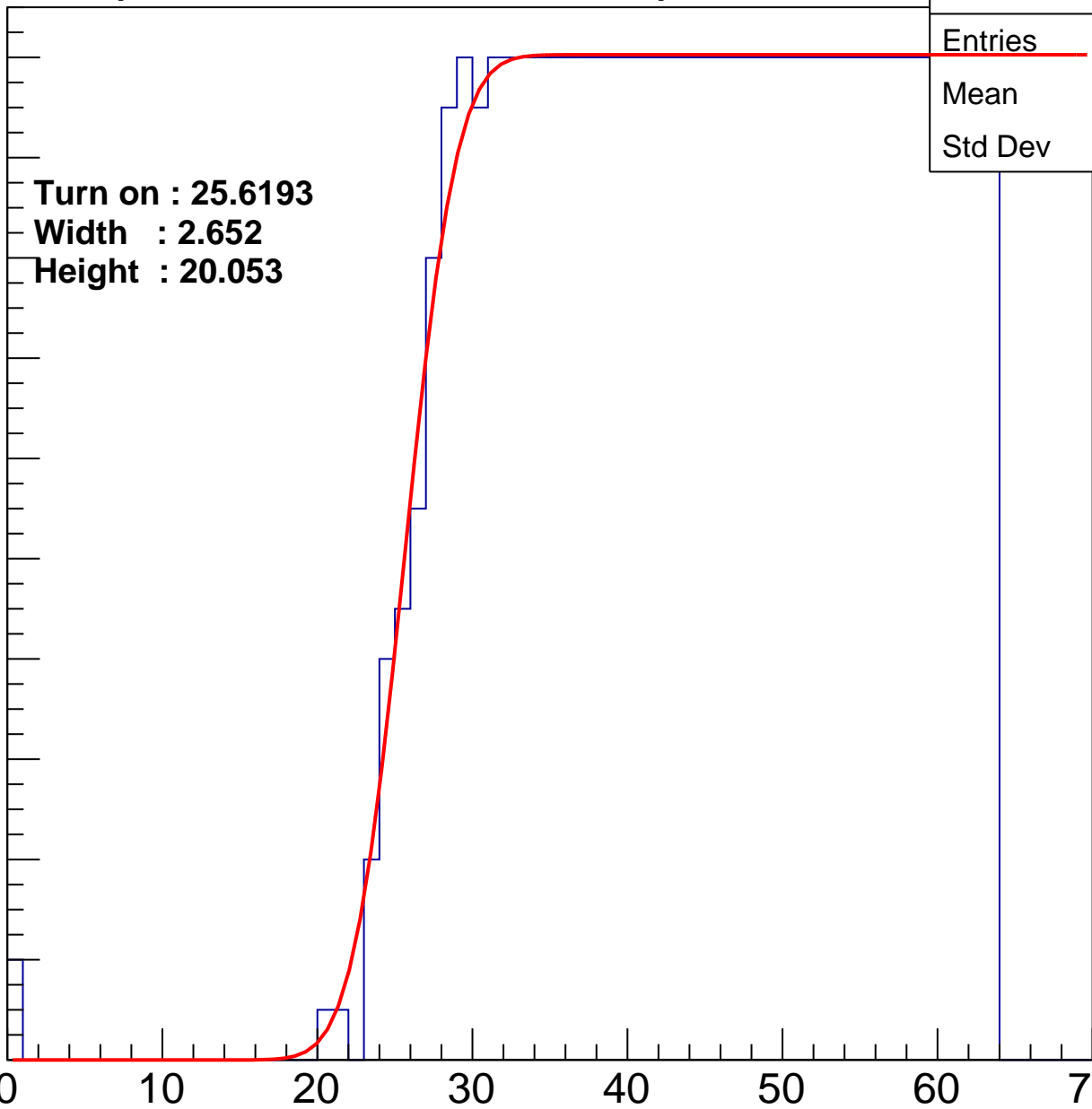
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6193
Width : 2.652
Height : 20.053

Entries	770
Mean	44.12
Std Dev	11.41

ampl



B0L101S, U23-ch72

calib_packv5_042523_0143.root, FC#1, port C1

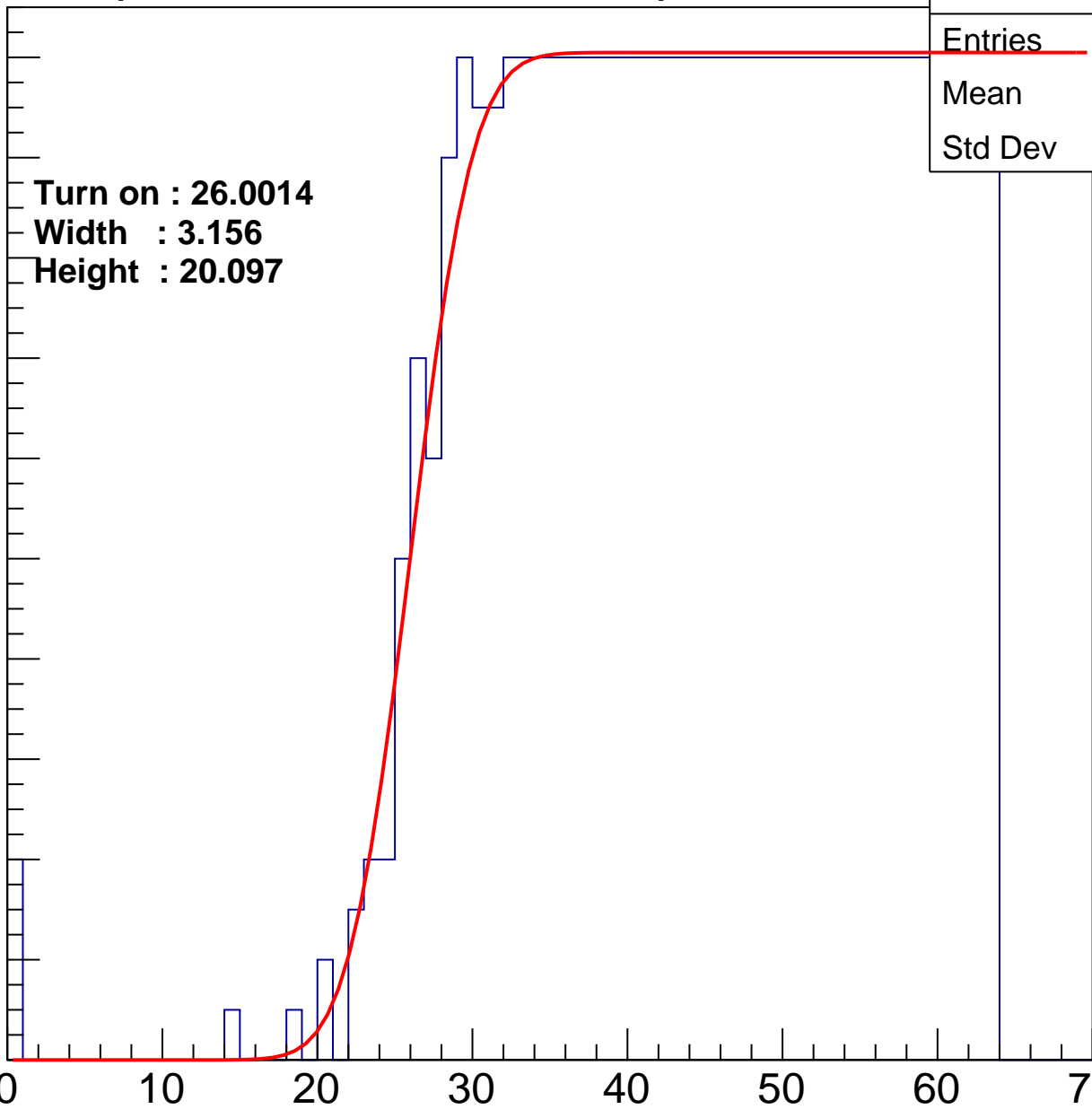
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0014
Width : 3.156
Height : 20.097

Entries	771
Mean	43.98
Std Dev	11.69

ampl



B0L101S, U23-ch73

calib_packv5_042523_0143.root, FC#1, port C1

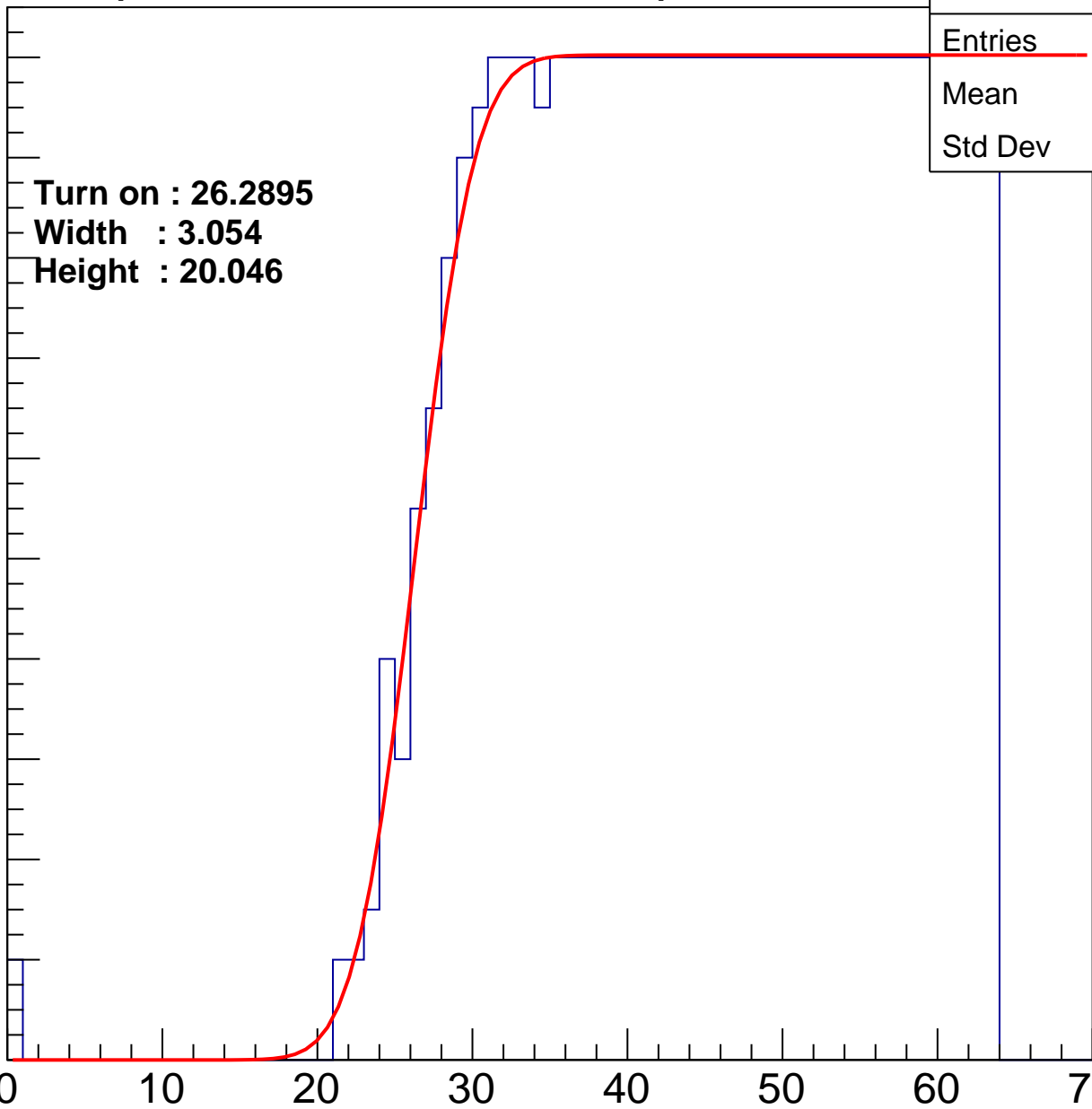
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2895
Width : 3.054
Height : 20.046

Entries	759
Mean	44.35
Std Dev	11.33

ampl



B0L101S, U23-ch74

calib_packv5_042523_0143.root, FC#1, port C1

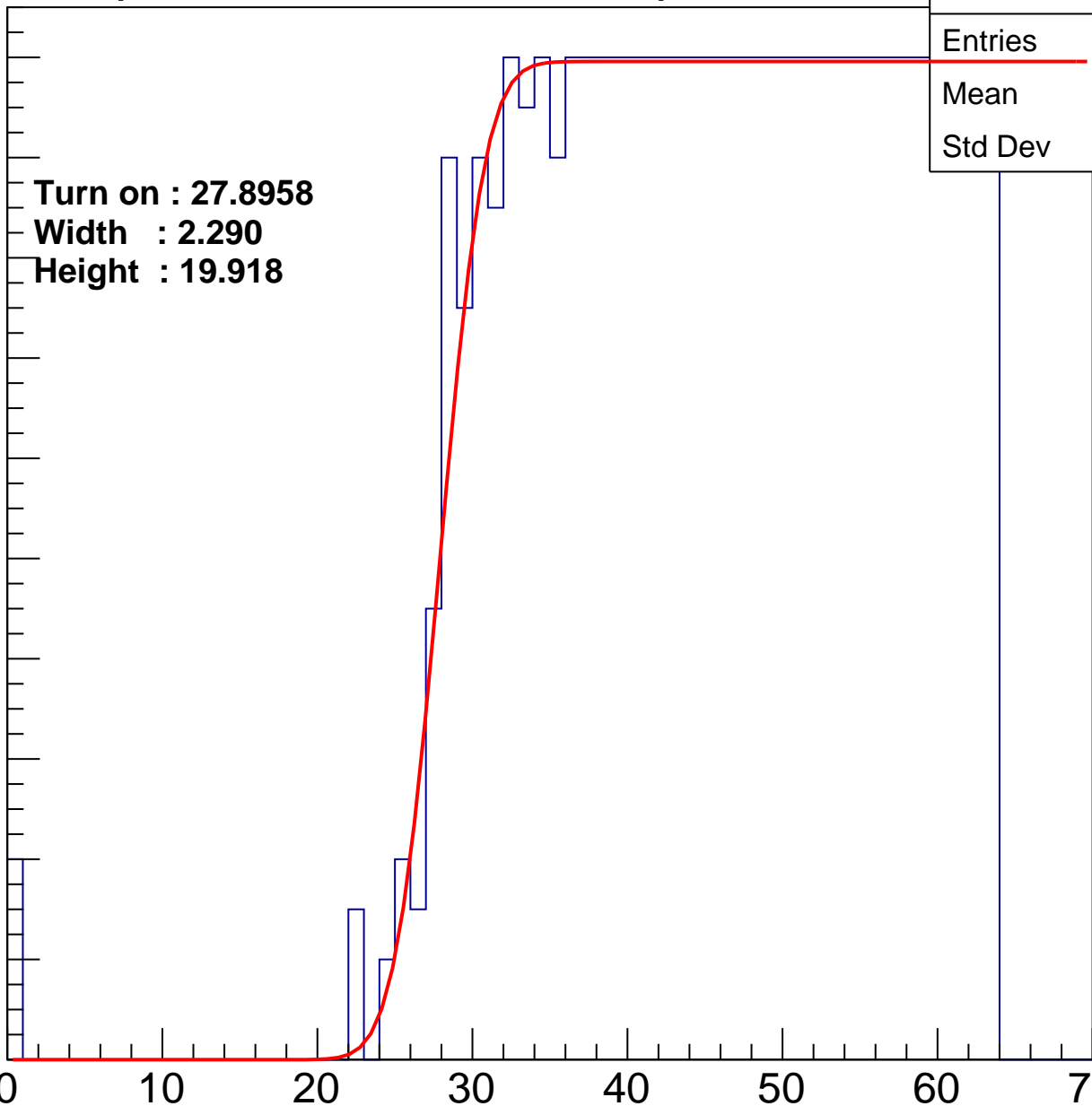
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8958
Width : 2.290
Height : 19.918

Entries	730
Mean	44.98
Std Dev	11.15

ampl



B0L101S, U23-ch75

calib_packv5_042523_0143.root, FC#1, port C1

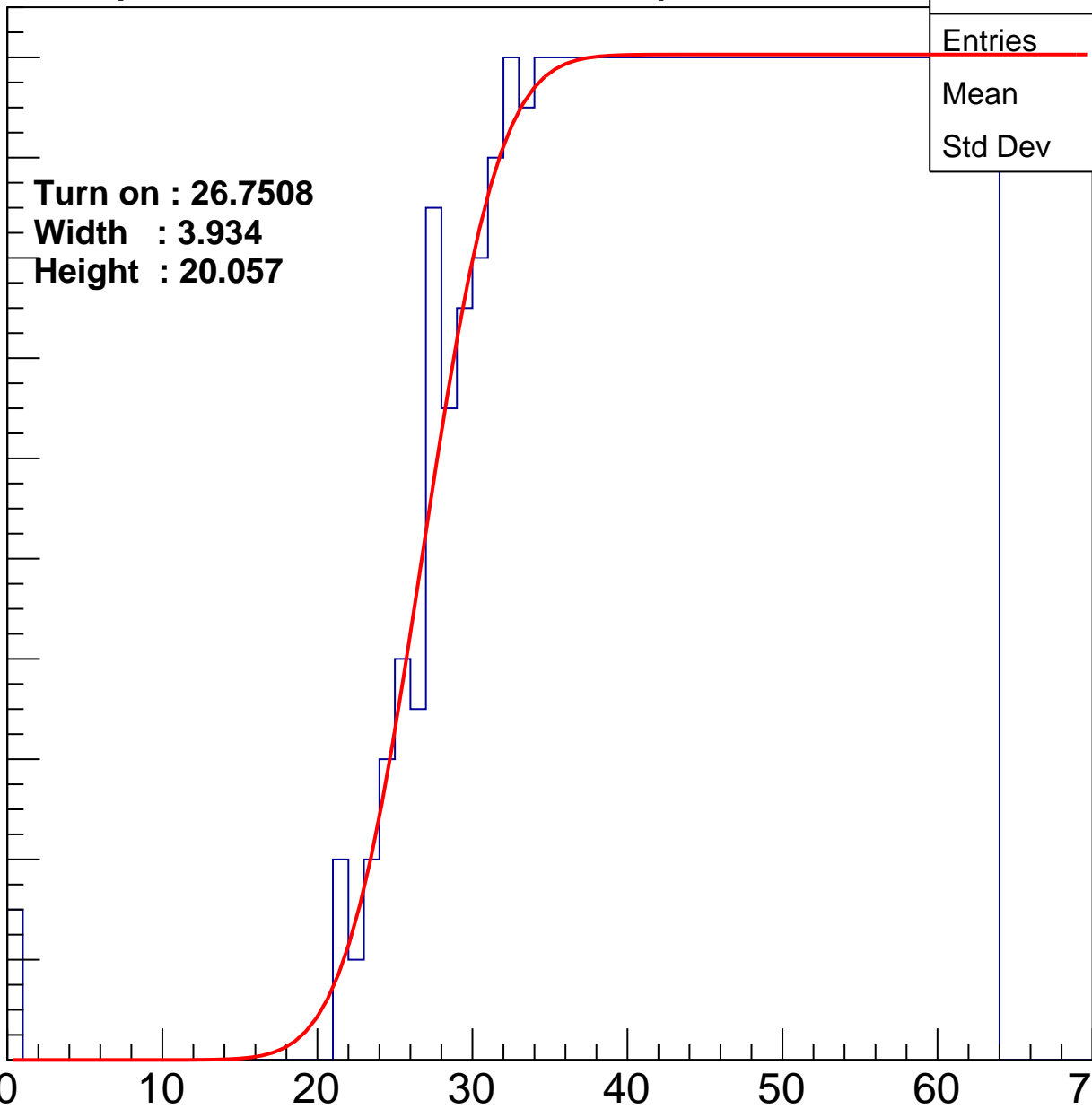
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7508
Width : 3.934
Height : 20.057

Entries	752
Mean	44.43
Std Dev	11.42

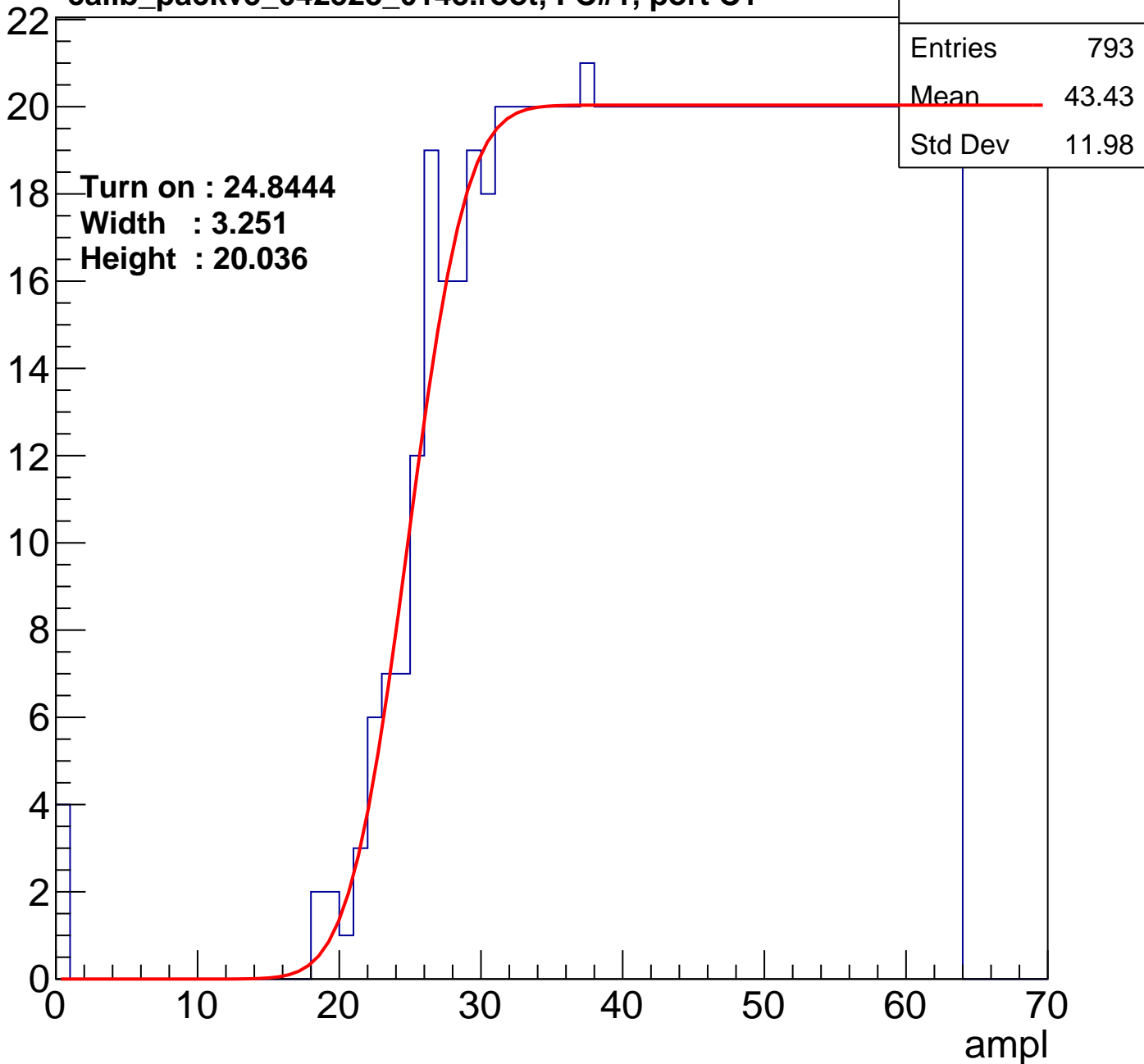
ampl



B0L101S, U23-ch76

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U23-ch77

calib_packv5_042523_0143.root, FC#1, port C1

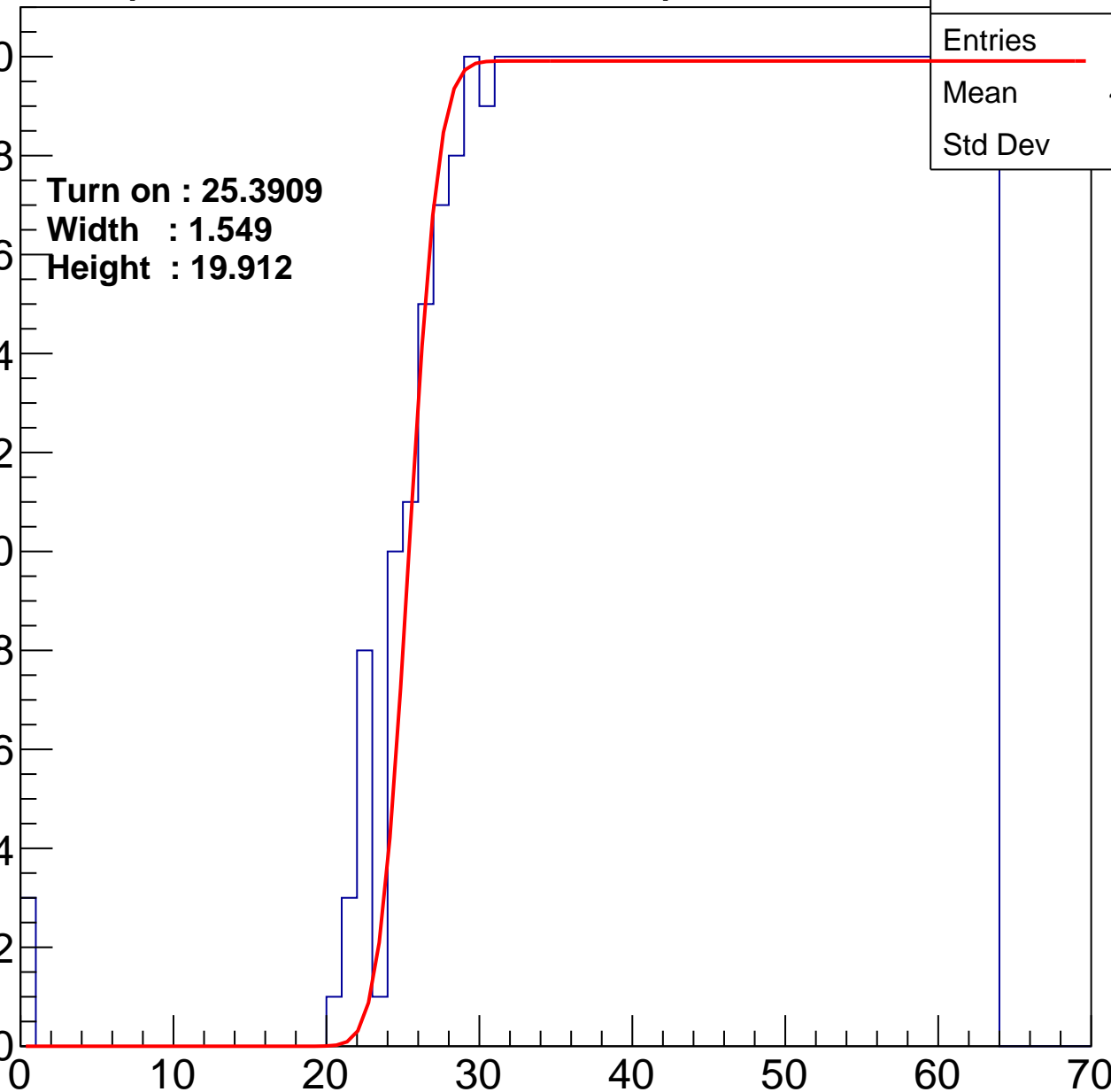
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3909
Width : 1.549
Height : 19.912

Entries	786
Mean	43.66
Std Dev	11.75

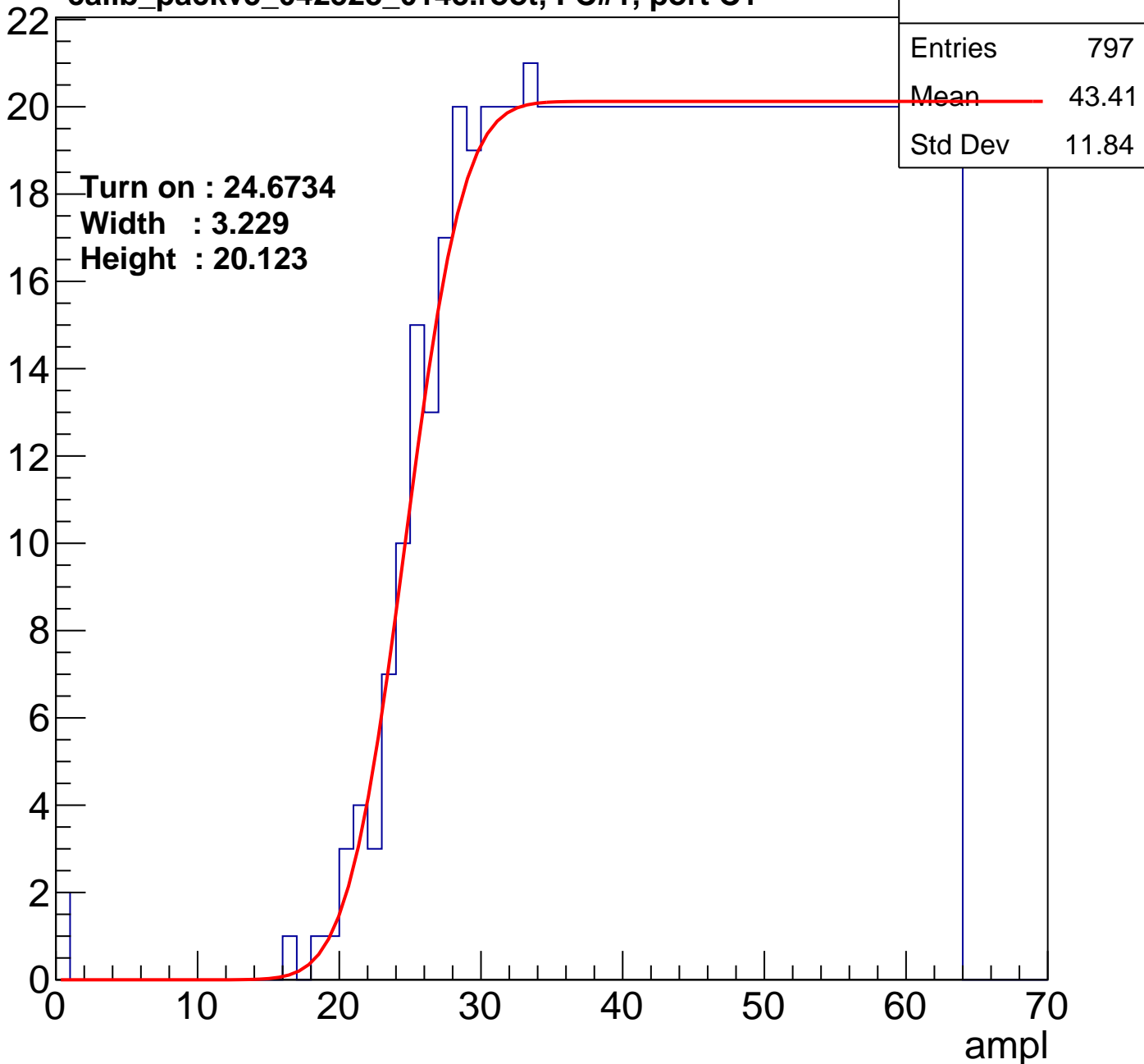
ampl



B0L101S, U23-ch78

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U23-ch79

calib_packv5_042523_0143.root, FC#1, port C1

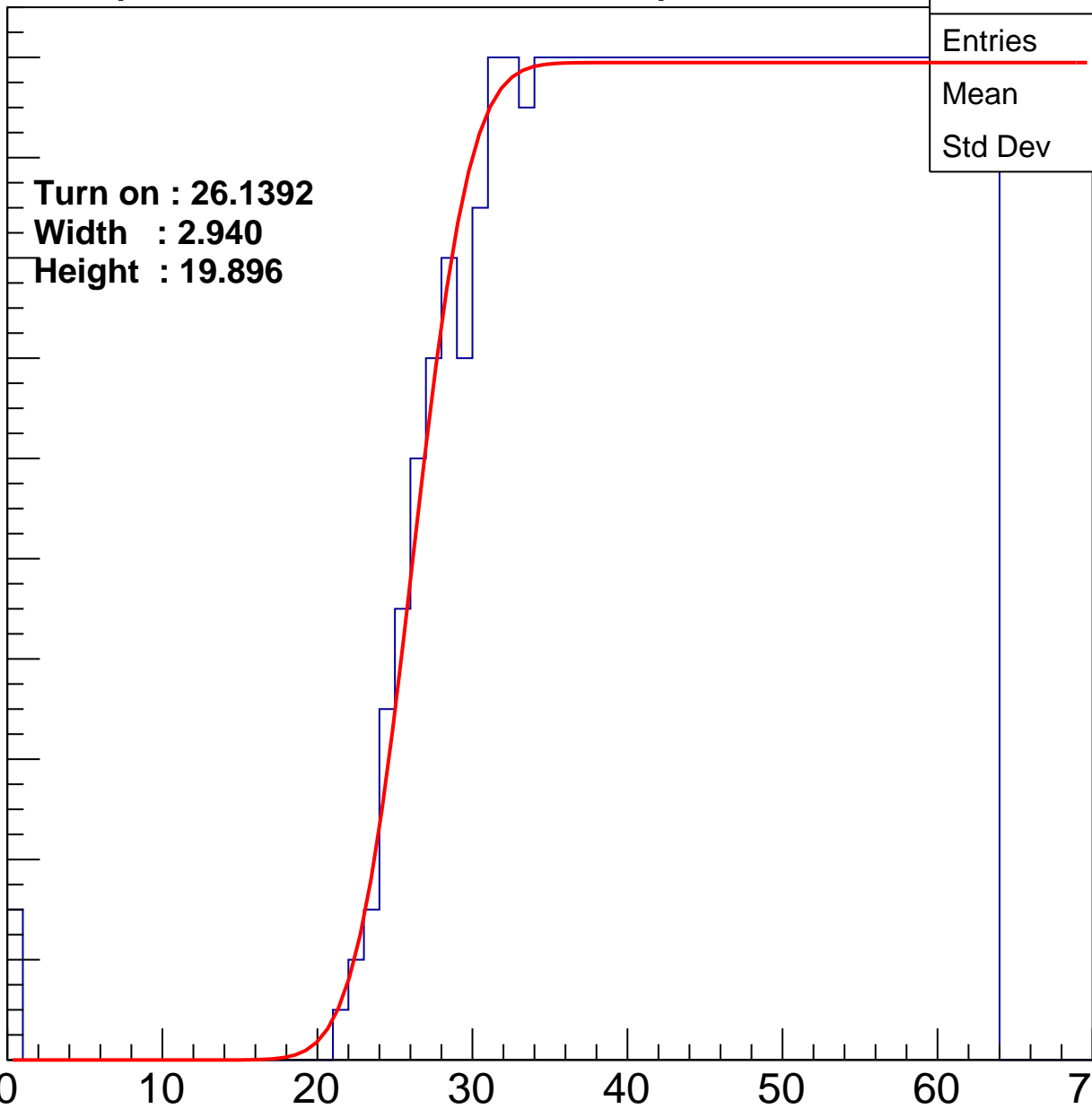
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1392
Width : 2.940
Height : 19.896

Entries	757
Mean	44.34
Std Dev	11.42

ampl



B0L101S, U23-ch80

calib_packv5_042523_0143.root, FC#1, port C1

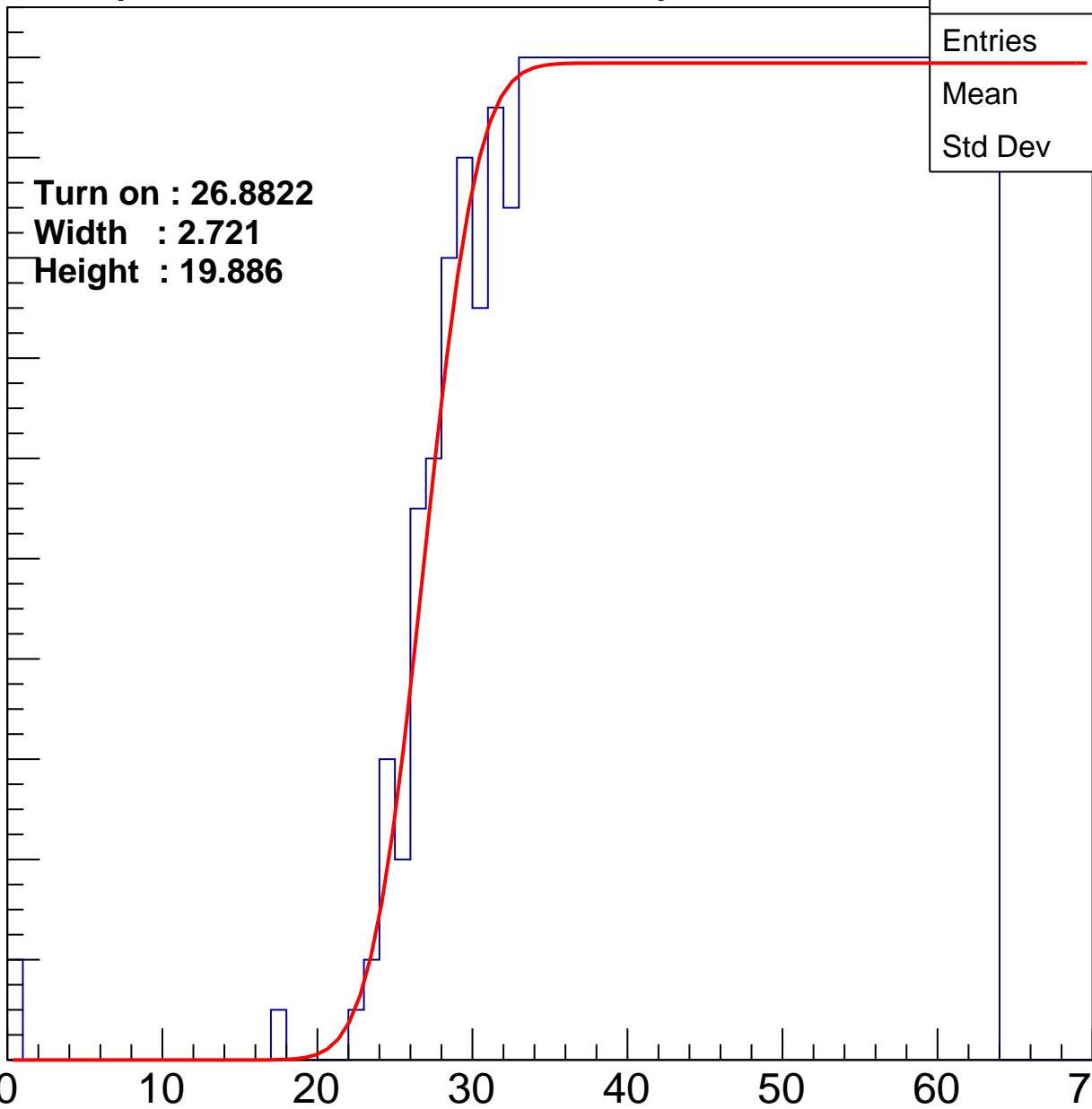
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8822
Width : 2.721
Height : 19.886

Entries	744
Mean	44.69
Std Dev	11.17

ampl



B0L101S, U23-ch81

calib_packv5_042523_0143.root, FC#1, port C1

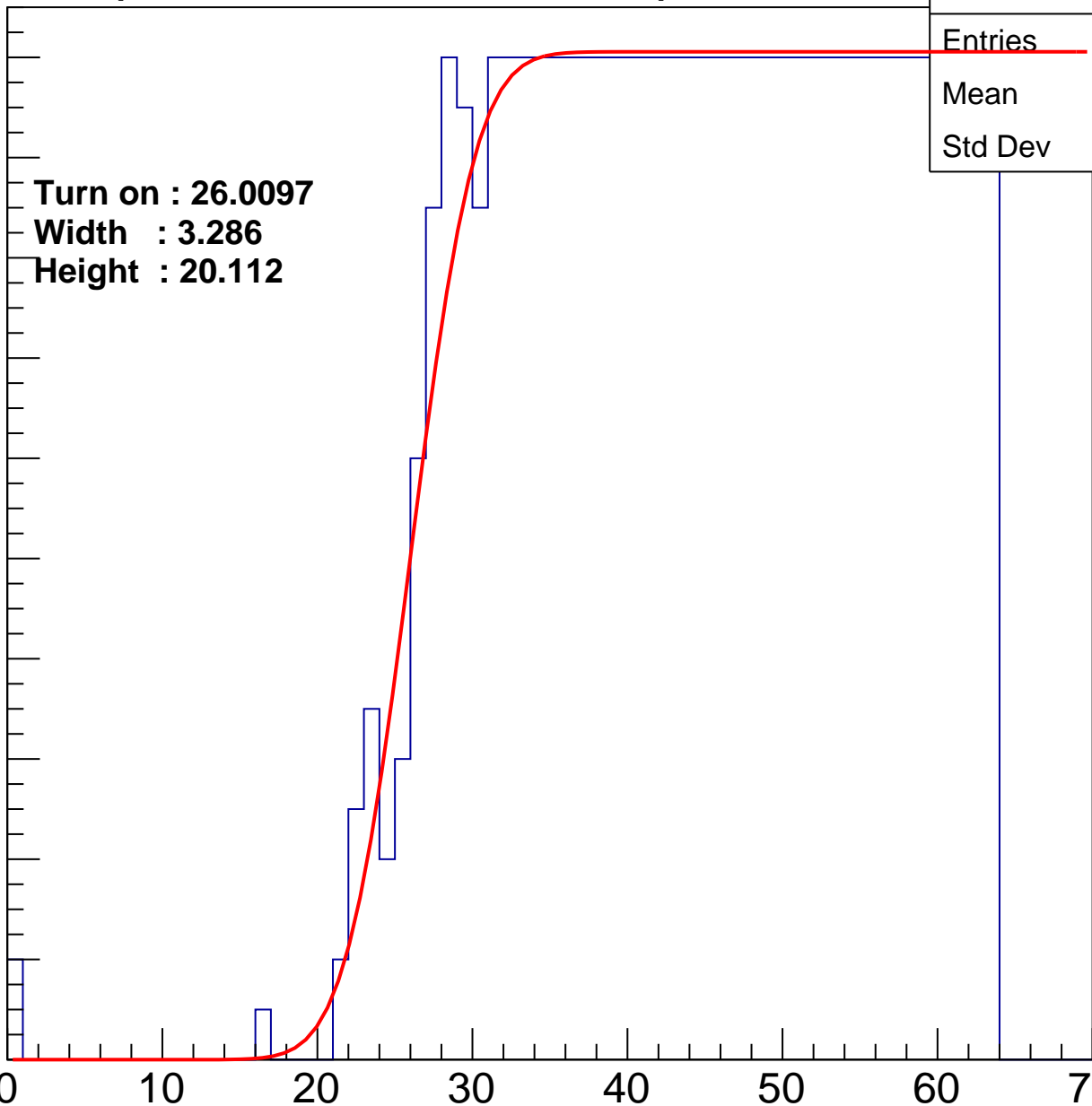
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0097
Width : 3.286
Height : 20.112

Entries	772
Mean	44.02
Std Dev	11.51

ampl



B0L101S, U23-ch82

calib_packv5_042523_0143.root, FC#1, port C1

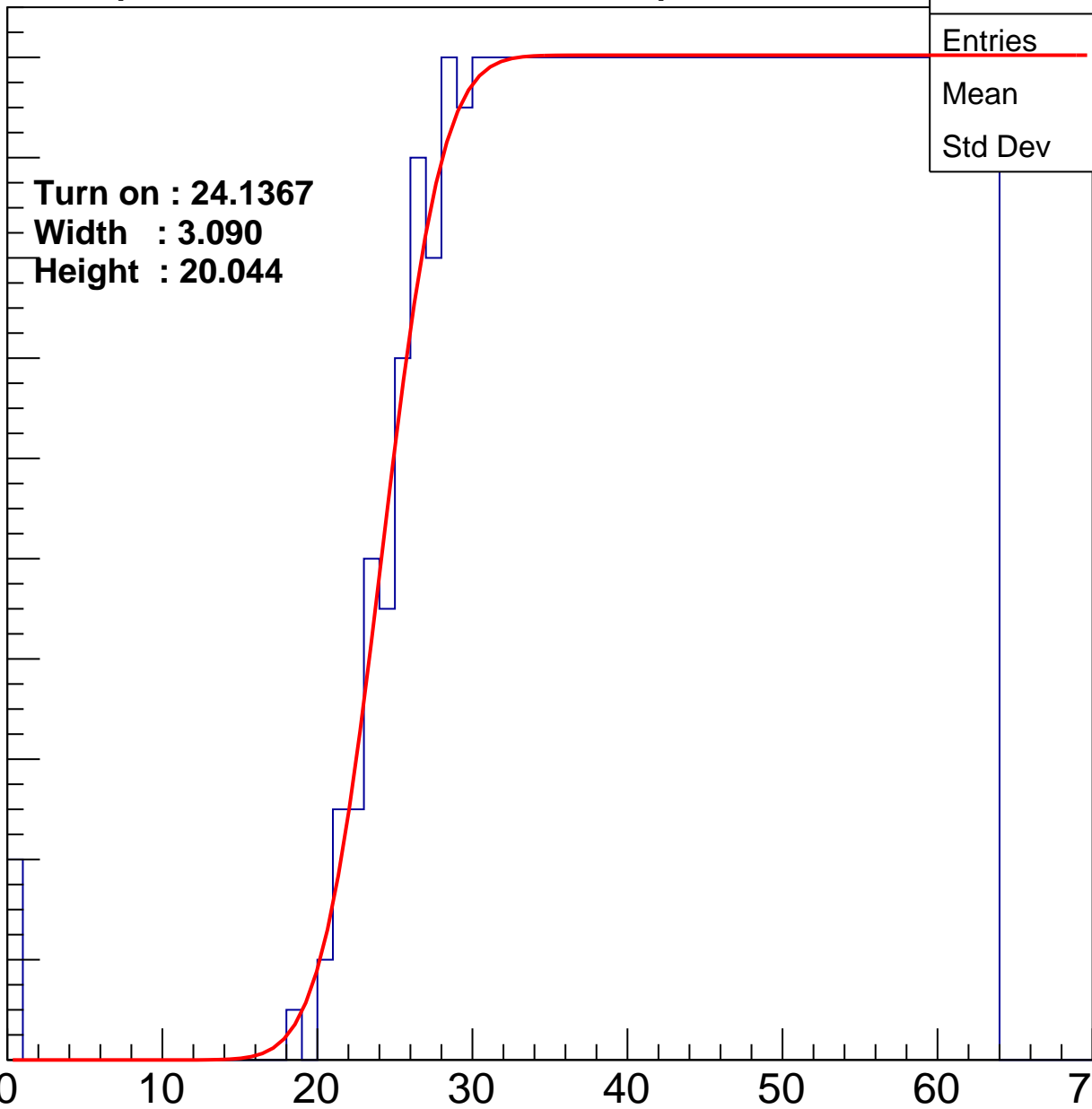
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.1367
Width : 3.090
Height : 20.044

Entries	803
Mean	43.21
Std Dev	12.06

ampl



B0L101S, U23-ch83

calib_packv5_042523_0143.root, FC#1, port C1

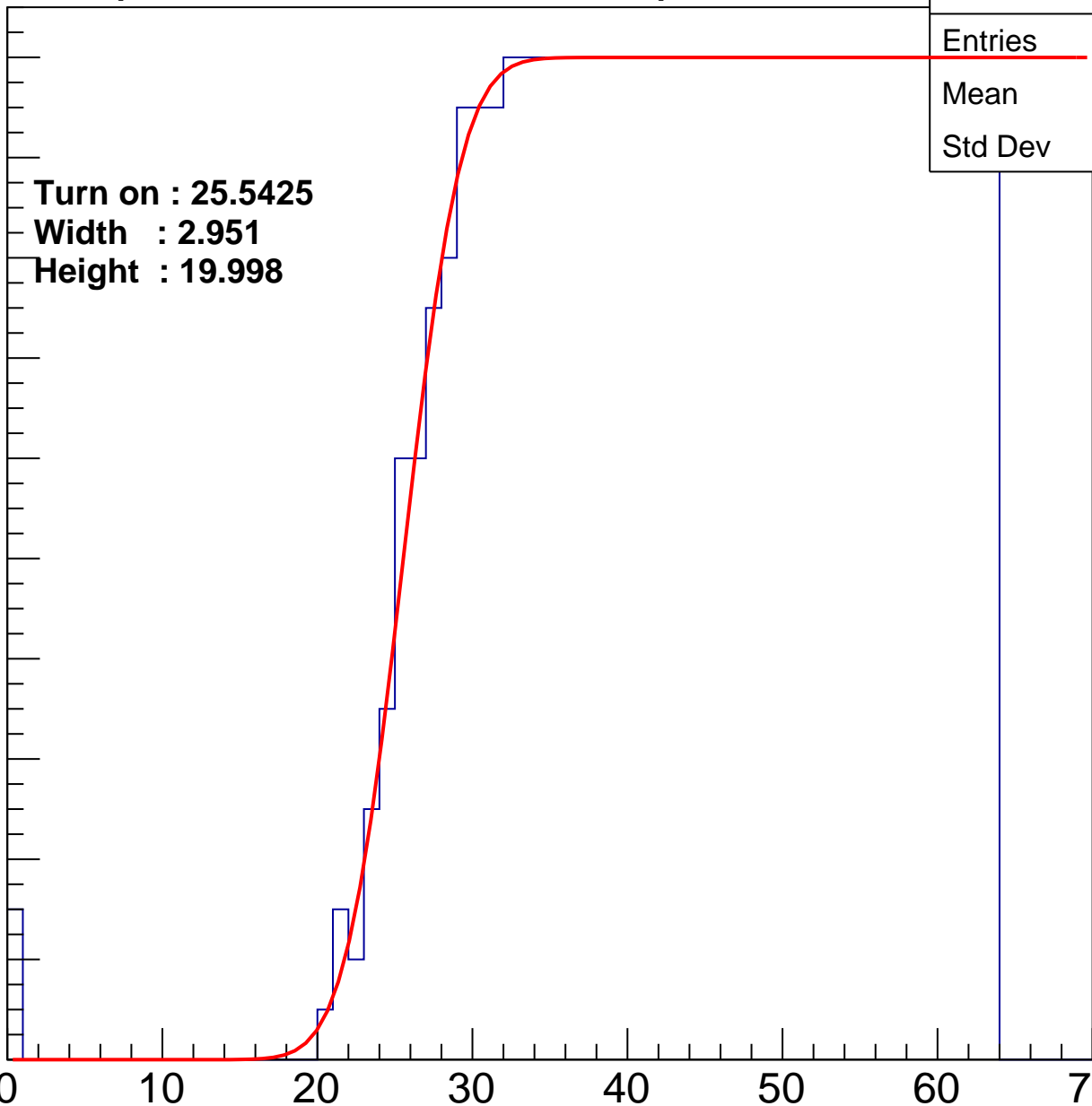
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5425
Width : 2.951
Height : 19.998

Entries	773
Mean	43.97
Std Dev	11.61

ampl



B0L101S, U23-ch84

calib_packv5_042523_0143.root, FC#1, port C1

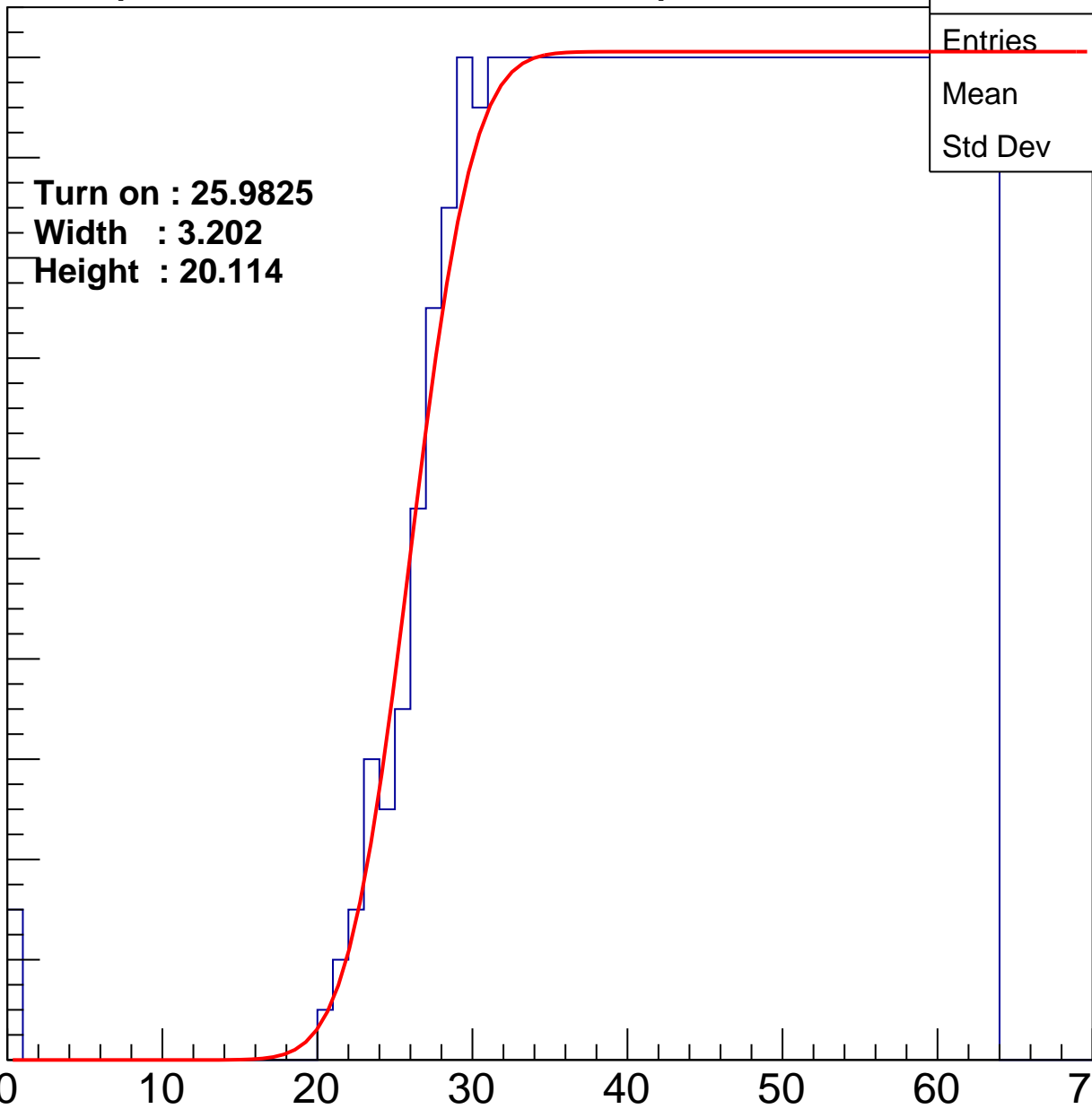
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9825
Width : 3.202
Height : 20.114

Entries	769
Mean	44.08
Std Dev	11.53

ampl



B0L101S, U23-ch85

calib_packv5_042523_0143.root, FC#1, port C1

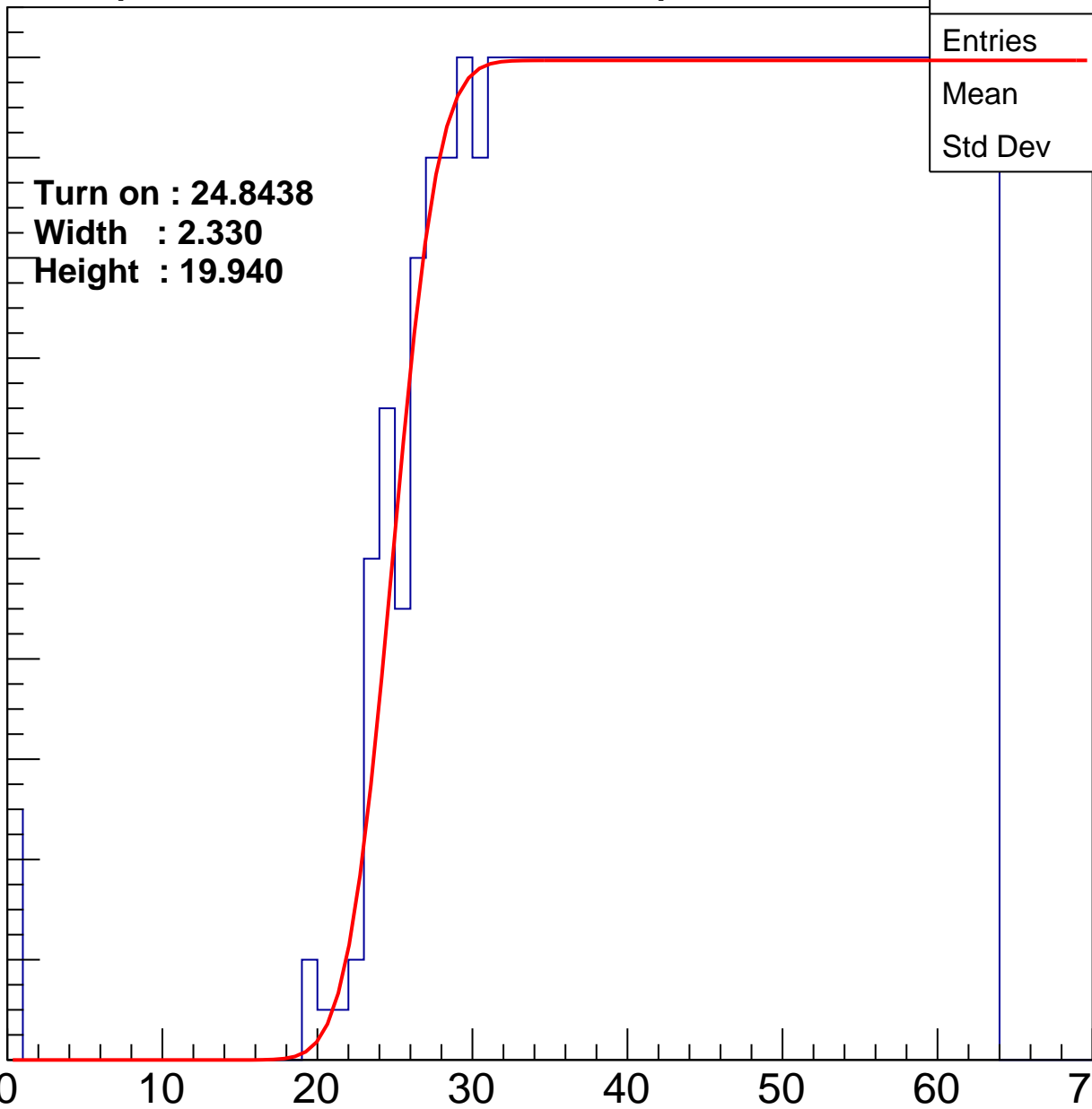
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8438
Width : 2.330
Height : 19.940

Entries	793
Mean	43.42
Std Dev	12.01

ampl



B0L101S, U23-ch86

calib_packv5_042523_0143.root, FC#1, port C1

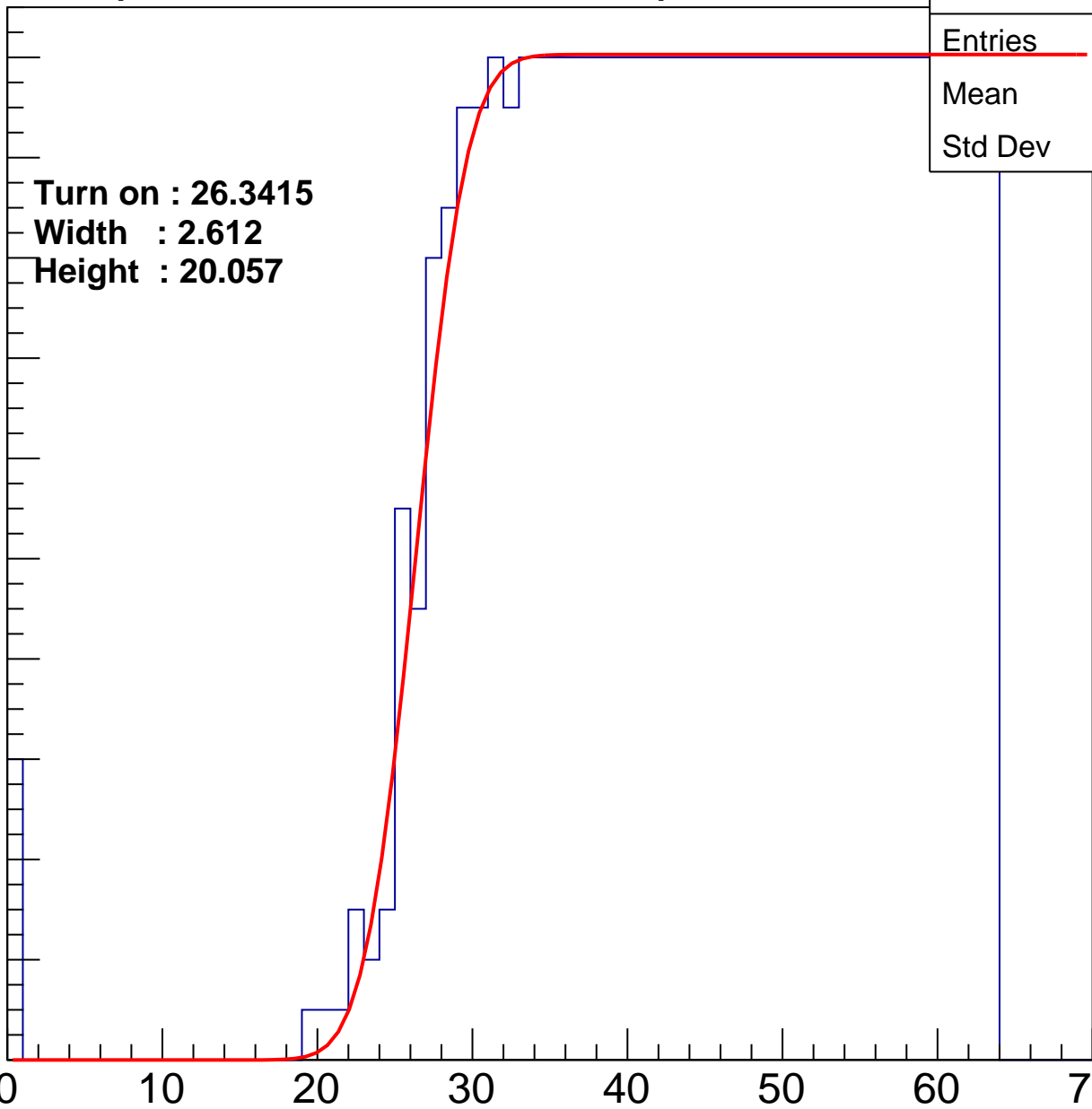
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3415
Width : 2.612
Height : 20.057

Entries	767
Mean	44.03
Std Dev	11.78

ampl



B0L101S, U23-ch87

calib_packv5_042523_0143.root, FC#1, port C1

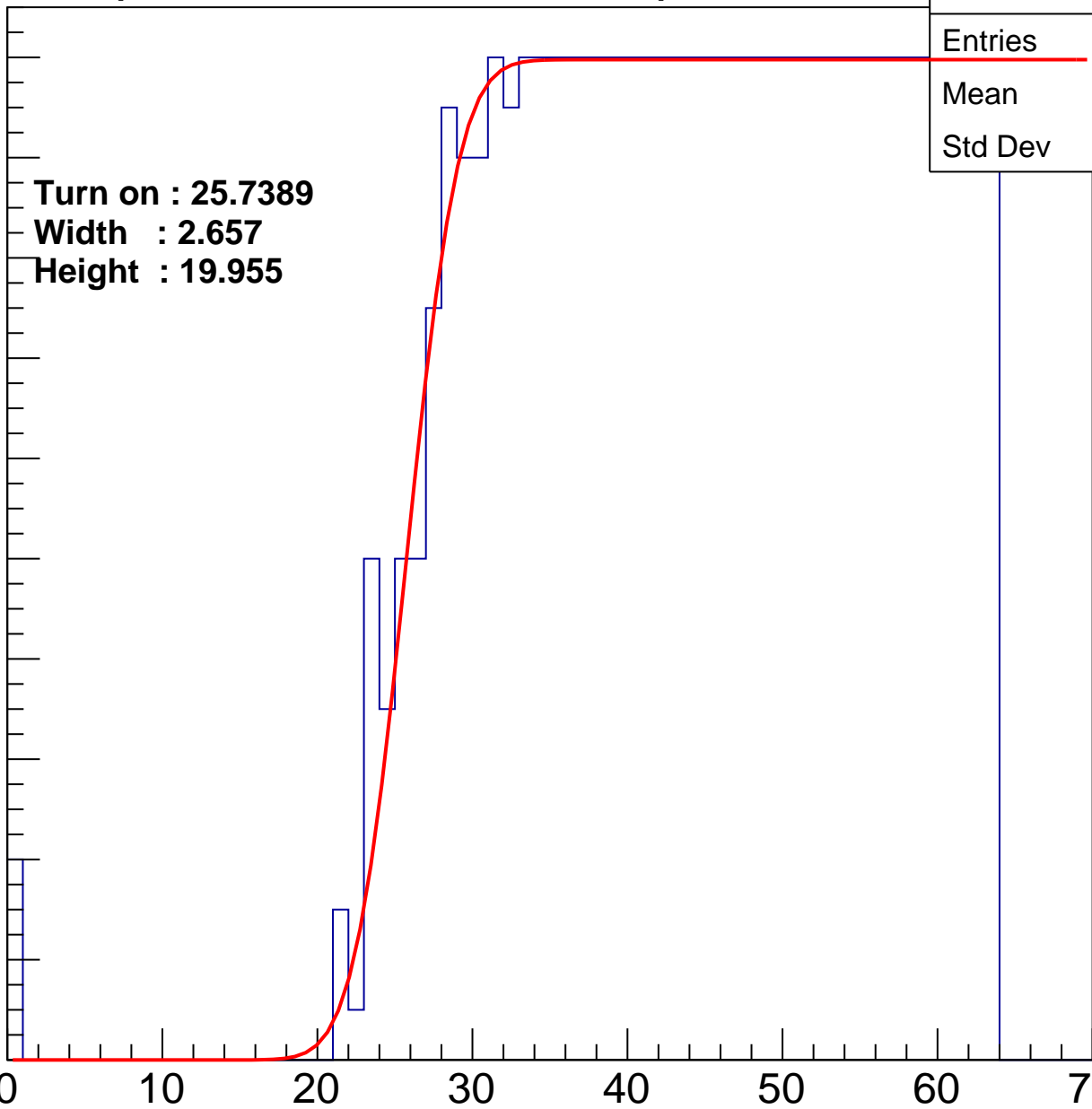
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7389
Width : 2.657
Height : 19.955

Entries	774
Mean	43.9
Std Dev	11.72

ampl



B0L101S, U23-ch88

calib_packv5_042523_0143.root, FC#1, port C1

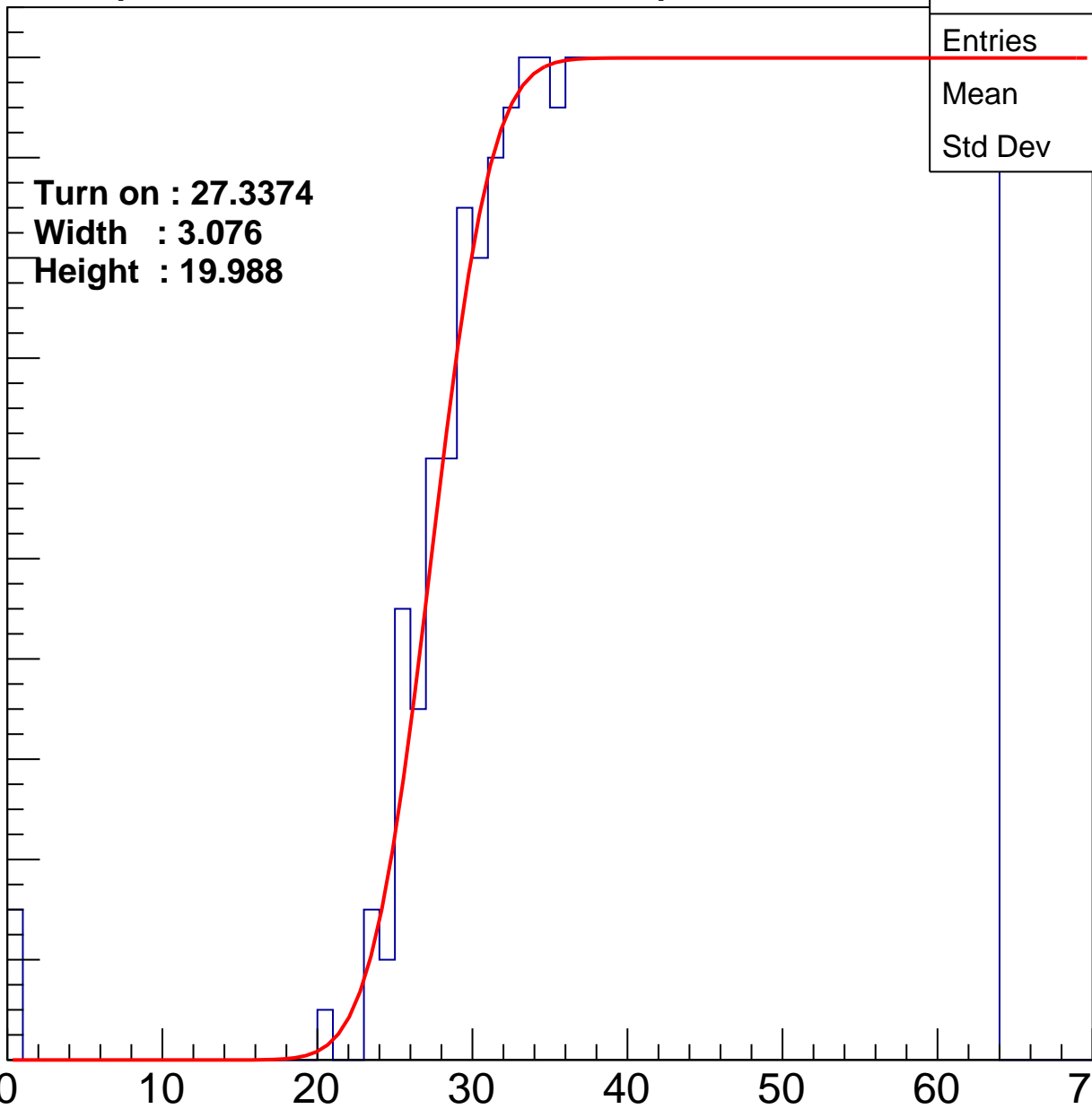
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3374
Width : 3.076
Height : 19.988

Entries	738
Mean	44.81
Std Dev	11.18

ampl



B0L101S, U23-ch89

calib_packv5_042523_0143.root, FC#1, port C1

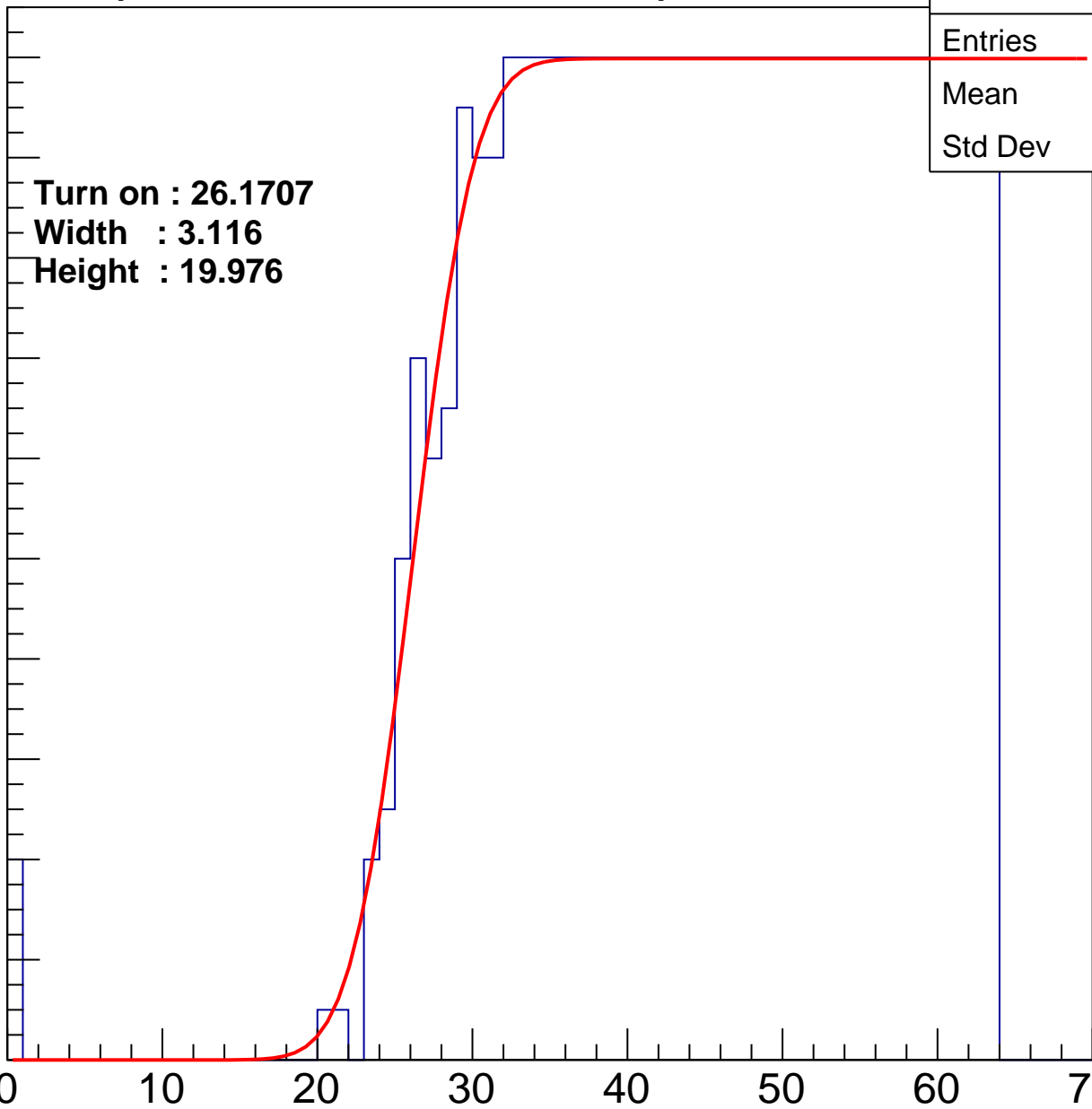
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1707
Width : 3.116
Height : 19.976

Entries	759
Mean	44.27
Std Dev	11.52

ampl



B0L101S, U23-ch90

calib_packv5_042523_0143.root, FC#1, port C1

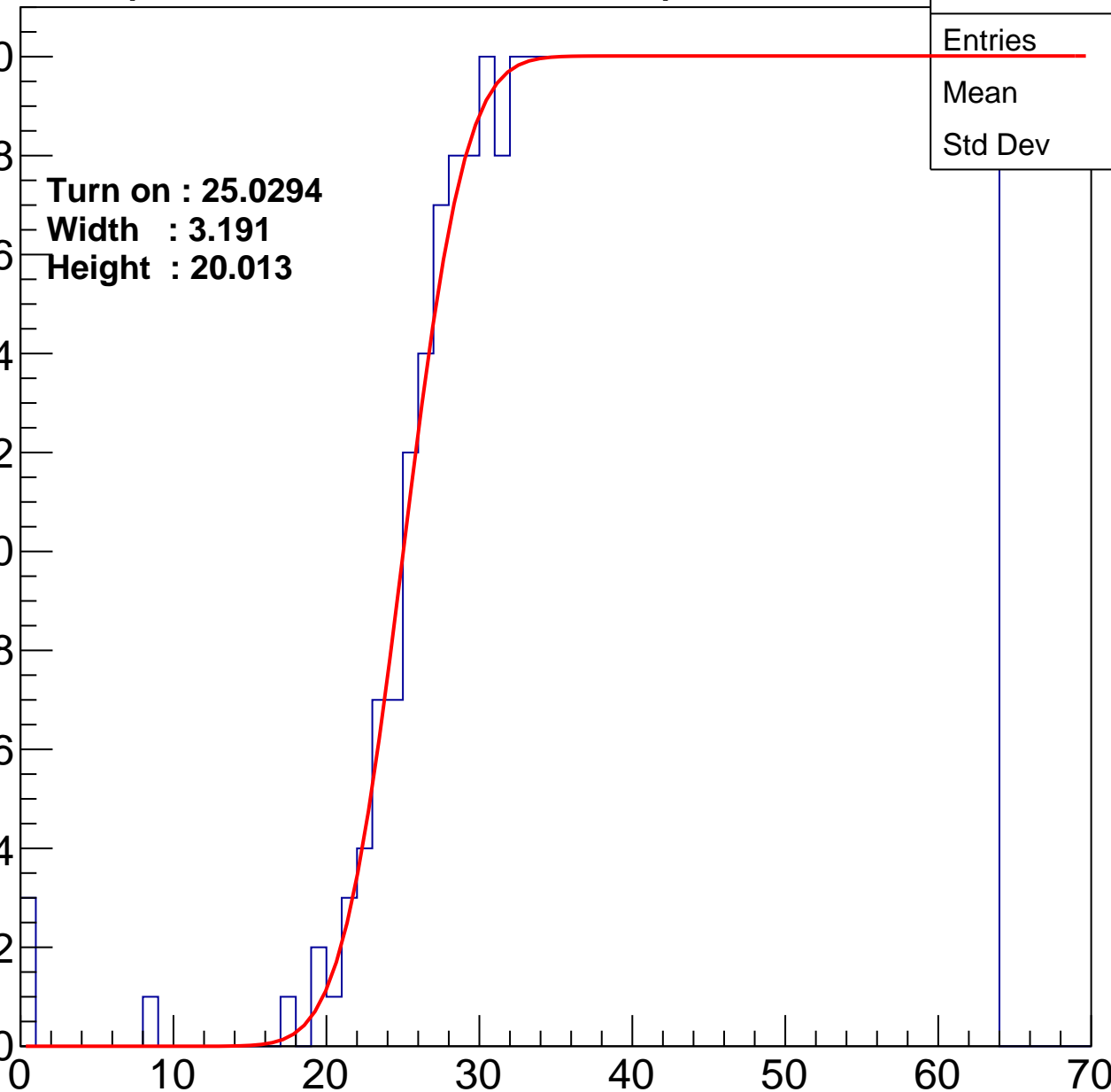
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0294
Width : 3.191
Height : 20.013

Entries	786
Mean	43.6
Std Dev	11.87

ampl



B0L101S, U23-ch91

calib_packv5_042523_0143.root, FC#1, port C1

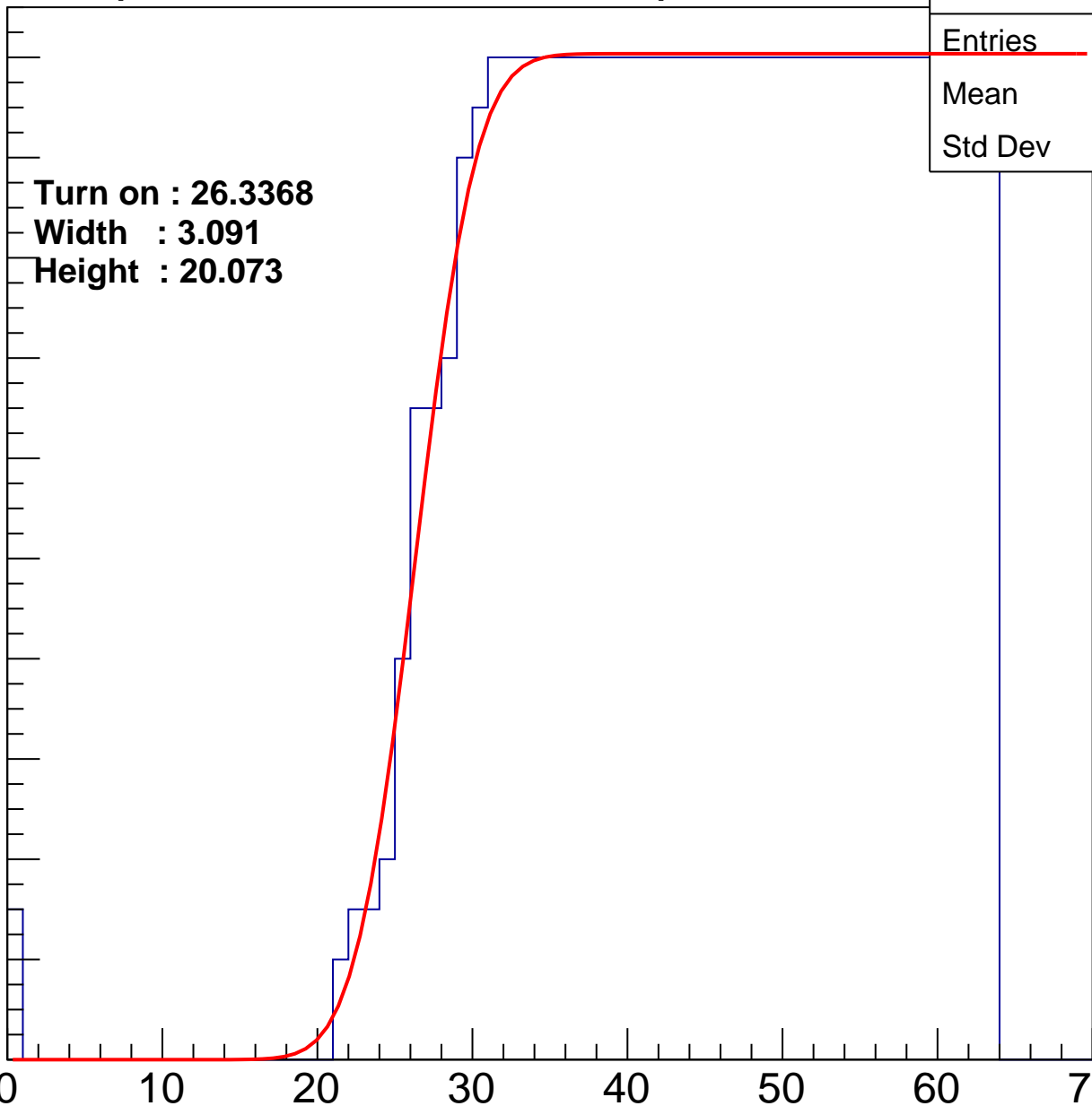
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3368
Width : 3.091
Height : 20.073

Entries	760
Mean	44.3
Std Dev	11.42

ampl



B0L101S, U23-ch92

calib_packv5_042523_0143.root, FC#1, port C1

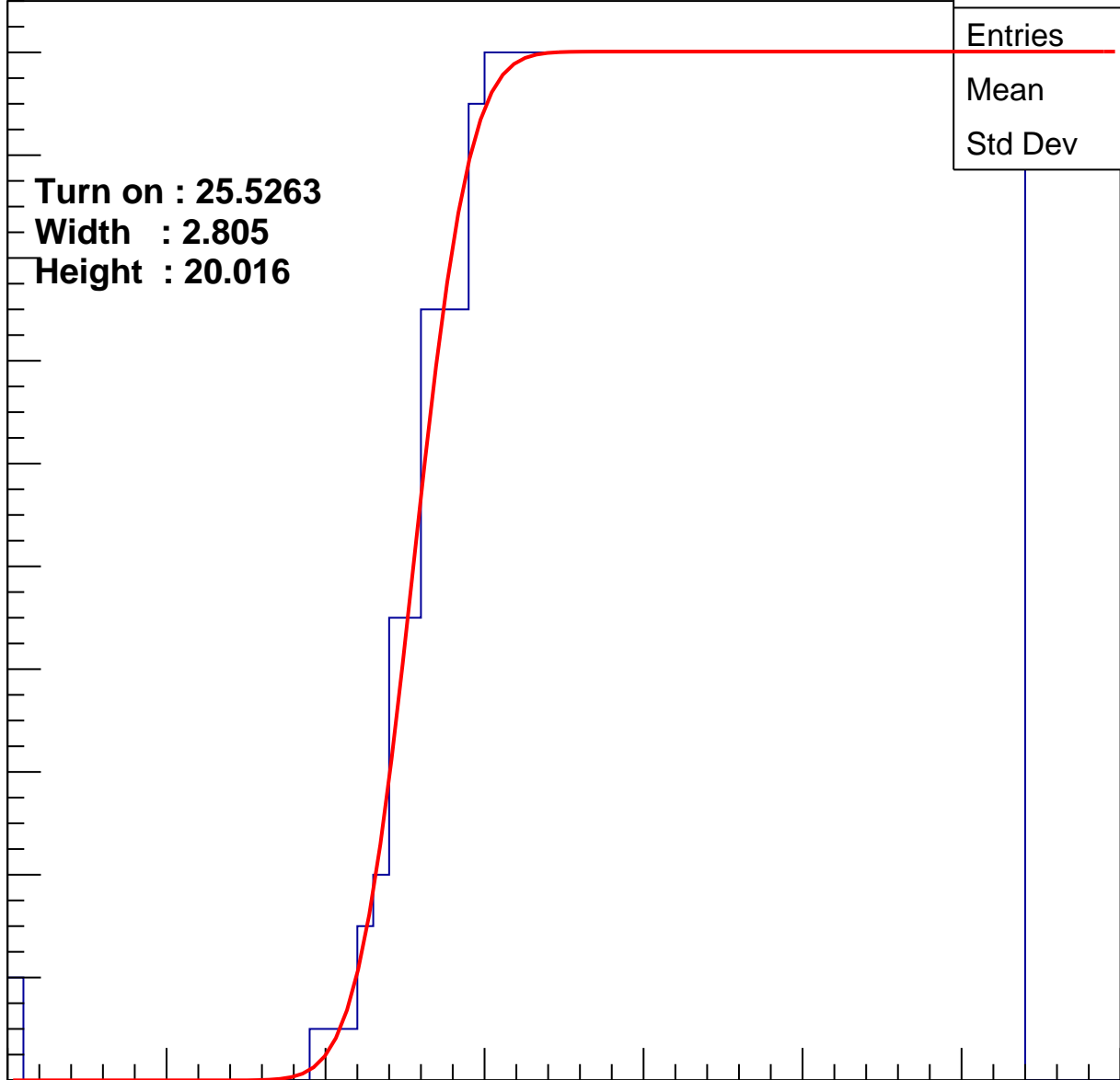
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5263
Width : 2.805
Height : 20.016

Entries	774
Mean	43.99
Std Dev	11.52

ampl



B0L101S, U23-ch93

calib_packv5_042523_0143.root, FC#1, port C1

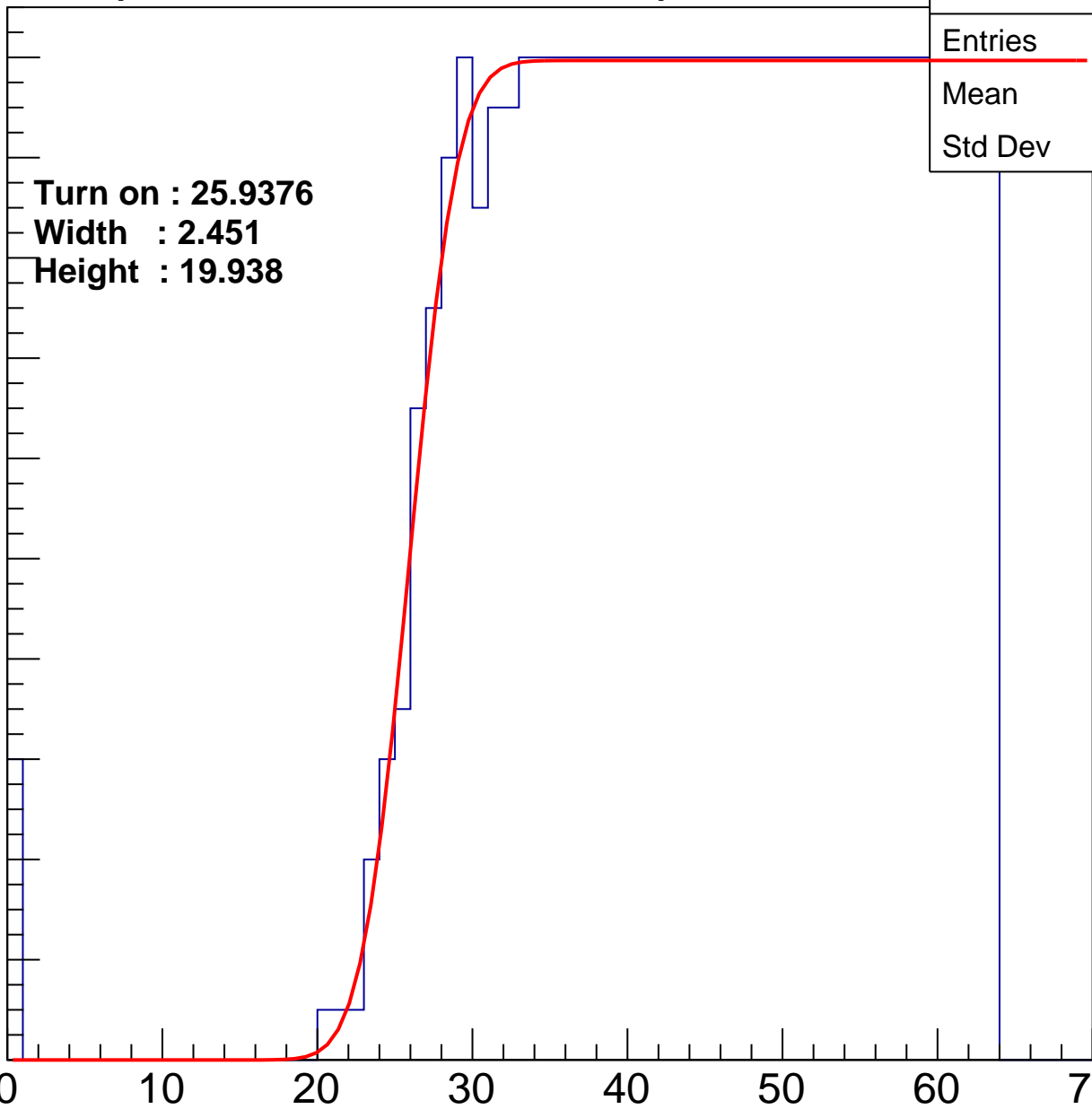
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9376
Width : 2.451
Height : 19.938

Entries	767
Mean	44.03
Std Dev	11.78

ampl



B0L101S, U23-ch94

calib_packv5_042523_0143.root, FC#1, port C1

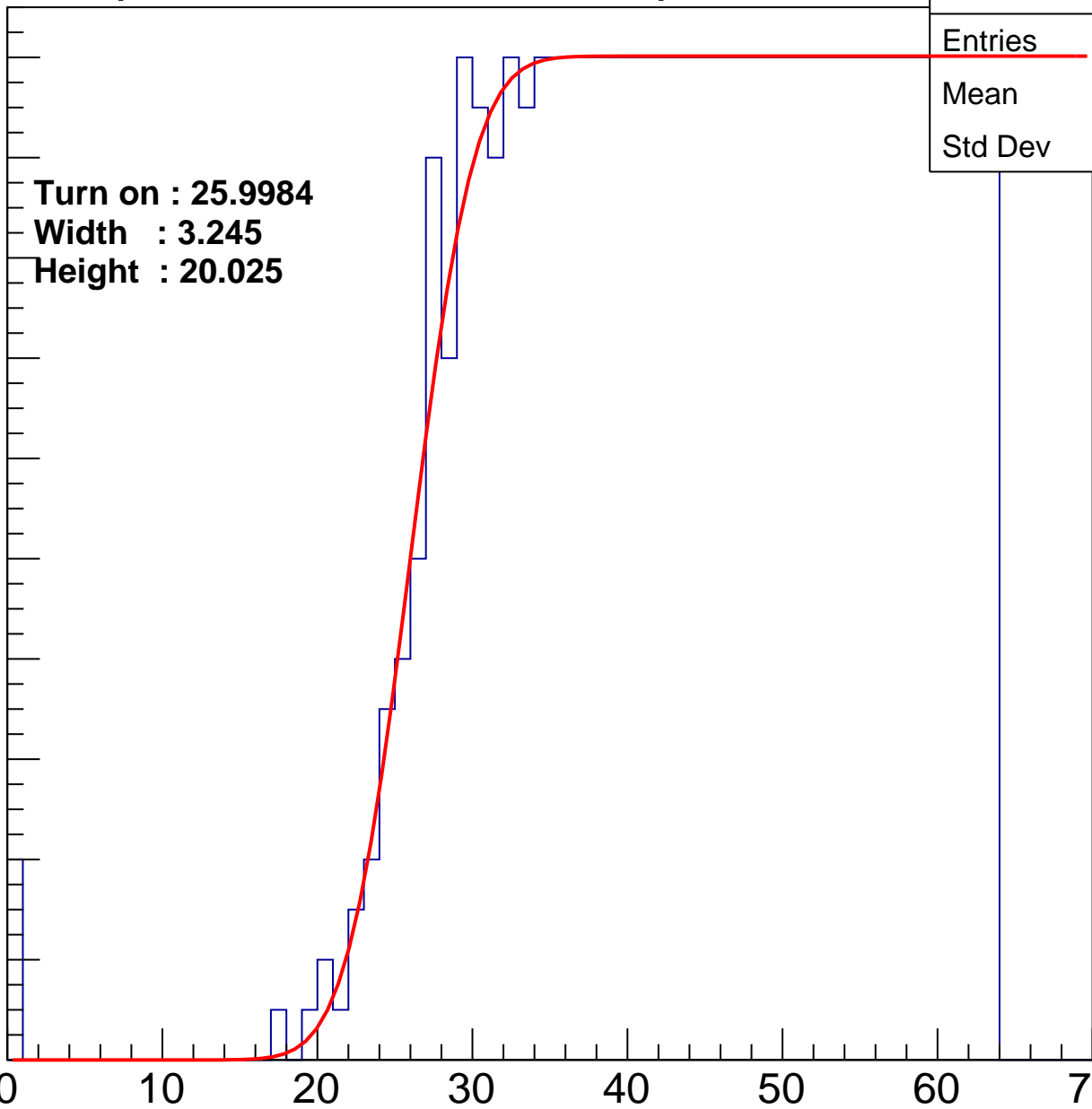
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9984
Width : 3.245
Height : 20.025

Entries	769
Mean	44
Std Dev	11.7

ampl



B0L101S, U23-ch95

calib_packv5_042523_0143.root, FC#1, port C1

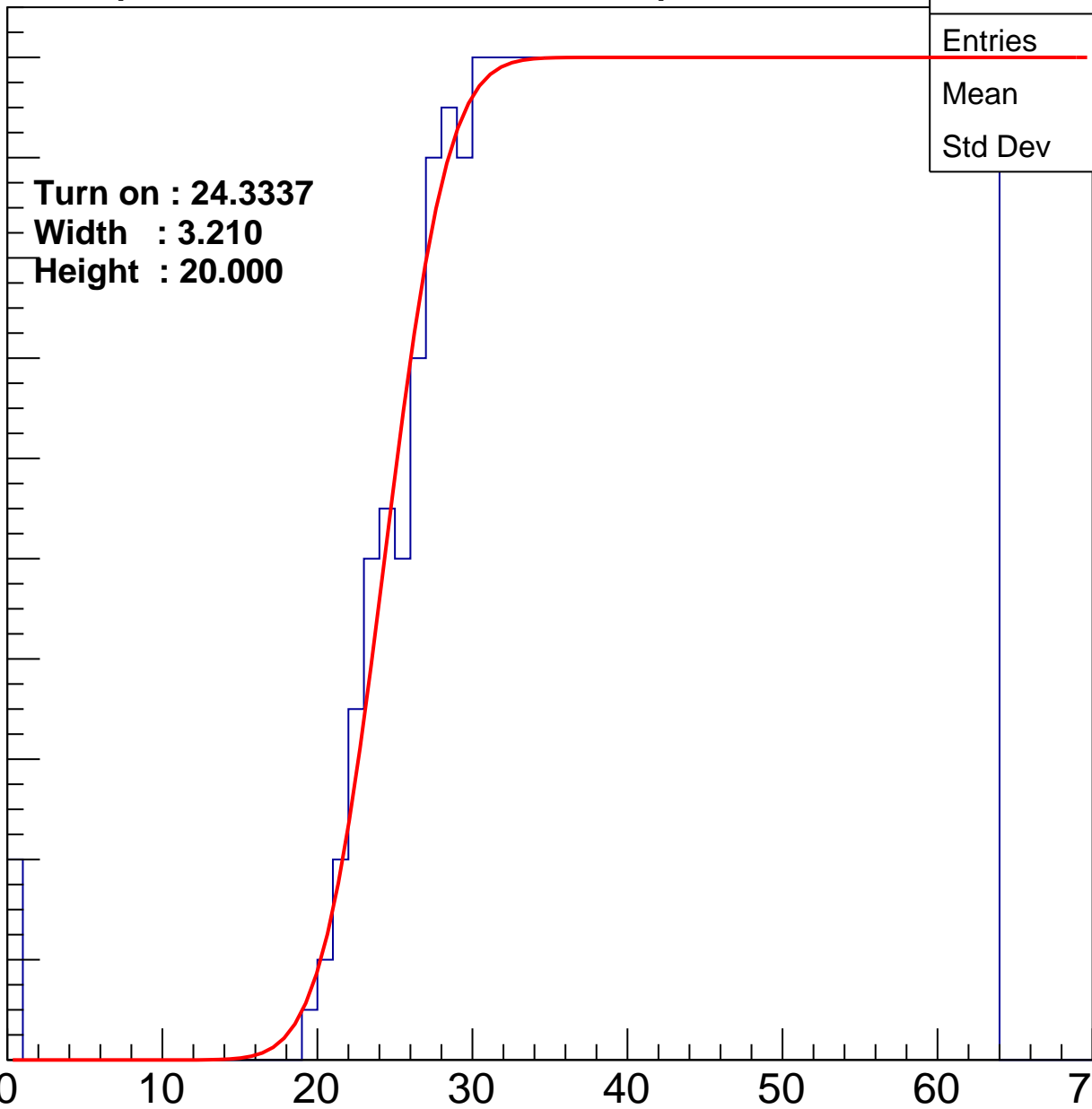
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.3337
Width : 3.210
Height : 20.000

Entries	798
Mean	43.31
Std Dev	12.03

ampl



B0L101S, U23-ch96

calib_packv5_042523_0143.root, FC#1, port C1

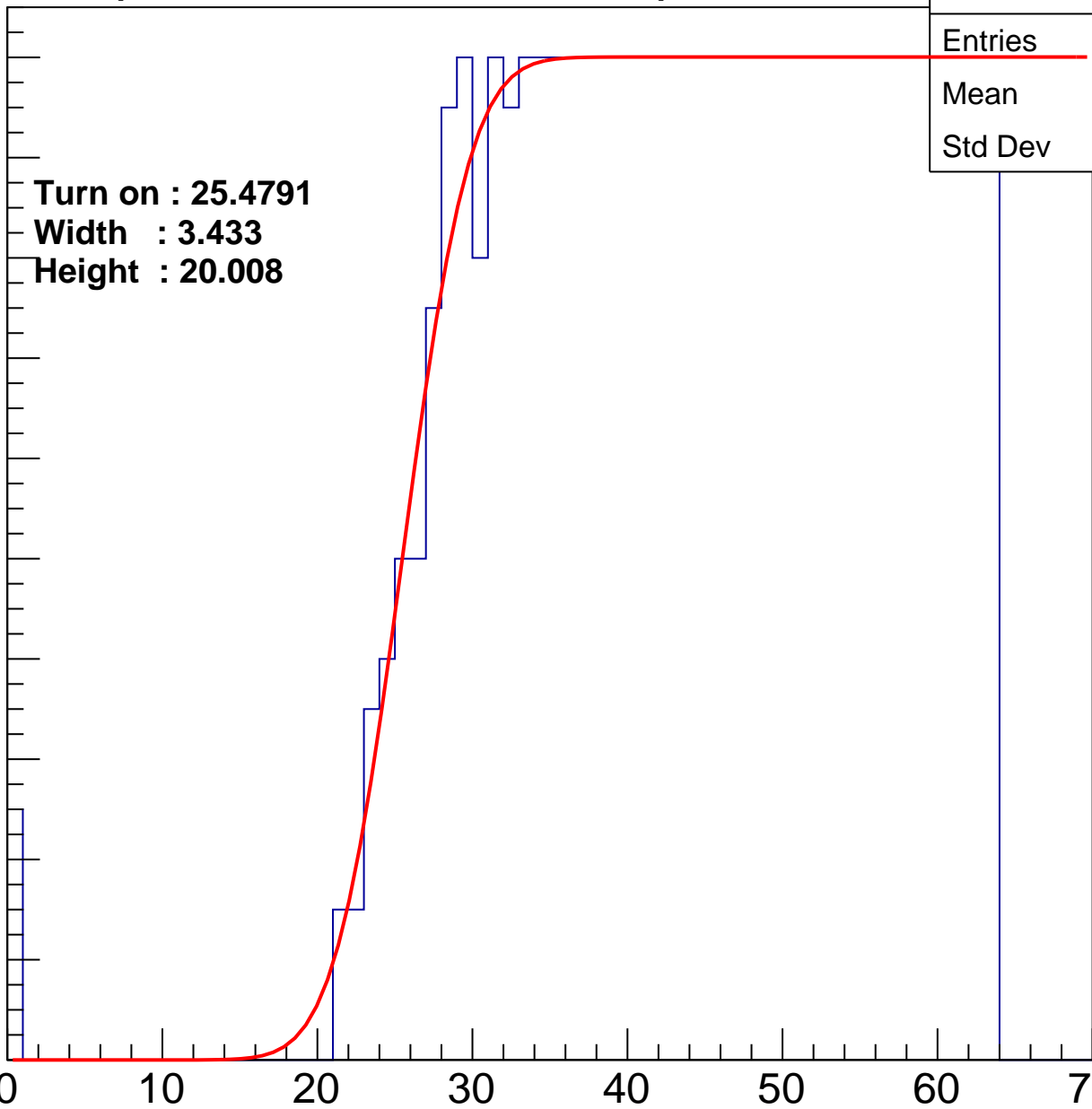
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4791
Width : 3.433
Height : 20.008

Entries	775
Mean	43.84
Std Dev	11.82

ampl



B0L101S, U23-ch97

calib_packv5_042523_0143.root, FC#1, port C1

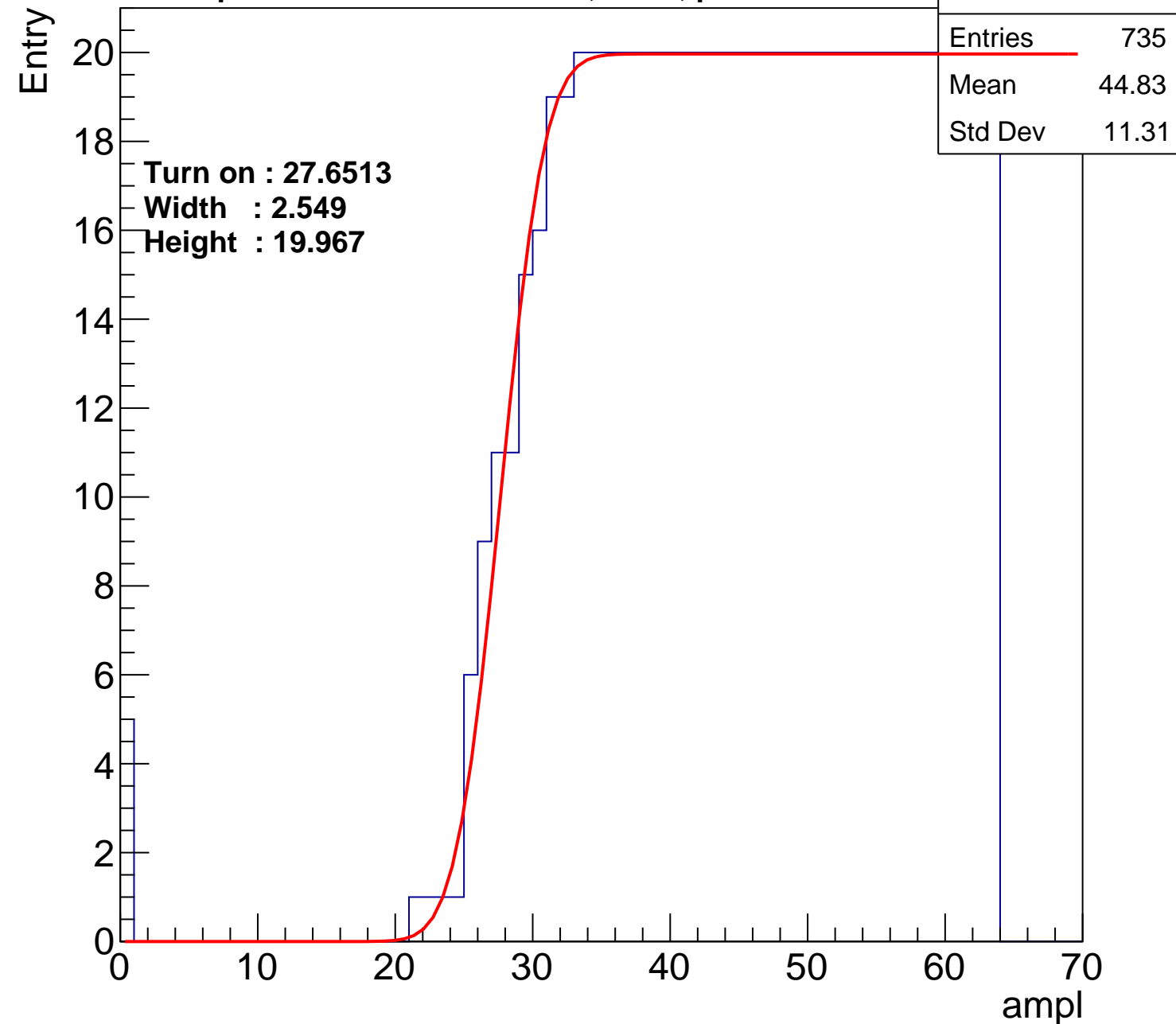
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6513
Width : 2.549
Height : 19.967

Entries	735
Mean	44.83
Std Dev	11.31

ampl



B0L101S, U23-ch98

calib_packv5_042523_0143.root, FC#1, port C1

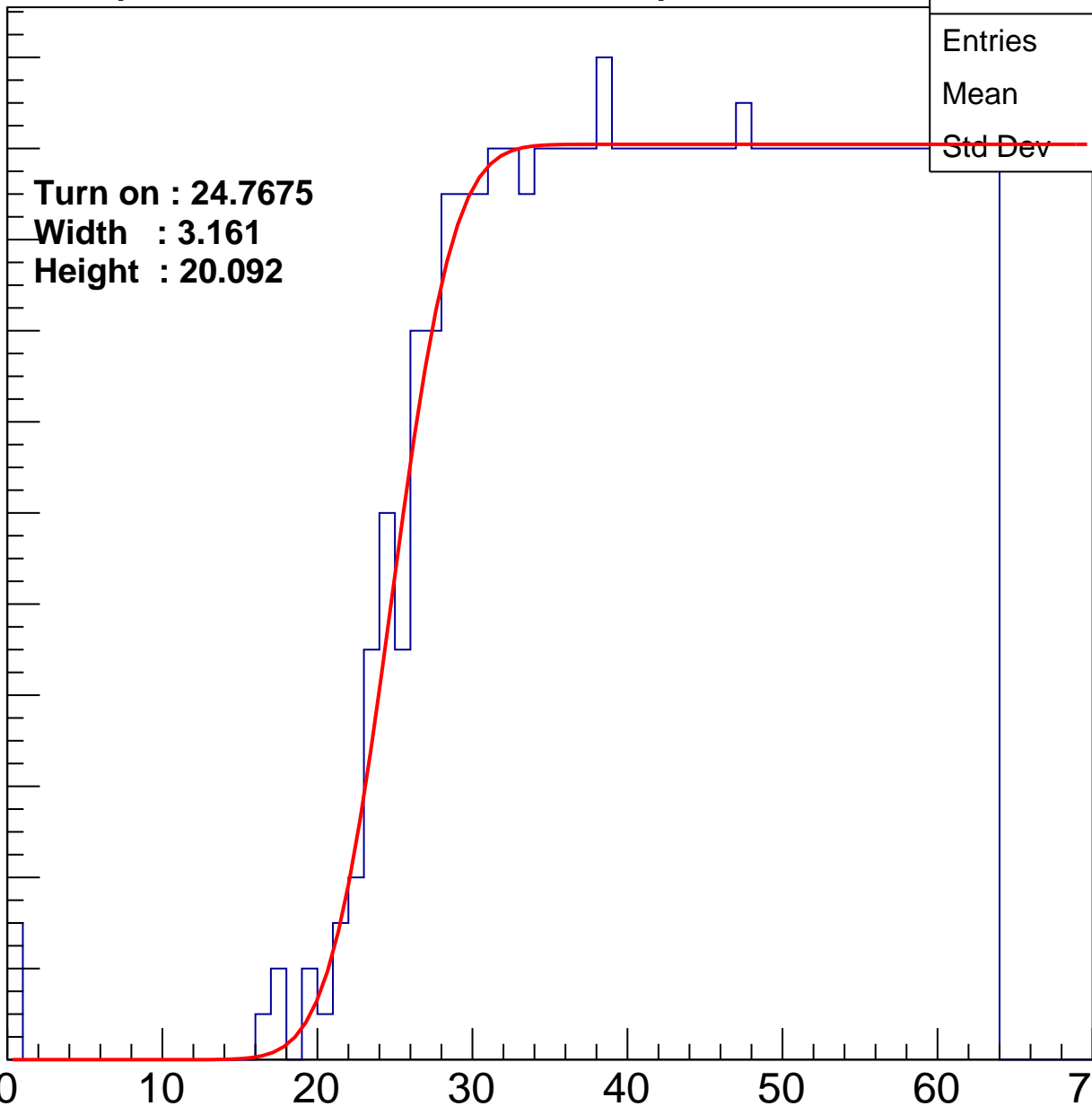
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.7675
Width : 3.161
Height : 20.092

Entries	797
Mean	43.4
Std Dev	11.94

ampl



B0L101S, U23-ch99

calib_packv5_042523_0143.root, FC#1, port C1

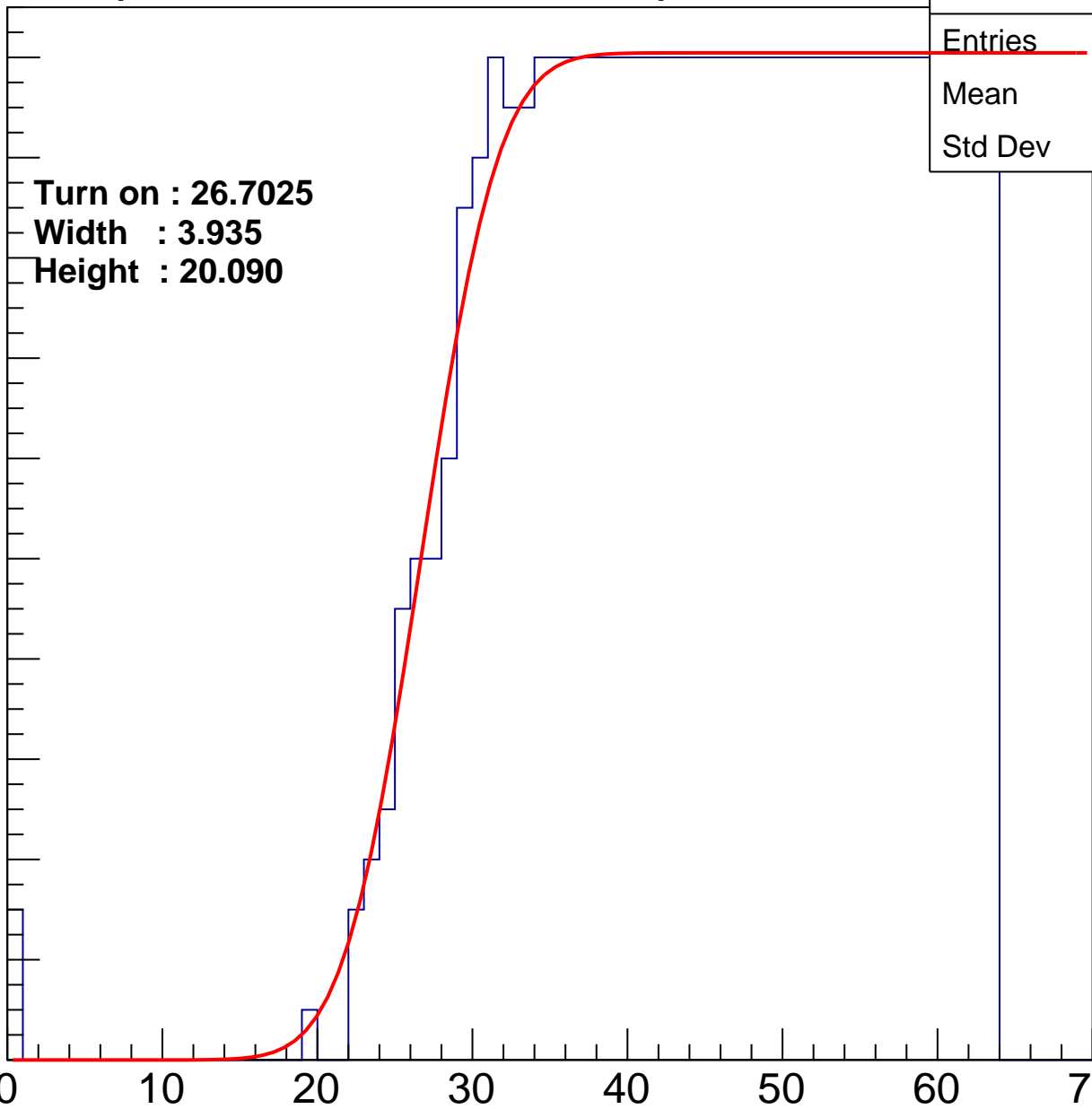
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7025
Width : 3.935
Height : 20.090

Entries	750
Mean	44.5
Std Dev	11.36

ampl

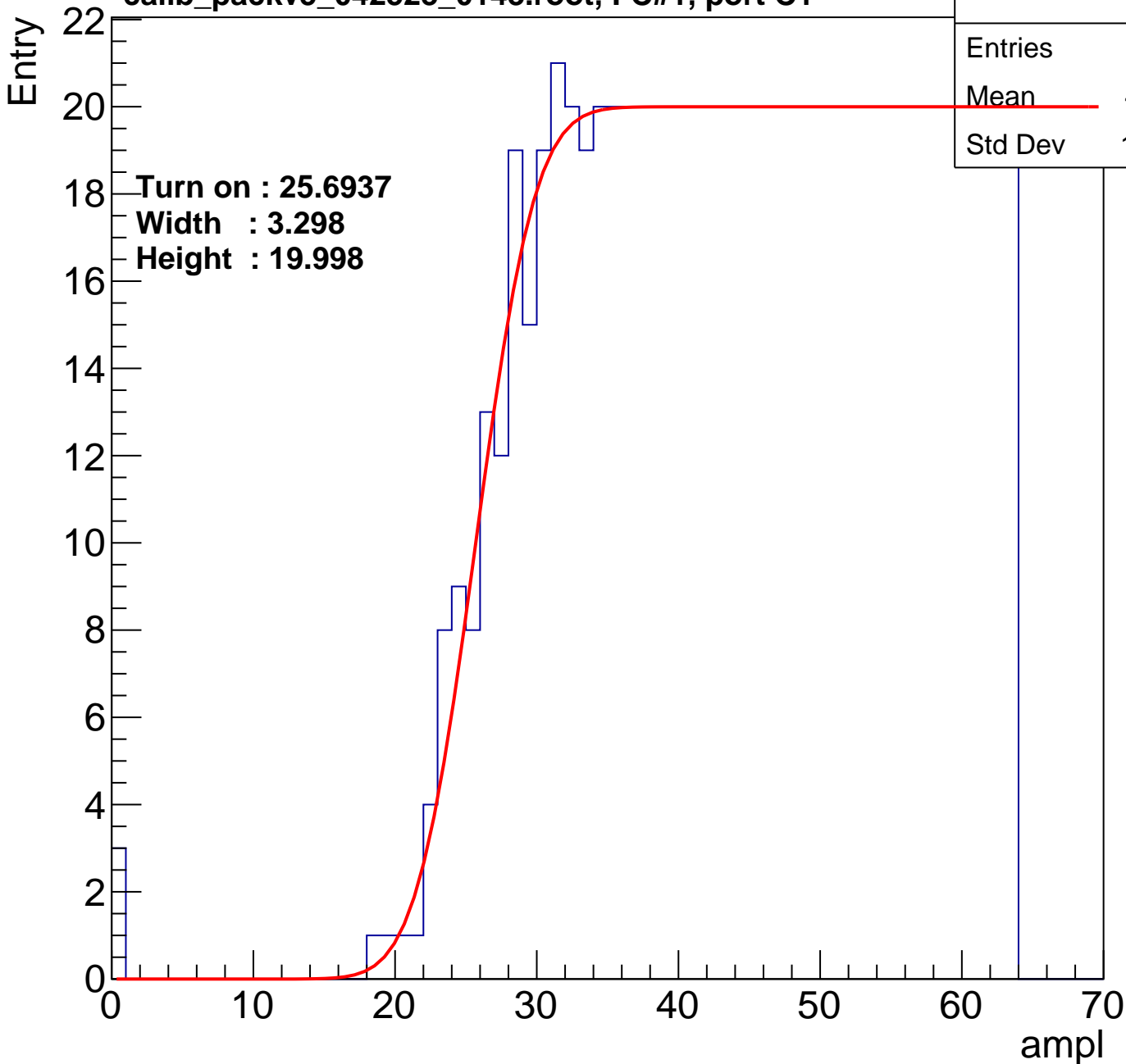


B0L101S, U23-ch100

calib_packv5_042523_0143.root, FC#1, port C1

Entries	774
Mean	43.91
Std Dev	11.68

Turn on : 25.6937
Width : 3.298
Height : 19.998



B0L101S, U23-ch101

calib_packv5_042523_0143.root, FC#1, port C1

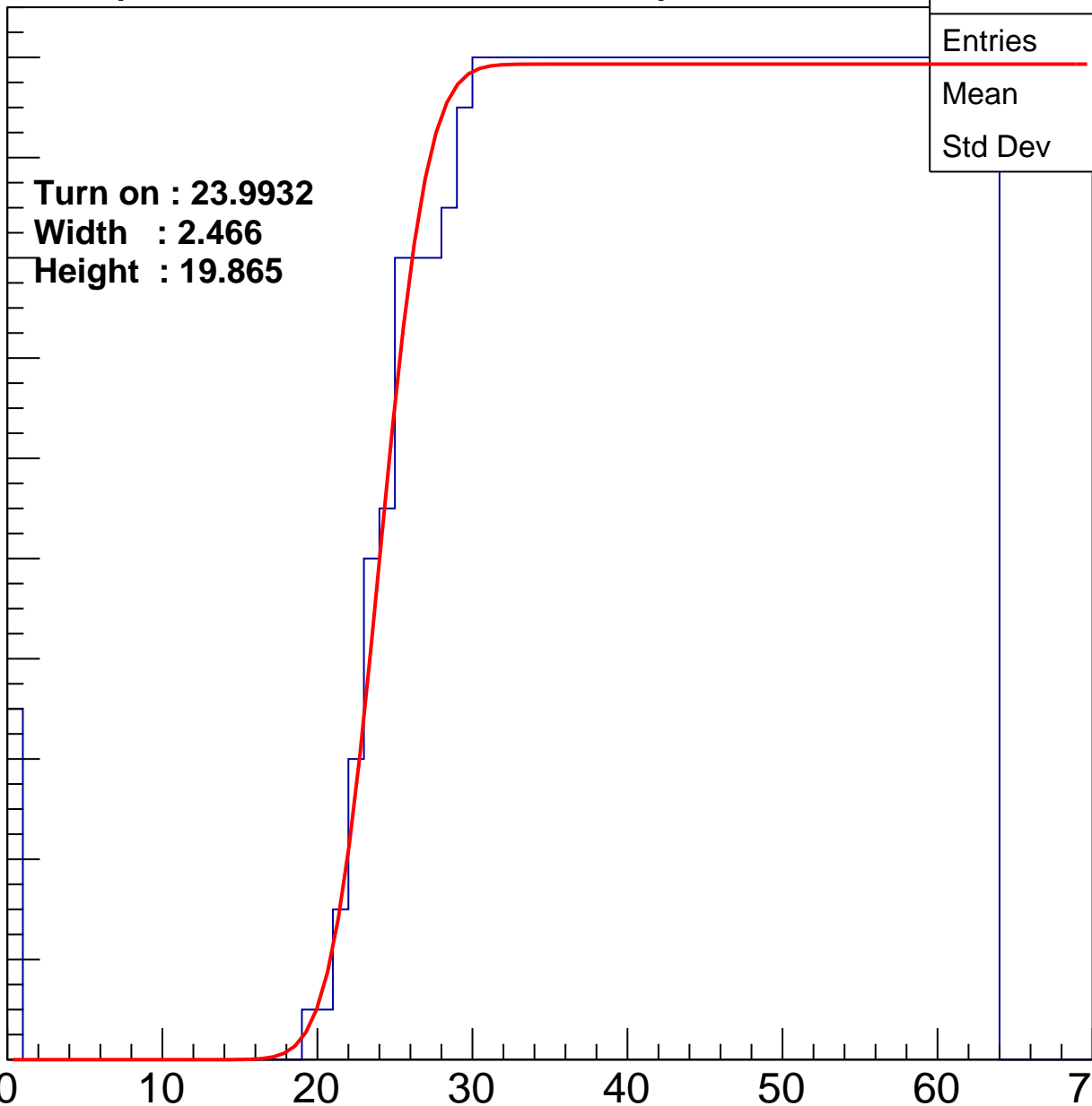
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 23.9932
Width : 2.466
Height : 19.865

Entries	803
Mean	43.12
Std Dev	12.29

ampl



B0L101S, U23-ch102

calib_packv5_042523_0143.root, FC#1, port C1

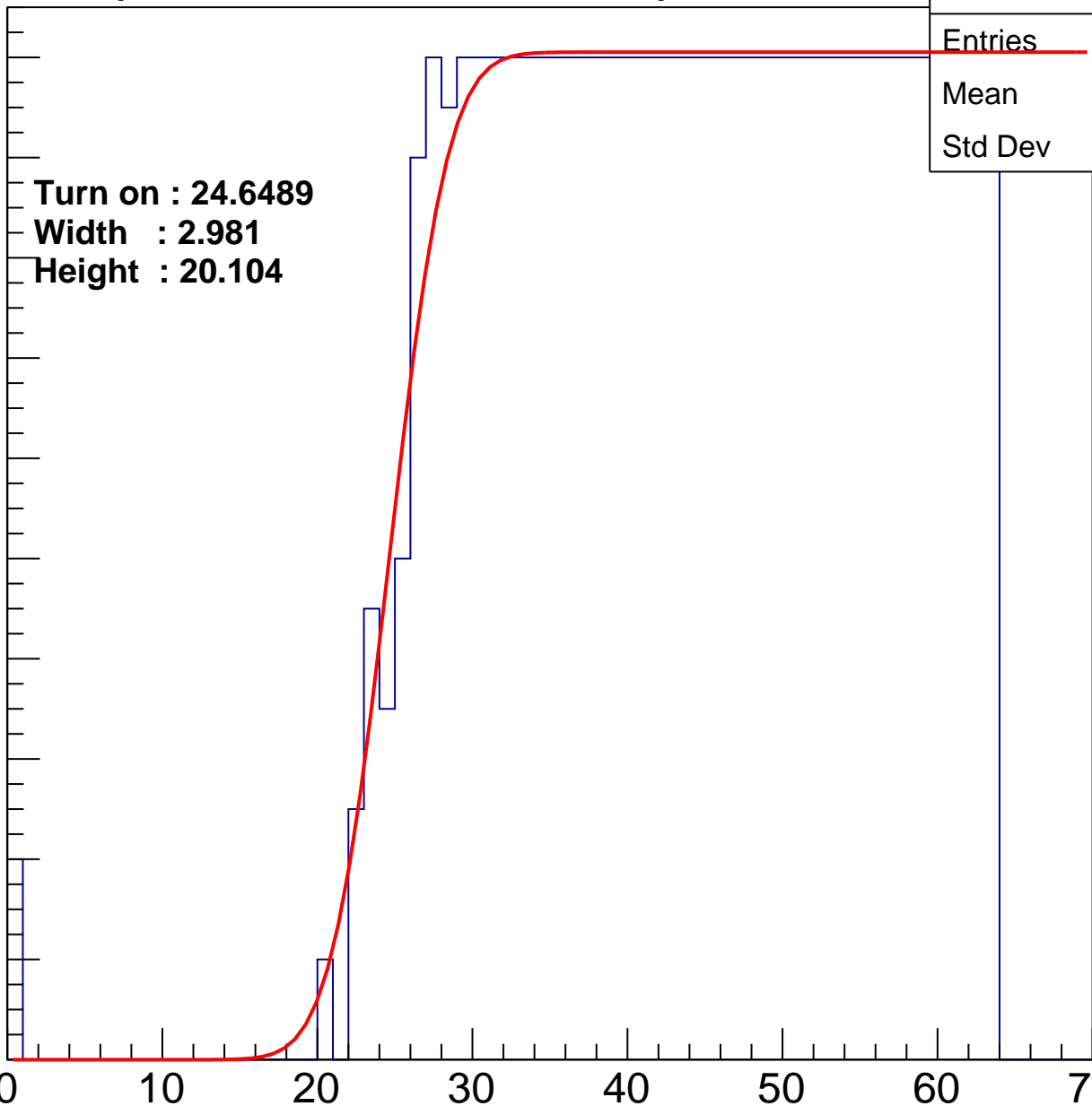
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6489
Width : 2.981
Height : 20.104

Entries	794
Mean	43.47
Std Dev	11.88

ampl



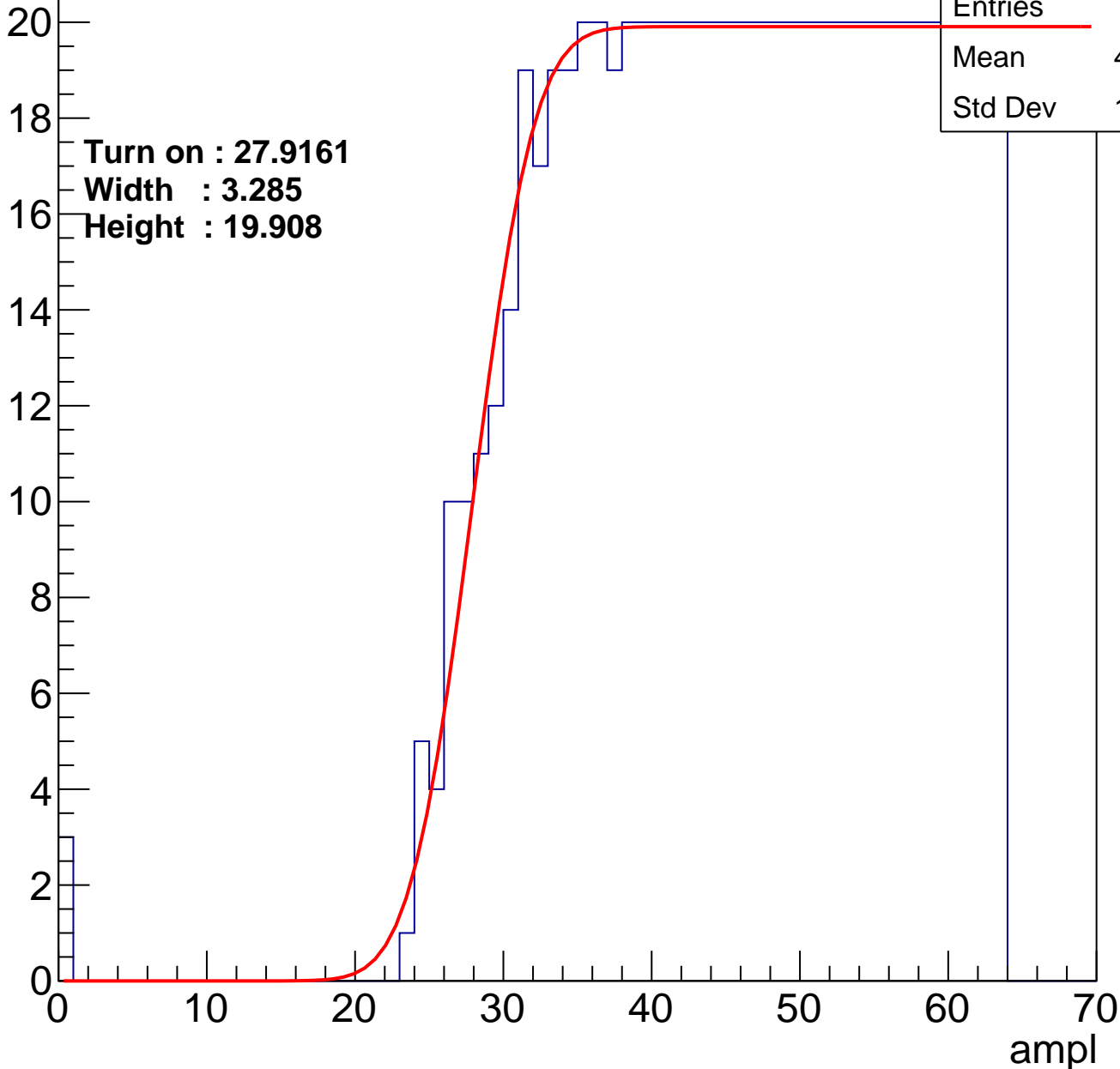
B0L101S, U23-ch103

calib_packv5_042523_0143.root, FC#1, port C1

Entries	723
Mean	45.14
Std Dev	11.04

Turn on : 27.9161
Width : 3.285
Height : 19.908

Entry



B0L101S, U23-ch104

calib_packv5_042523_0143.root, FC#1, port C1

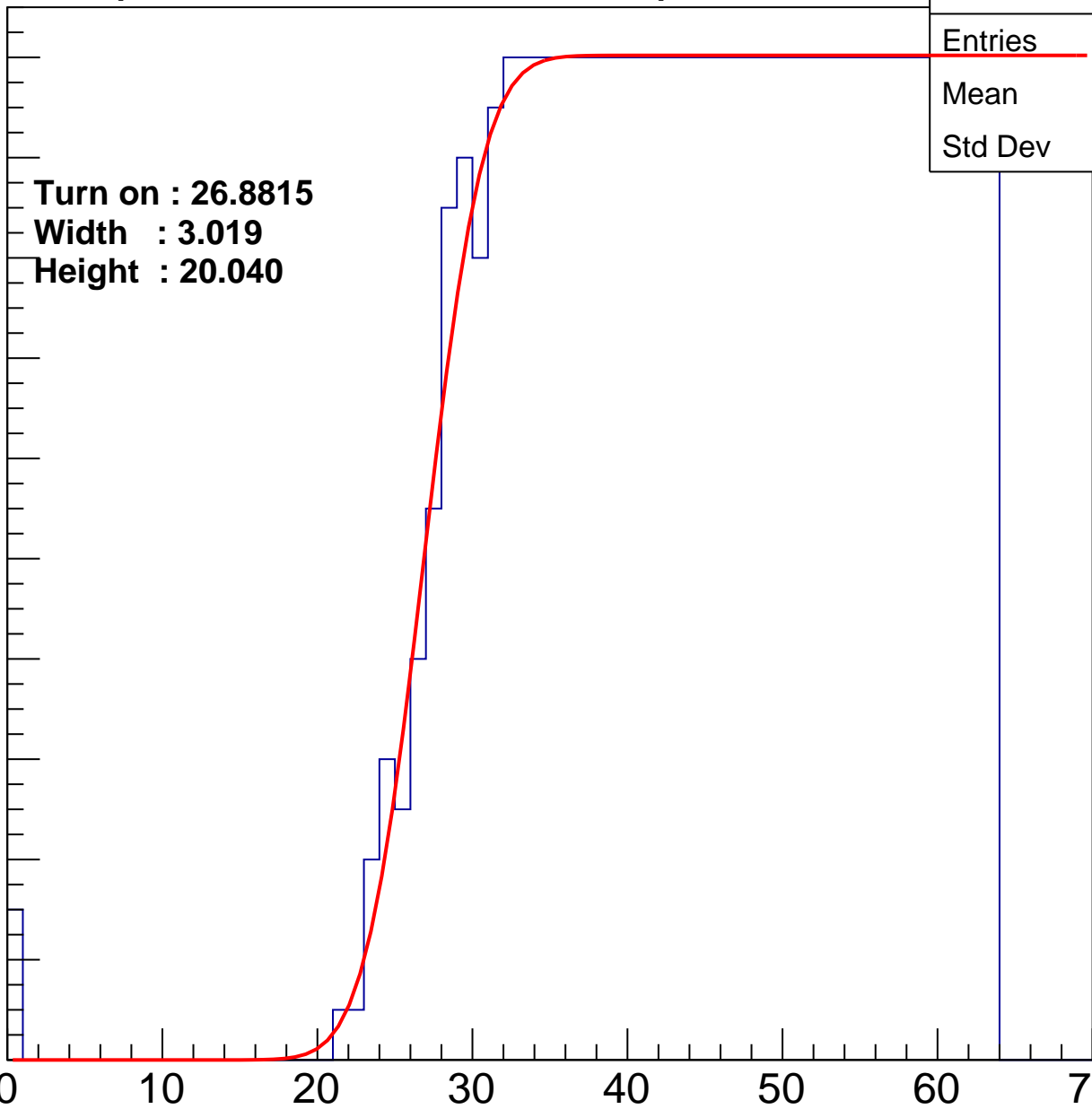
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8815
Width : 3.019
Height : 20.040

Entries	749
Mean	44.56
Std Dev	11.29

ampl



B0L101S, U23-ch105

calib_packv5_042523_0143.root, FC#1, port C1

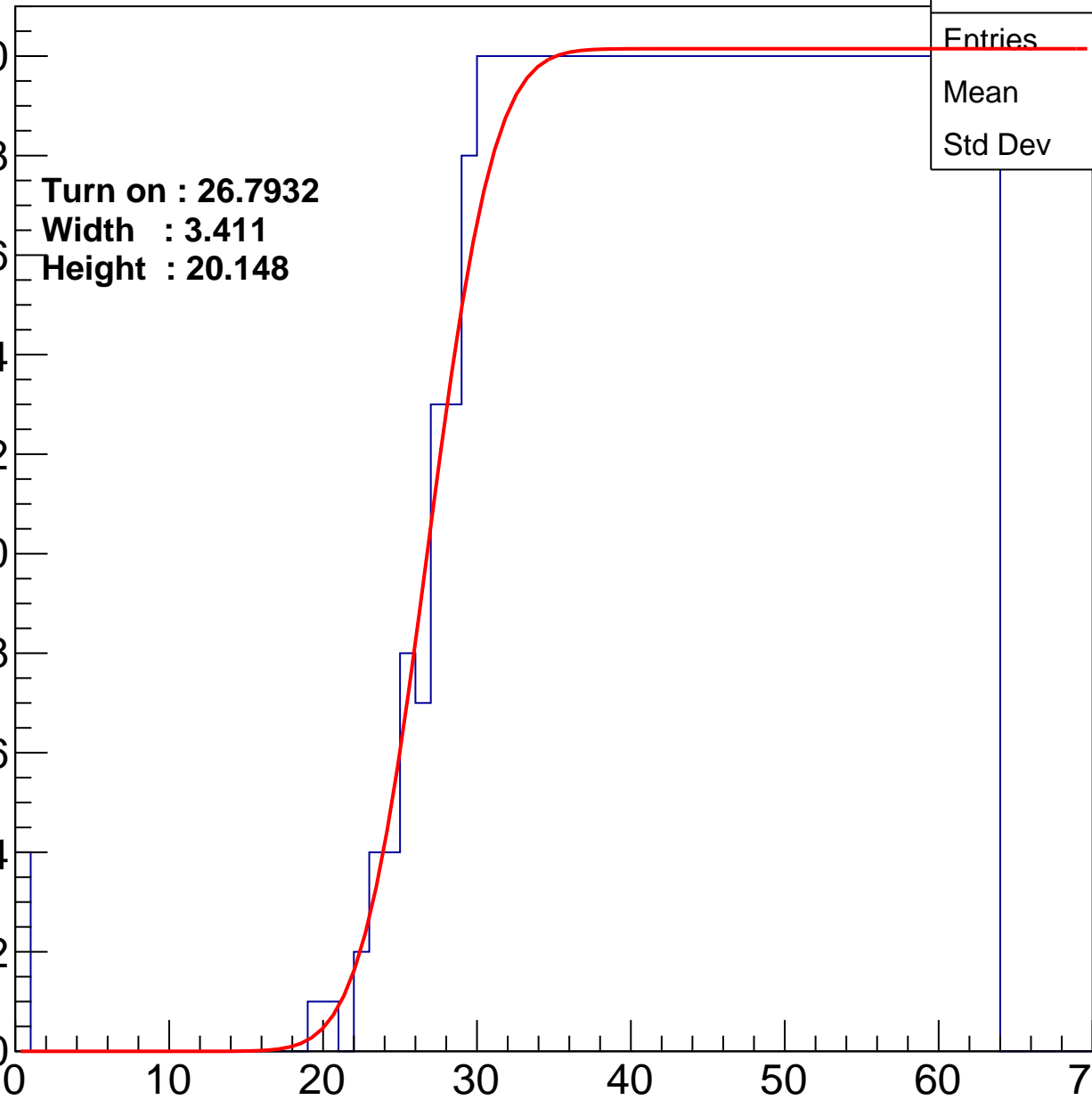
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7932
Width : 3.411
Height : 20.148

Entries	755
Mean	44.38
Std Dev	11.46

ampl



B0L101S, U23-ch106

calib_packv5_042523_0143.root, FC#1, port C1

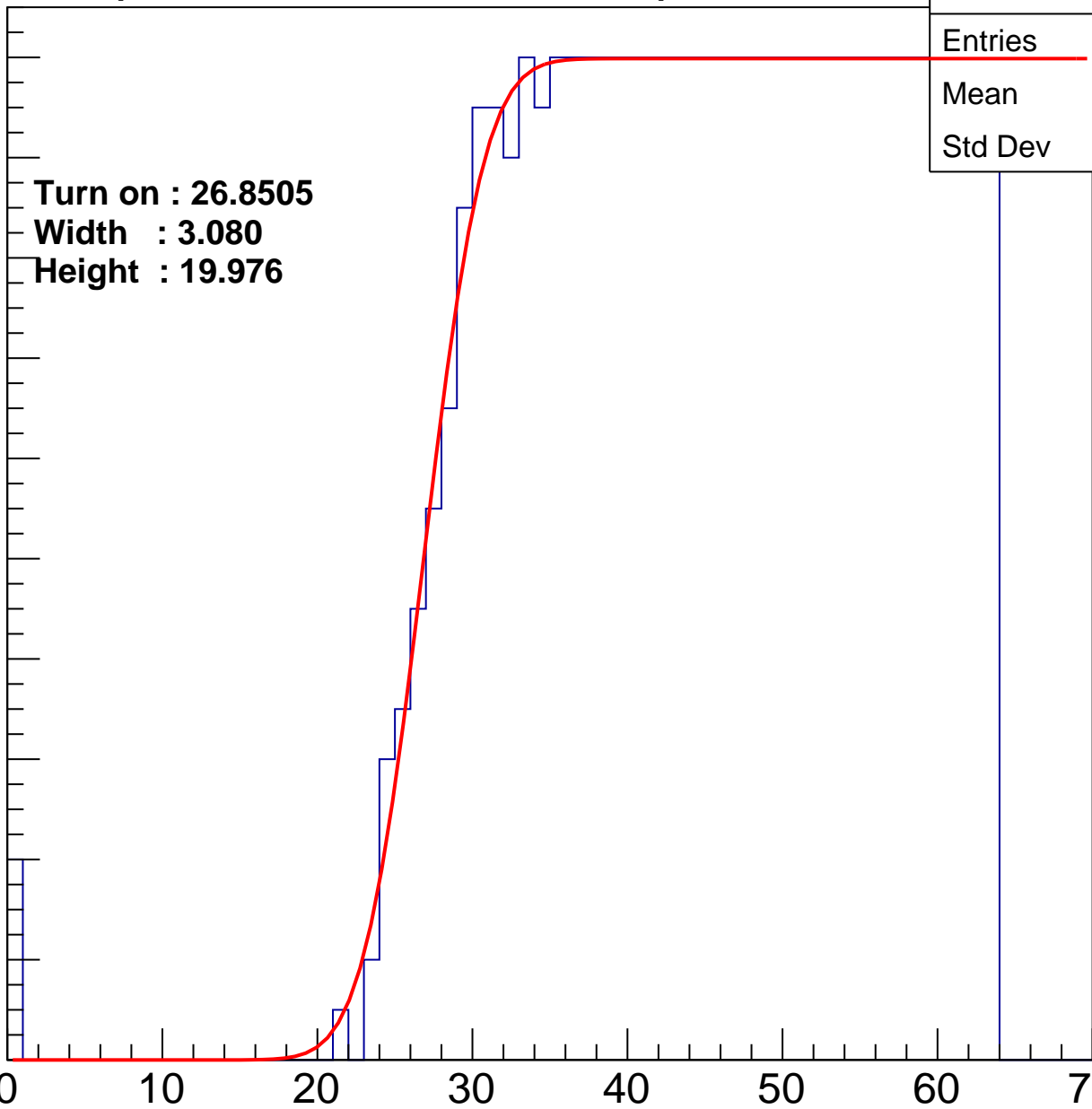
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8505
Width : 3.080
Height : 19.976

Entries	745
Mean	44.61
Std Dev	11.35

ampl



B0L101S, U23-ch107

calib_packv5_042523_0143.root, FC#1, port C1

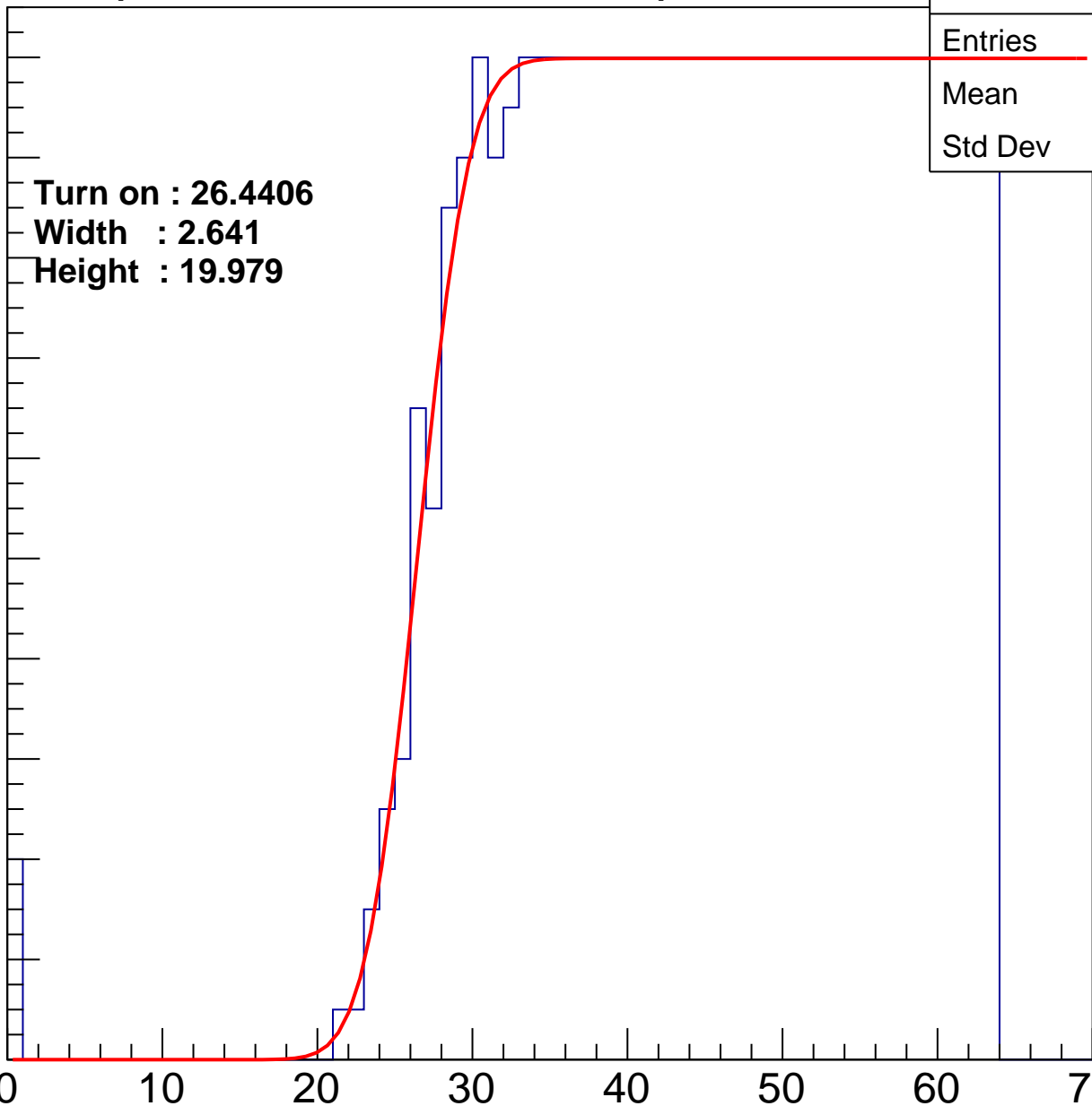
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4406
Width : 2.641
Height : 19.979

Entries	756
Mean	44.37
Std Dev	11.45

ampl



B0L101S, U23-ch108

calib_packv5_042523_0143.root, FC#1, port C1

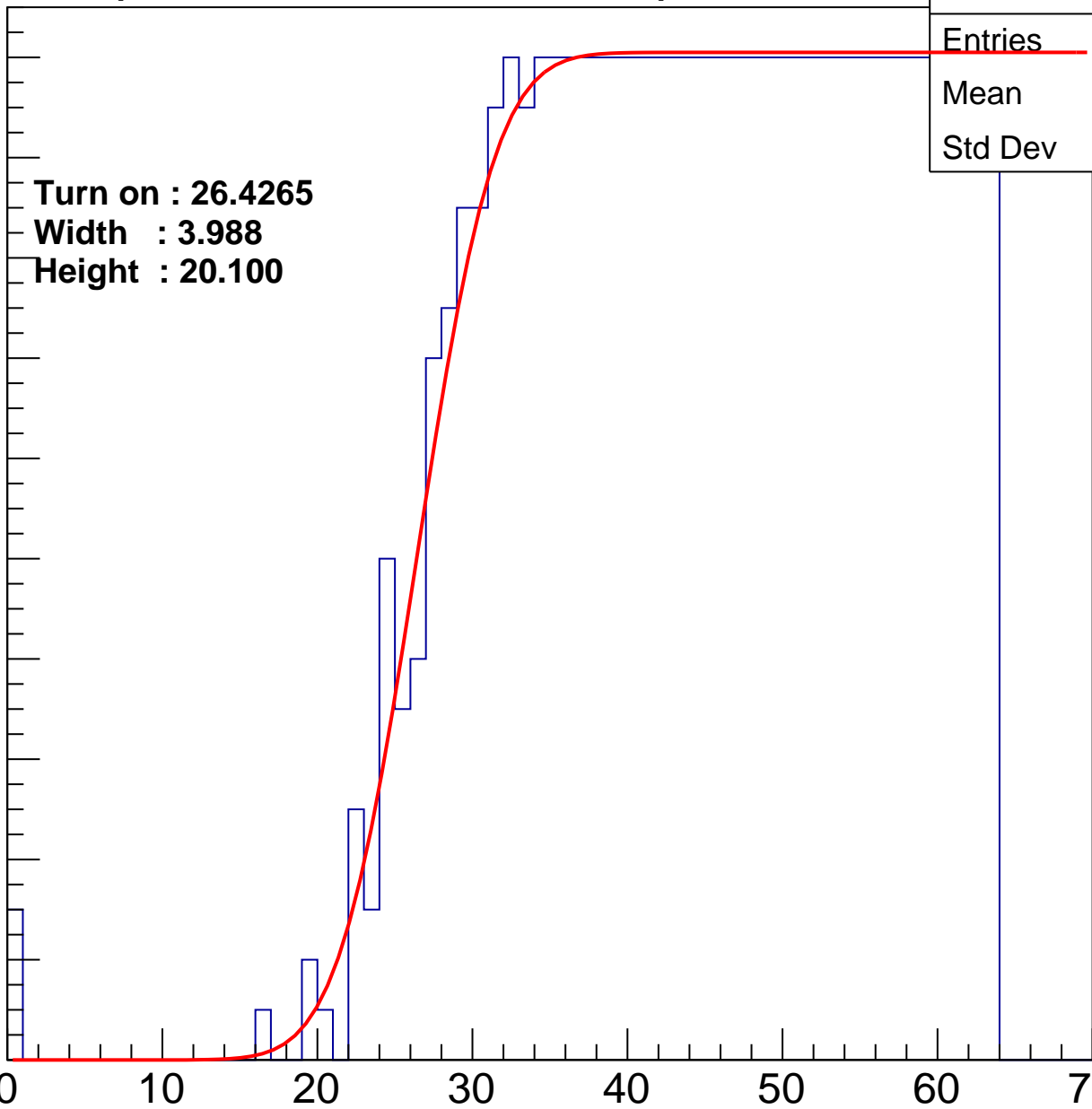
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4265
Width : 3.988
Height : 20.100

Entries	761
Mean	44.2
Std Dev	11.57

ampl



B0L101S, U23-ch109

calib_packv5_042523_0143.root, FC#1, port C1

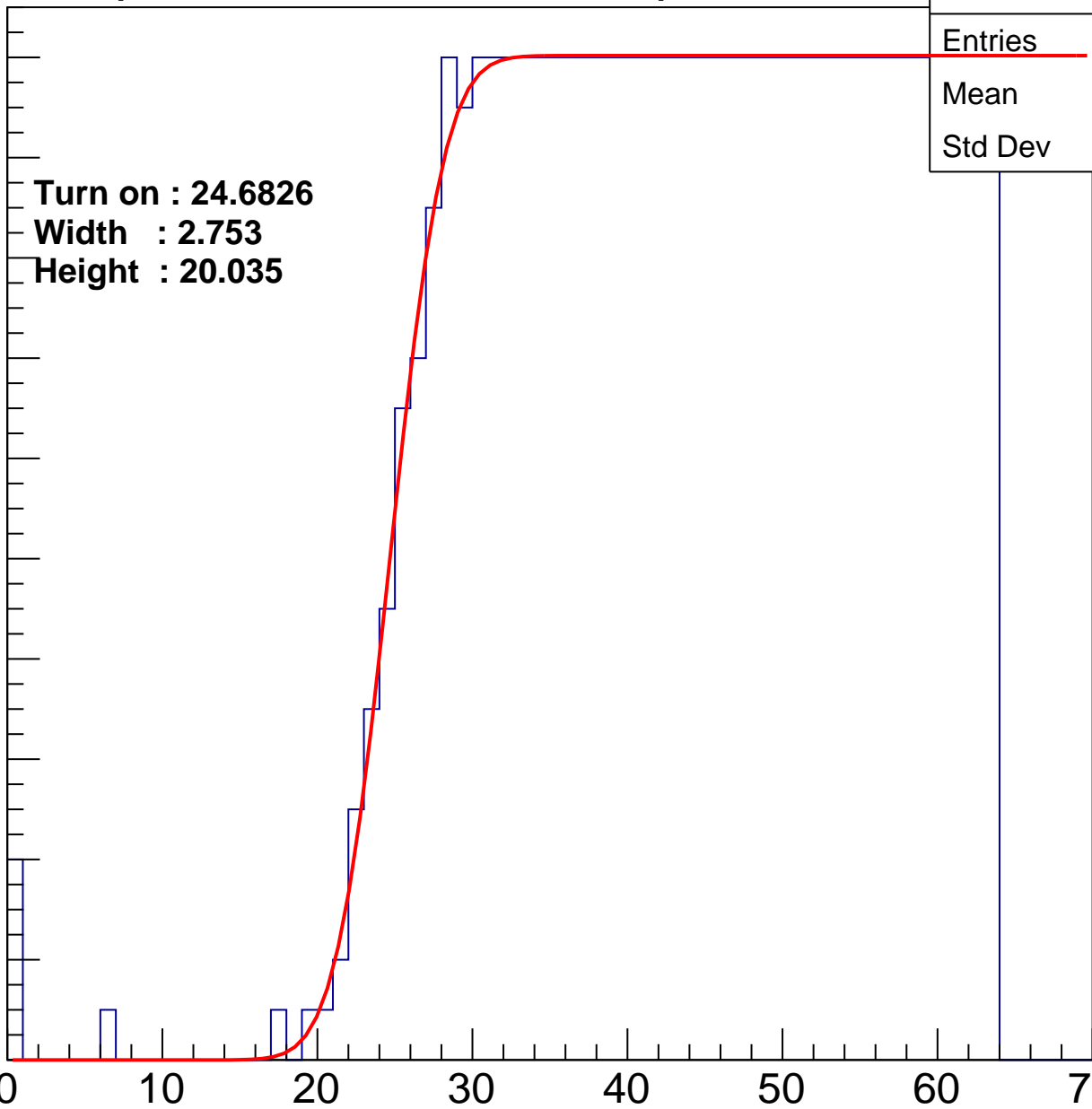
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6826
Width : 2.753
Height : 20.035

Entries	794
Mean	43.41
Std Dev	12

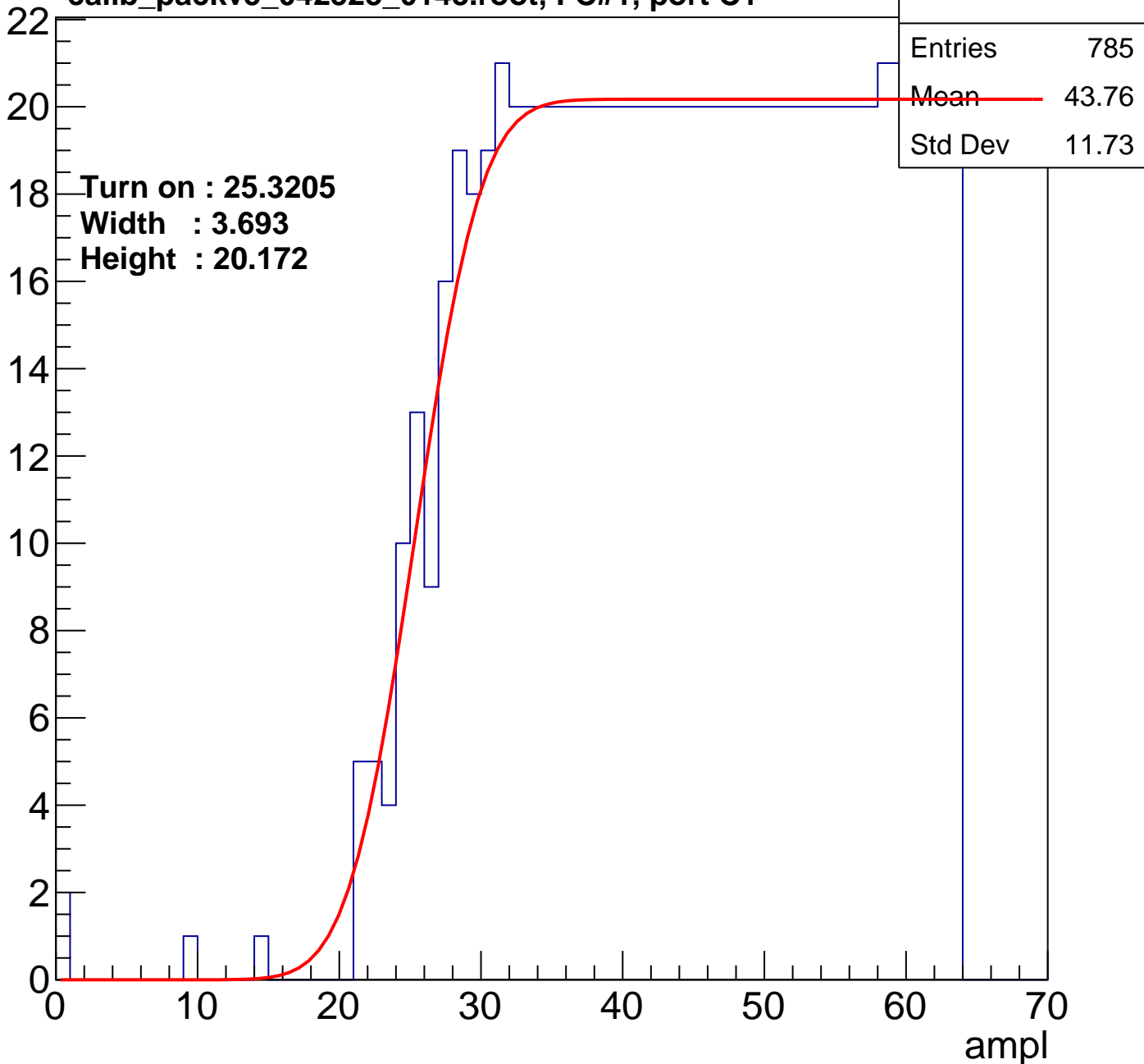
ampl



B0L101S, U23-ch110

calib_packv5_042523_0143.root, FC#1, port C1

Entry



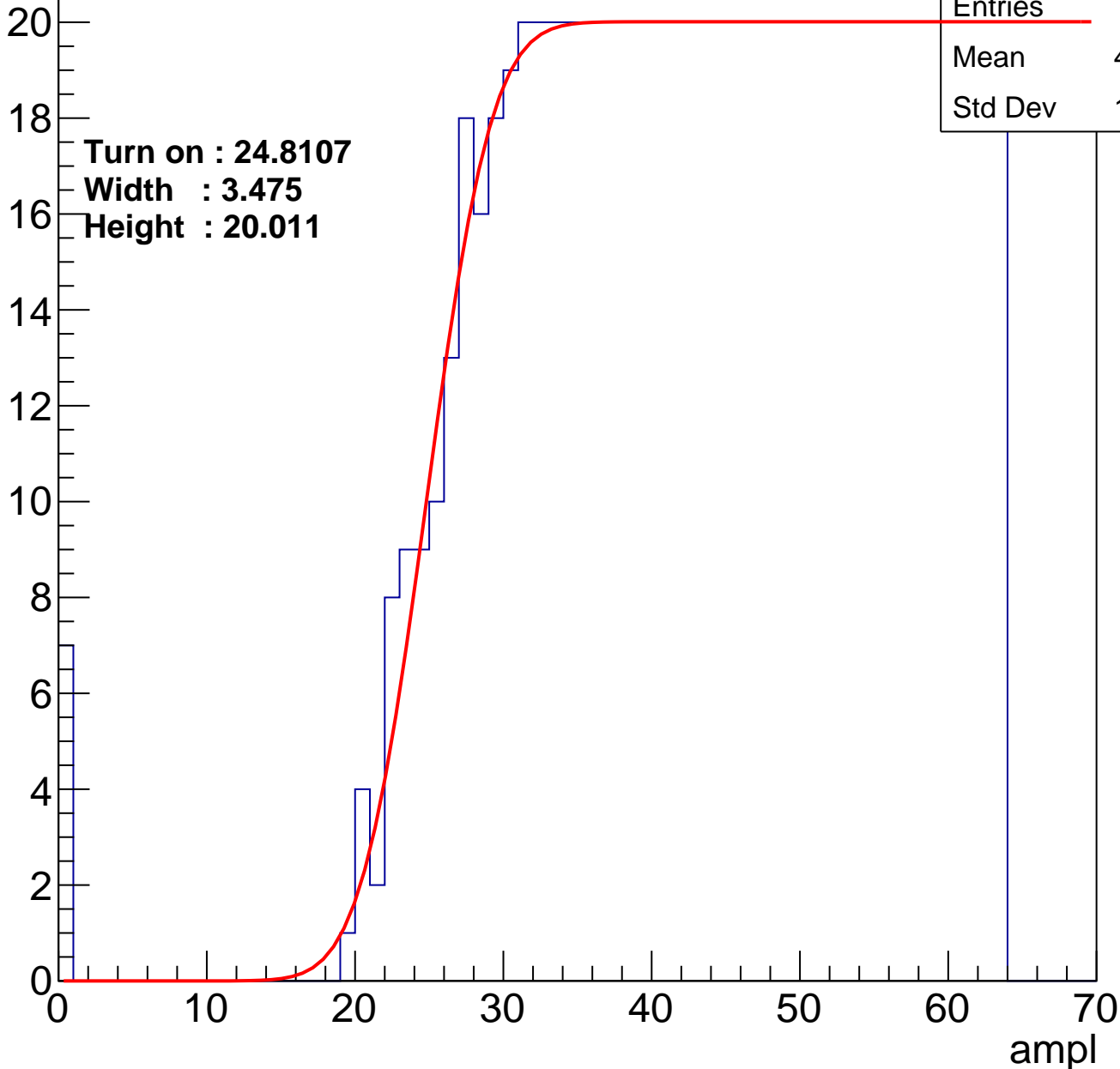
B0L101S, U23-ch111

calib_packv5_042523_0143.root, FC#1, port C1

Entries	794
Mean	43.29
Std Dev	12.26

Turn on : 24.8107
Width : 3.475
Height : 20.011

Entry



B0L101S, U23-ch112

calib_packv5_042523_0143.root, FC#1, port C1

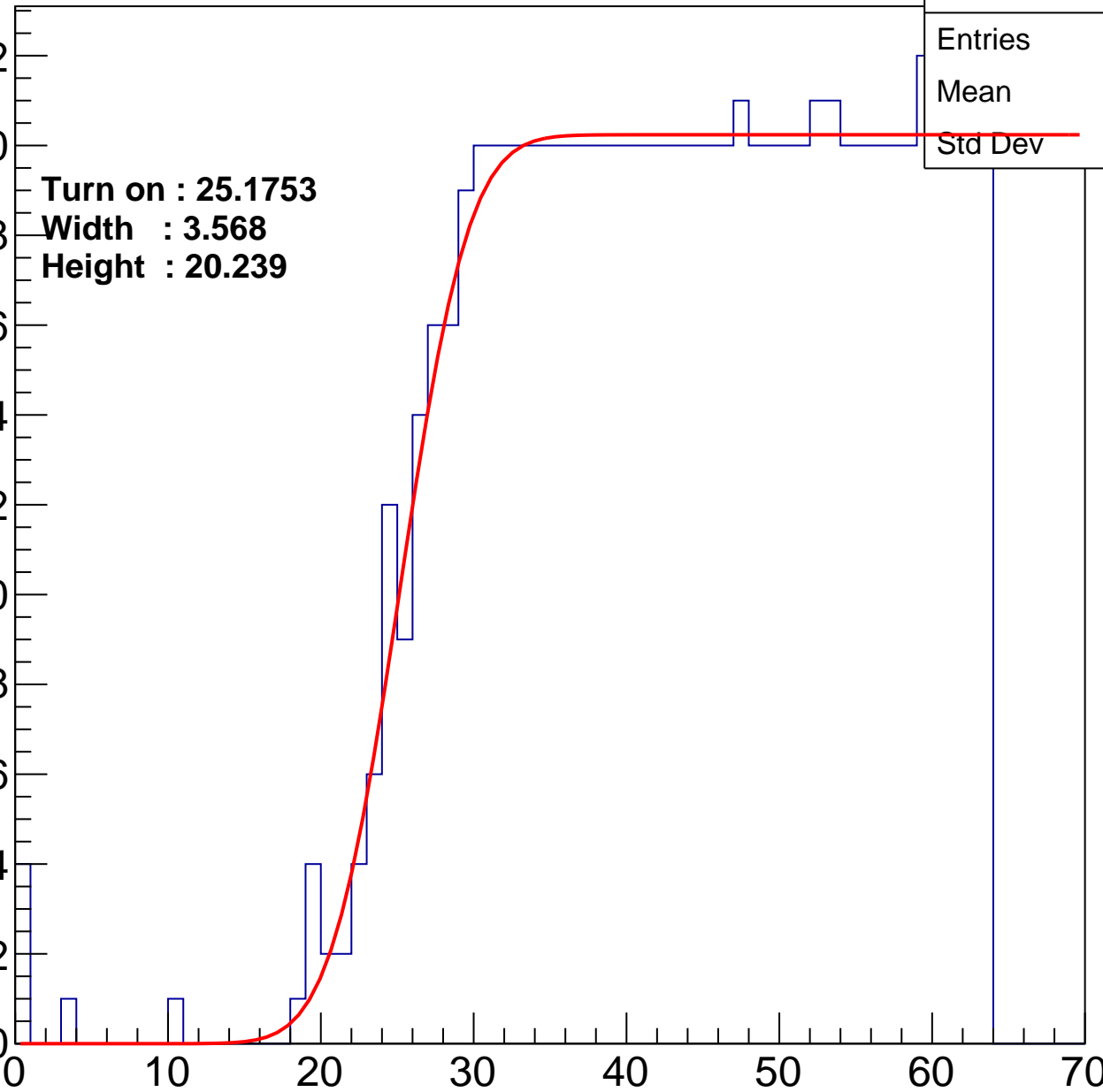
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1753
Width : 3.568
Height : 20.239

Entries	796
Mean	43.48
Std Dev	12.08

ampl



B0L101S, U23-ch113

calib_packv5_042523_0143.root, FC#1, port C1

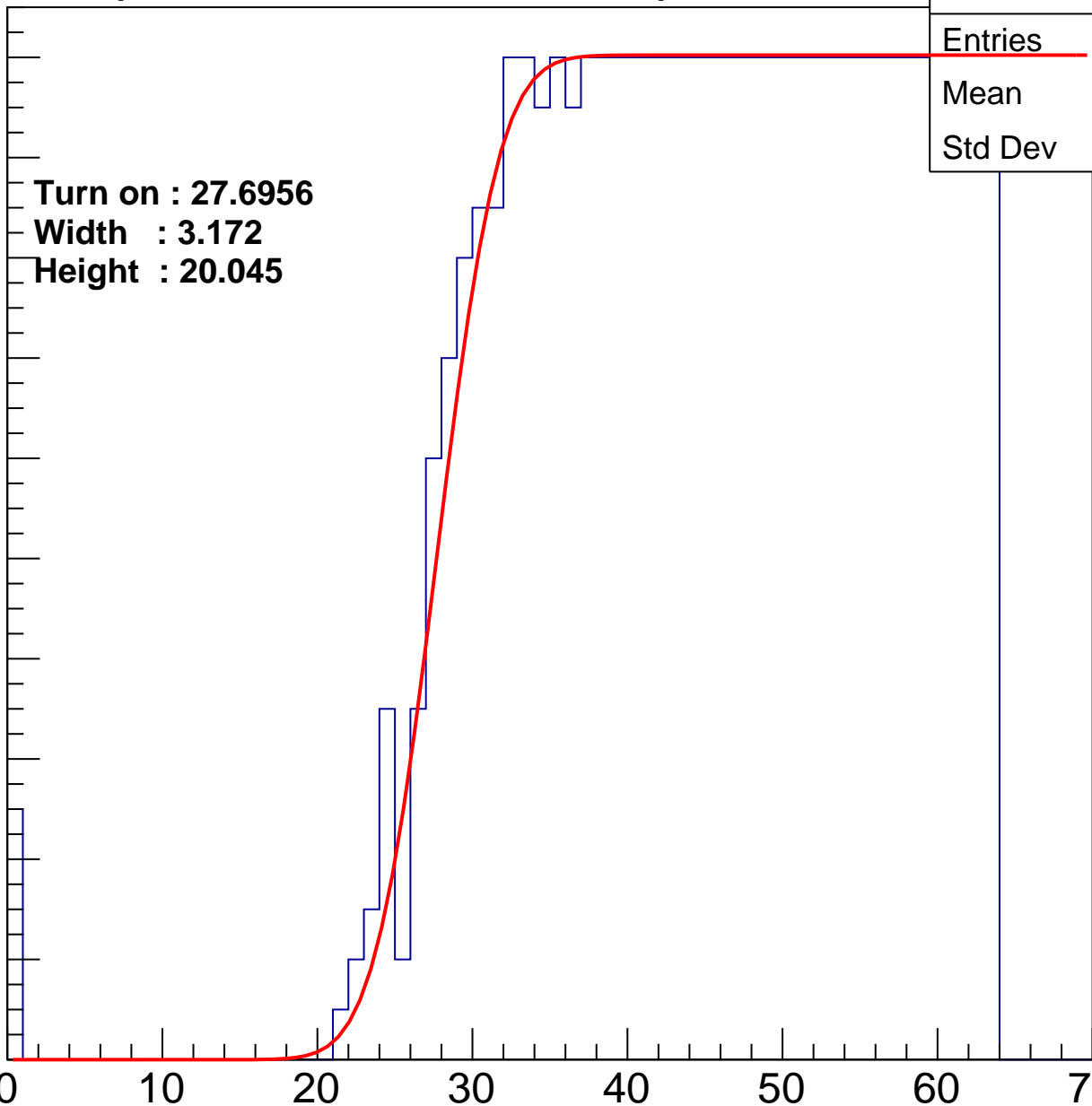
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6956
Width : 3.172
Height : 20.045

Entries	741
Mean	44.64
Std Dev	11.45

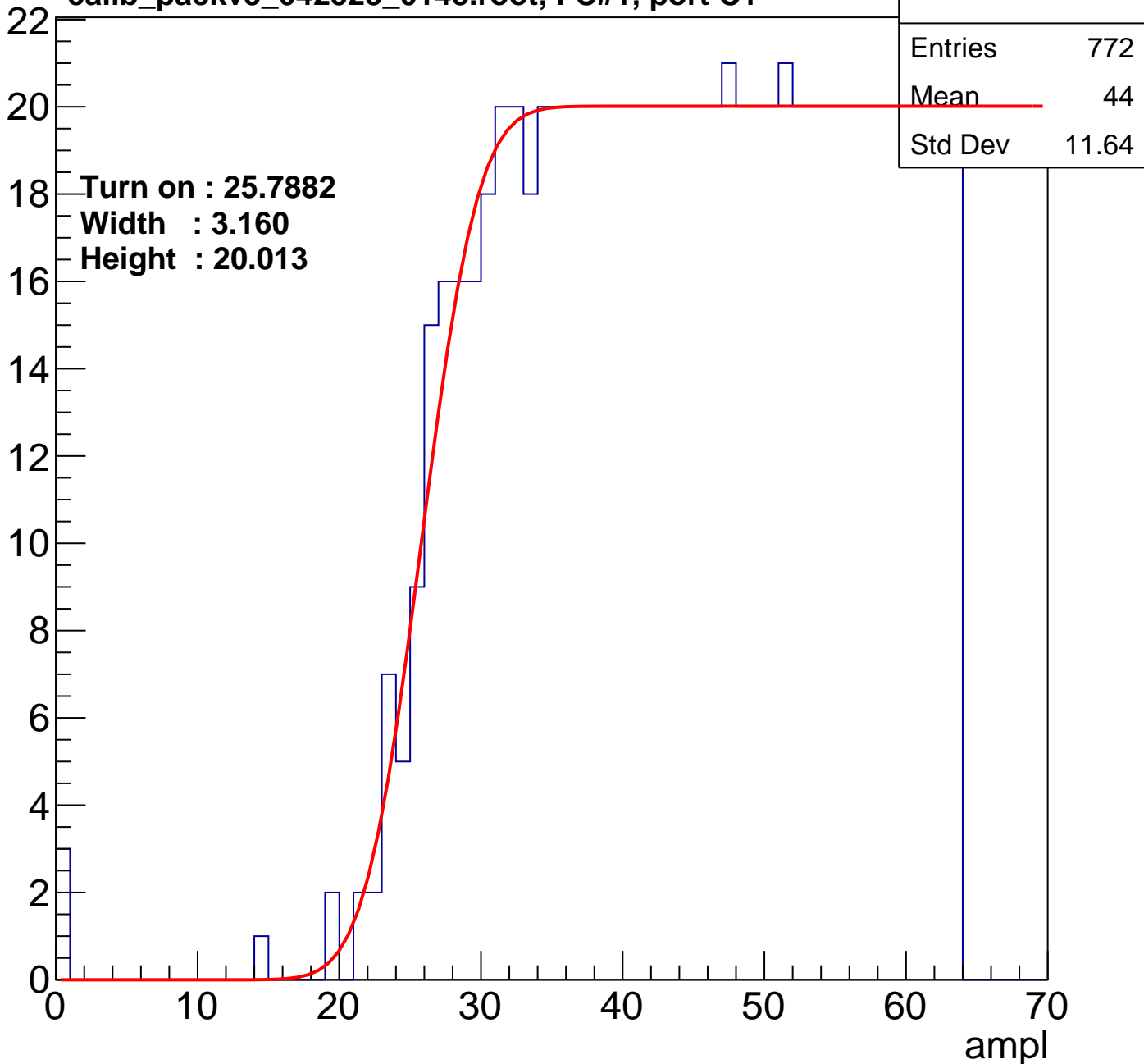
ampl



B0L101S, U23-ch114

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U23-ch115

calib_packv5_042523_0143.root, FC#1, port C1

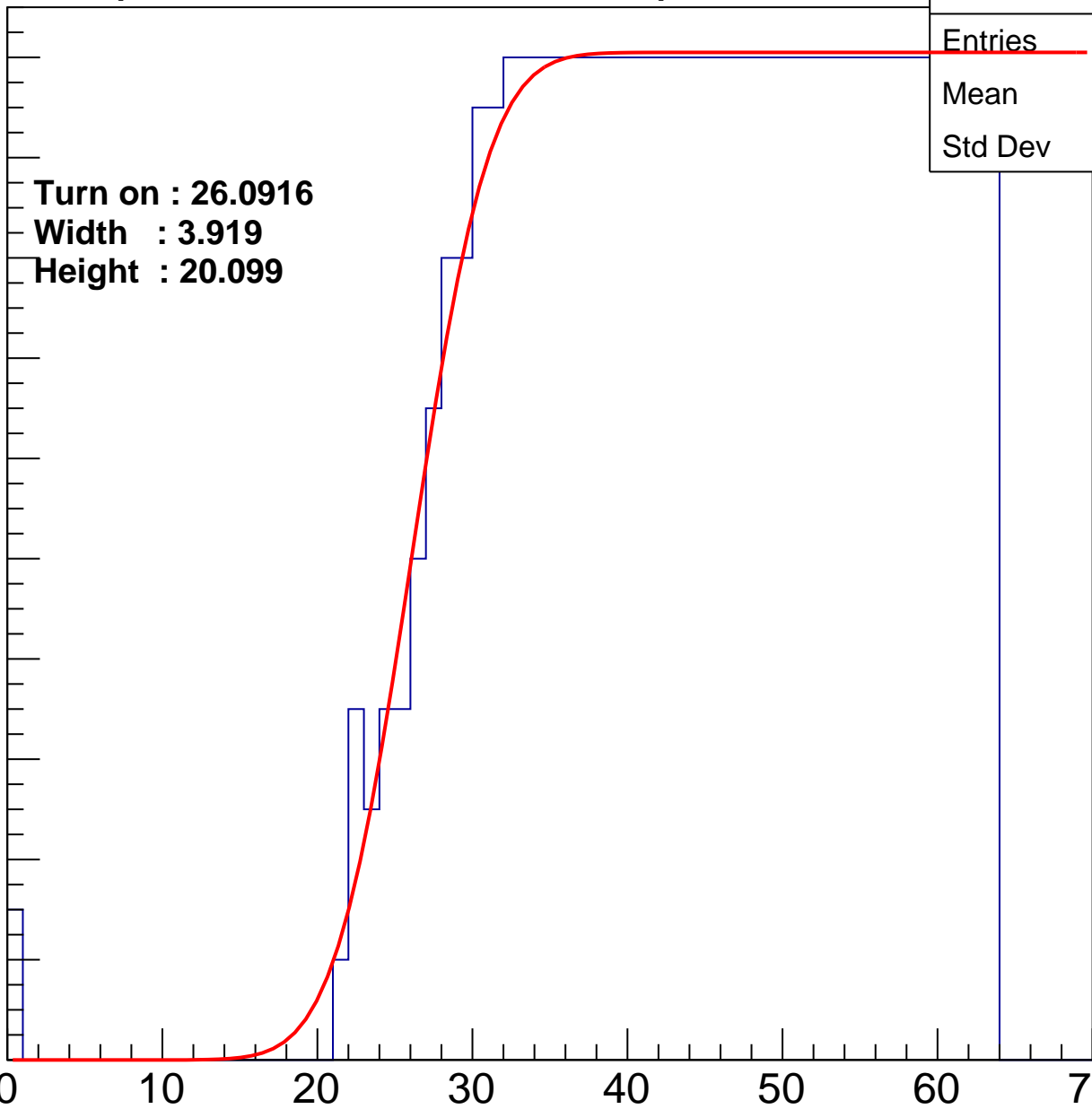
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0916
Width : 3.919
Height : 20.099

Entries	764
Mean	44.16
Std Dev	11.54

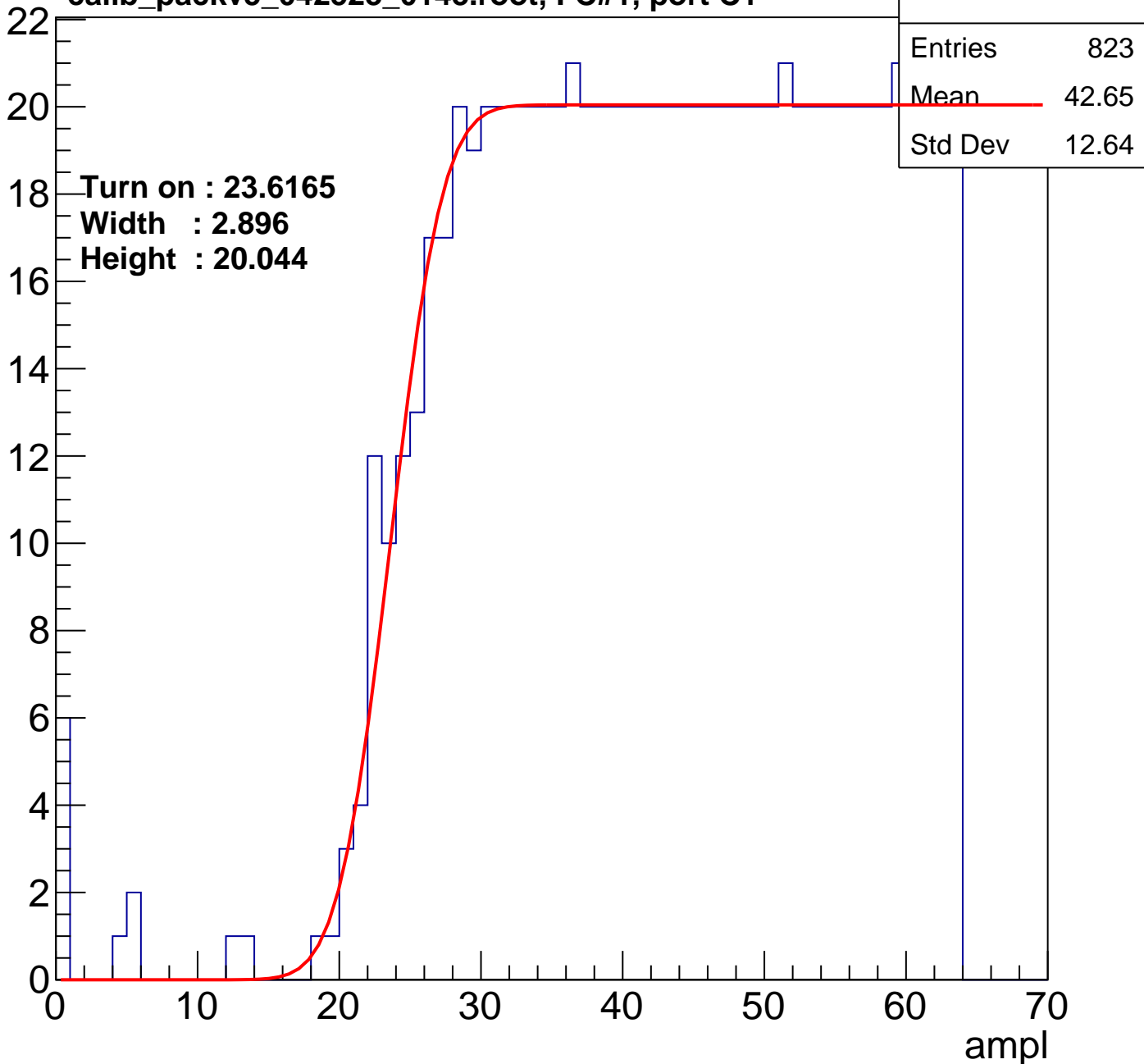
ampl



B0L101S, U23-ch116

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U23-ch117

calib_packv5_042523_0143.root, FC#1, port C1

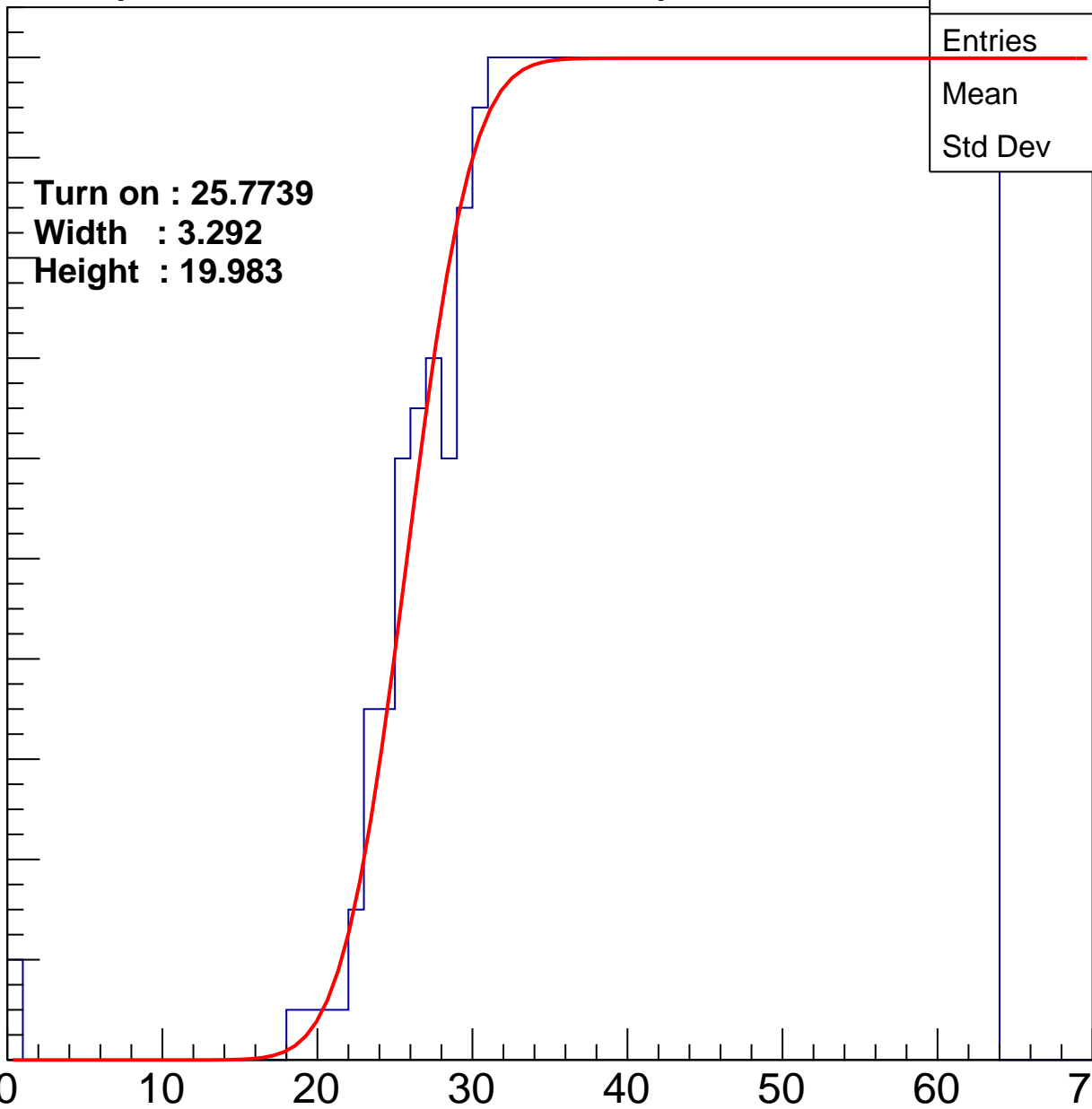
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7739
Width : 3.292
Height : 19.983

Entries	770
Mean	44.04
Std Dev	11.54

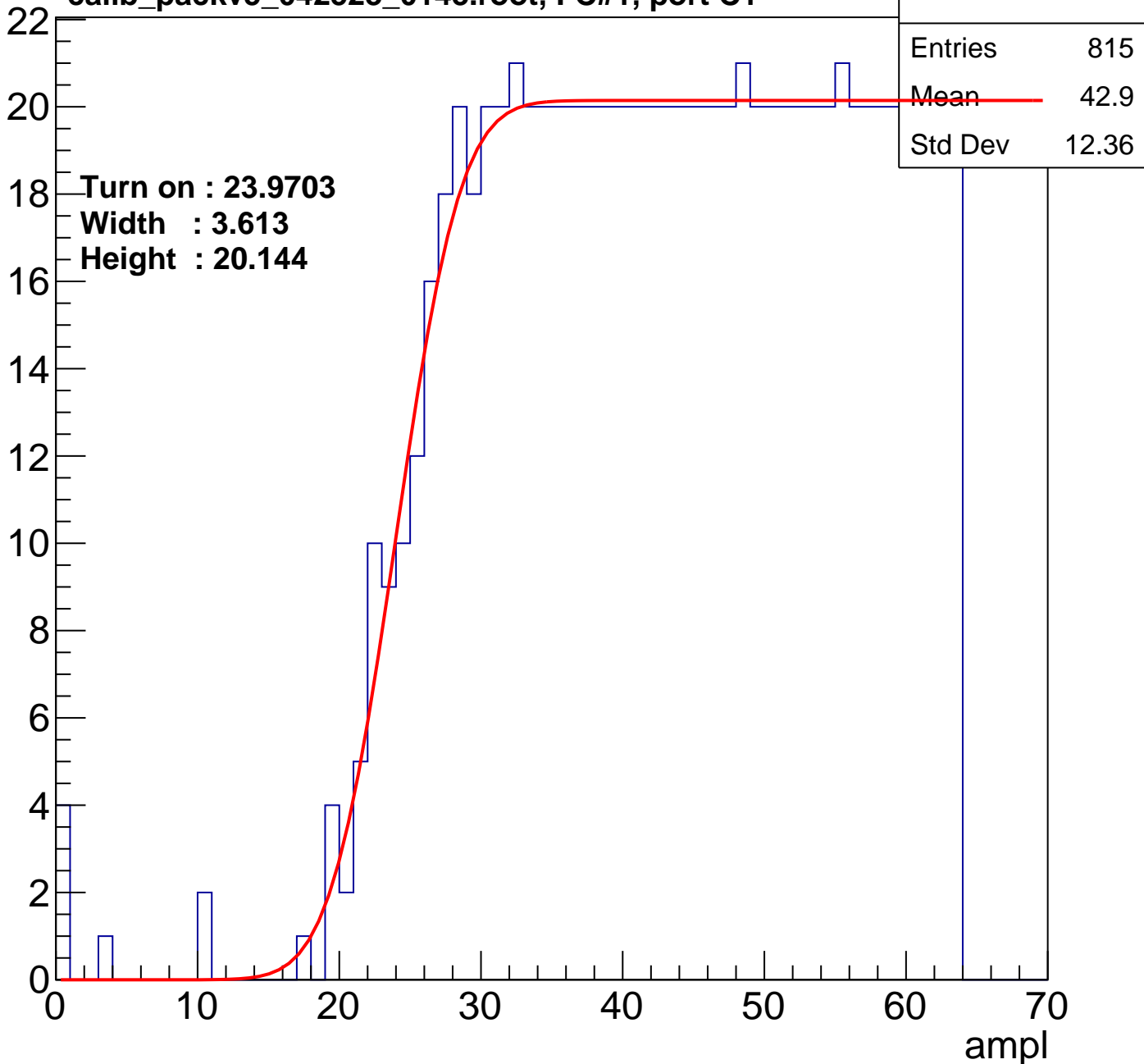
ampl



B0L101S, U23-ch118

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U23-ch119

calib_packv5_042523_0143.root, FC#1, port C1

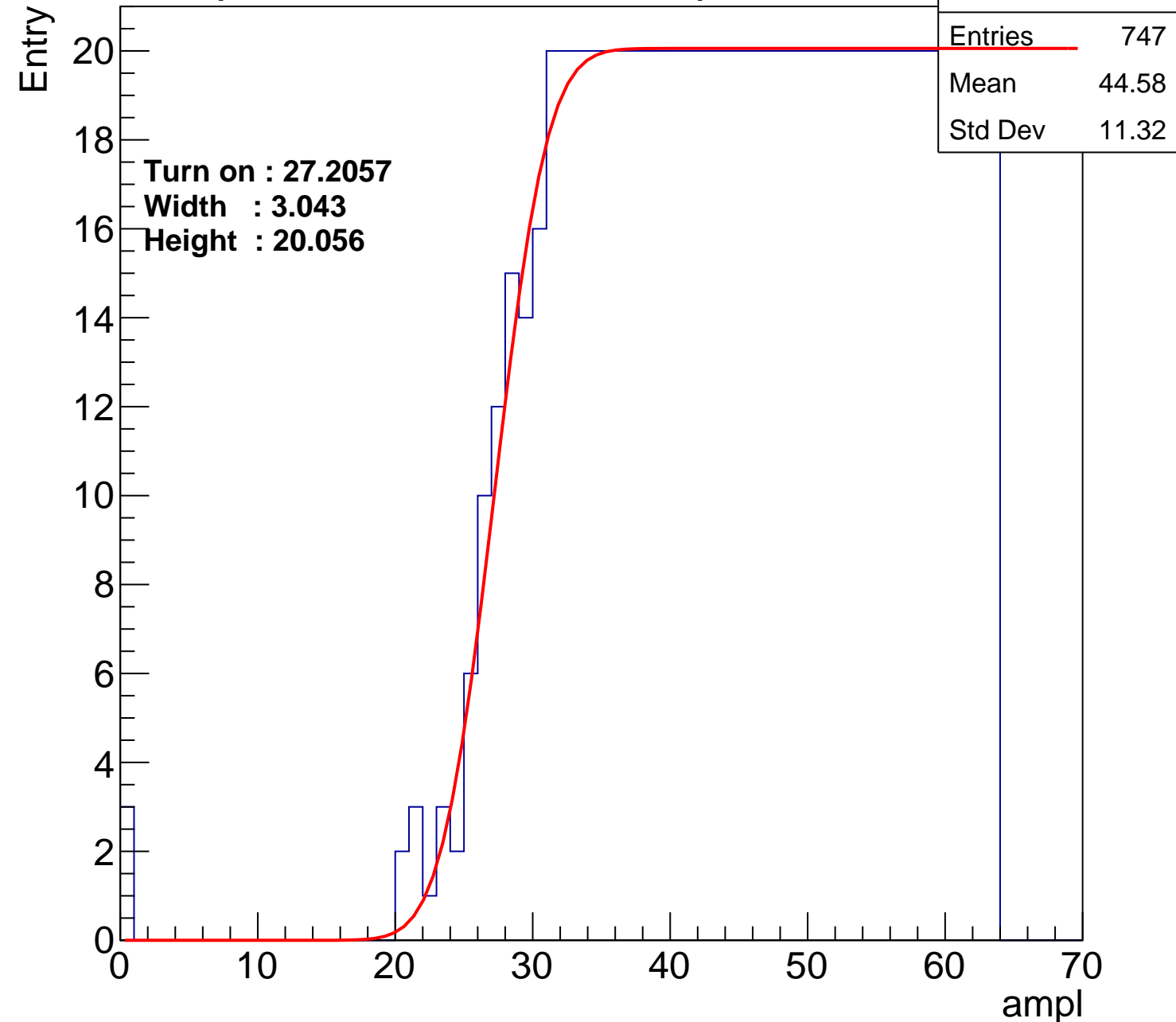
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2057
Width : 3.043
Height : 20.056

Entries	747
Mean	44.58
Std Dev	11.32

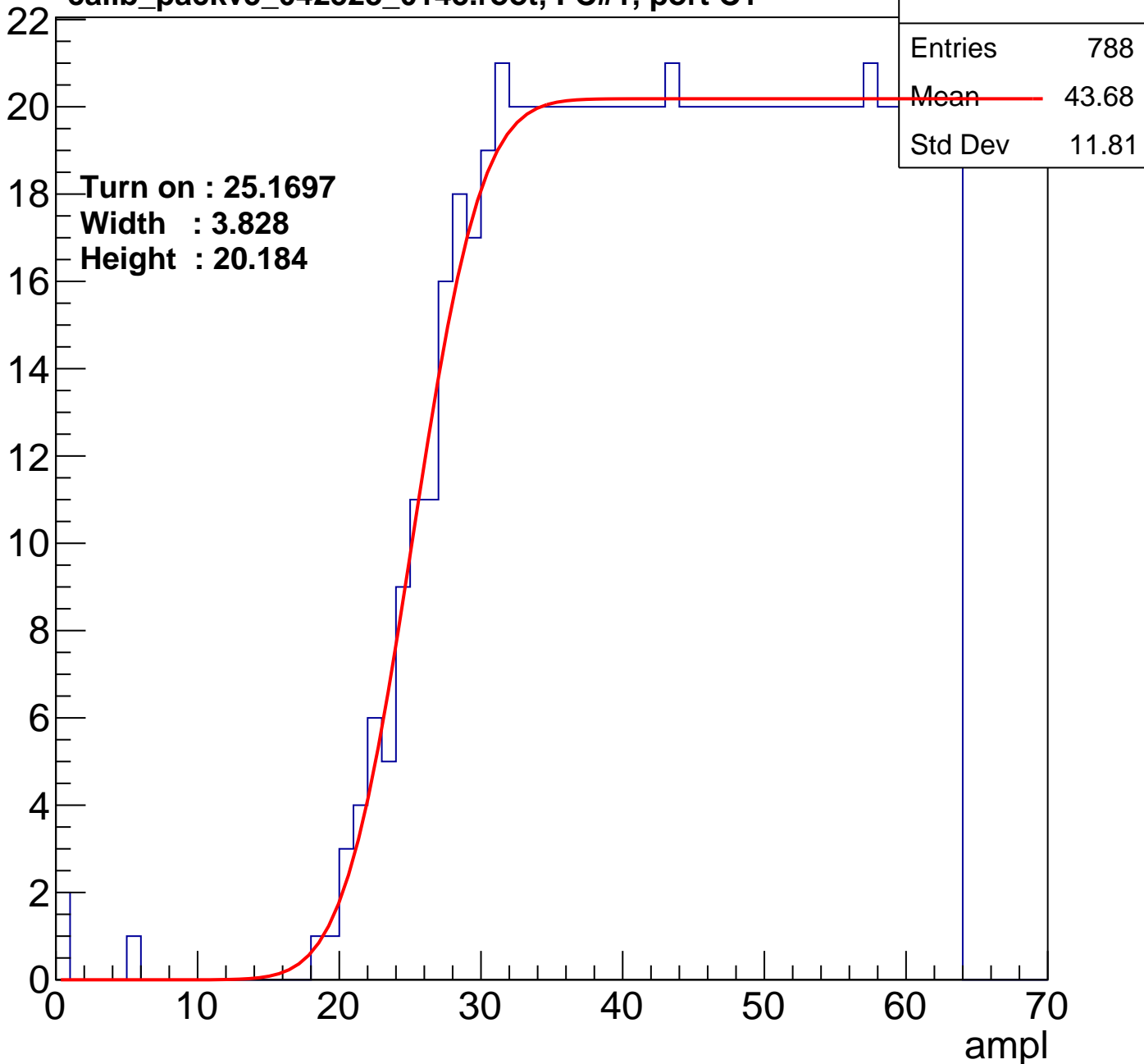
ampl



B0L101S, U23-ch120

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U23-ch121

calib_packv5_042523_0143.root, FC#1, port C1

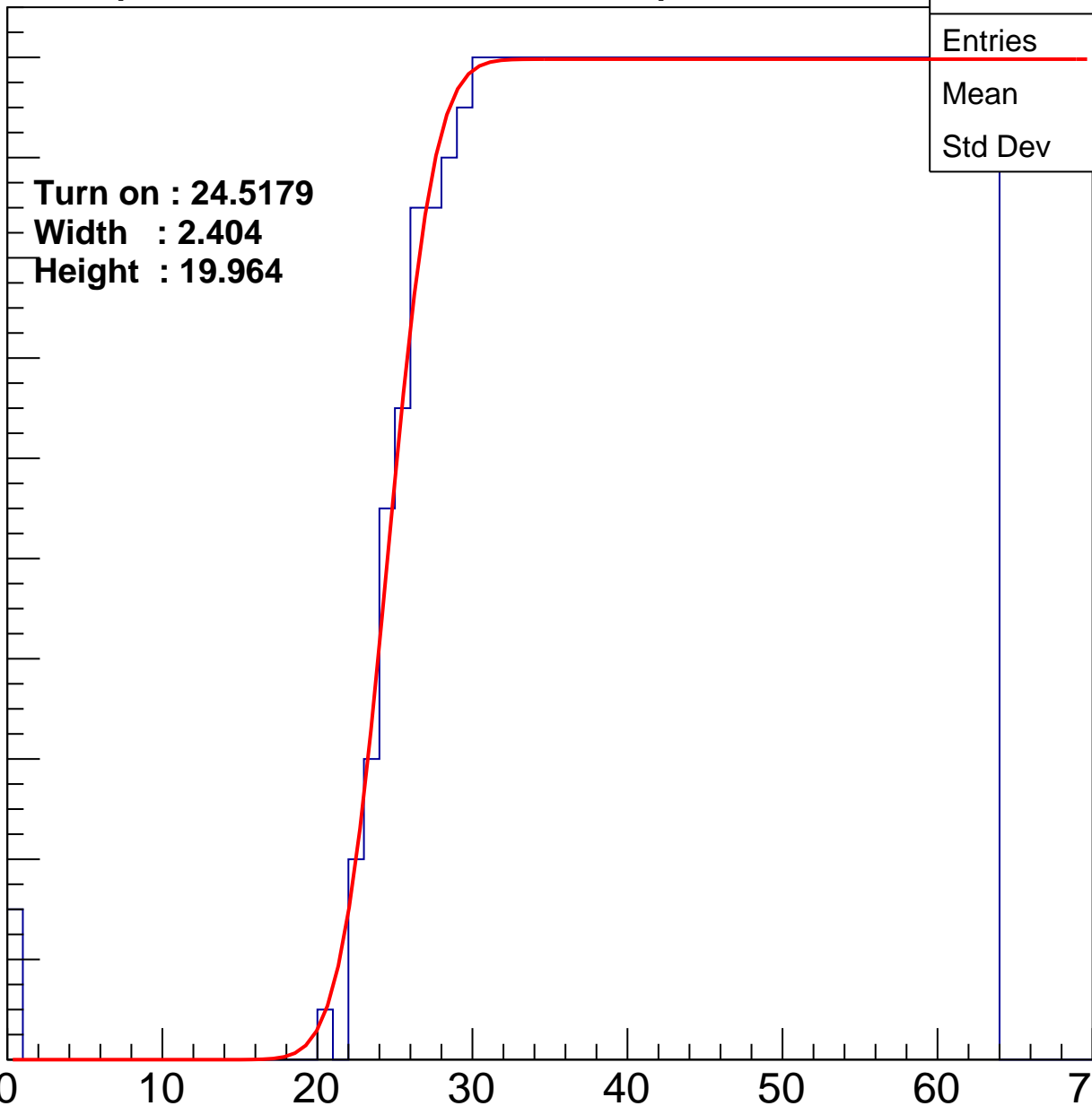
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.5179
Width : 2.404
Height : 19.964

Entries	789
Mean	43.61
Std Dev	11.75

ampl



B0L101S, U23-ch122

calib_packv5_042523_0143.root, FC#1, port C1

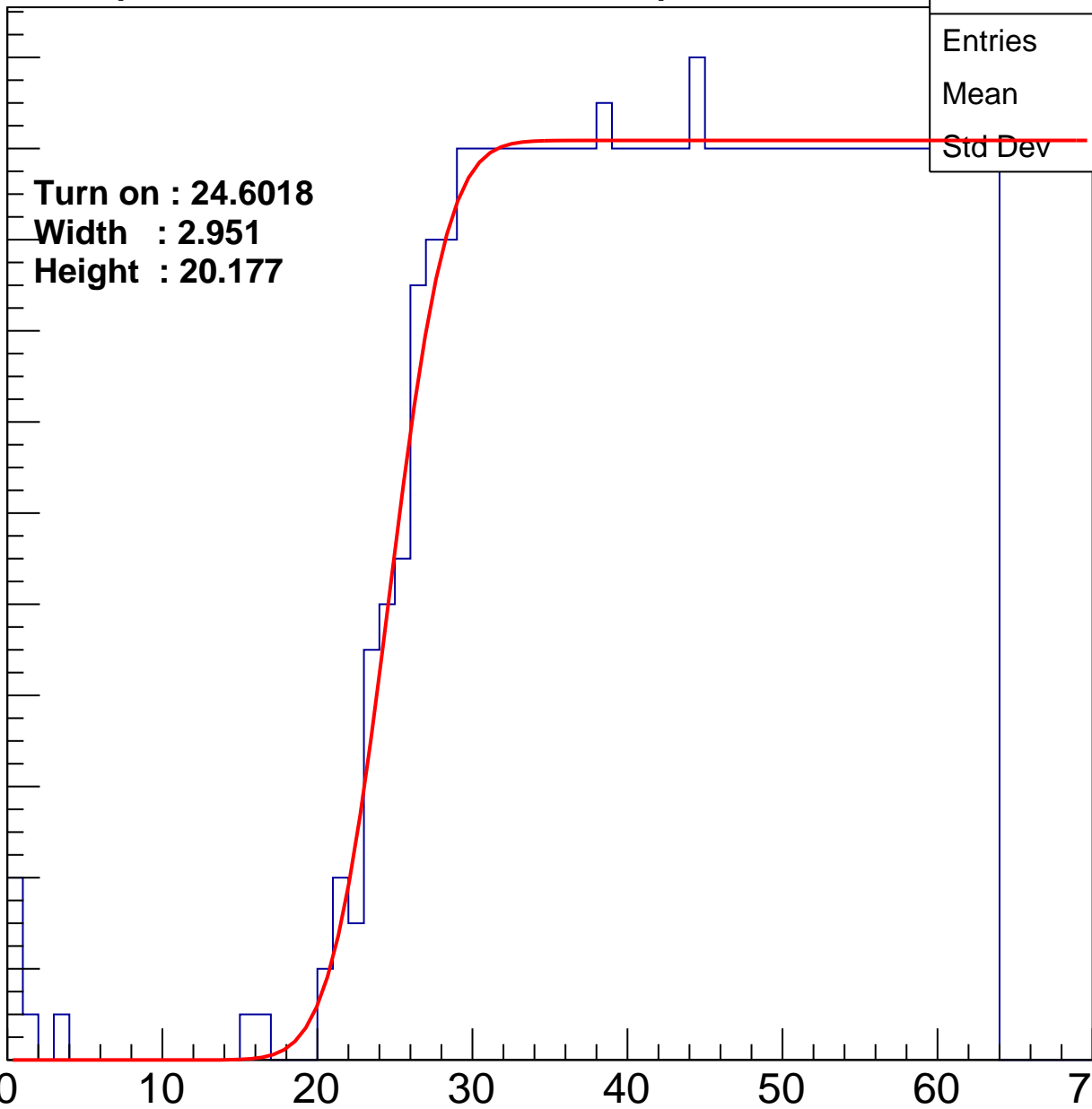
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6018
Width : 2.951
Height : 20.177

Entries	804
Mean	43.24
Std Dev	12.17

ampl



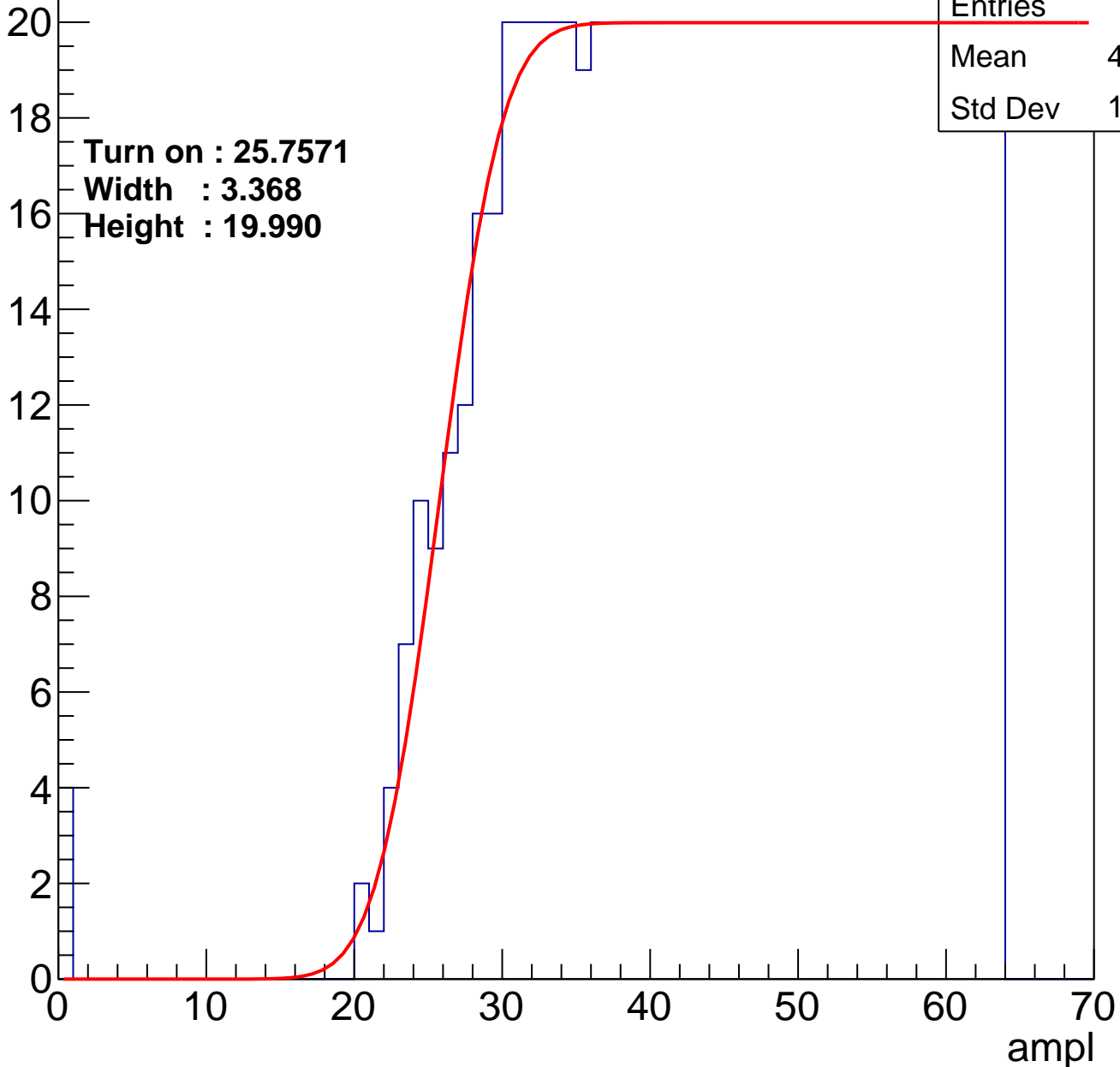
B0L101S, U23-ch123

calib_packv5_042523_0143.root, FC#1, port C1

Entries	771
Mean	43.95
Std Dev	11.73

Turn on : 25.7571
Width : 3.368
Height : 19.990

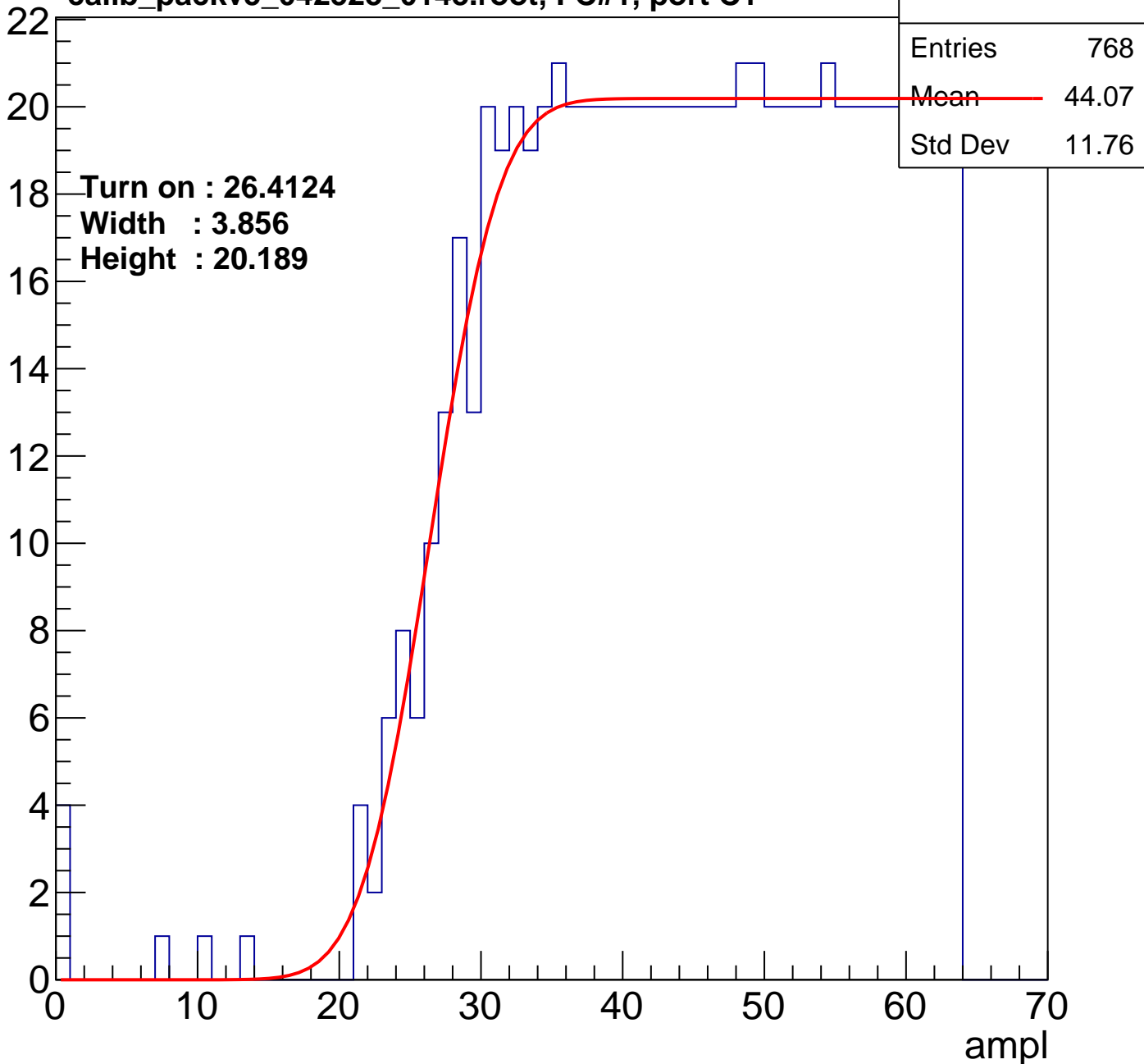
Entry



B0L101S, U23-ch124

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U23-ch125

calib_packv5_042523_0143.root, FC#1, port C1

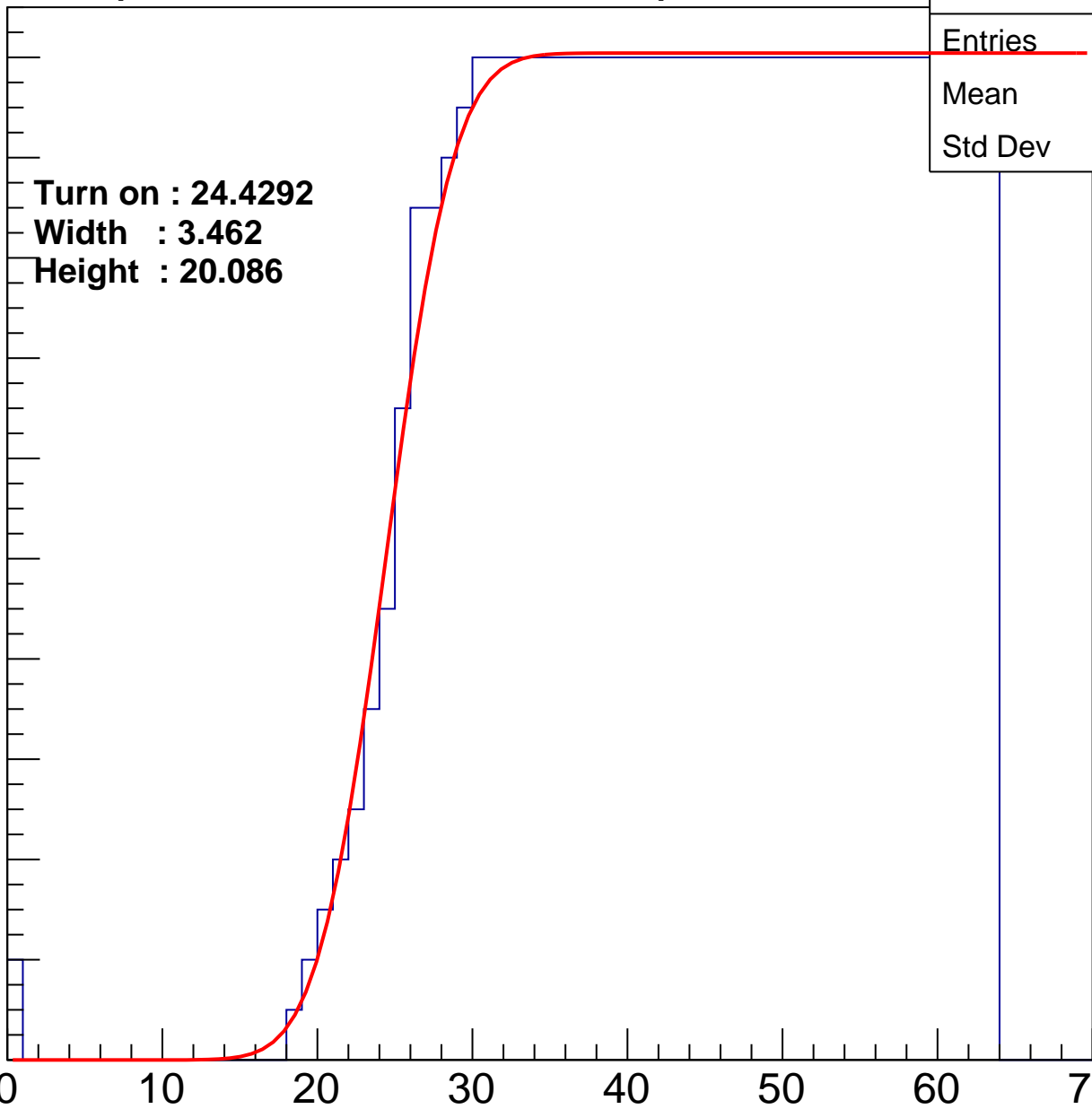
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.4292
Width : 3.462
Height : 20.086

Entries	797
Mean	43.4
Std Dev	11.86

ampl



B0L101S, U23-ch126

calib_packv5_042523_0143.root, FC#1, port C1

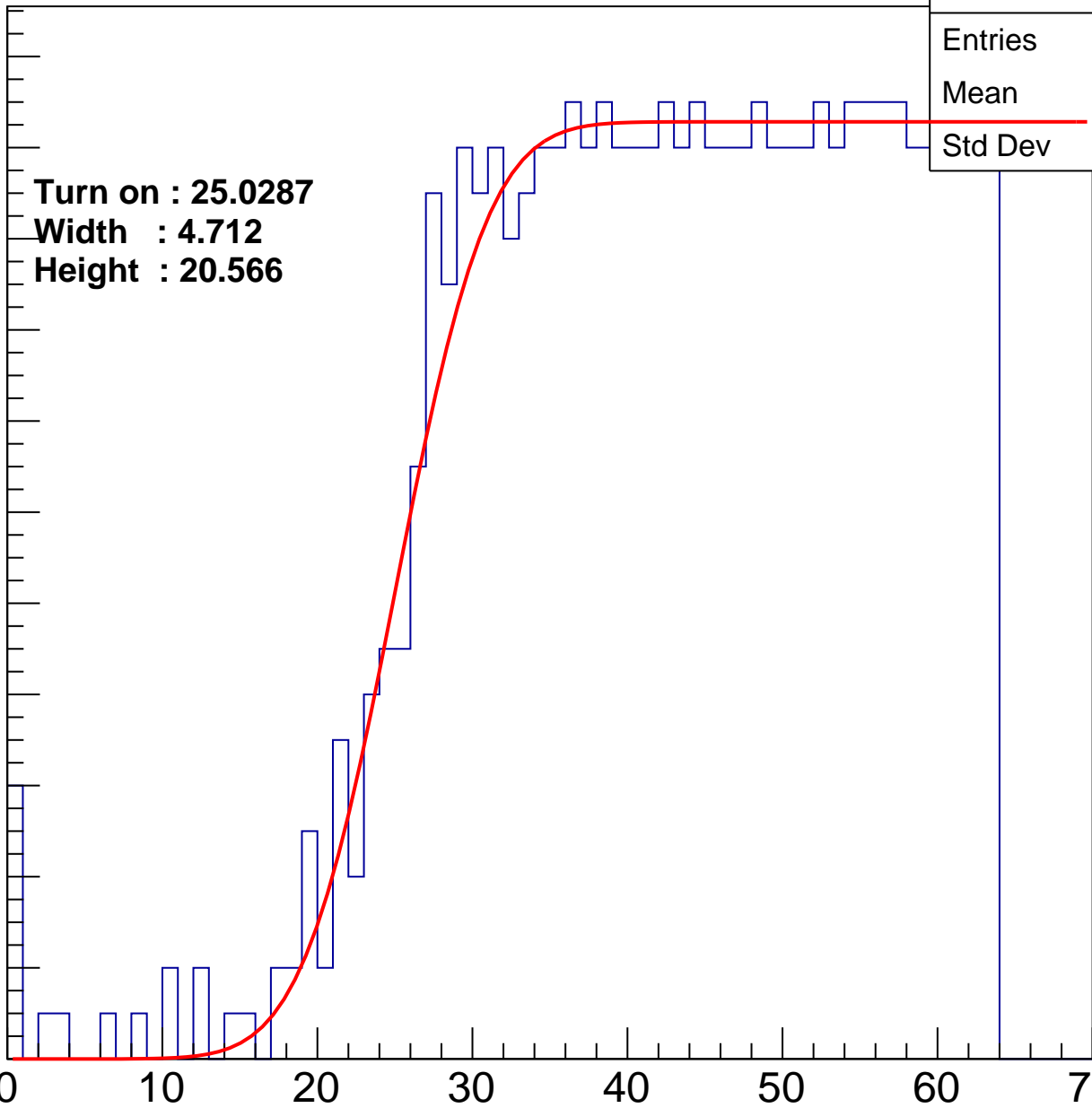
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0287
Width : 4.712
Height : 20.566

Entries	823
Mean	42.88
Std Dev	12.84

ampl



B0L101S, U23-ch127

calib_packv5_042523_0143.root, FC#1, port C1

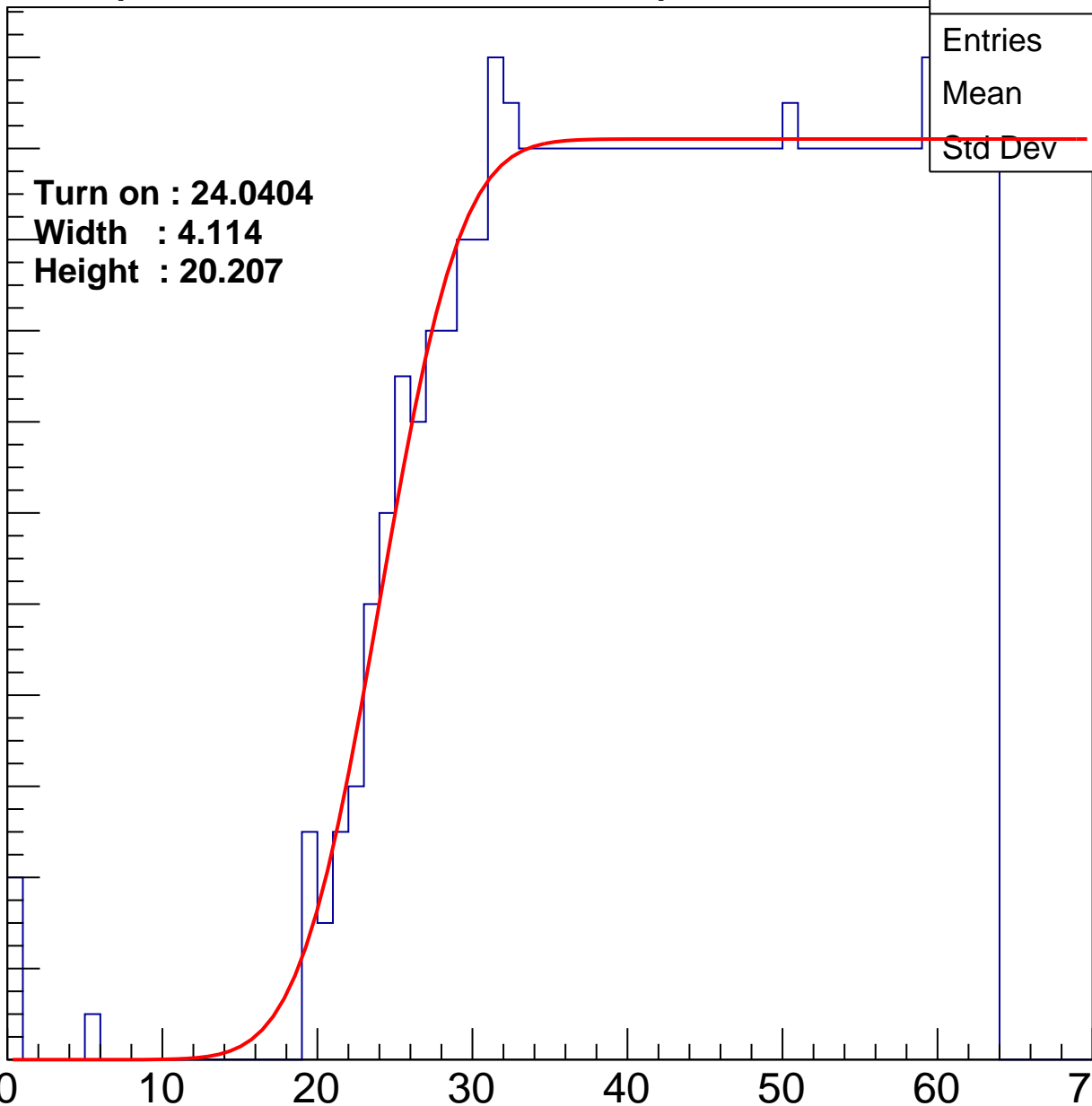
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.0404
Width : 4.114
Height : 20.207

Entries	811
Mean	43.16
Std Dev	12.26

ampl



B0L101S, U23-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entries	811
Mean	43.16
Std Dev	12.26

Turn on : 24.0404

Width : 4.114

Height : 20.207

Entry

