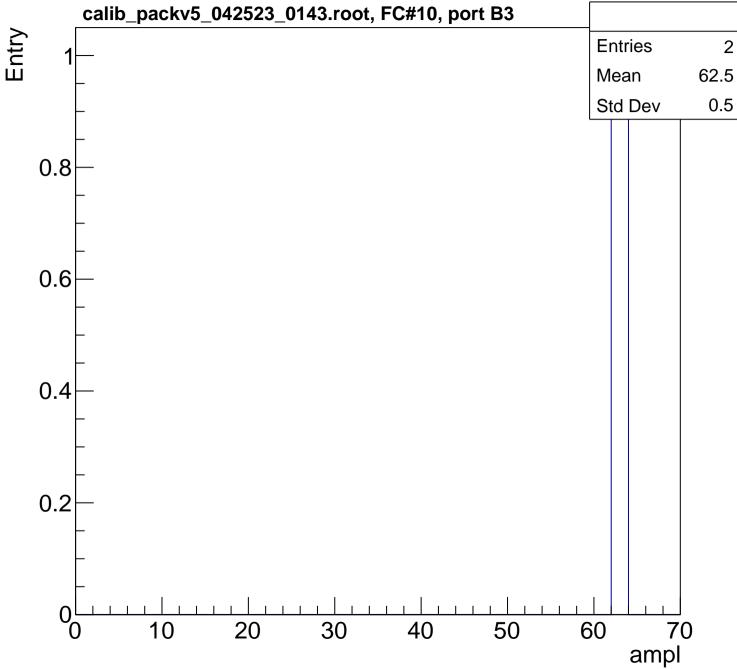
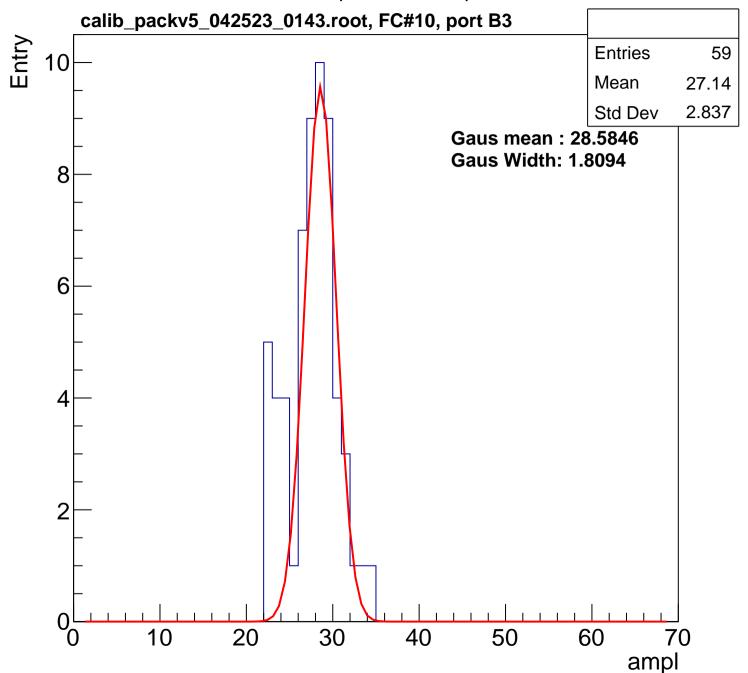
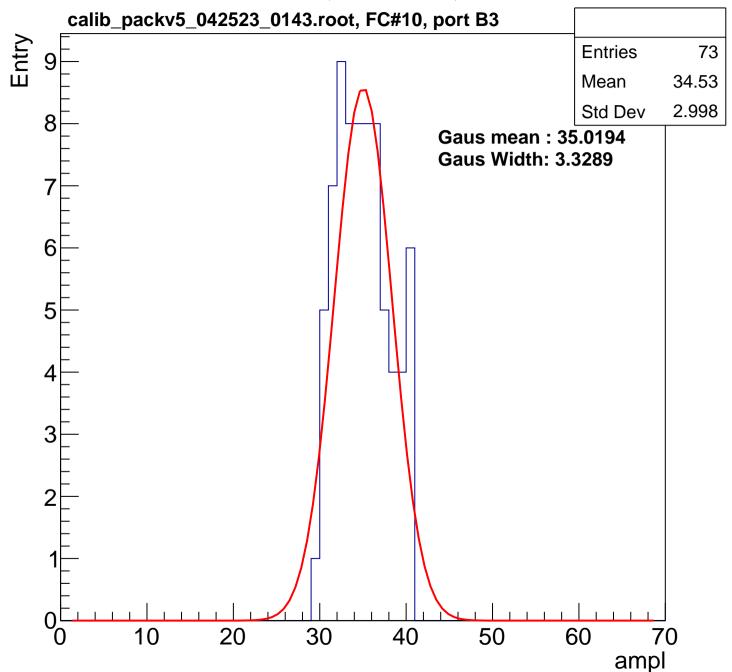
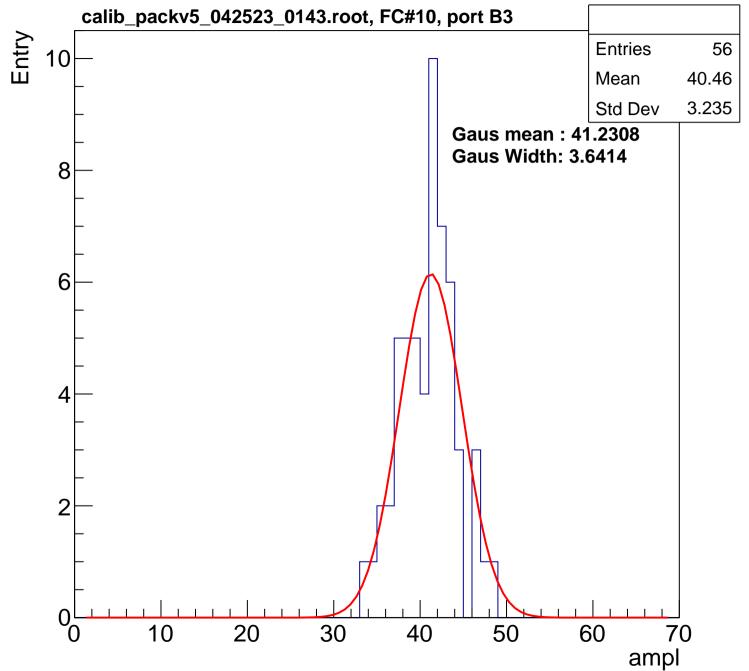


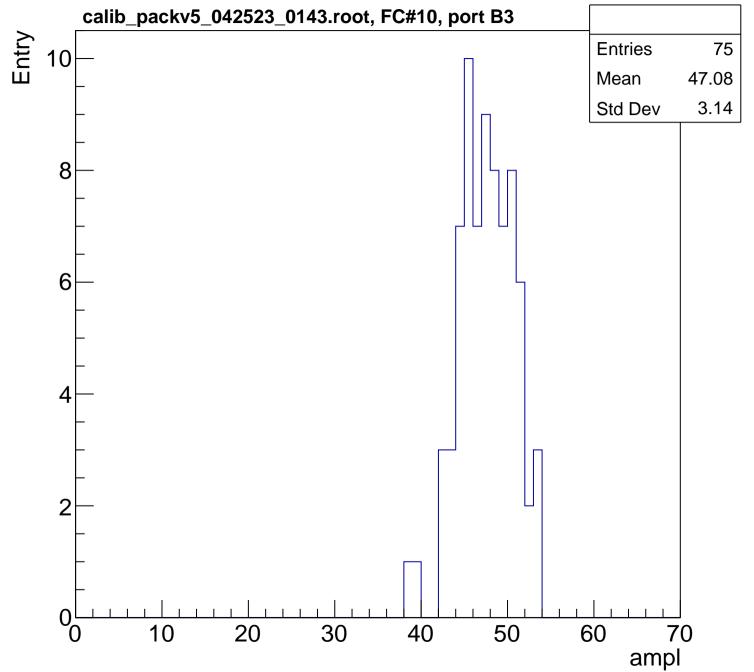
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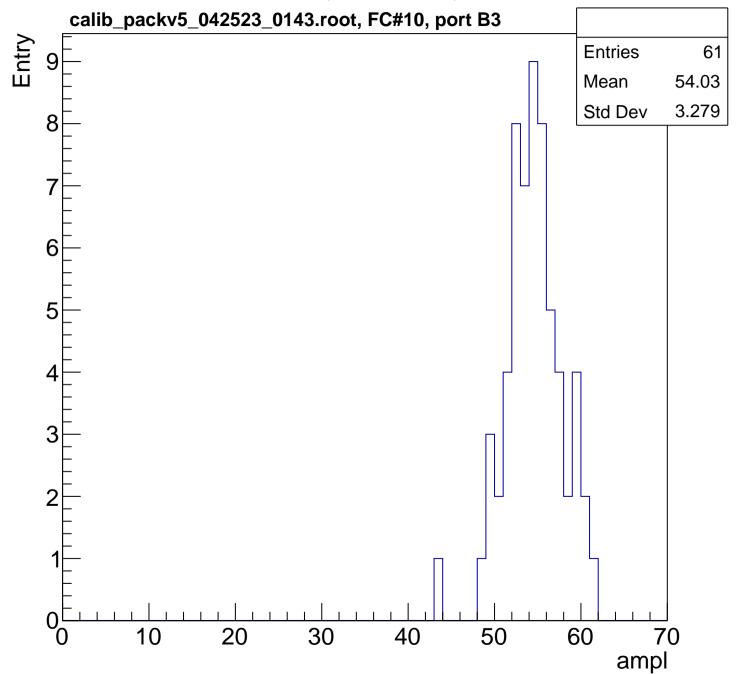


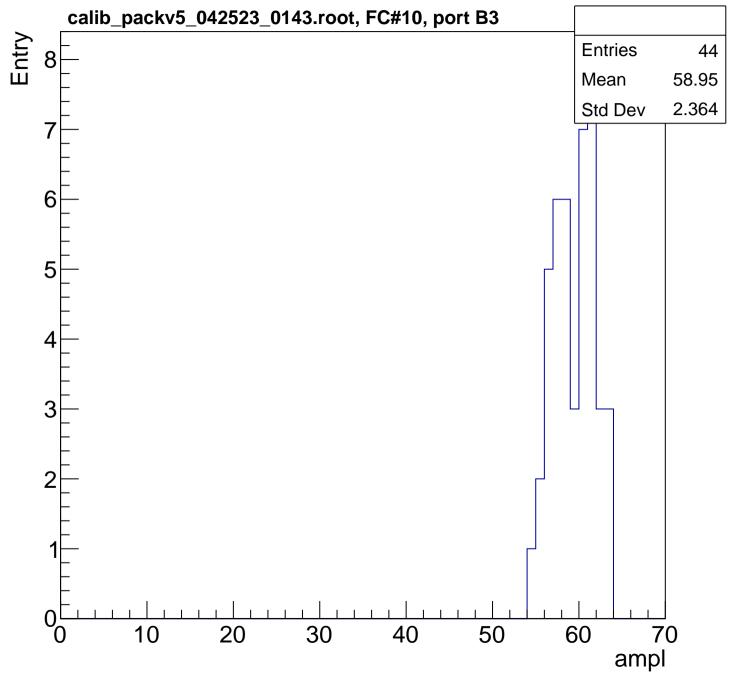


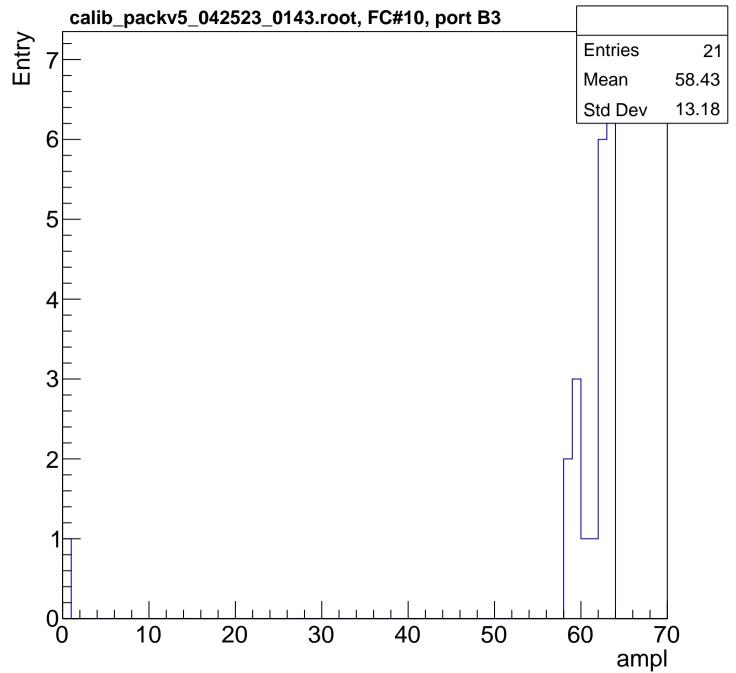


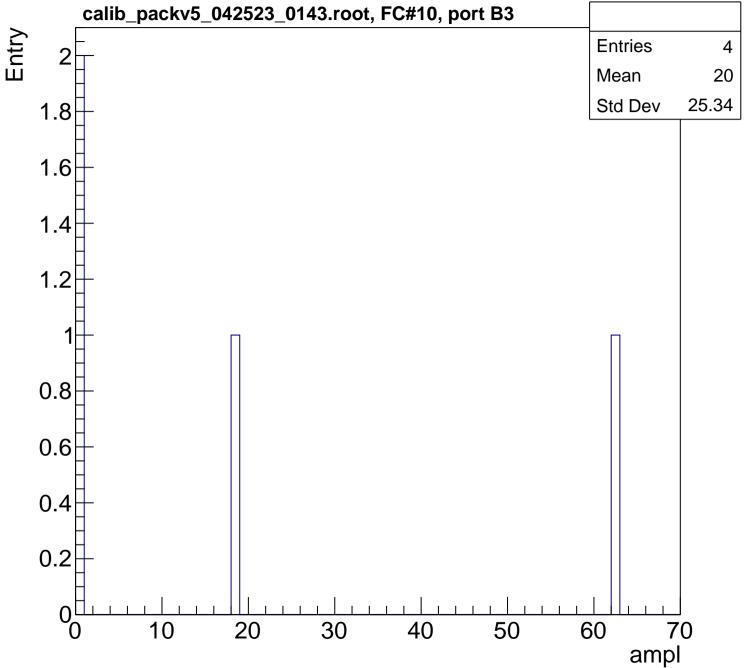


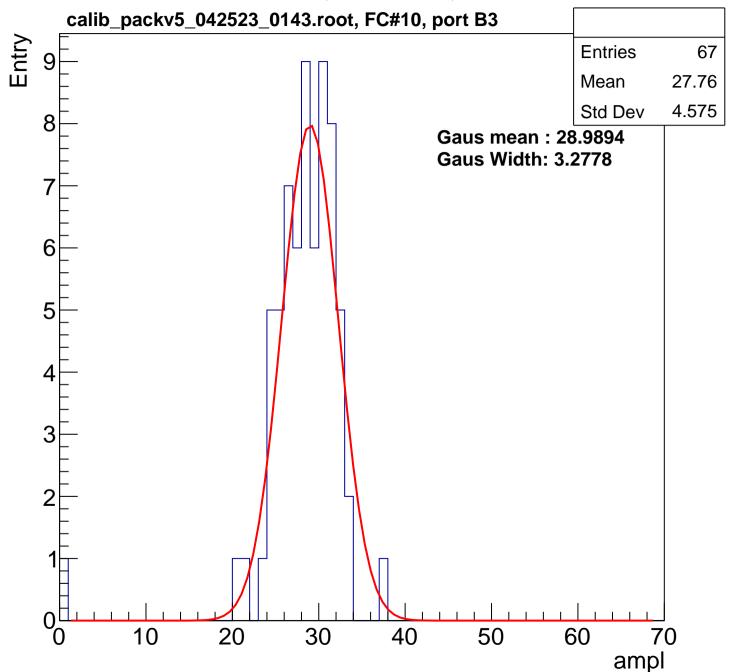


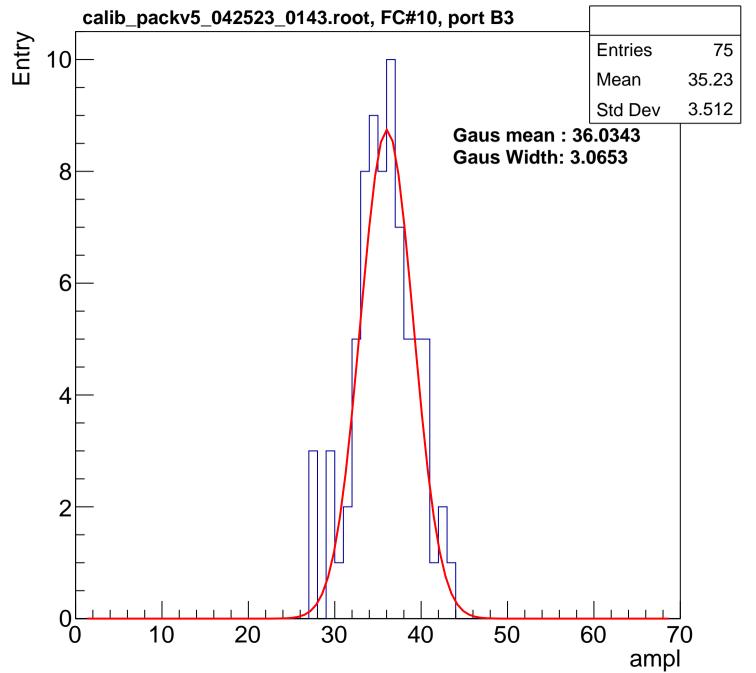


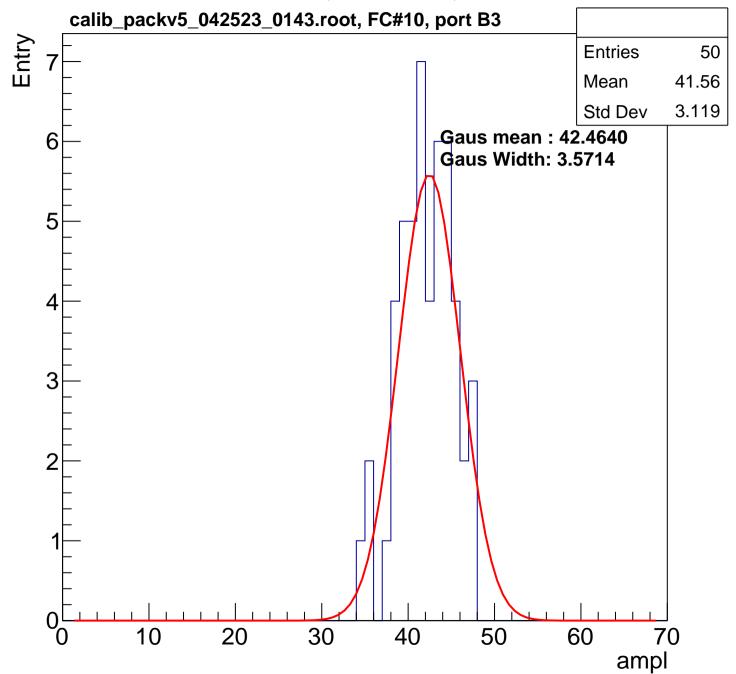


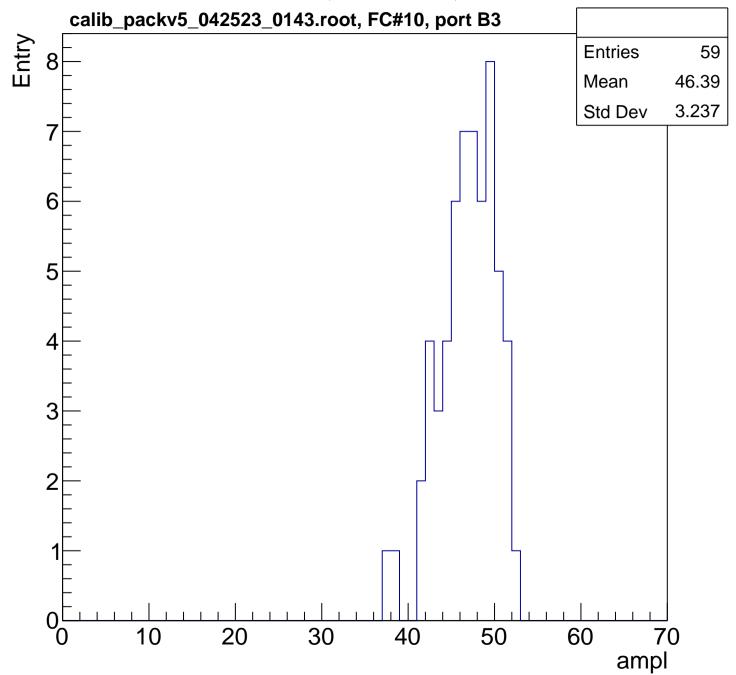


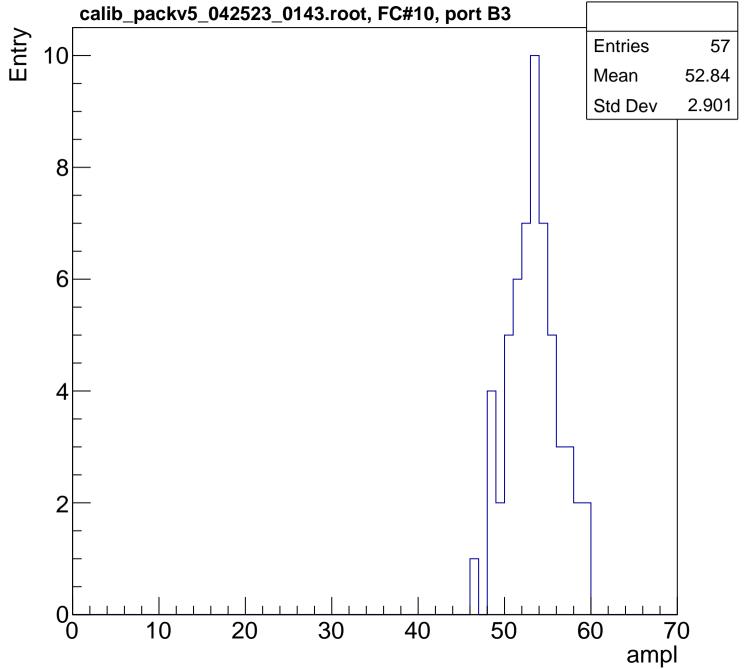


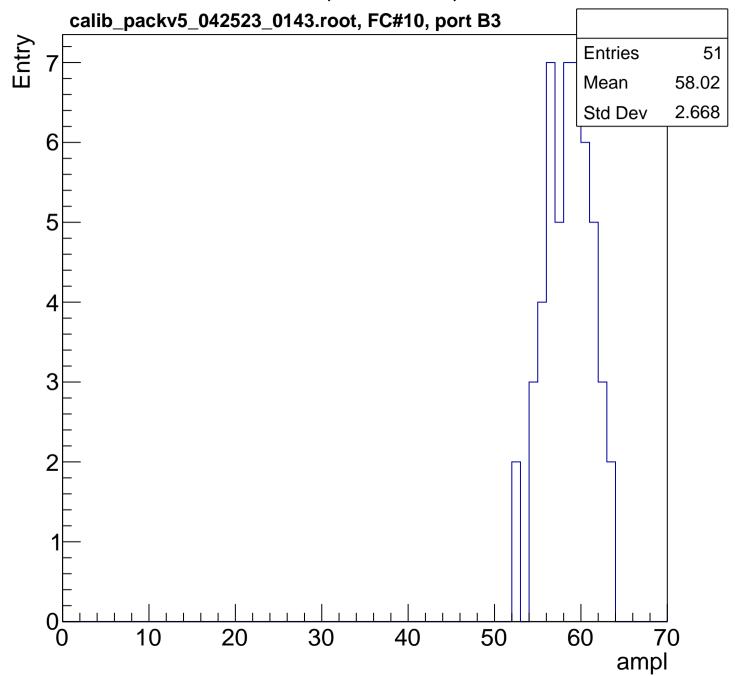


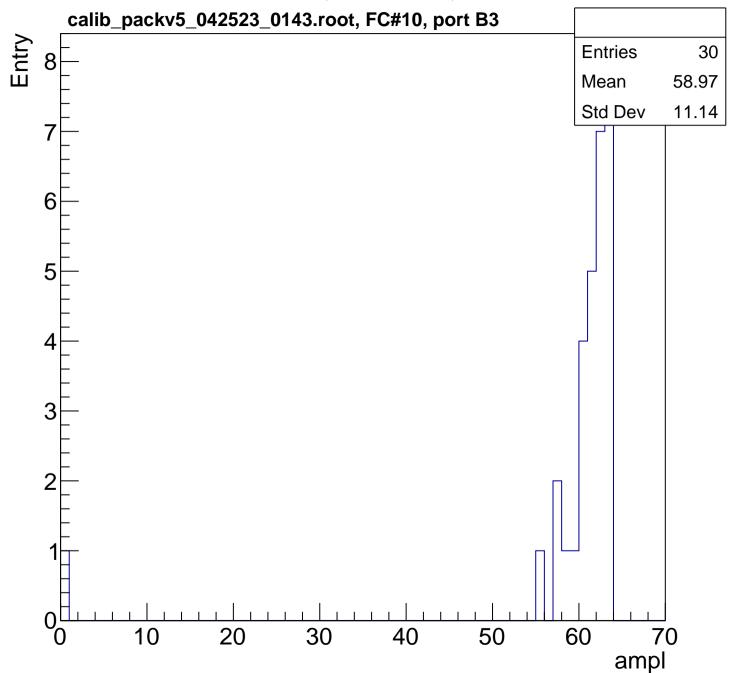


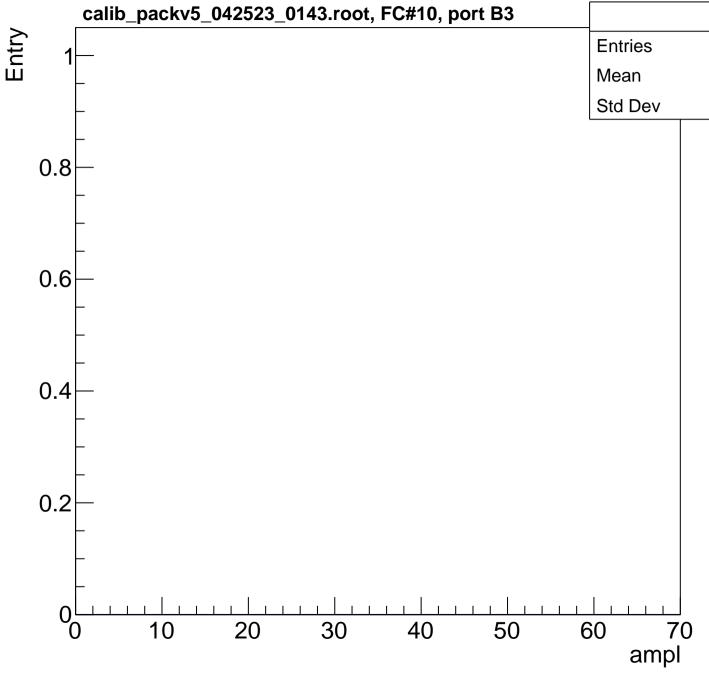


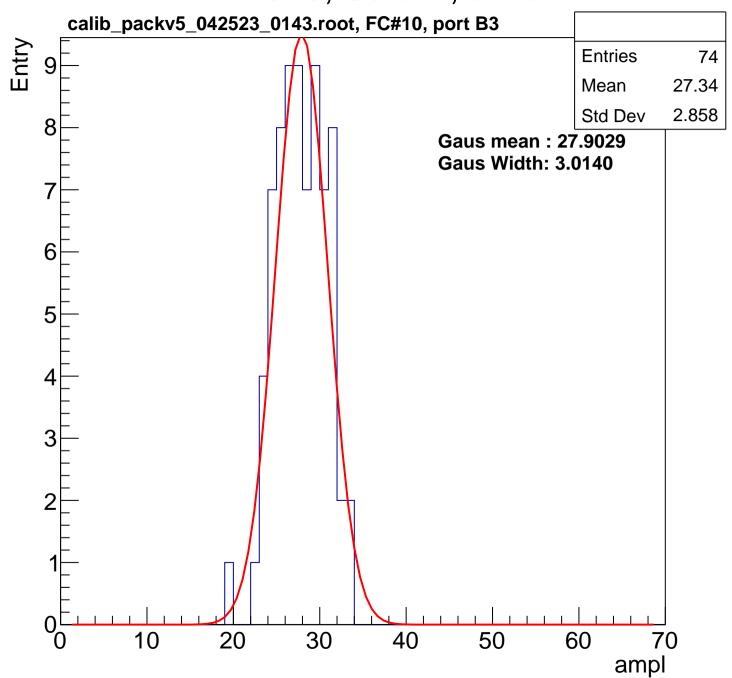


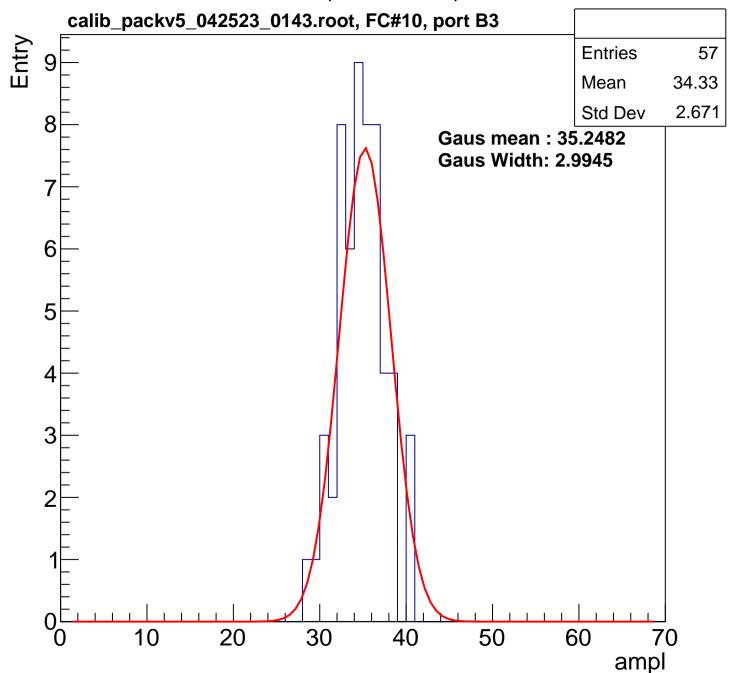


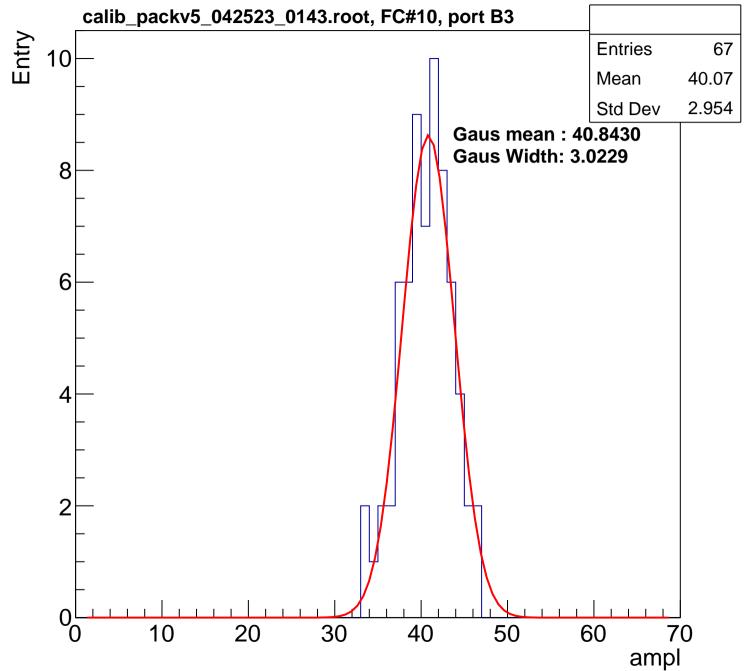


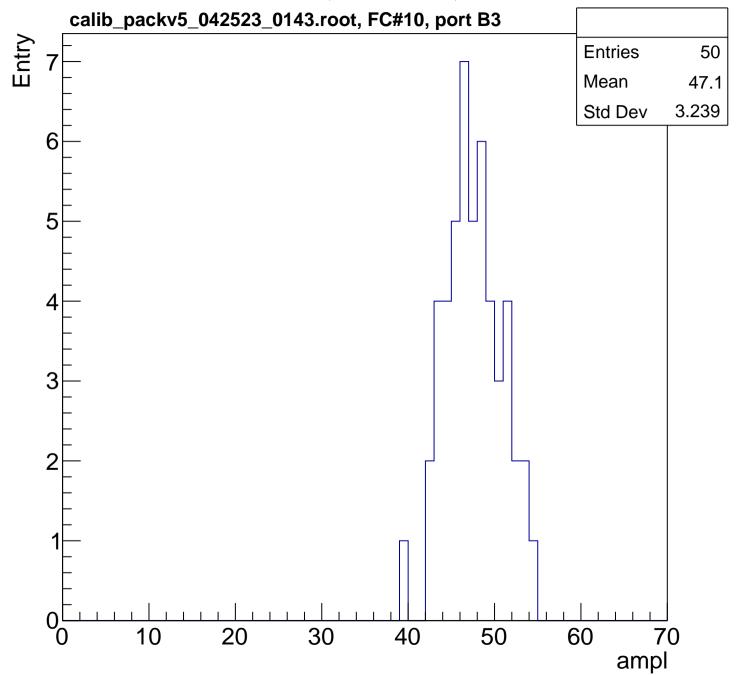


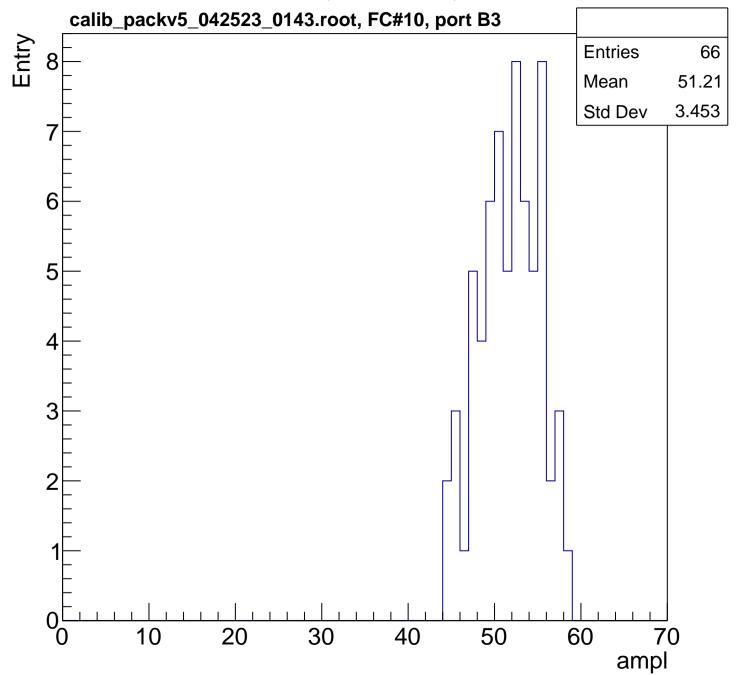


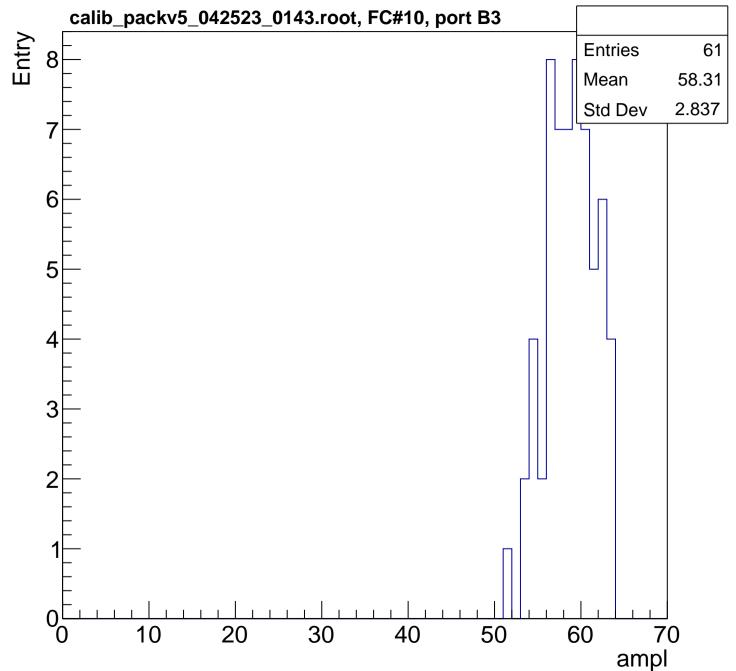


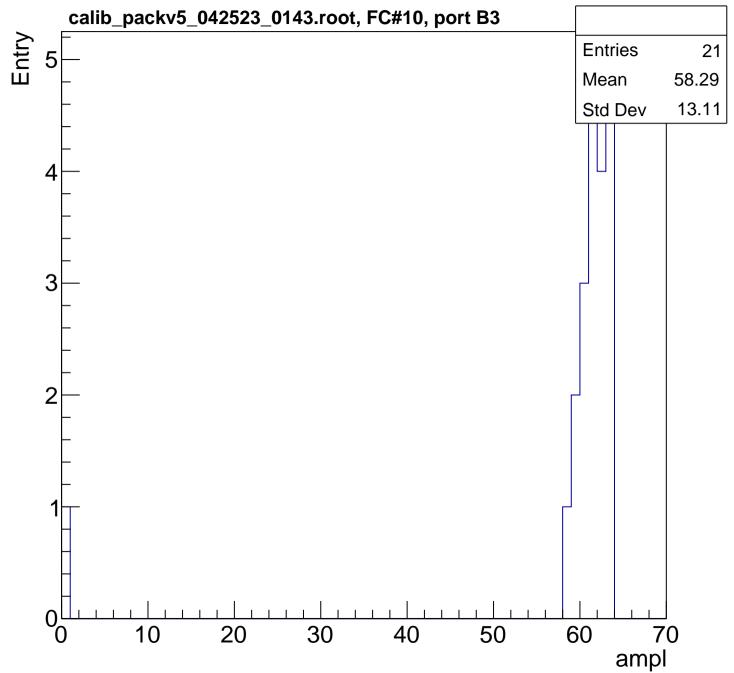


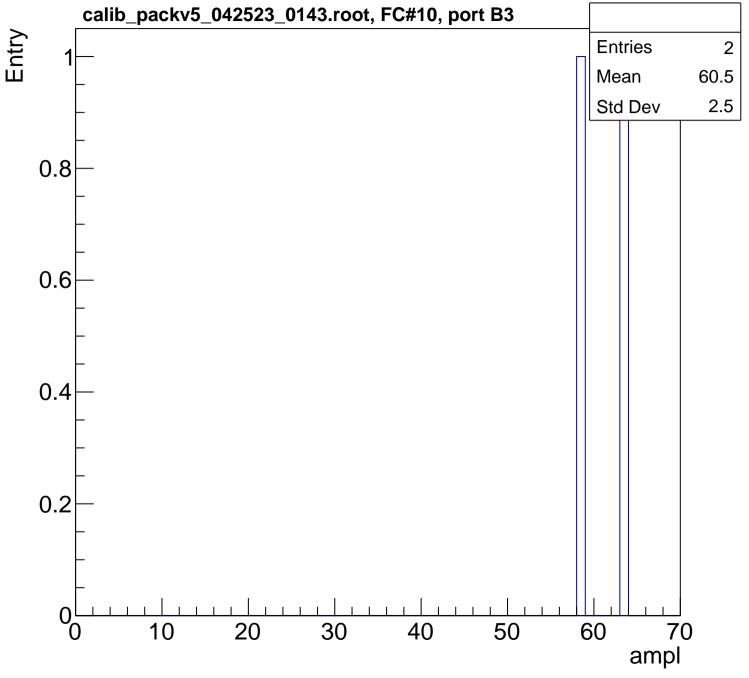


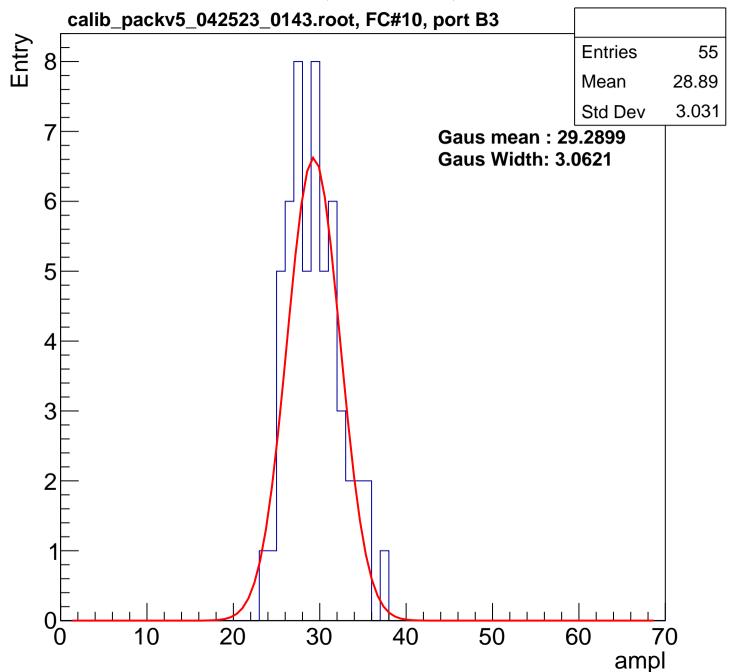


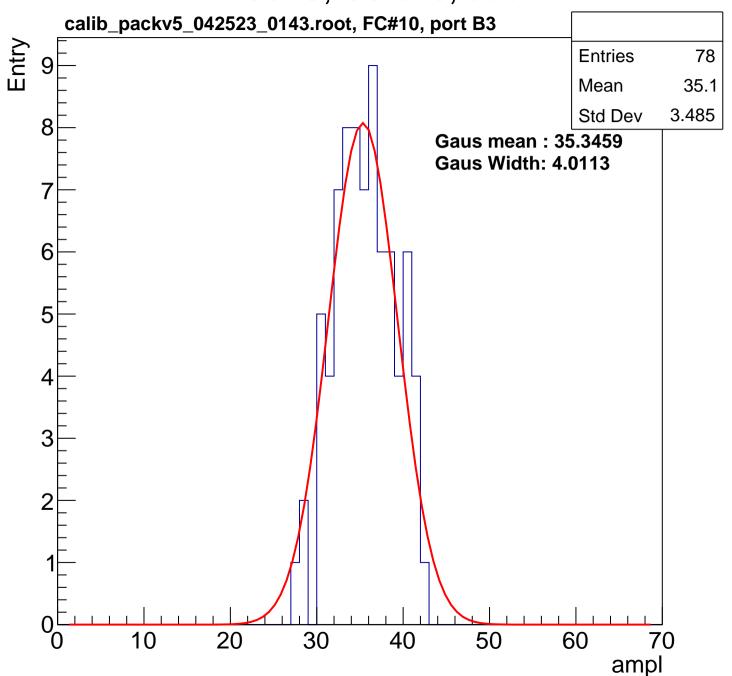


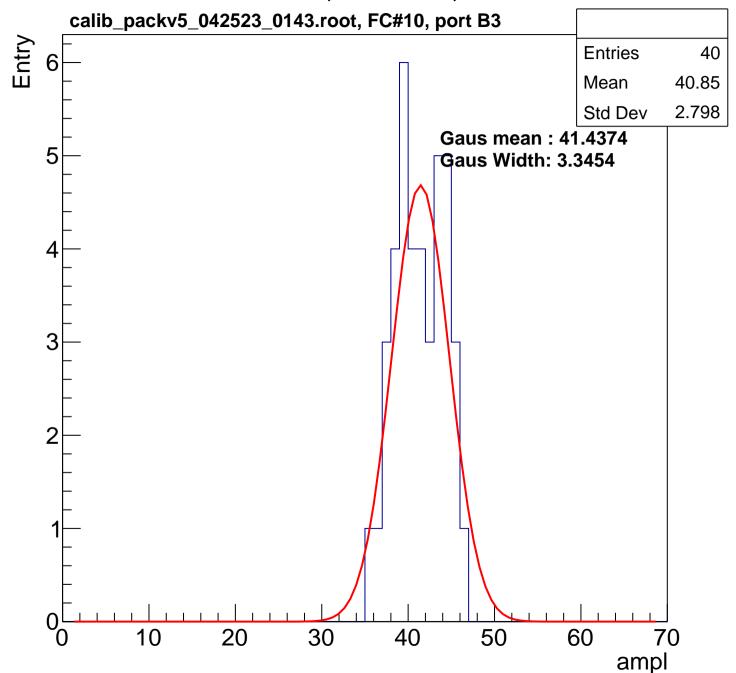


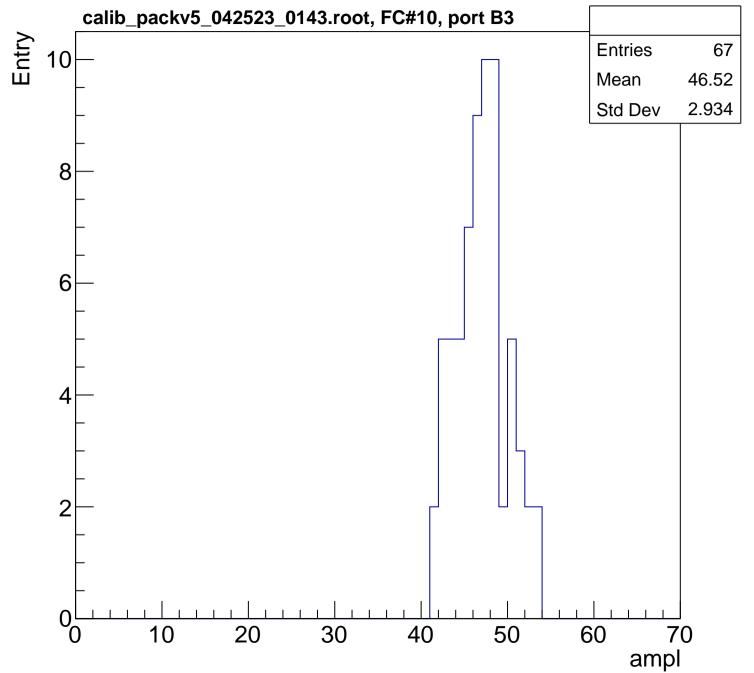


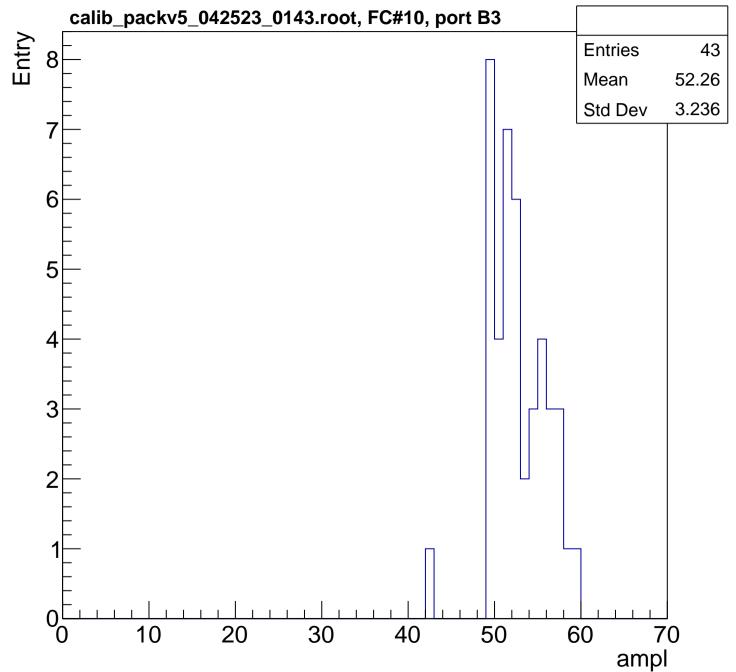


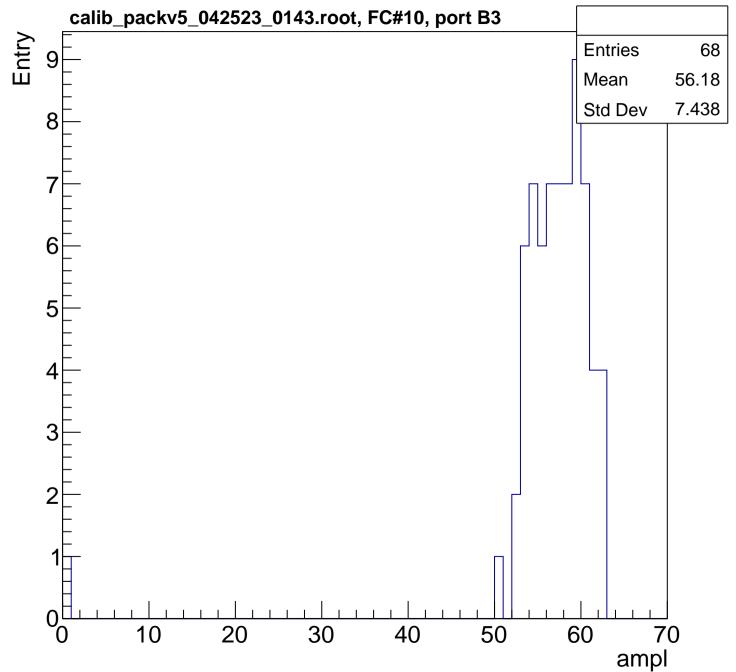


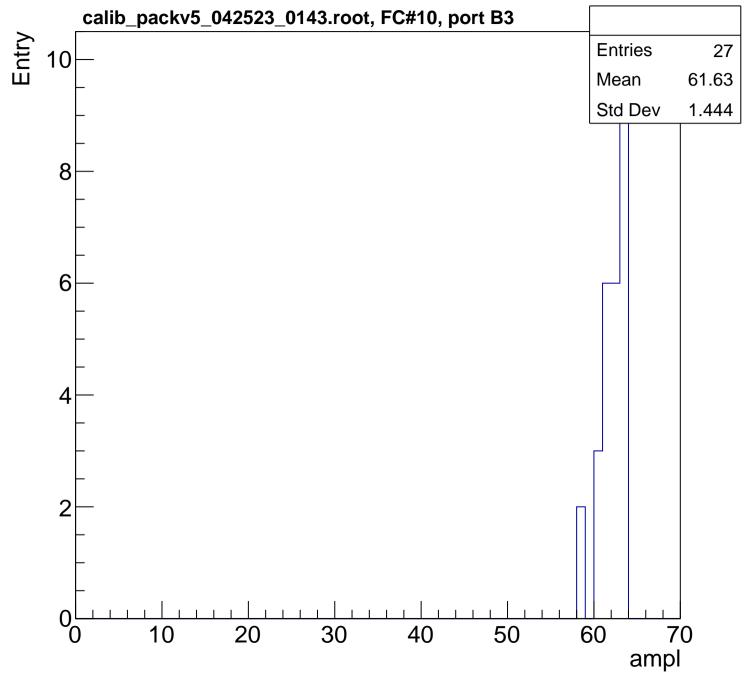


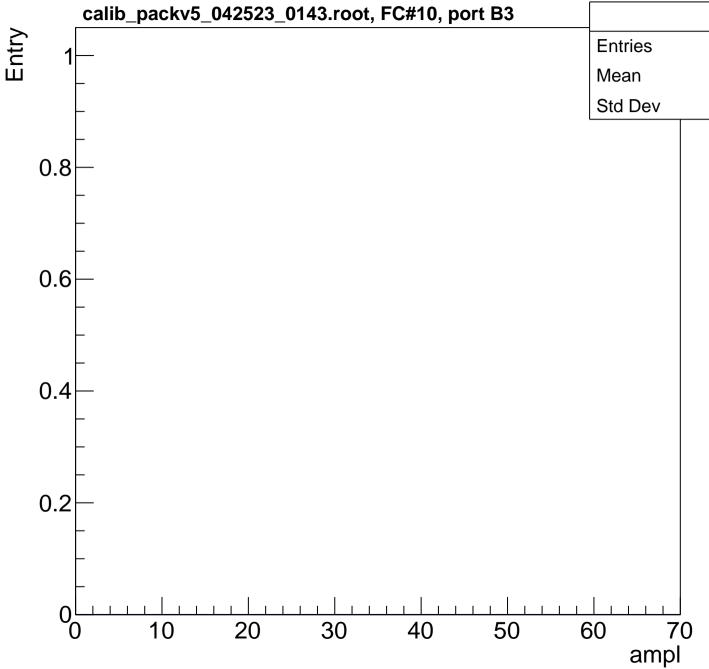


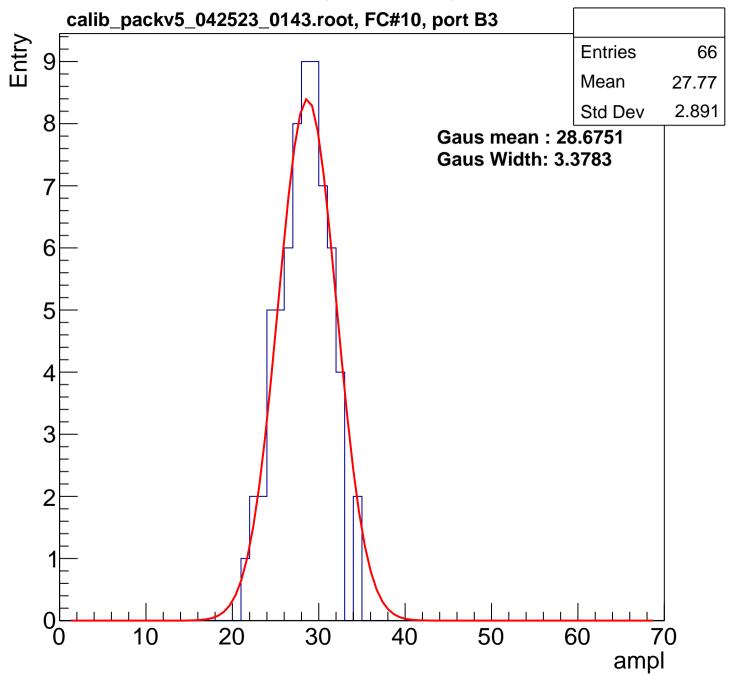


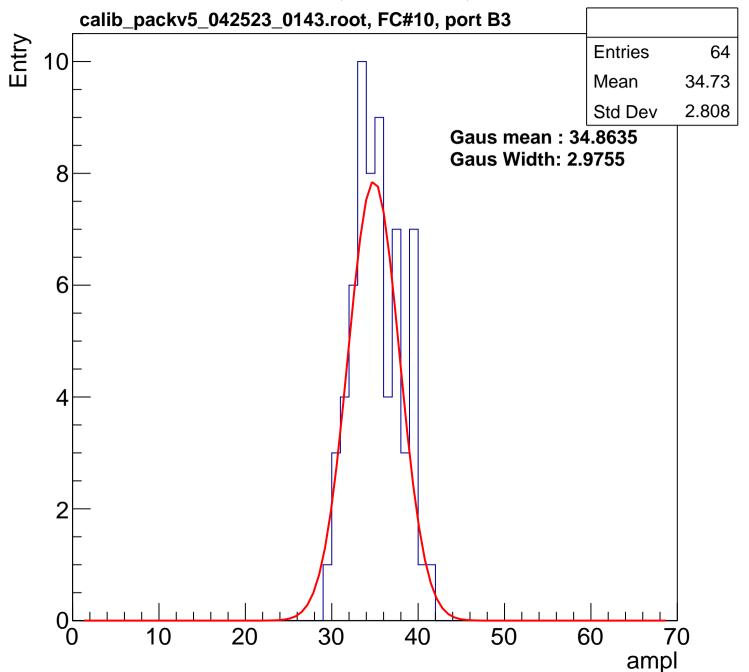


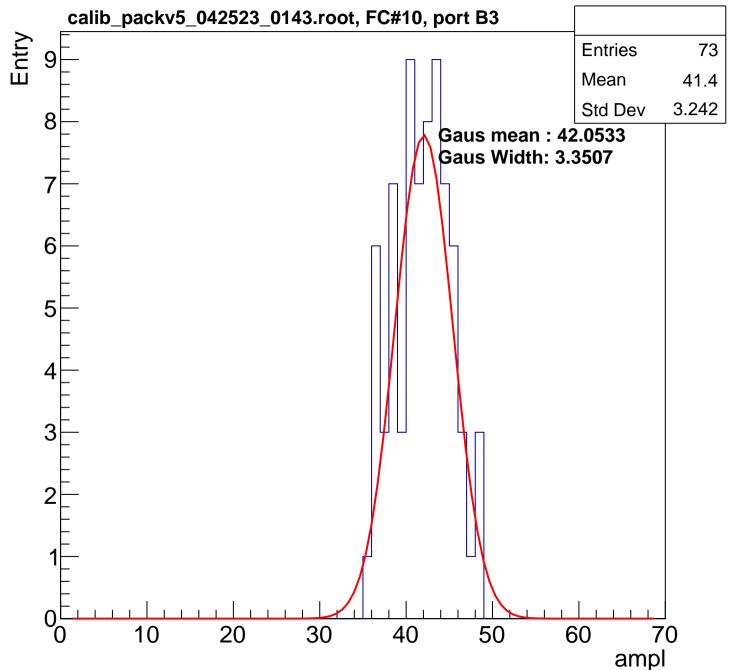


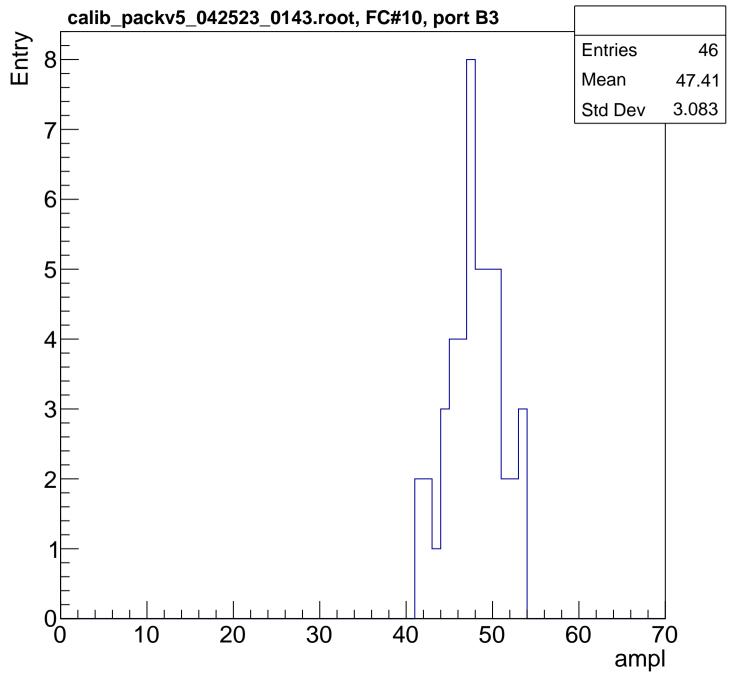


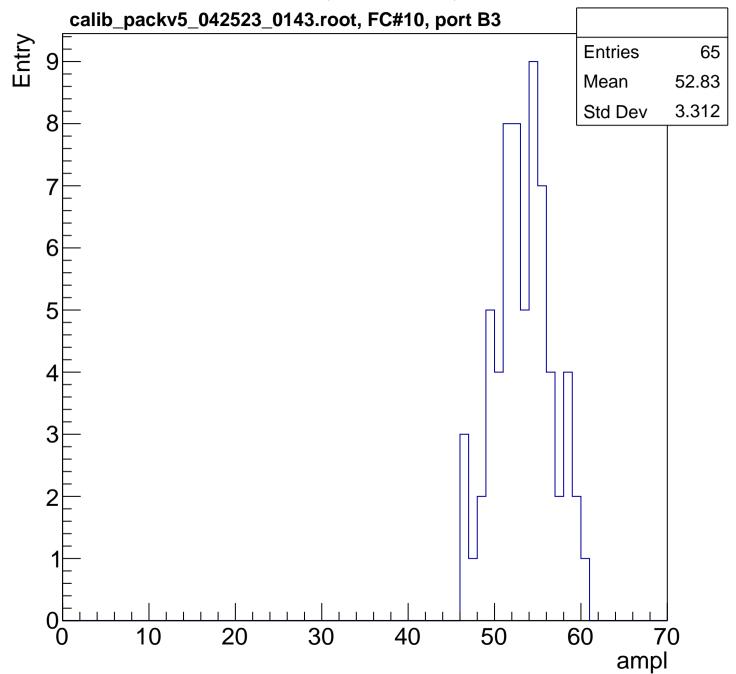


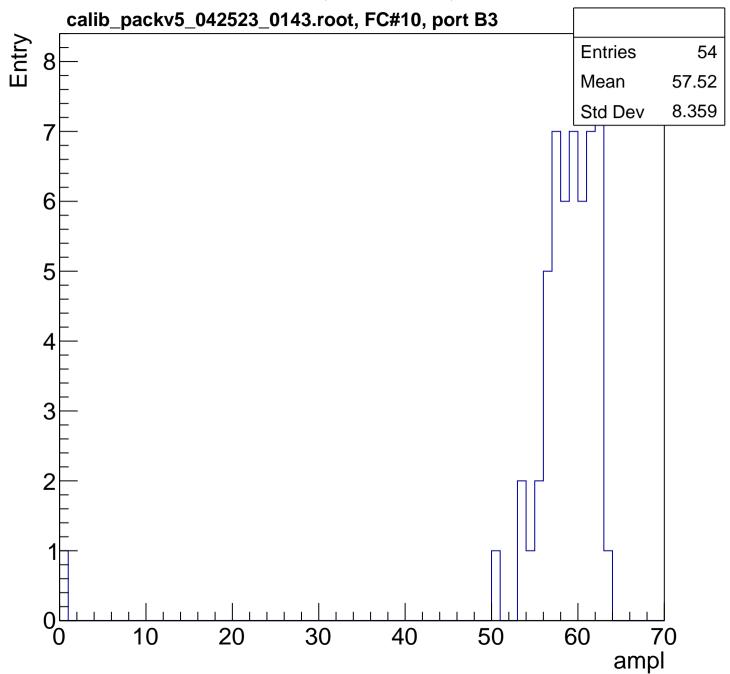


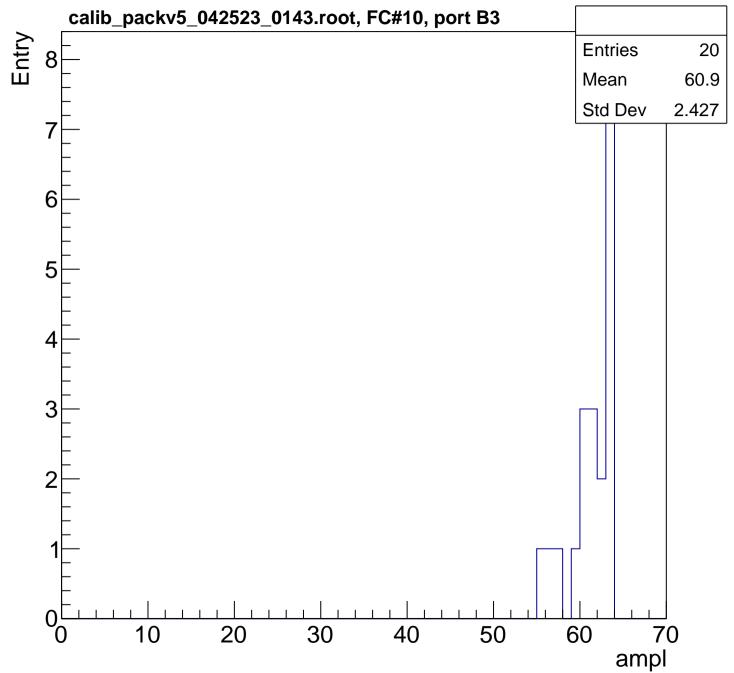


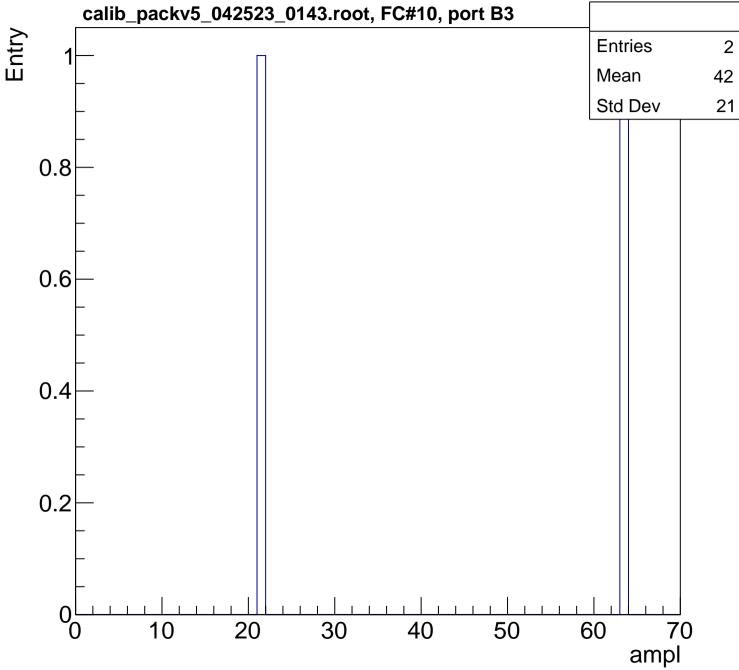


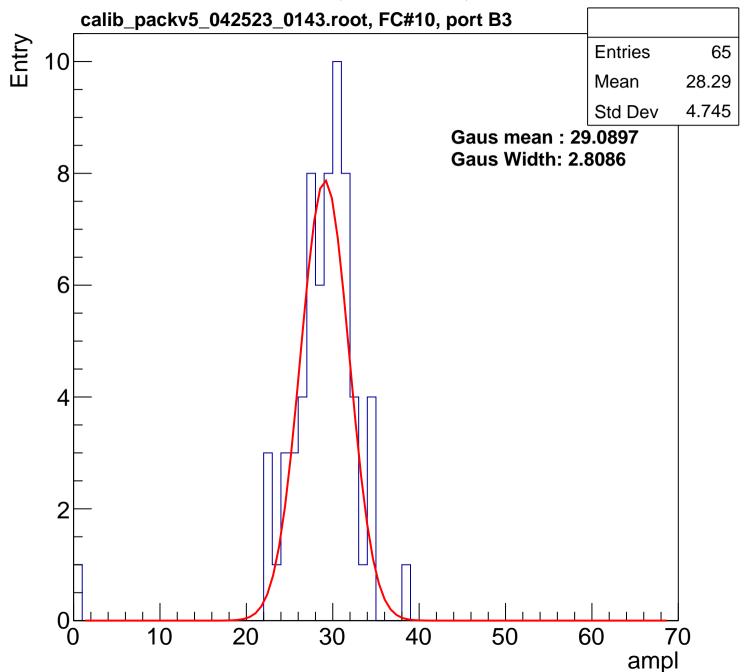


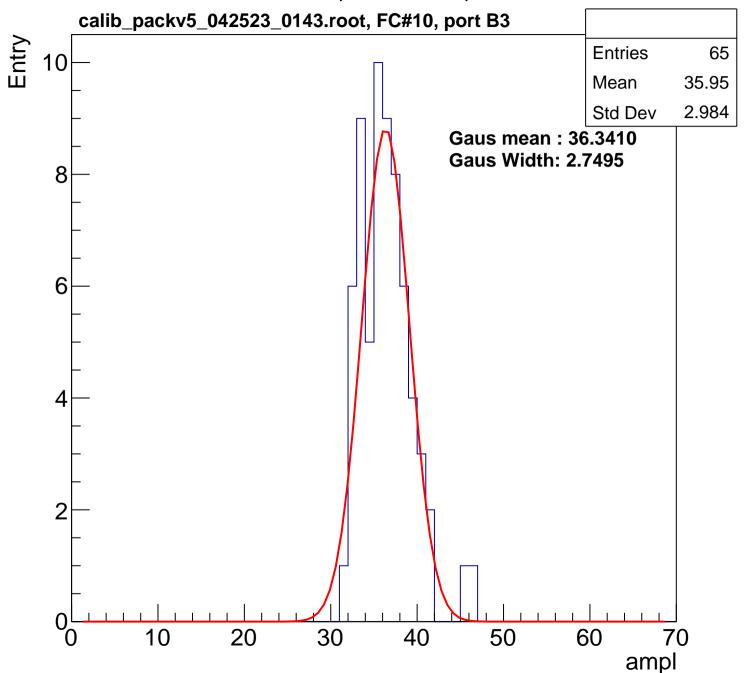


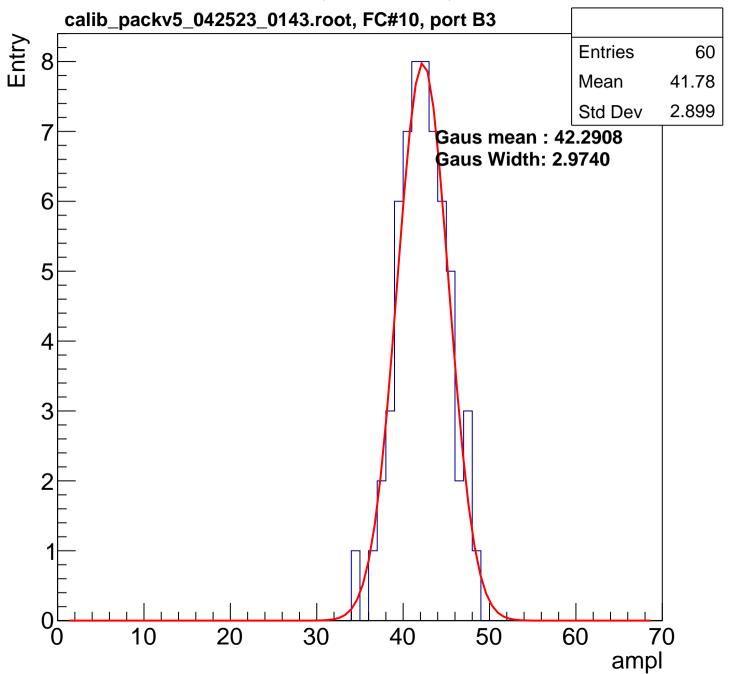


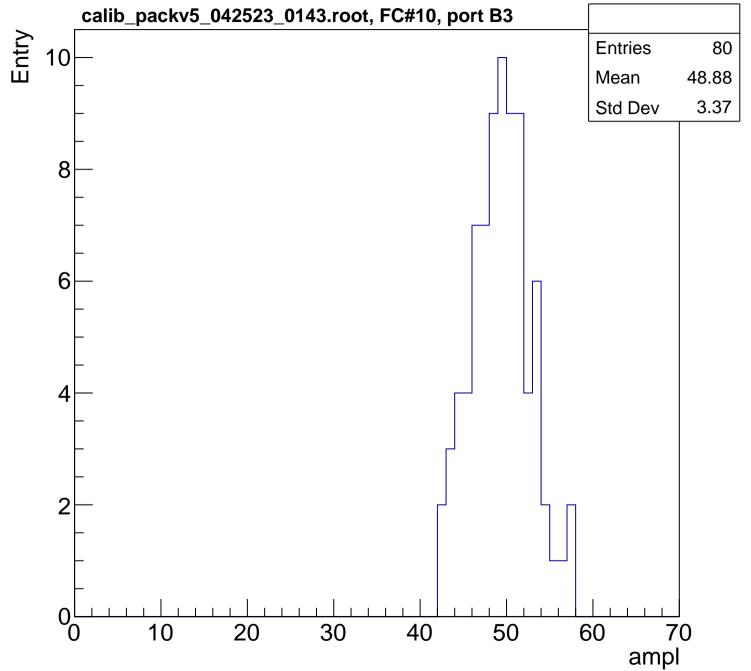


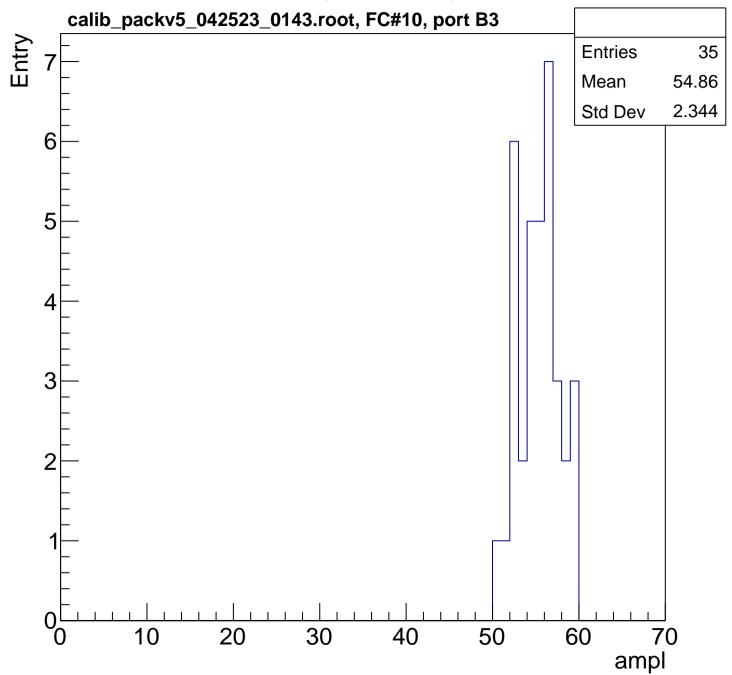


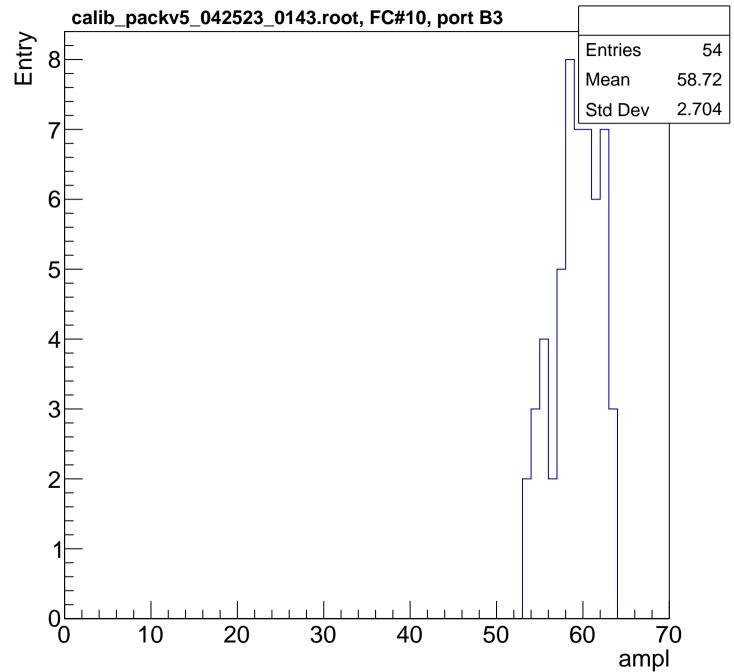


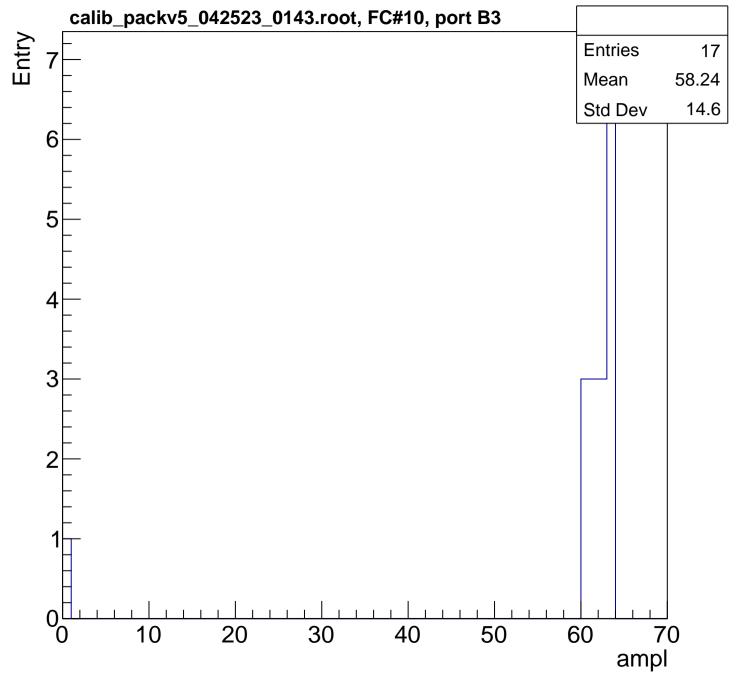


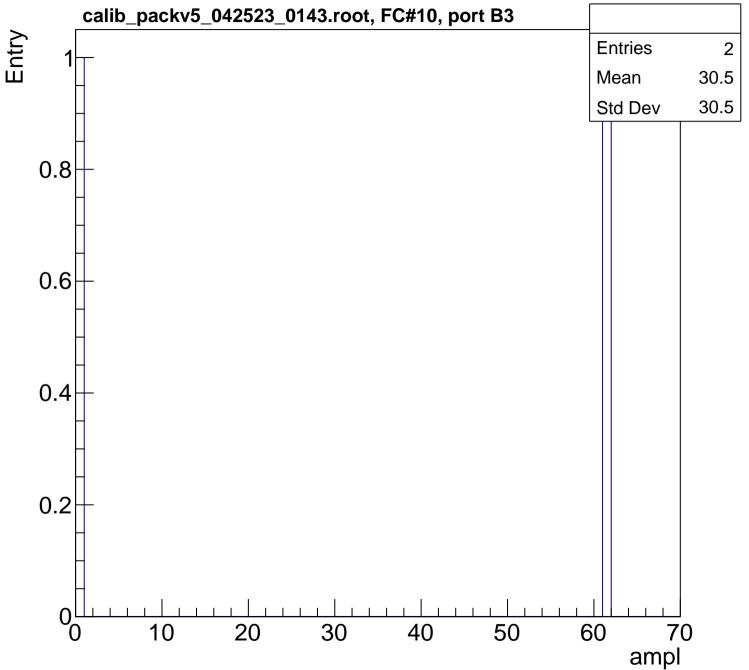


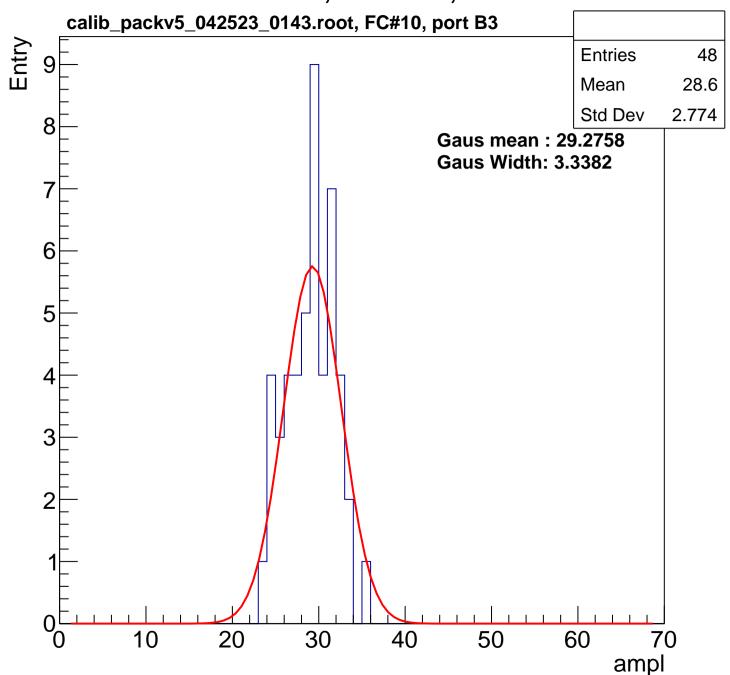


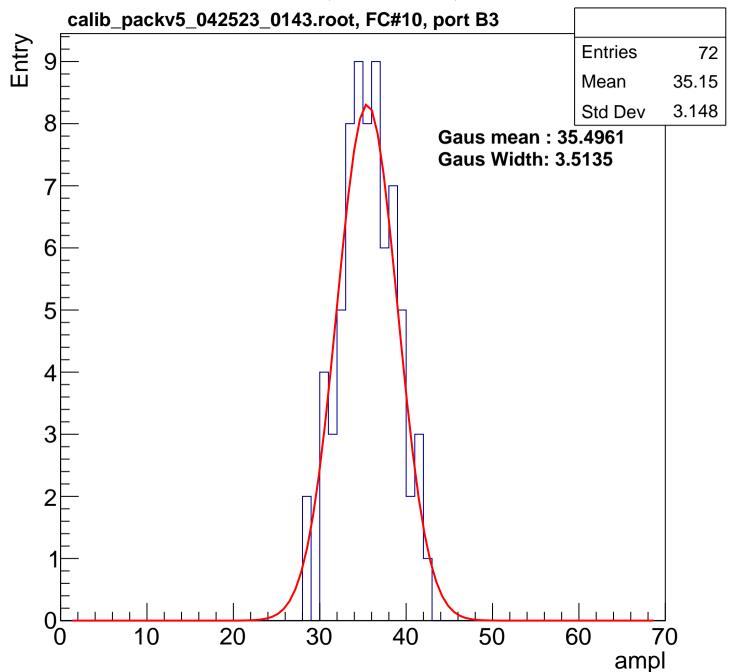


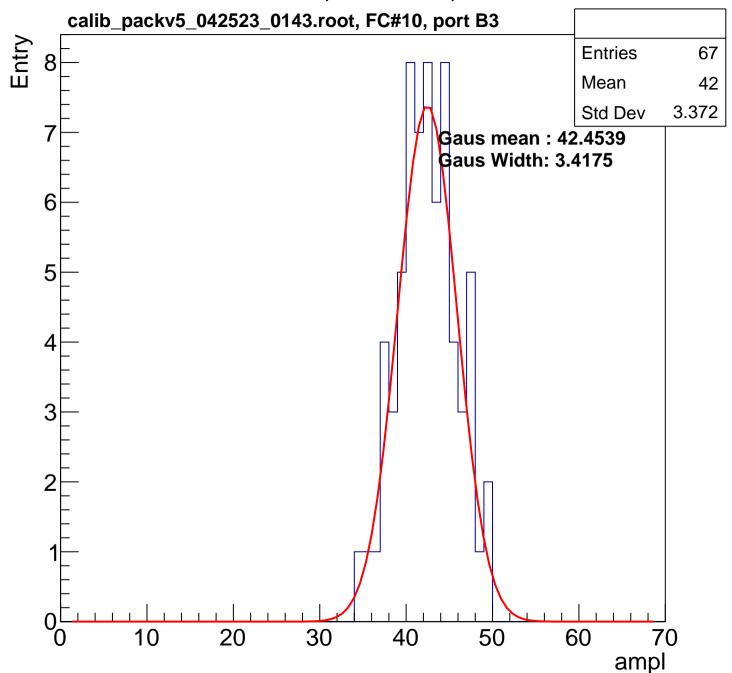


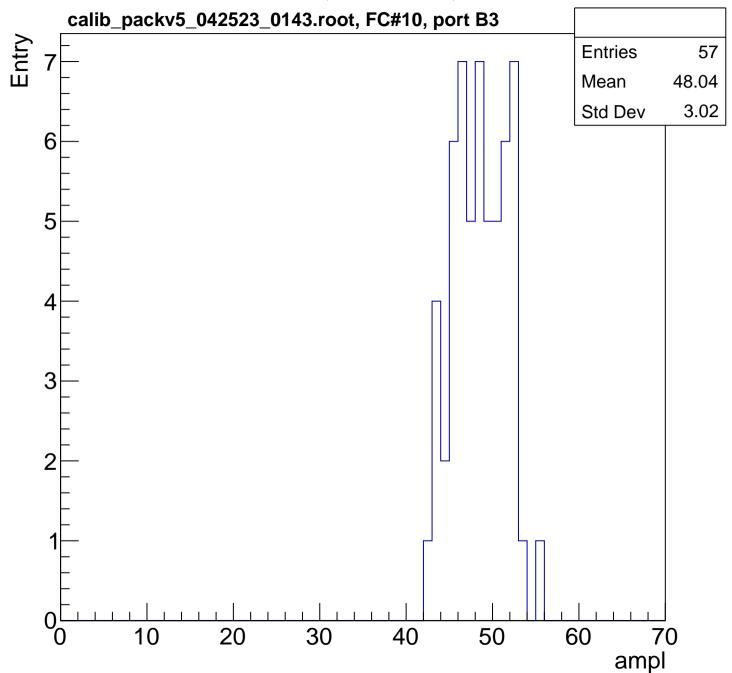


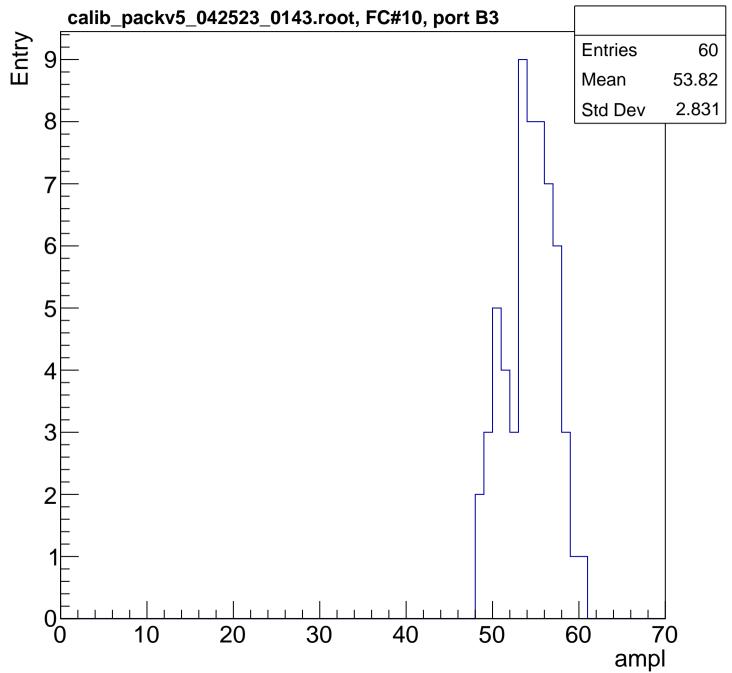


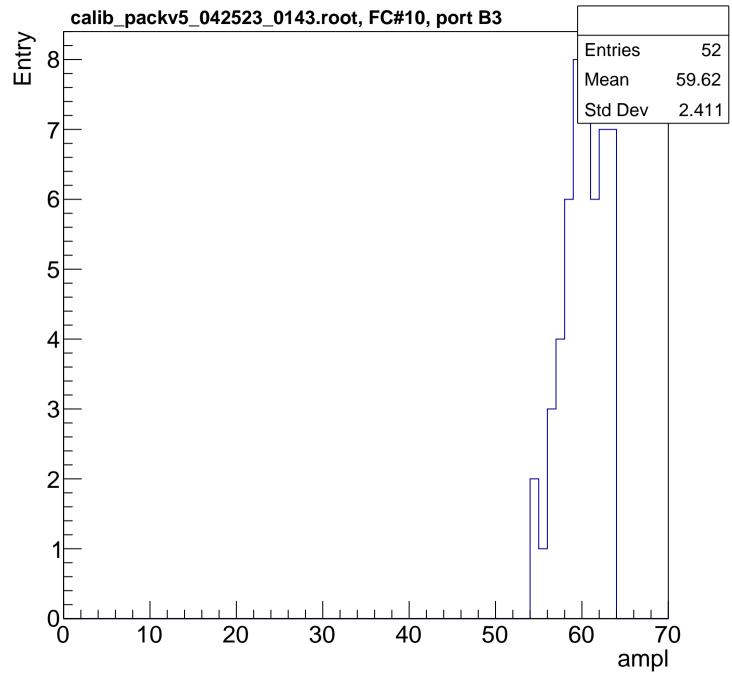


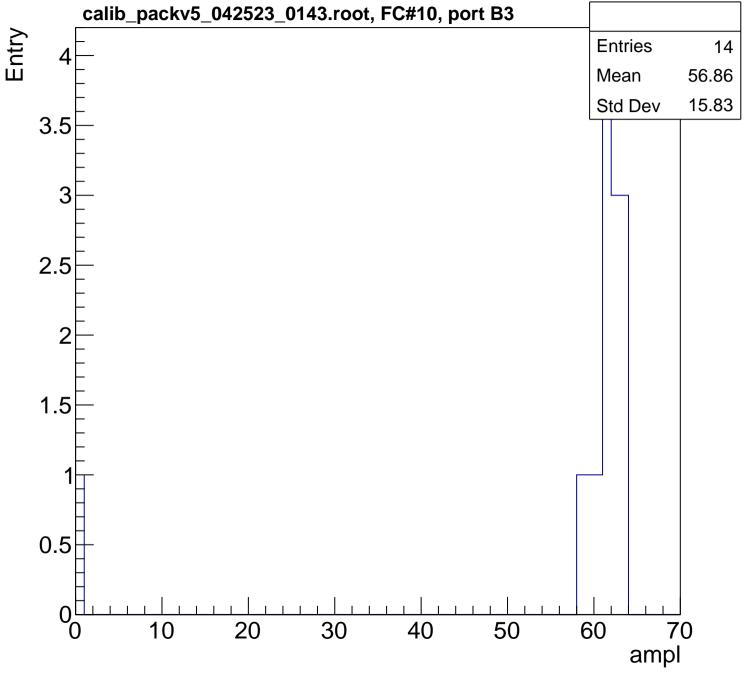


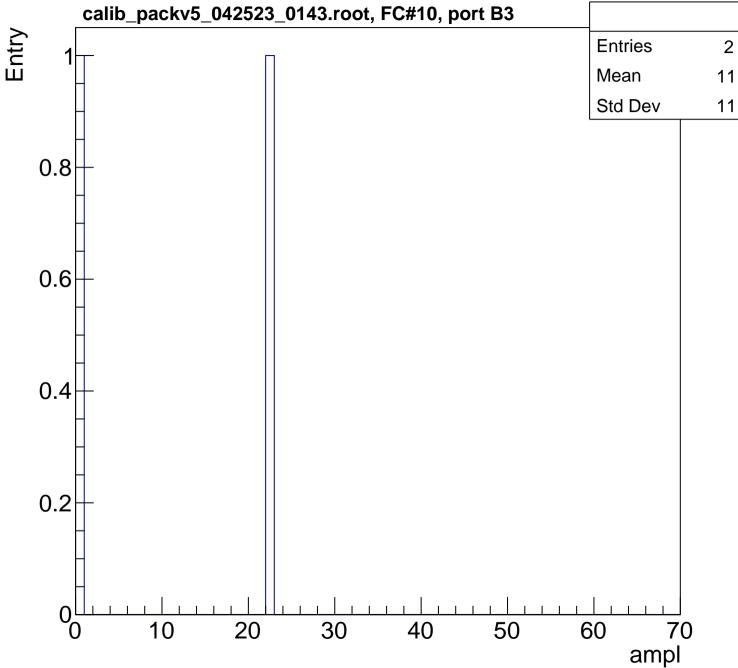


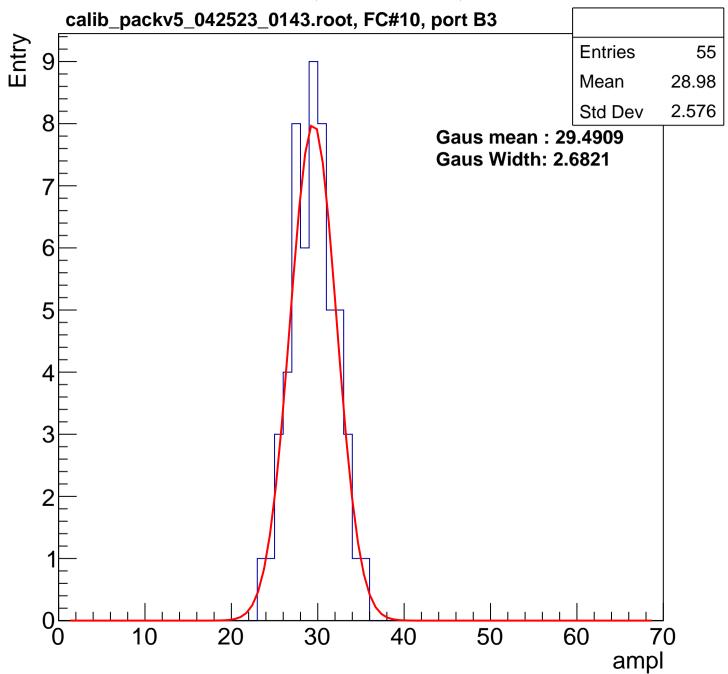


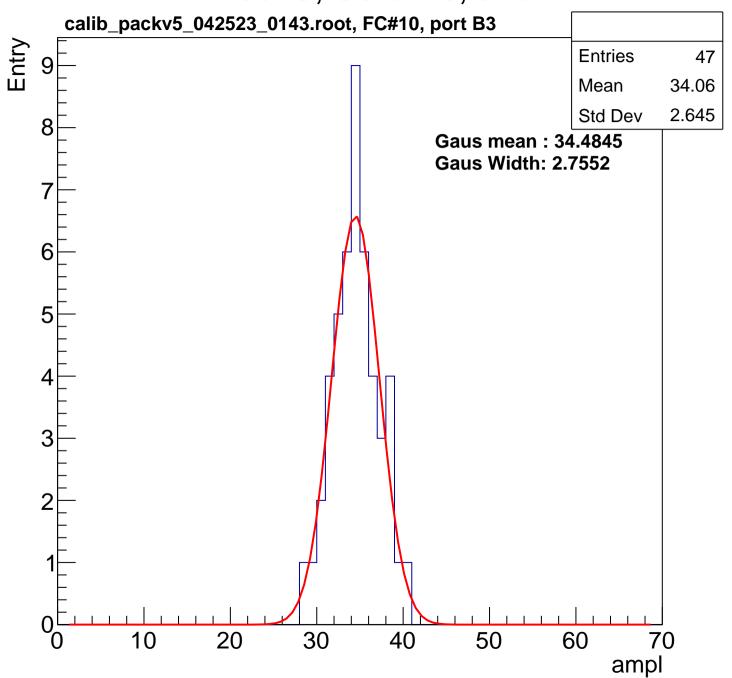


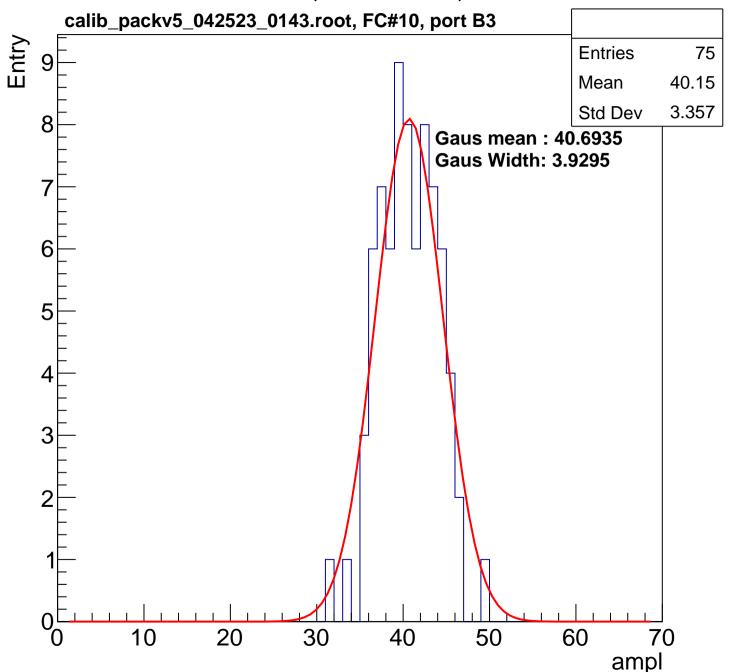


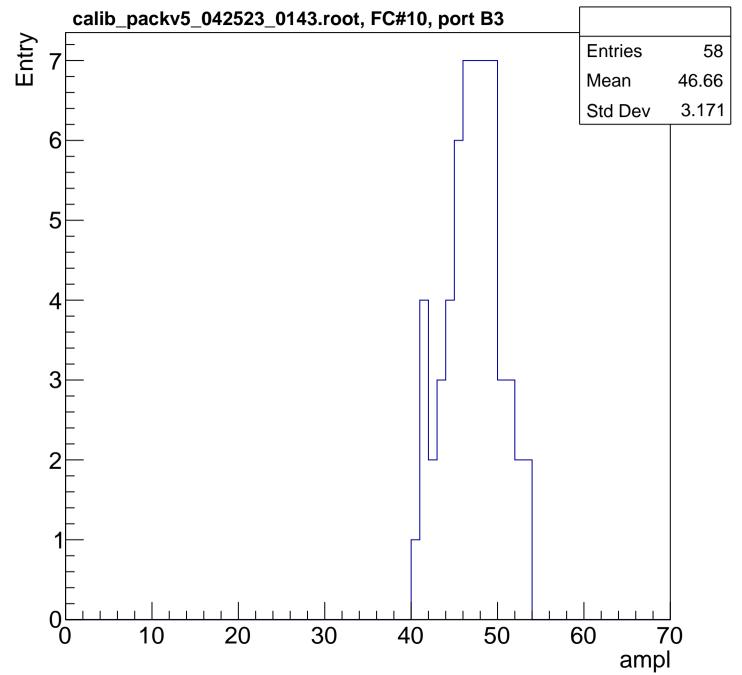


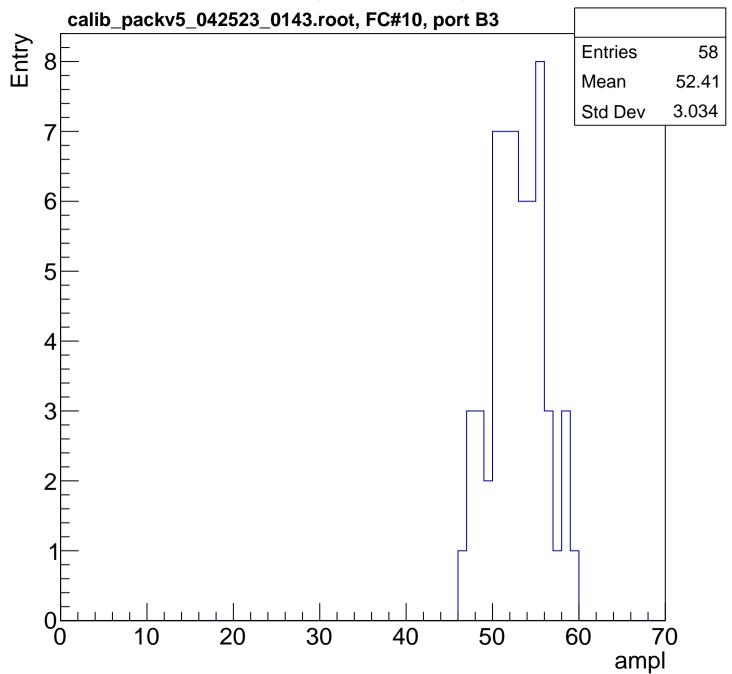


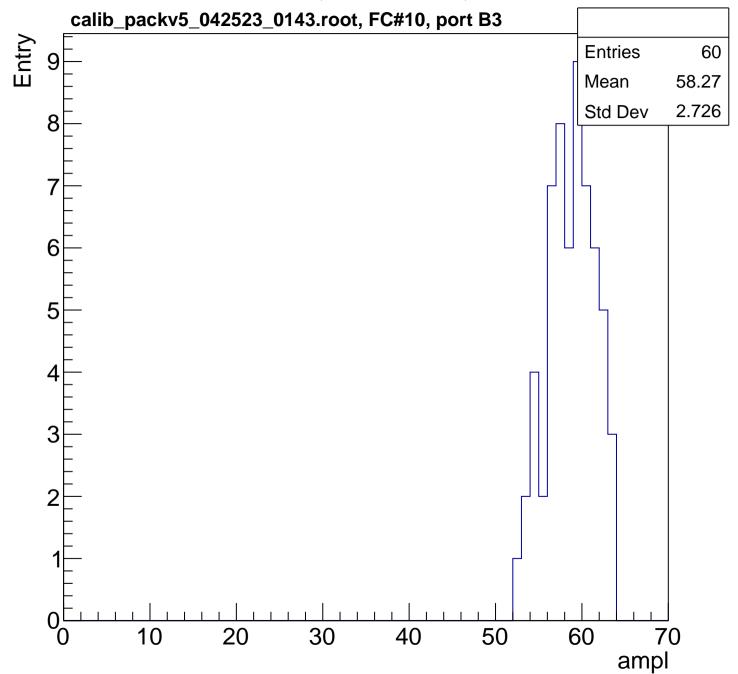


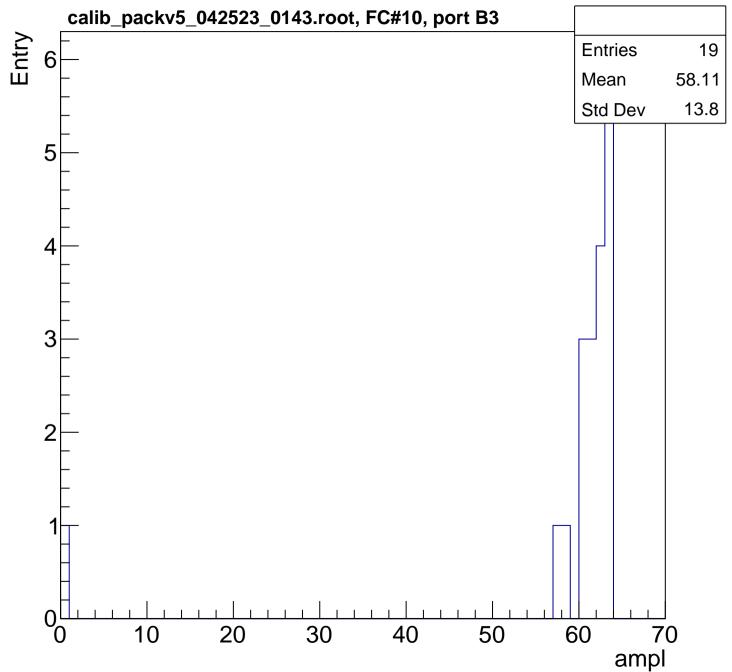


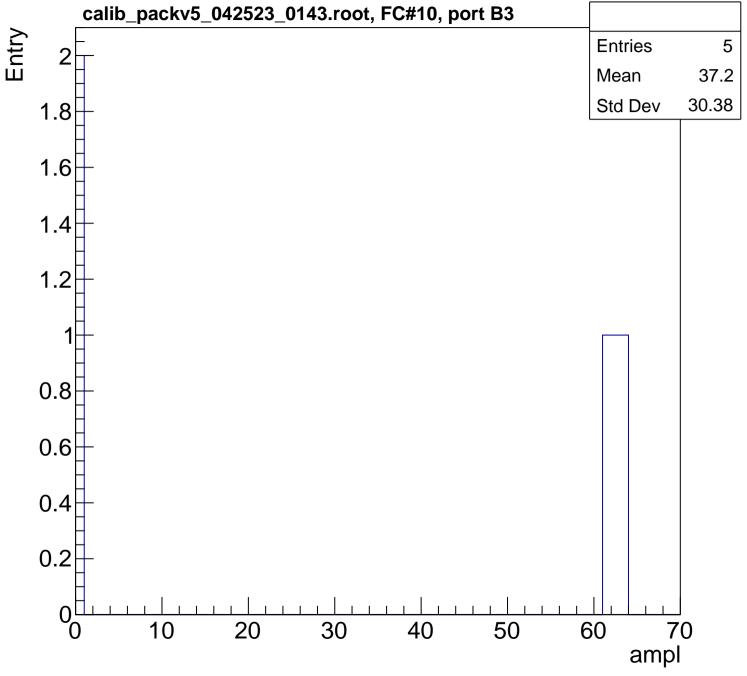


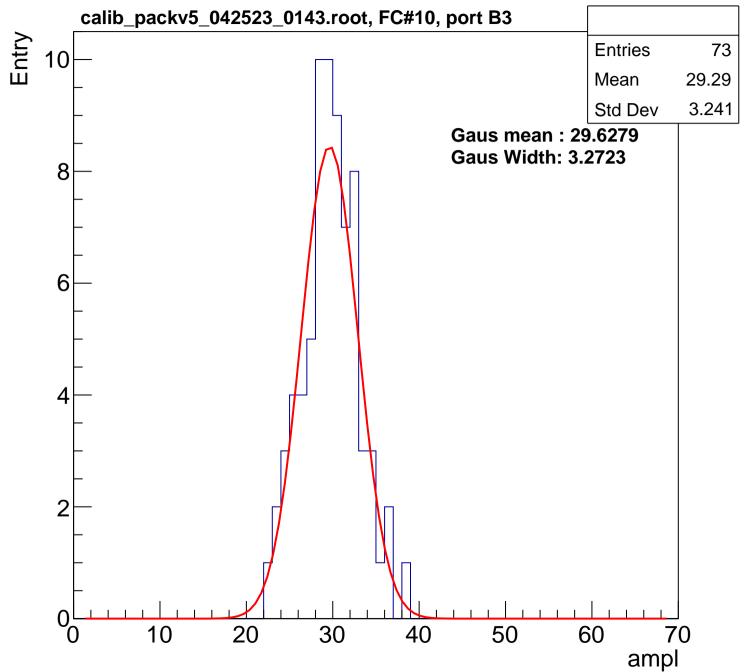


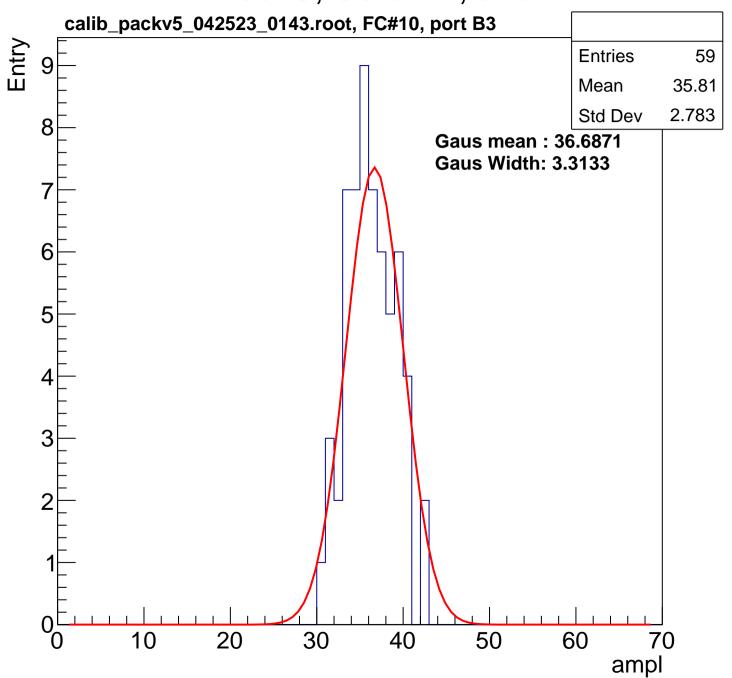


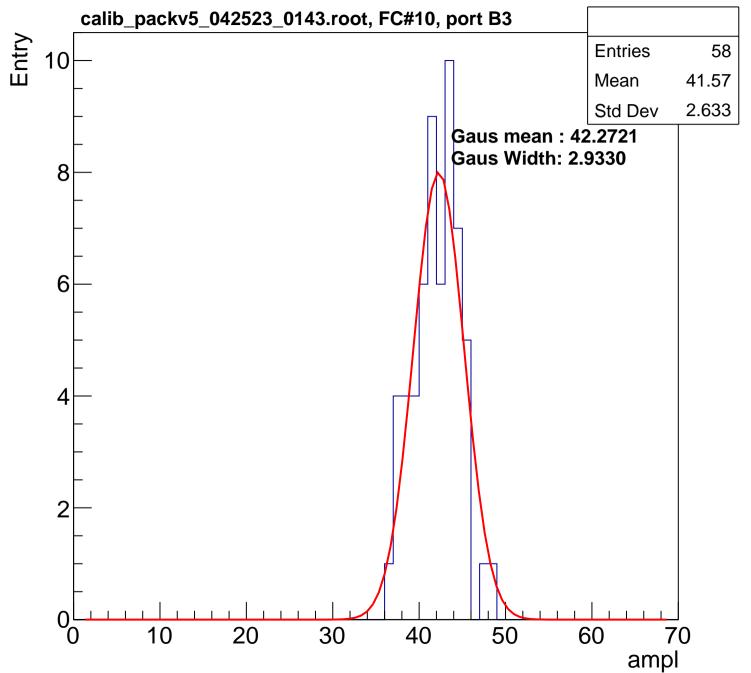


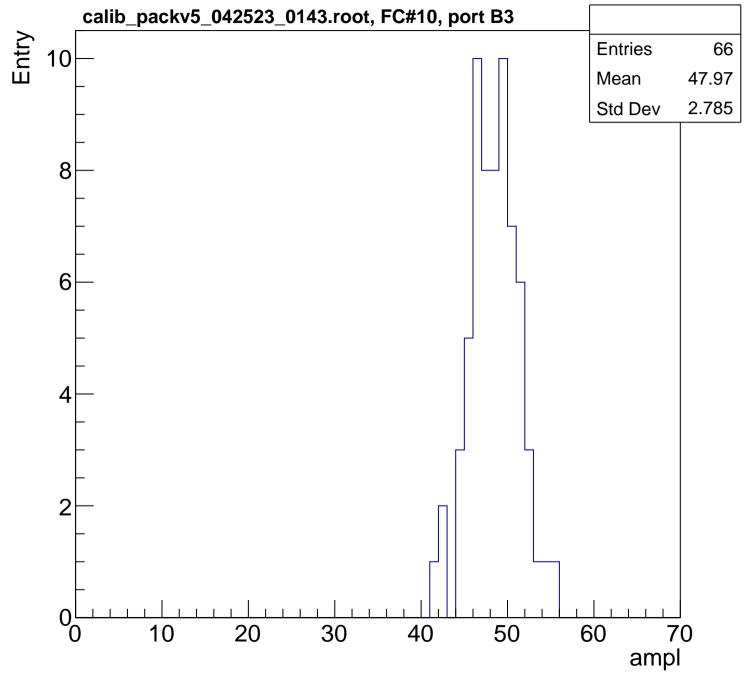


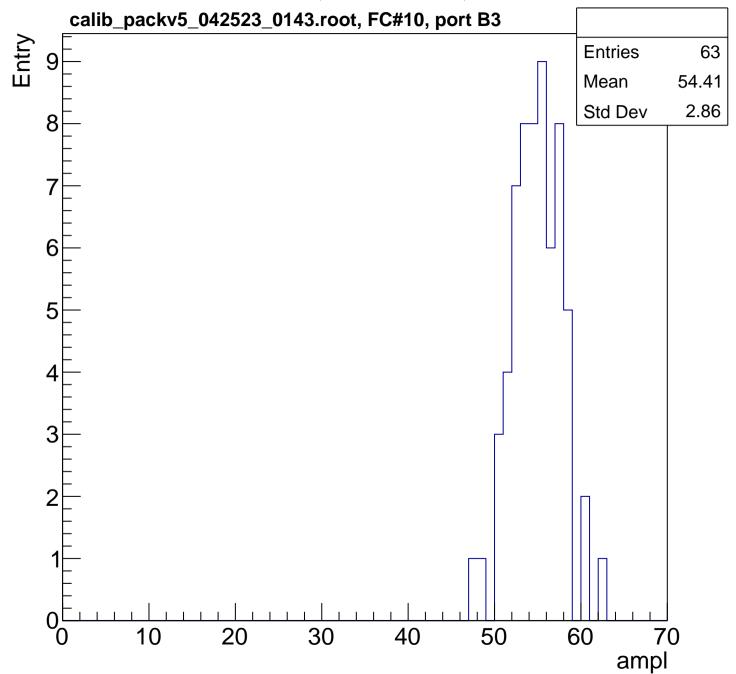


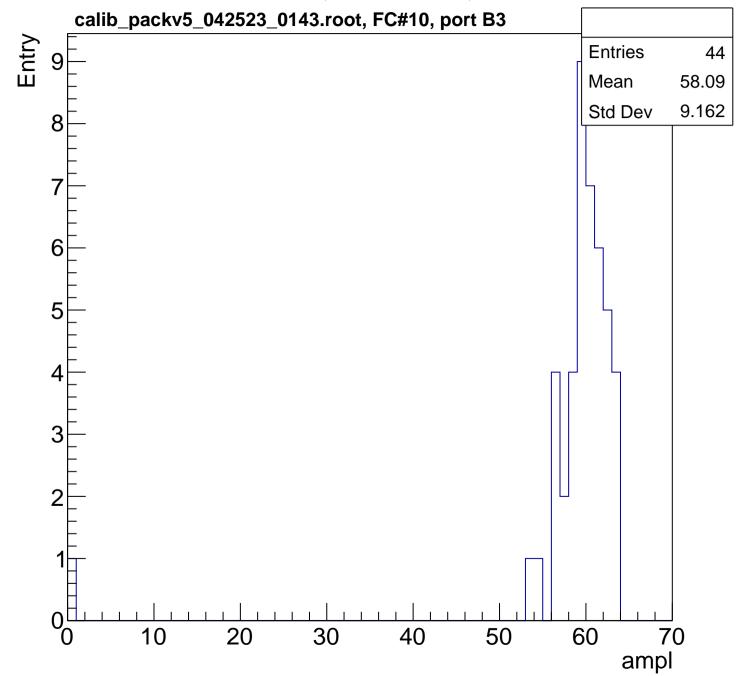


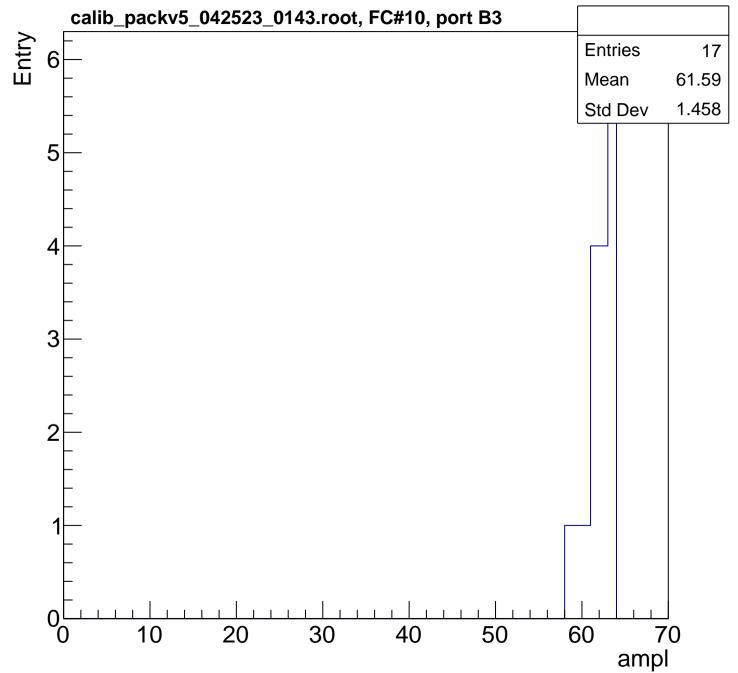




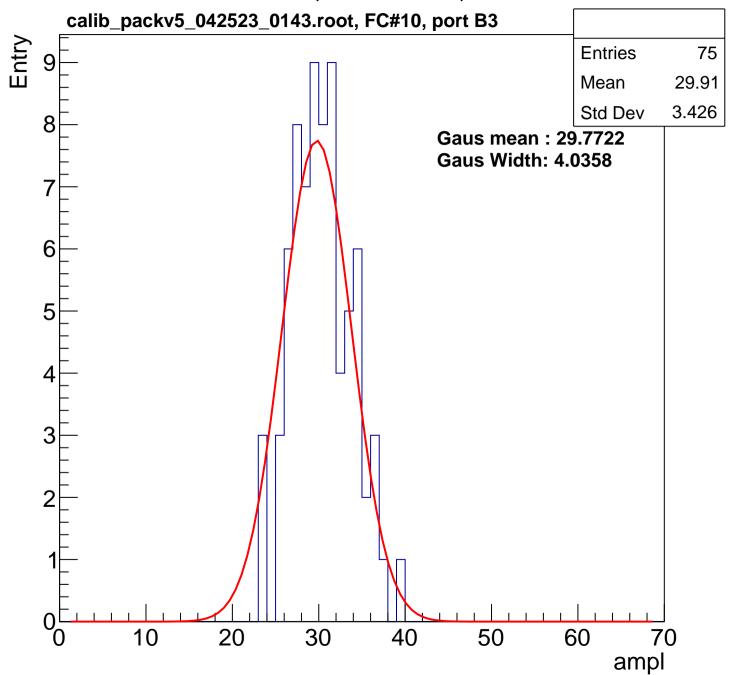


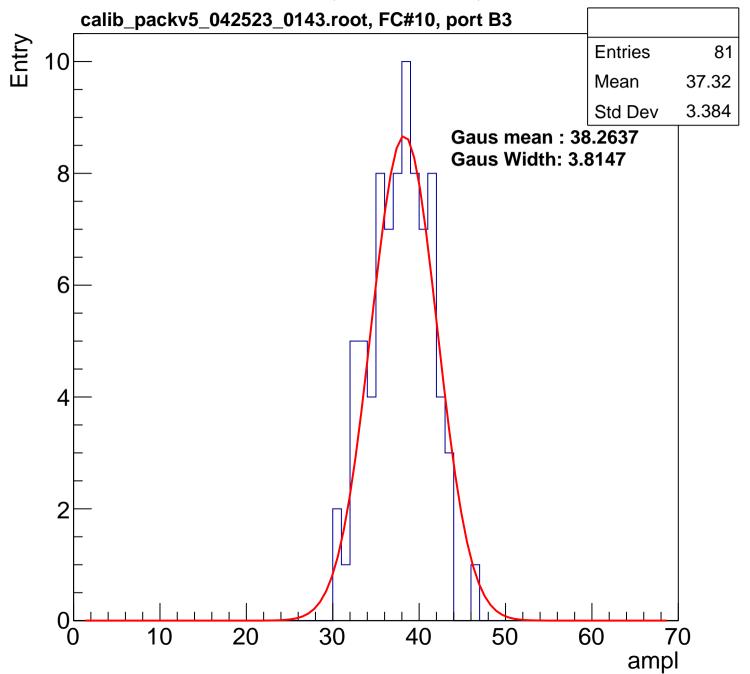


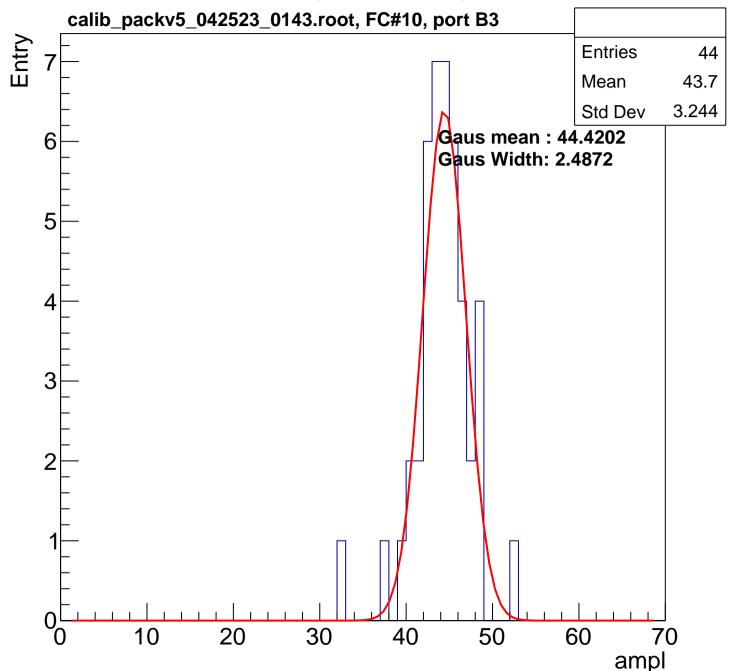


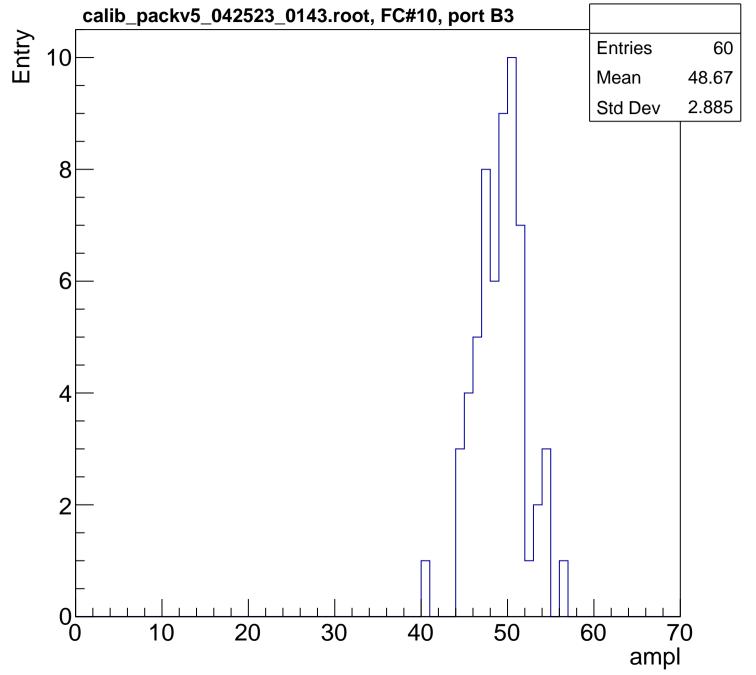


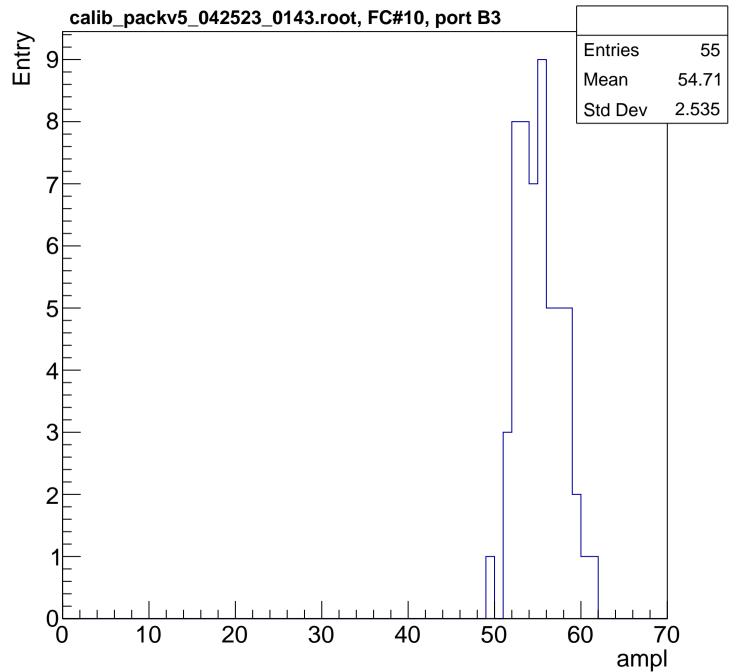


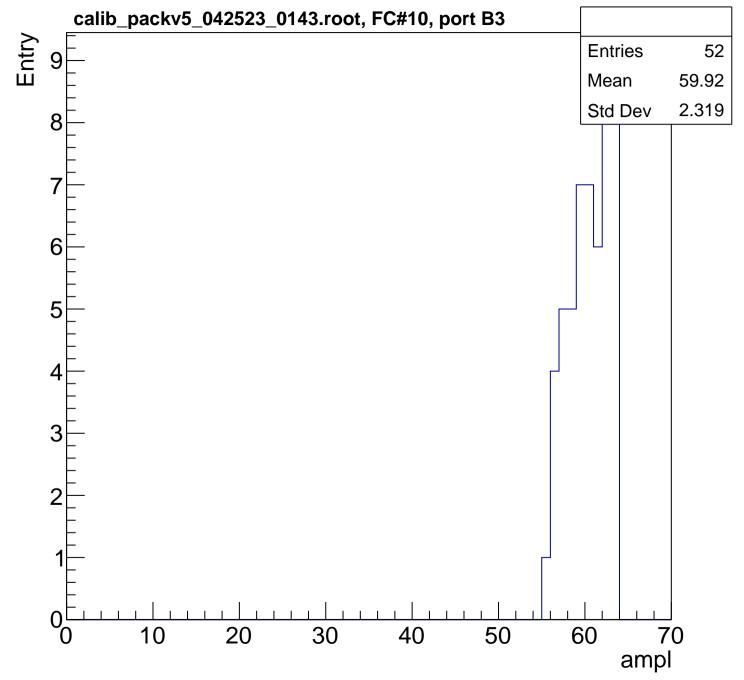


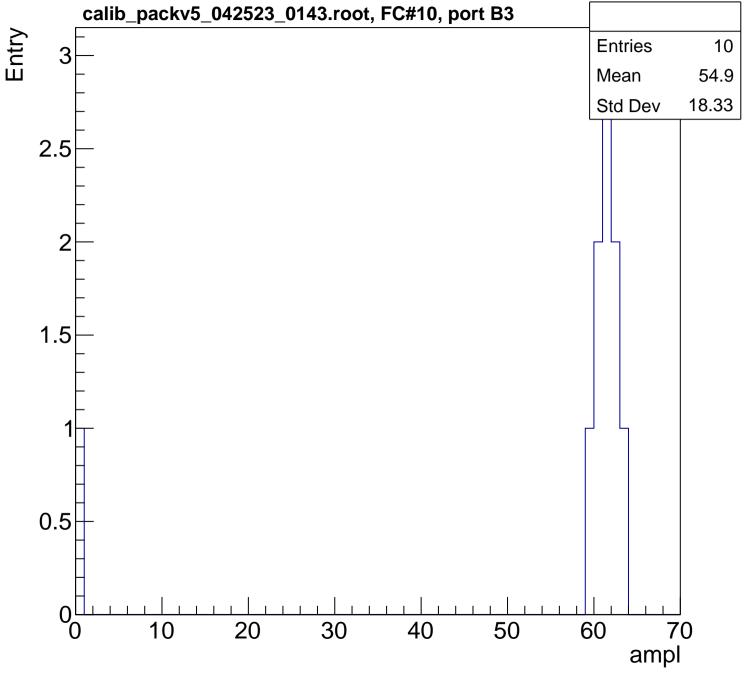




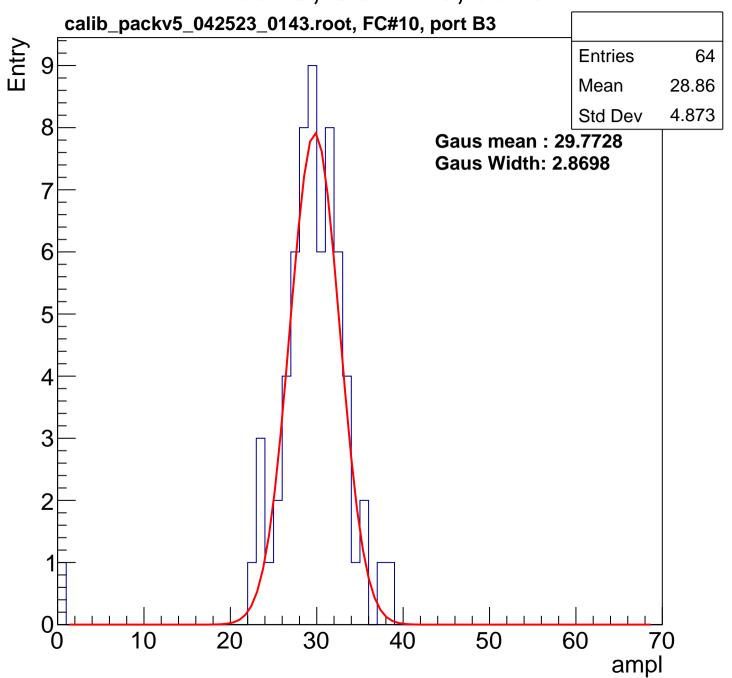


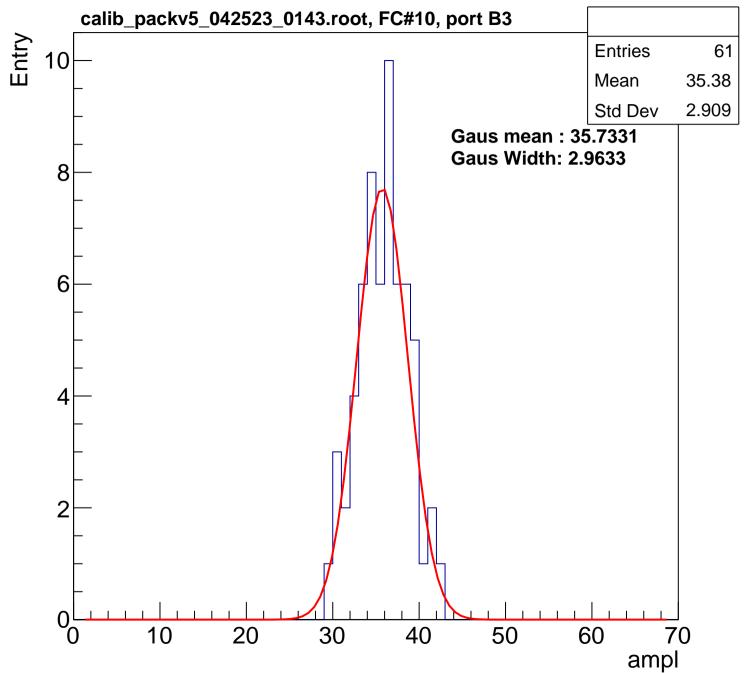


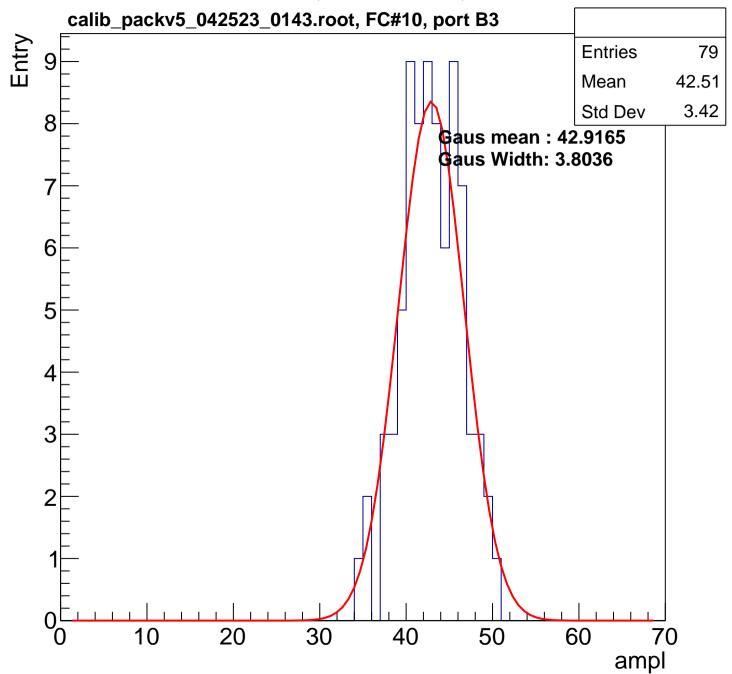


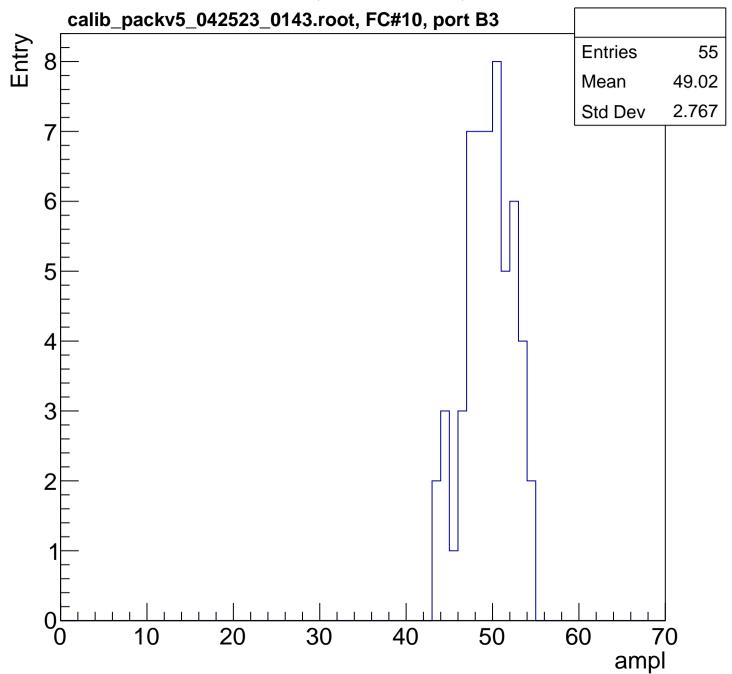


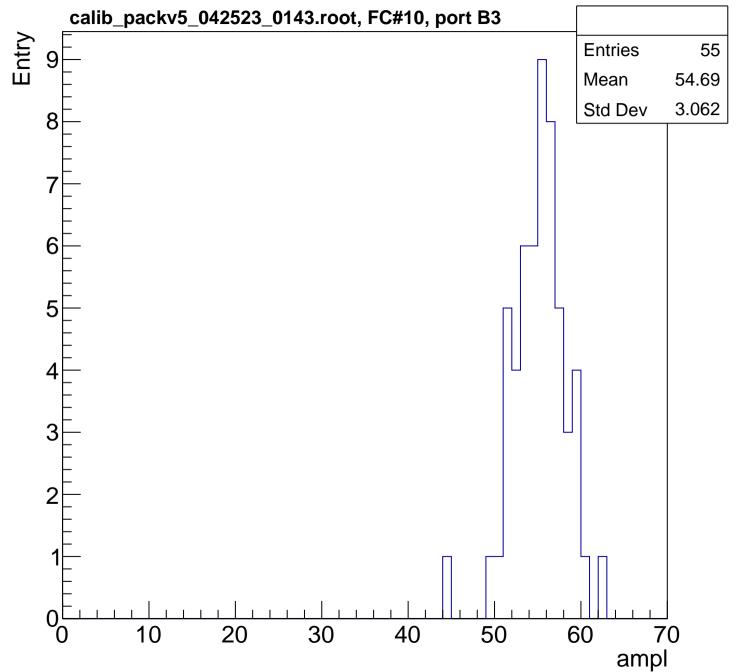


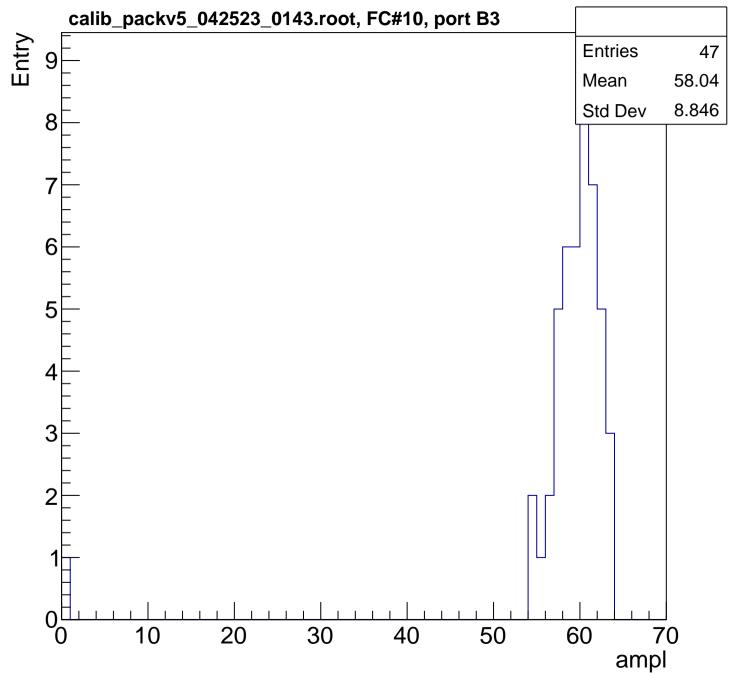


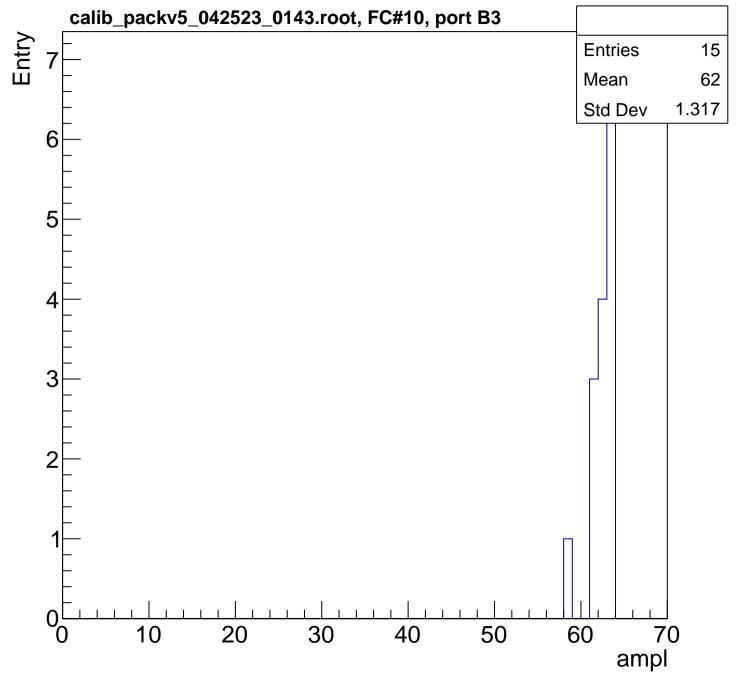


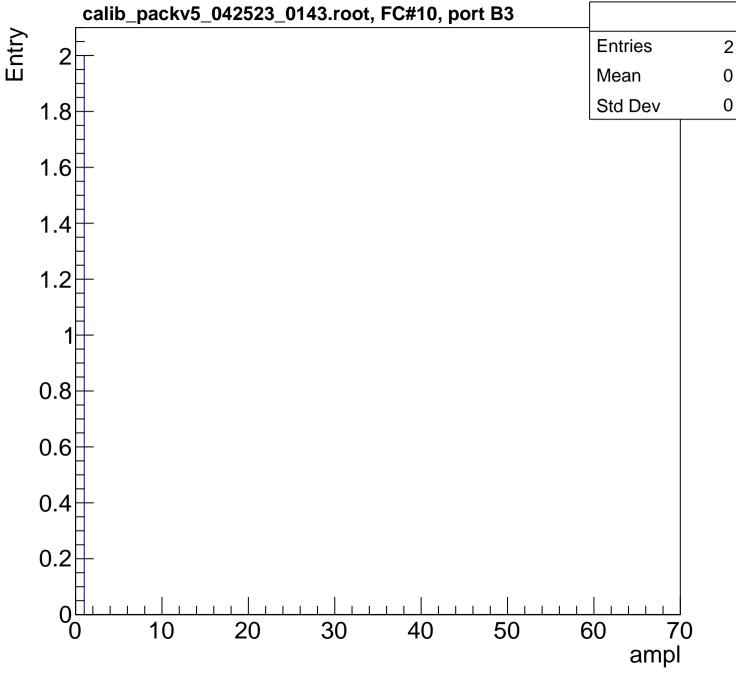


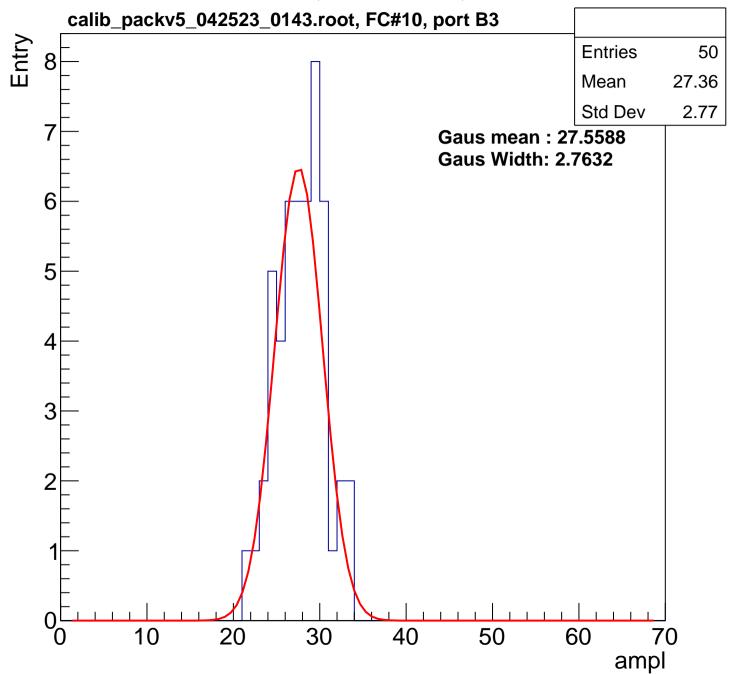


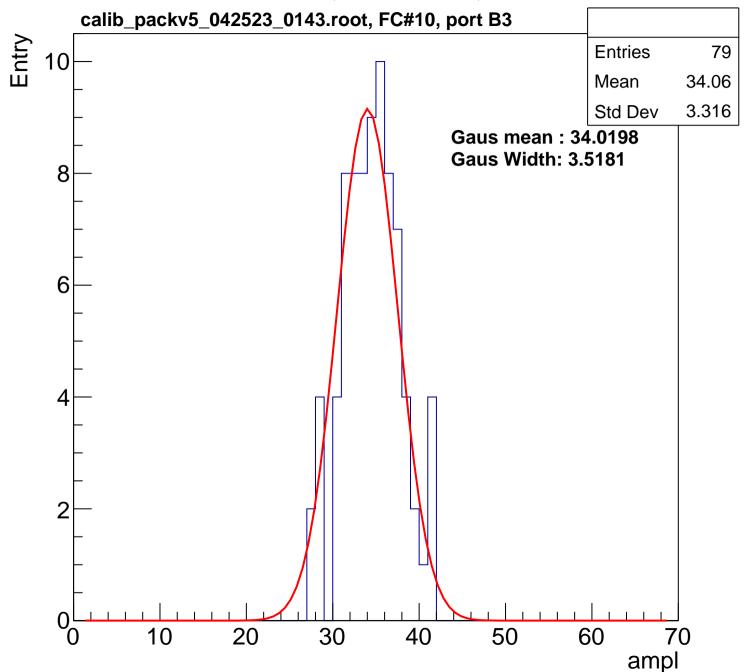


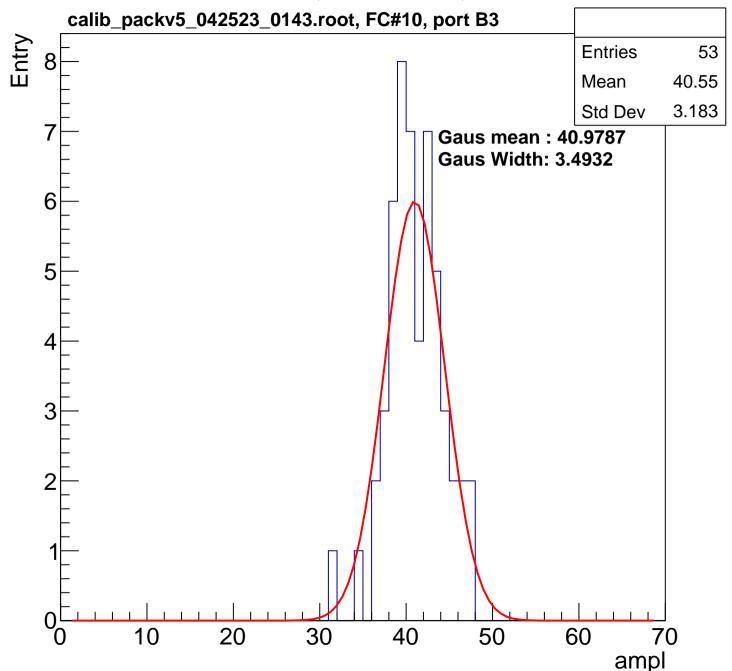


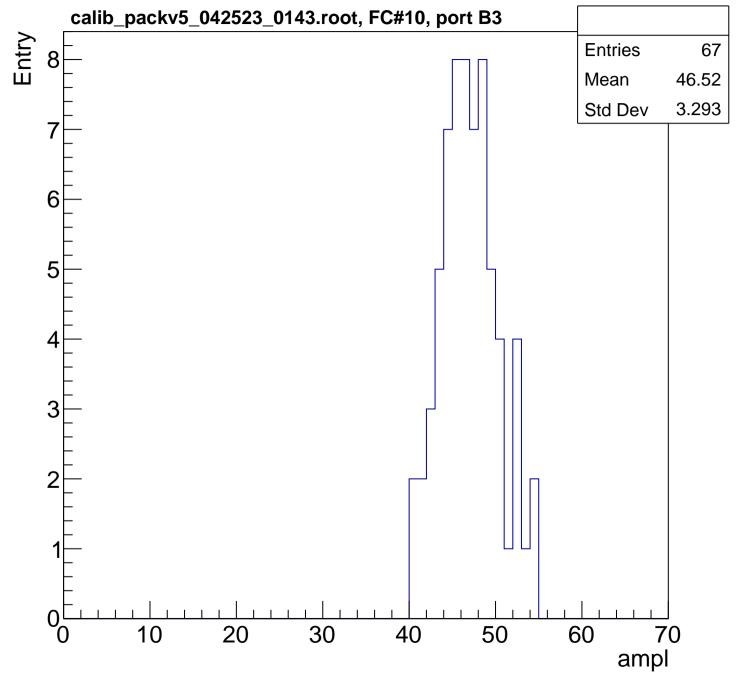


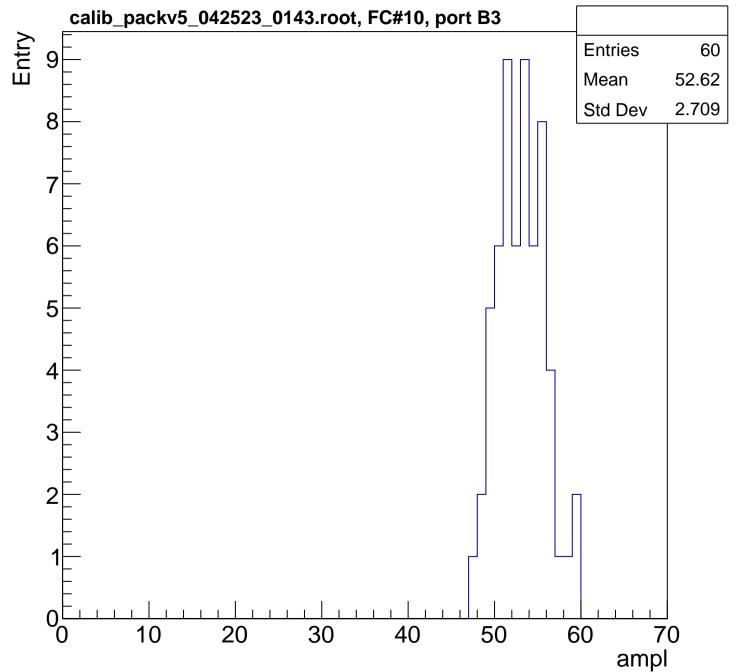


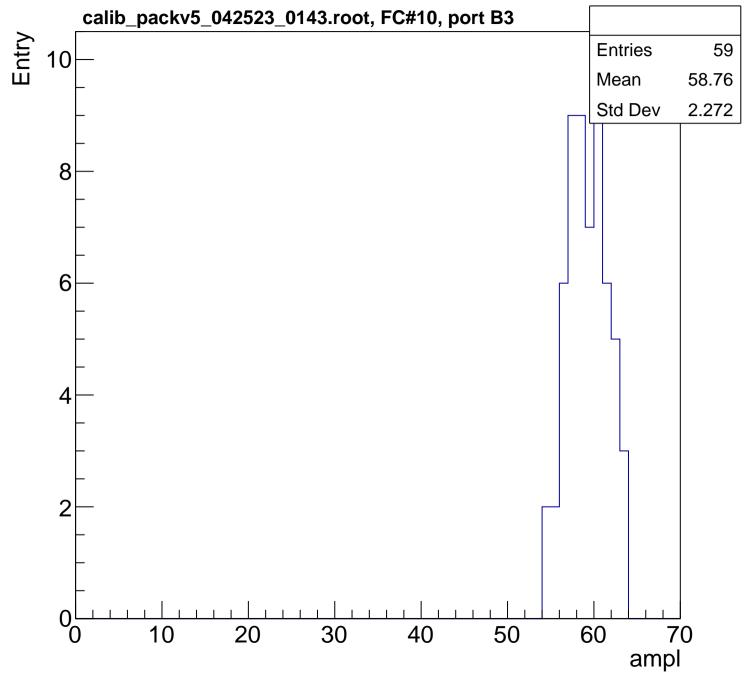


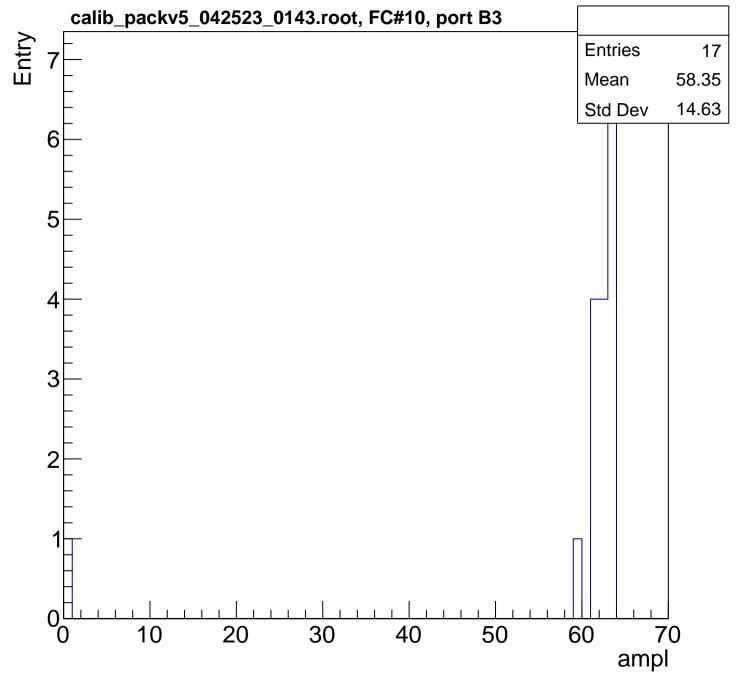


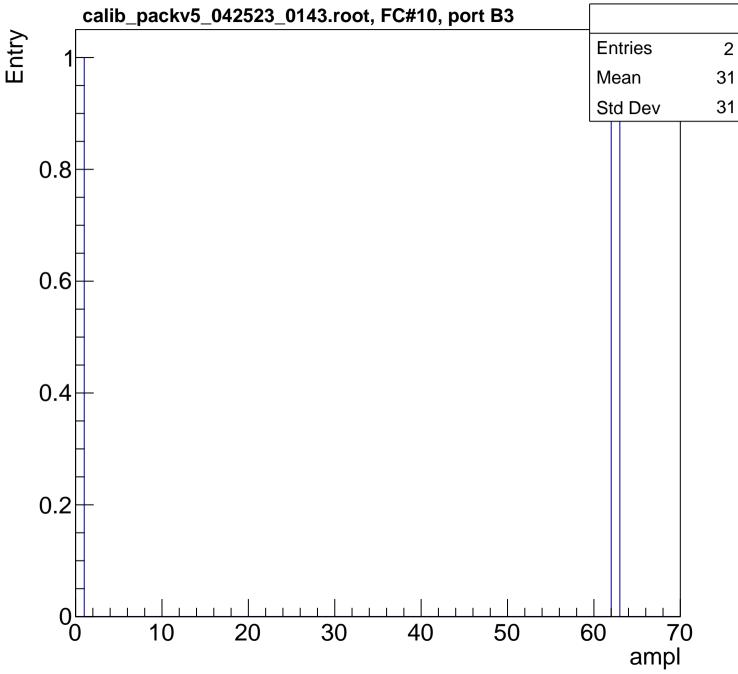


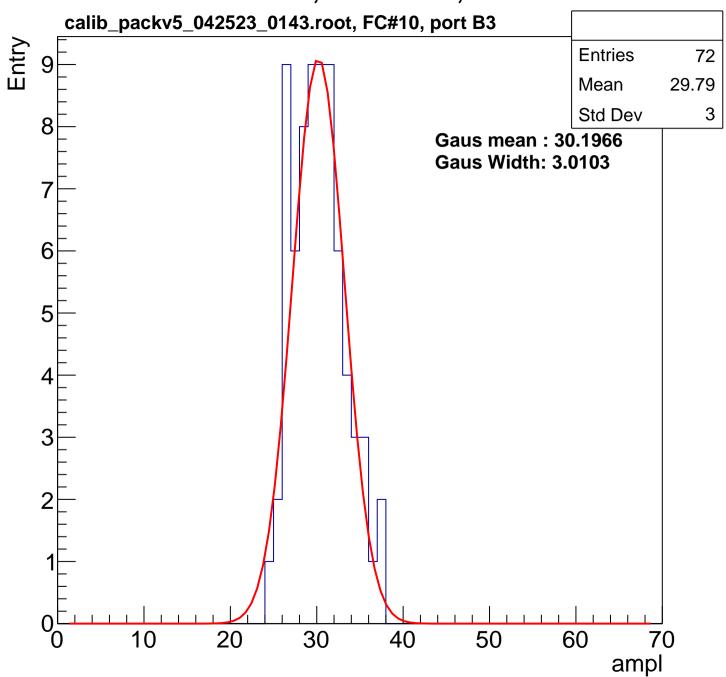


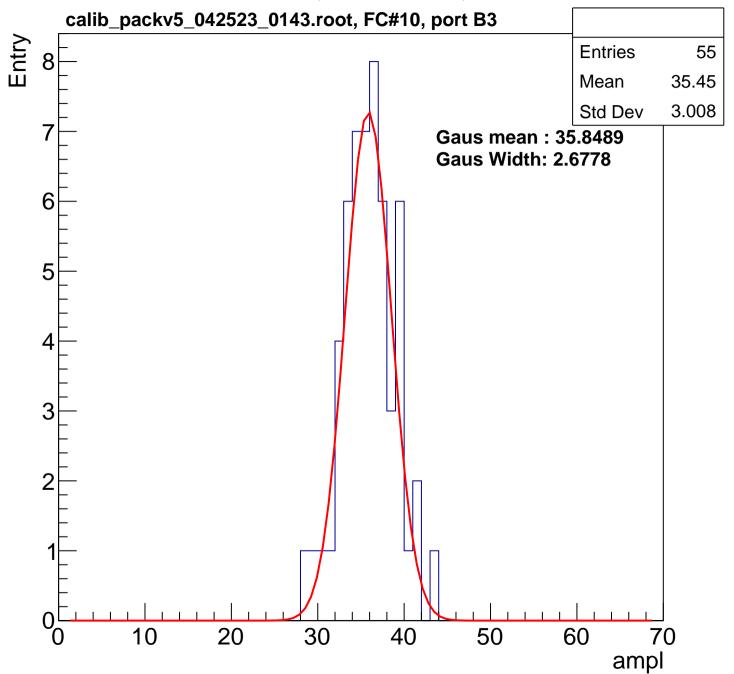


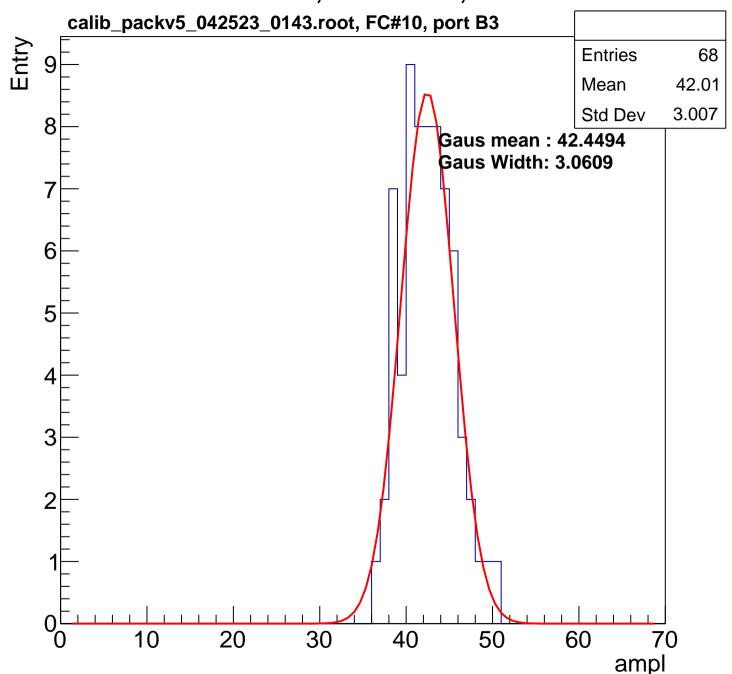


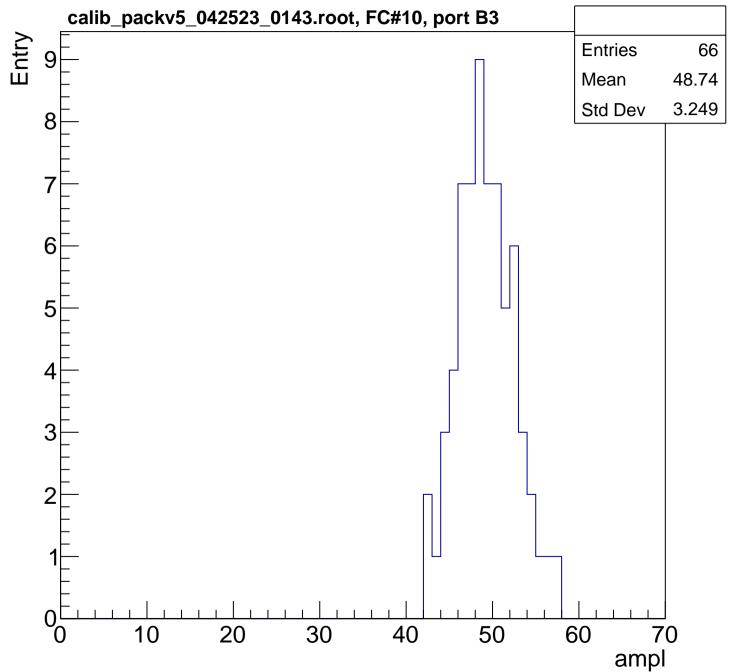


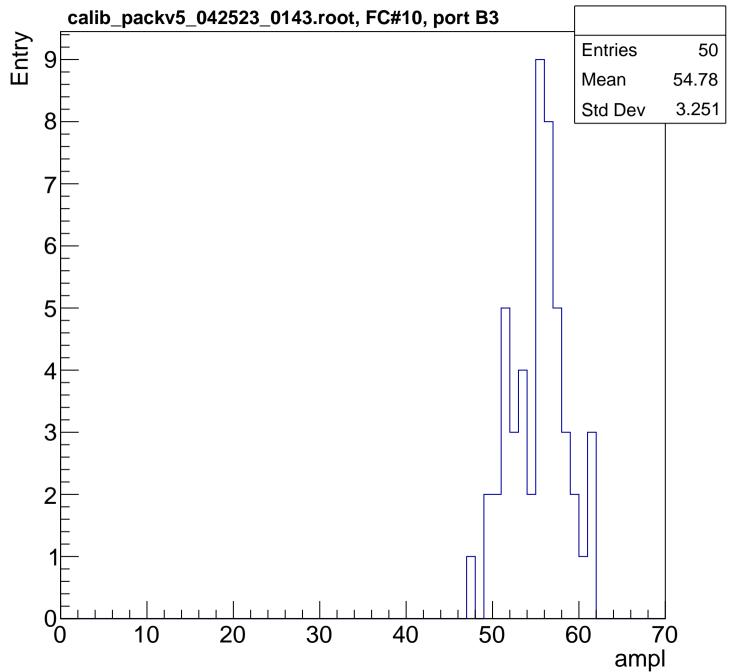


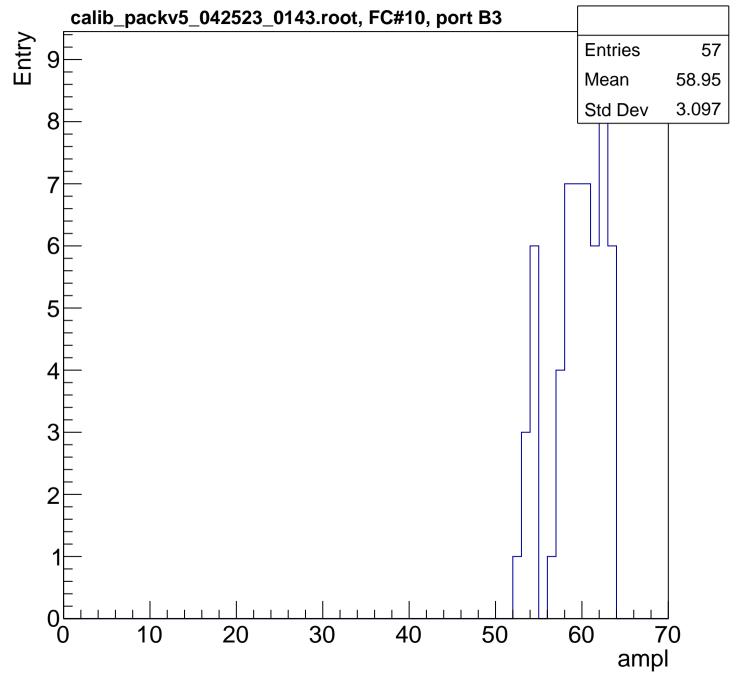


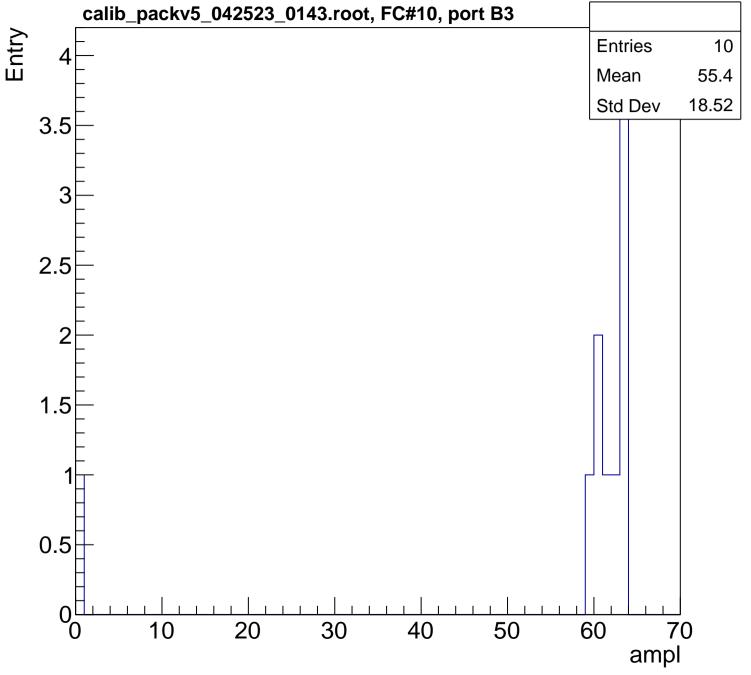


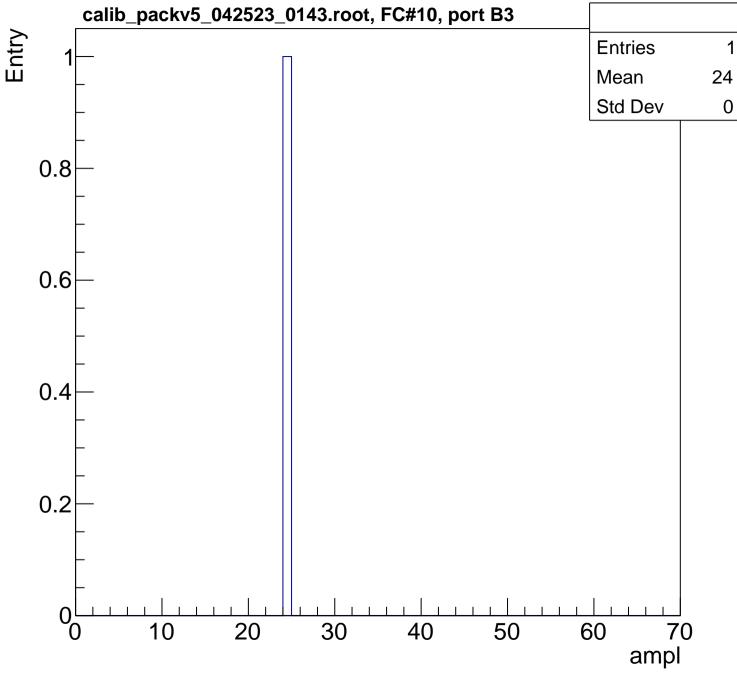


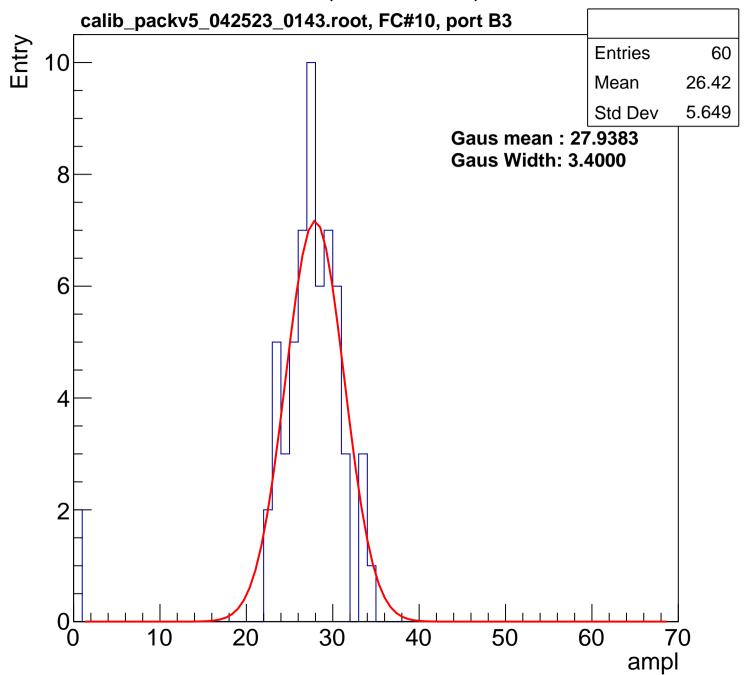


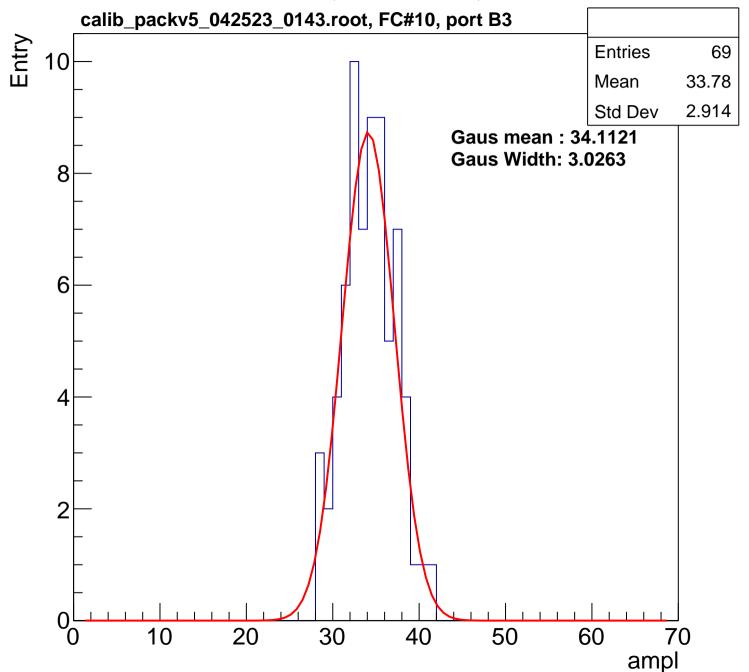


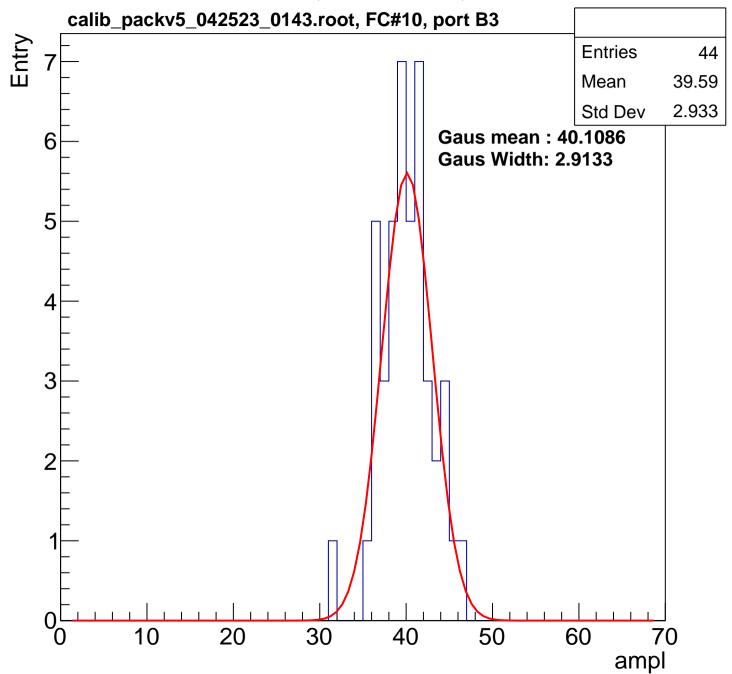


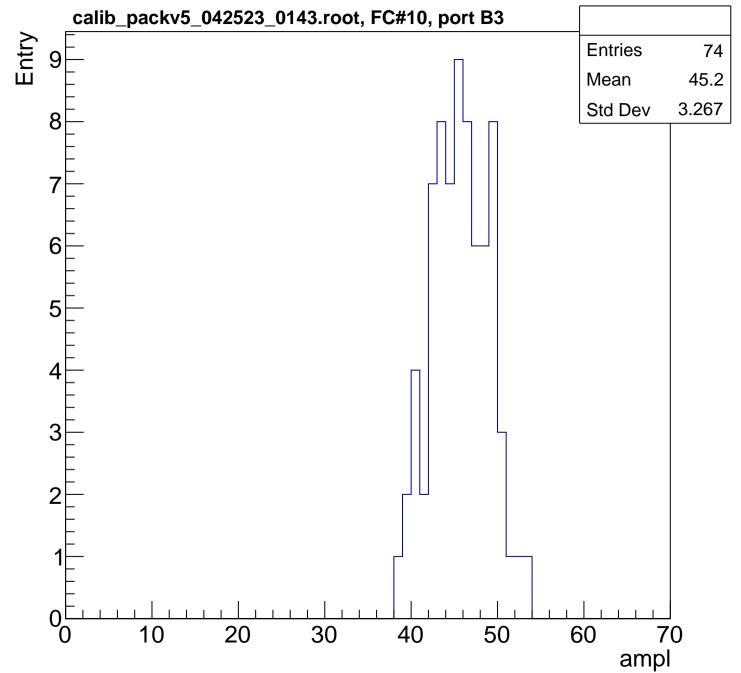


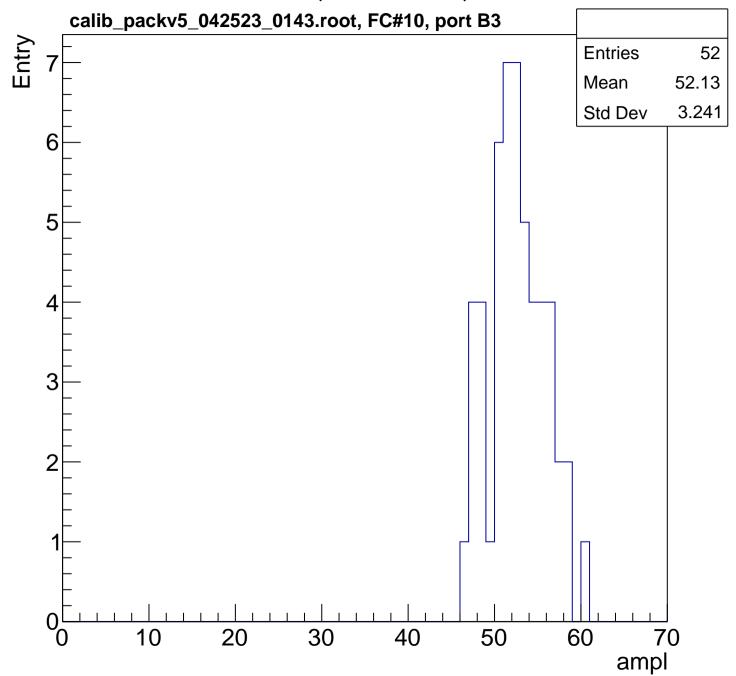


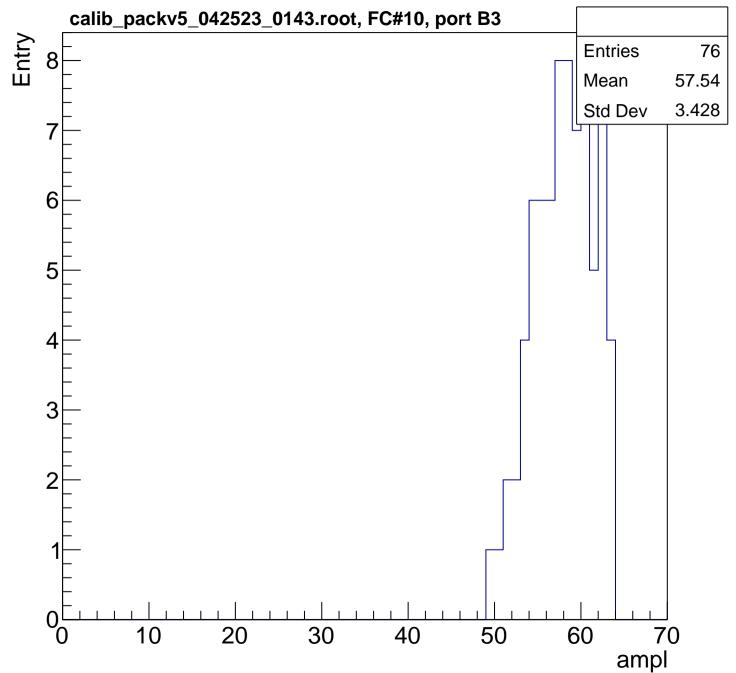


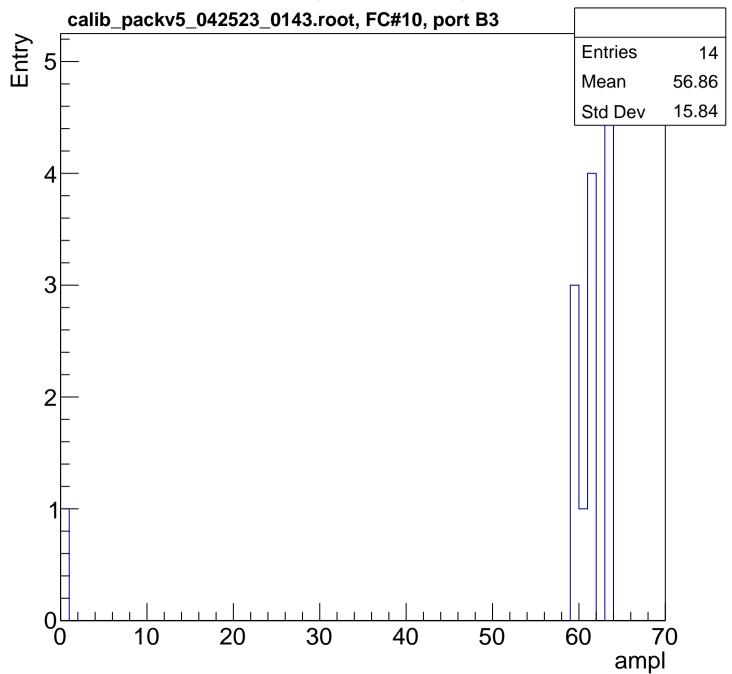


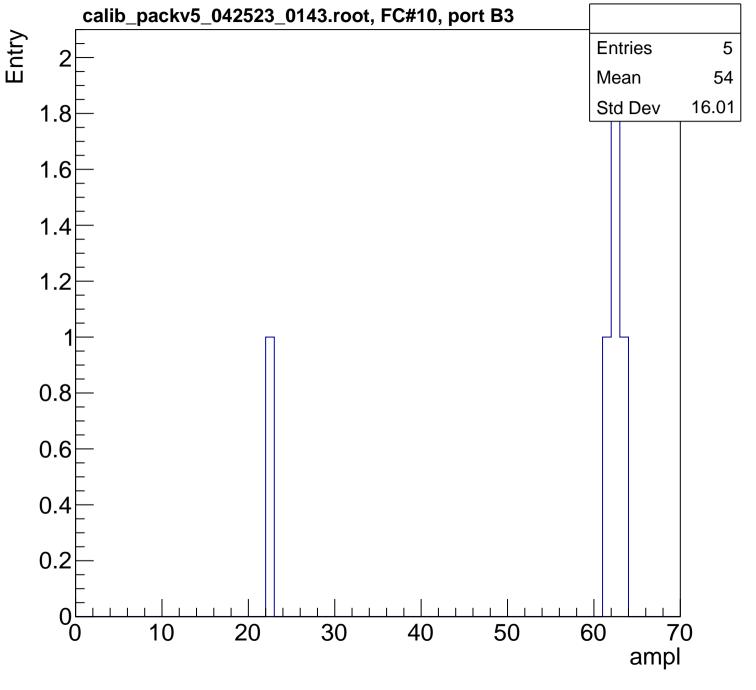


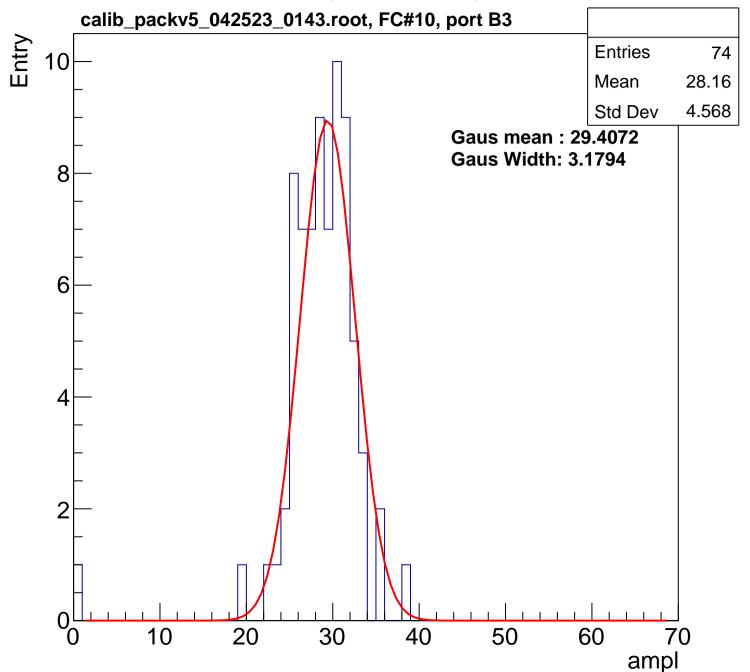


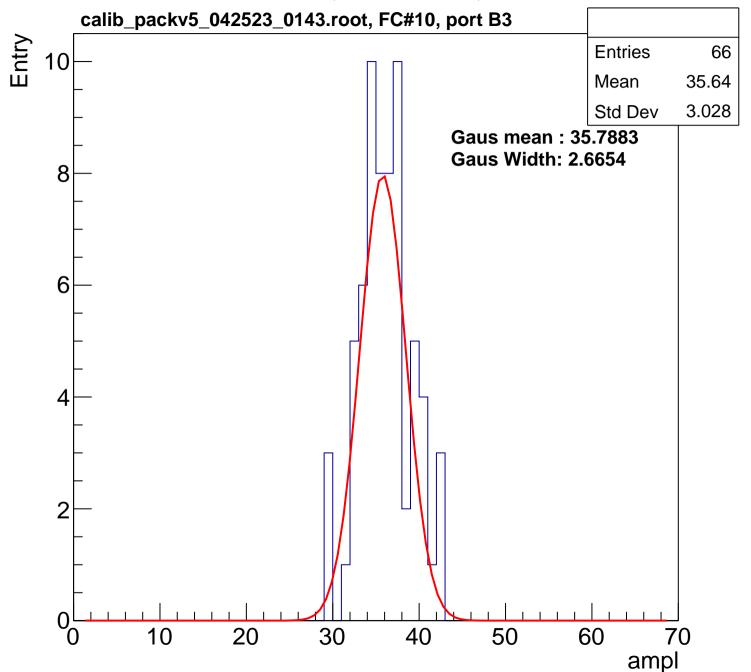


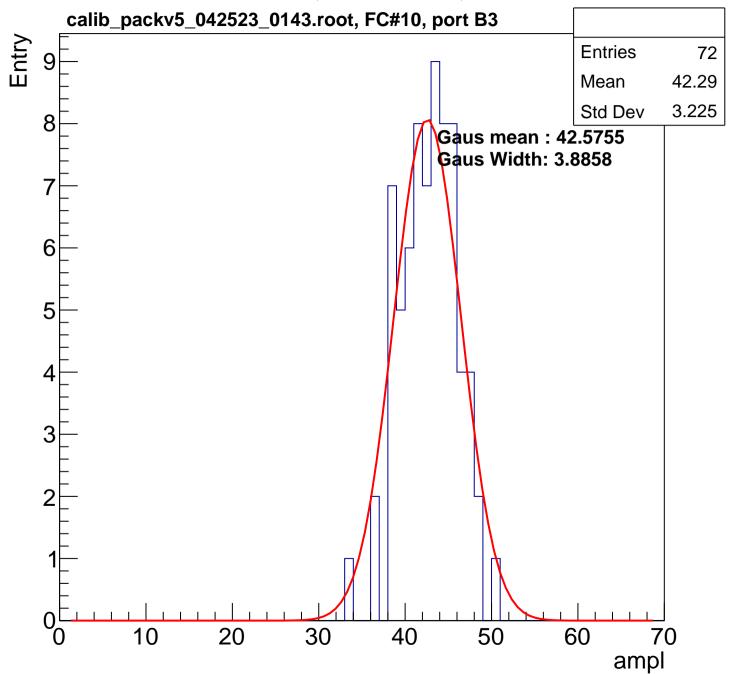


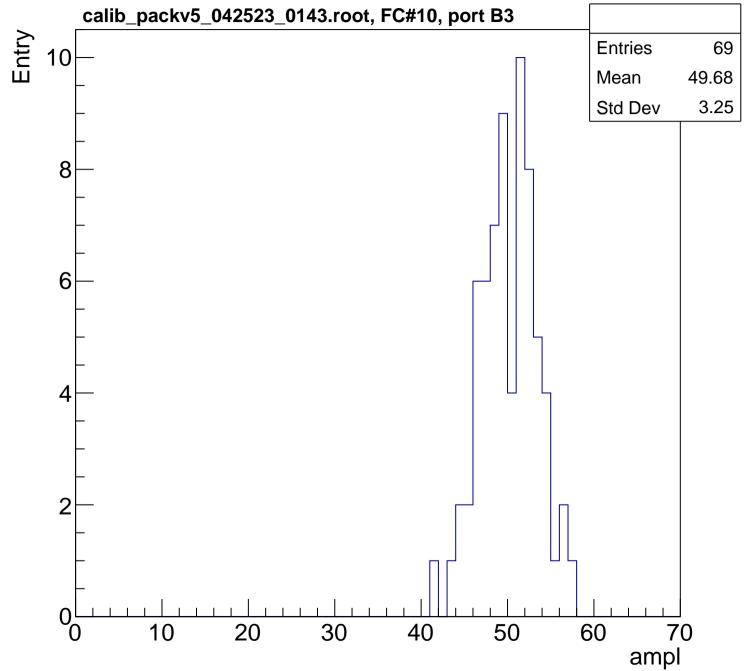


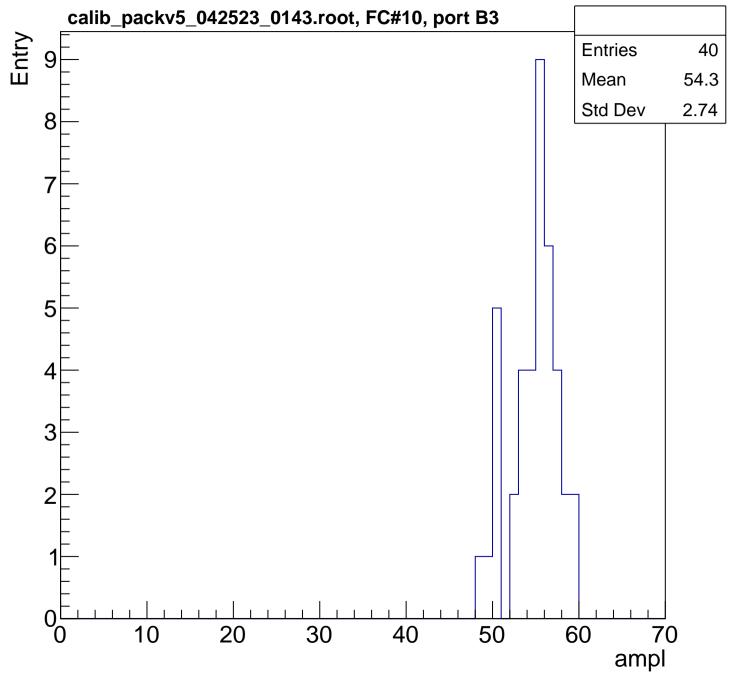


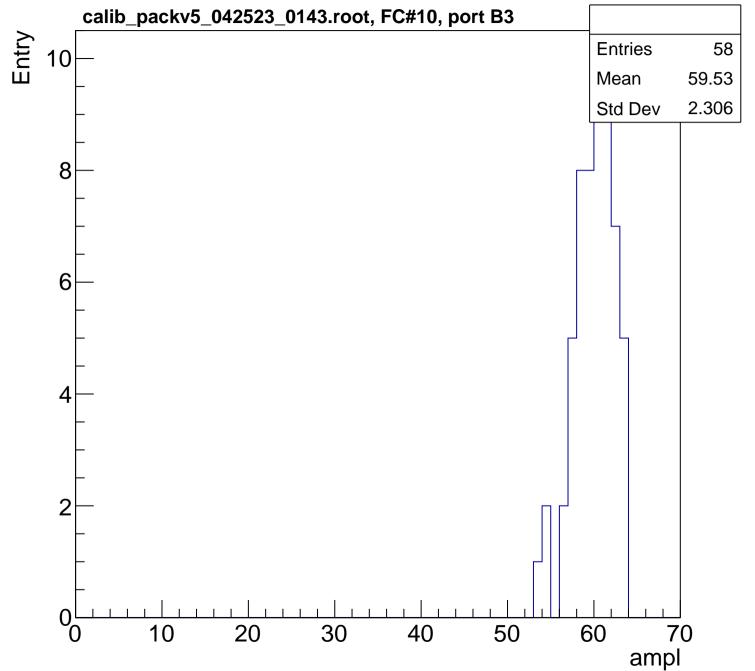


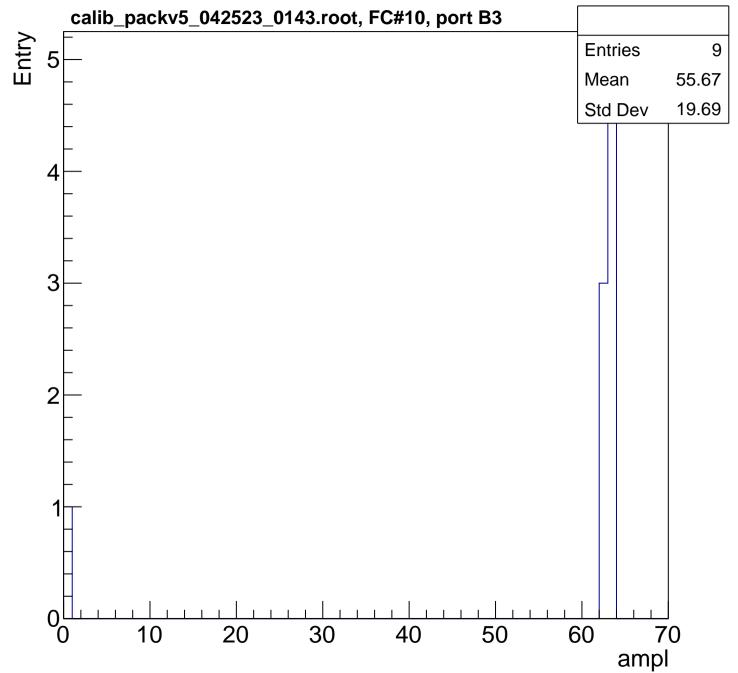




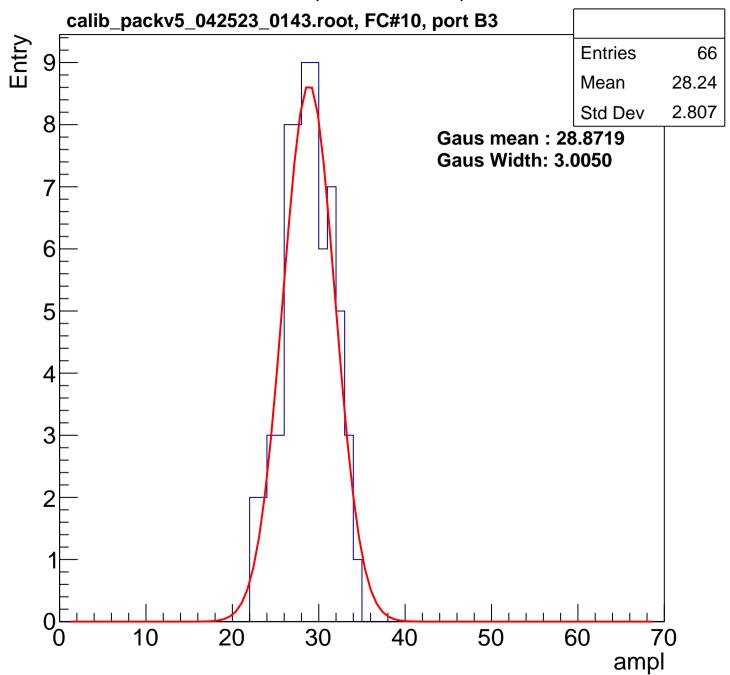


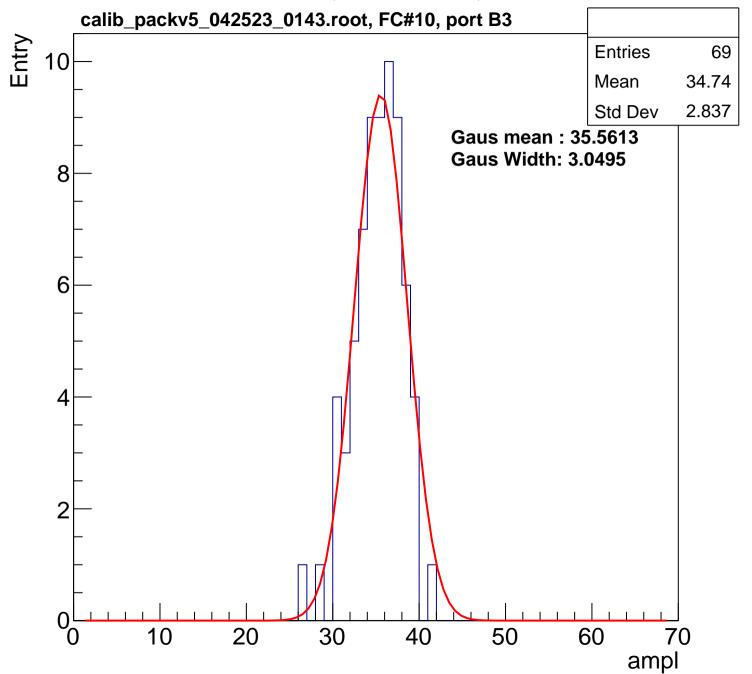


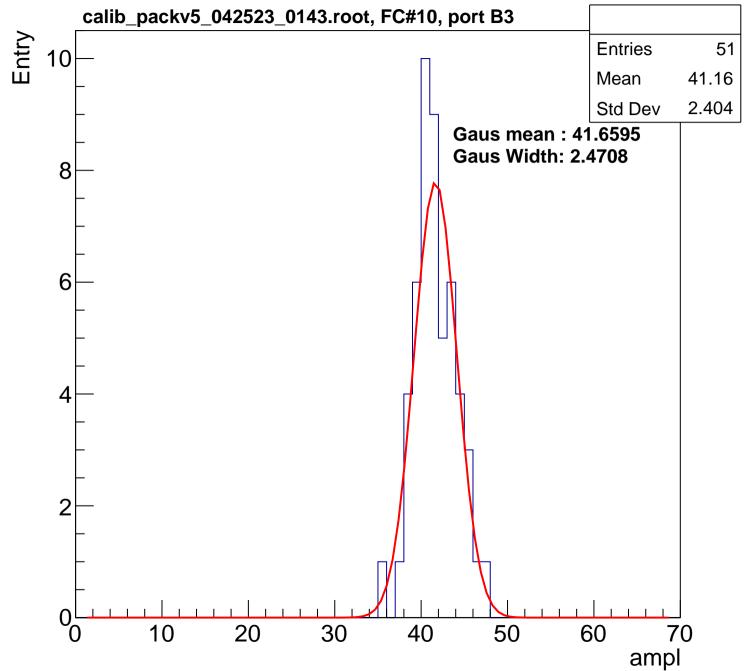


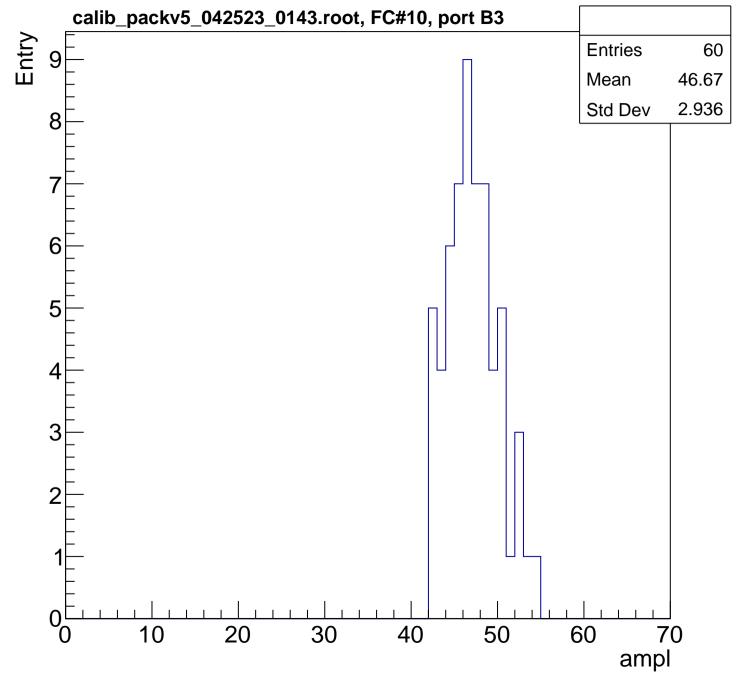


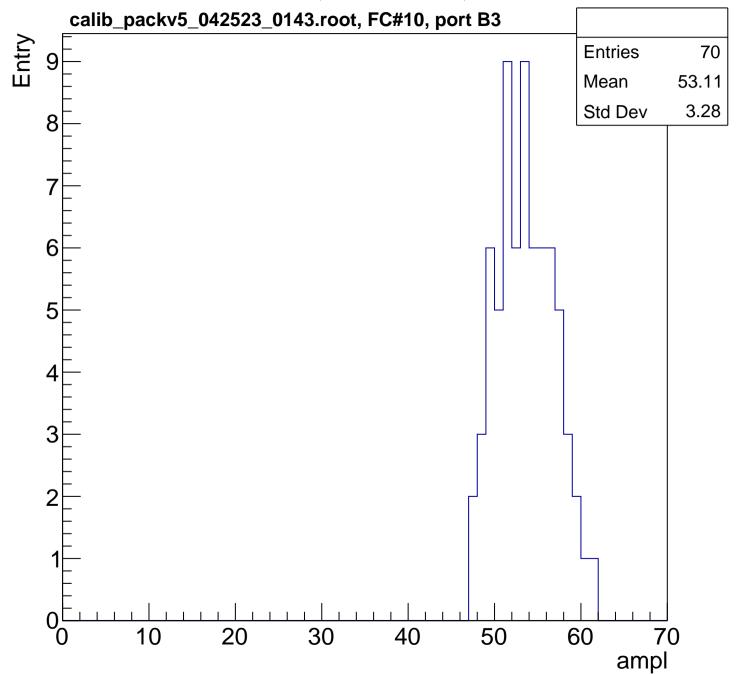


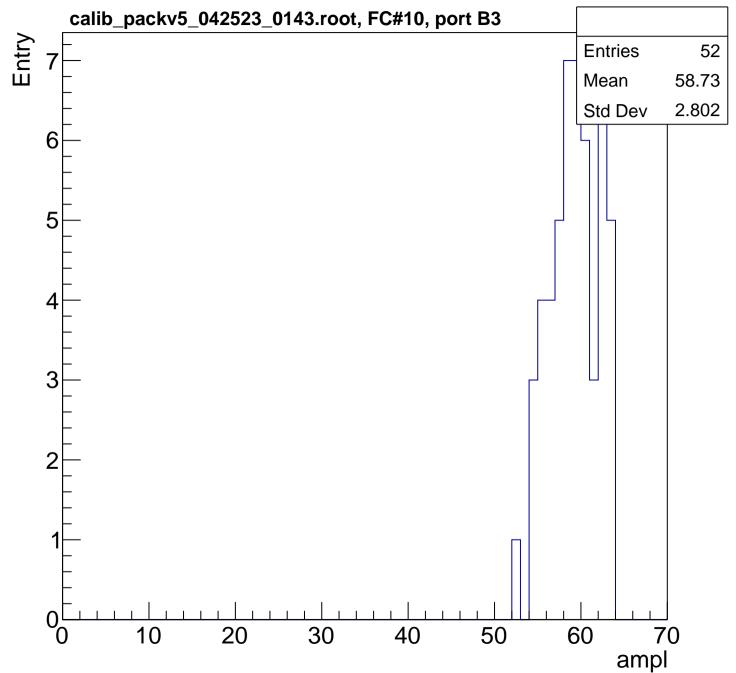


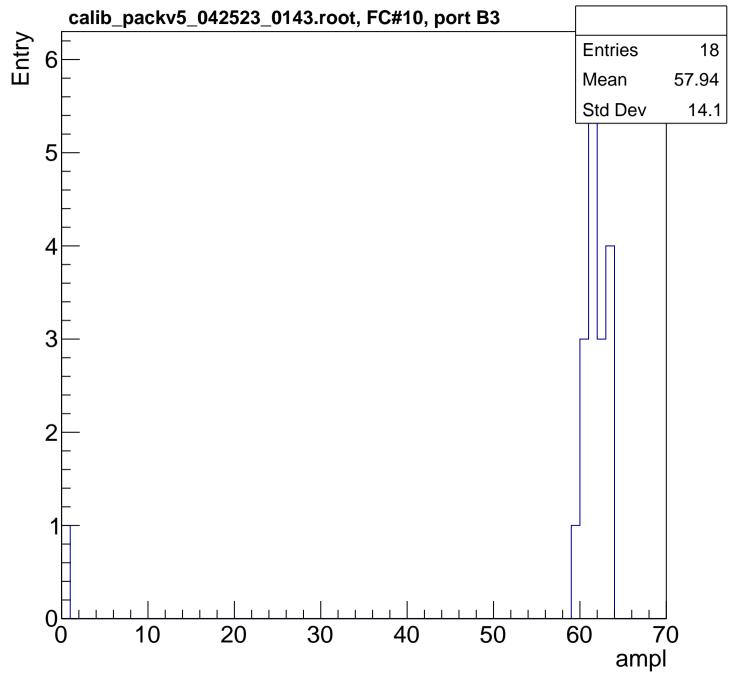


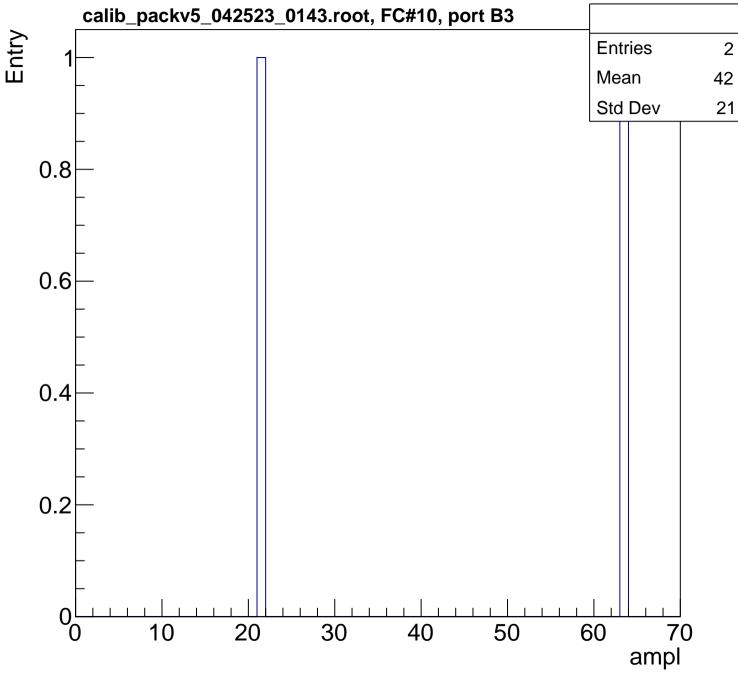


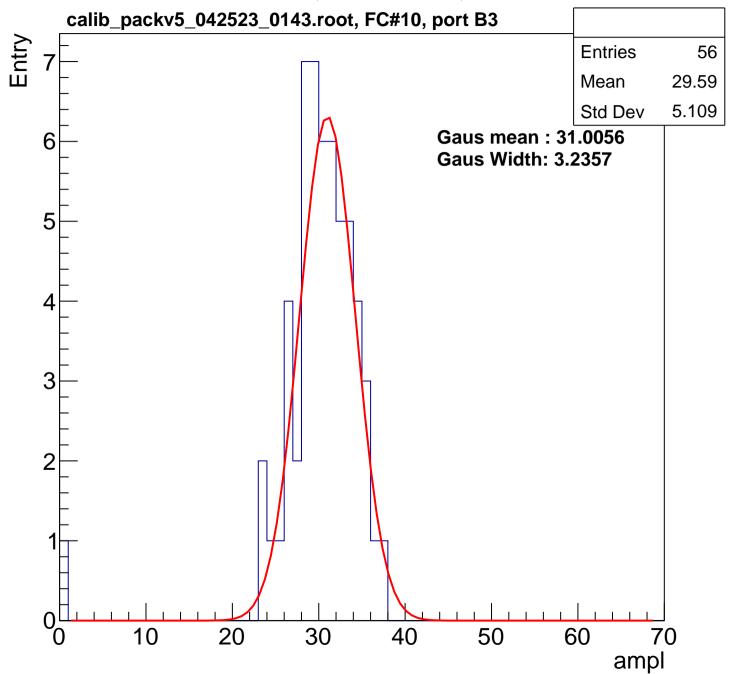


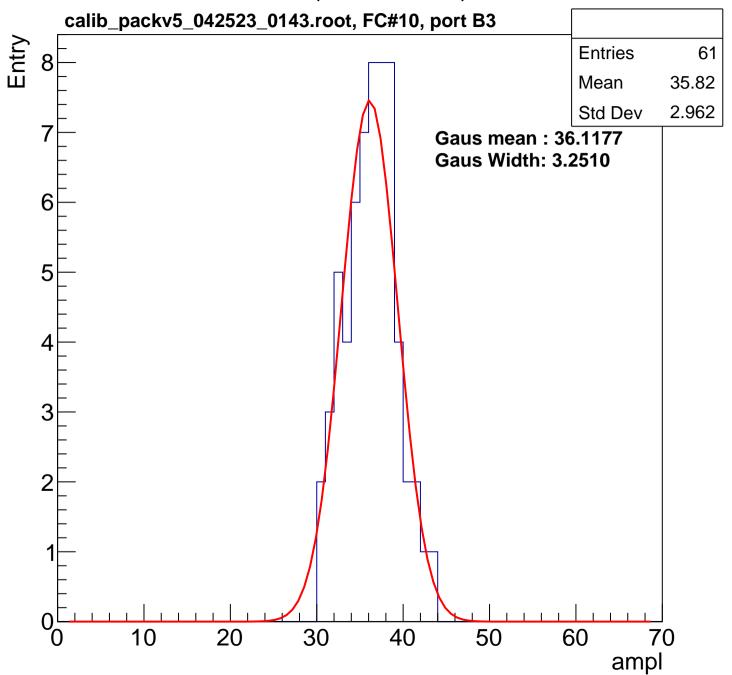


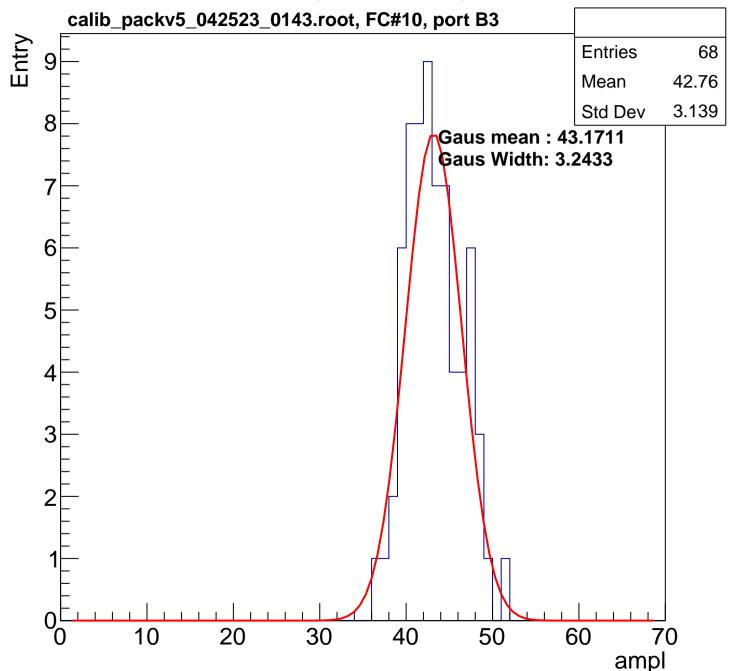


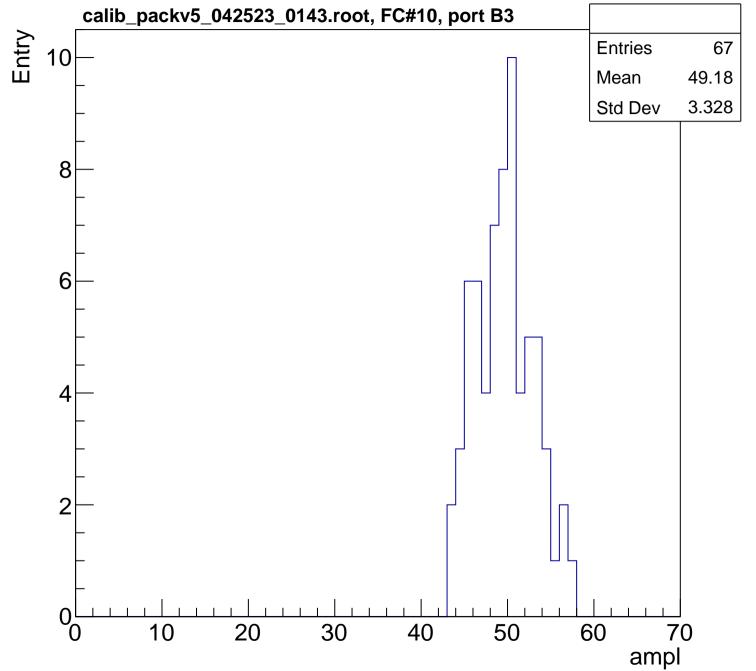


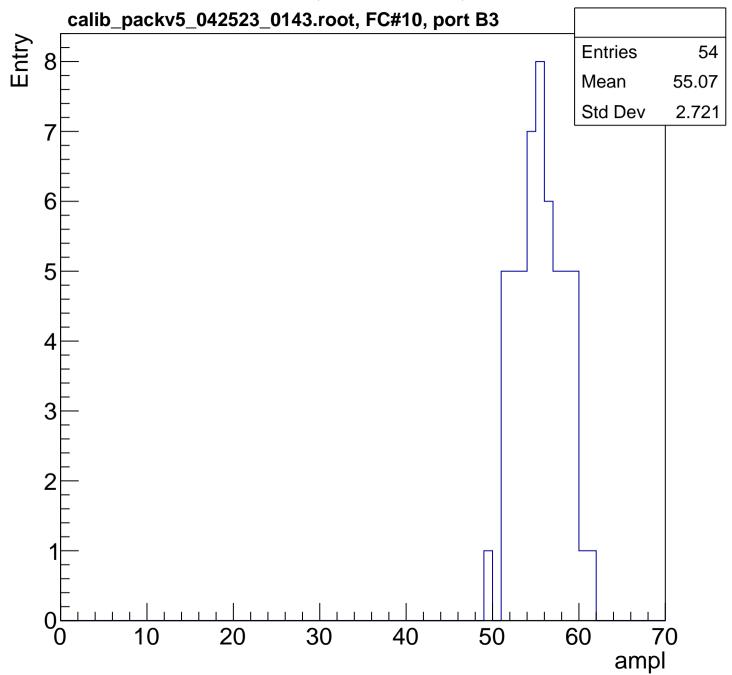


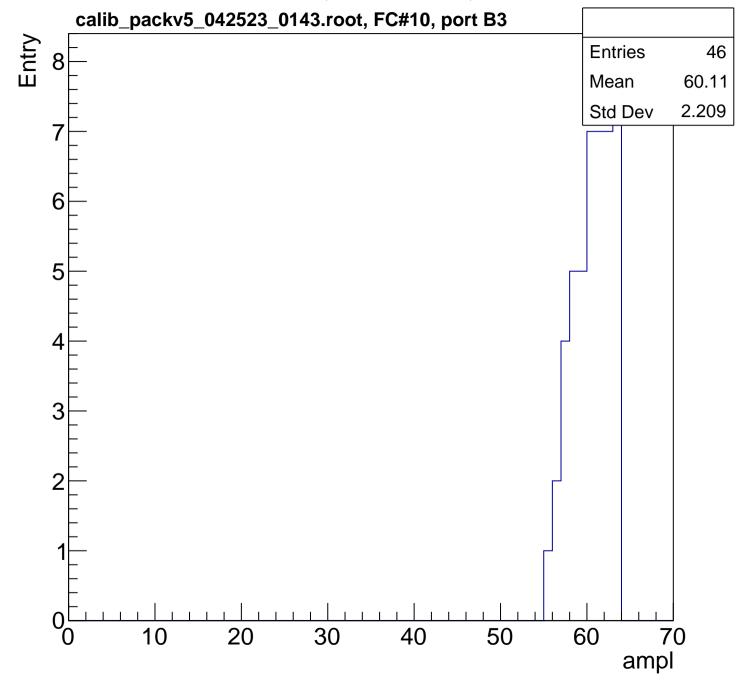


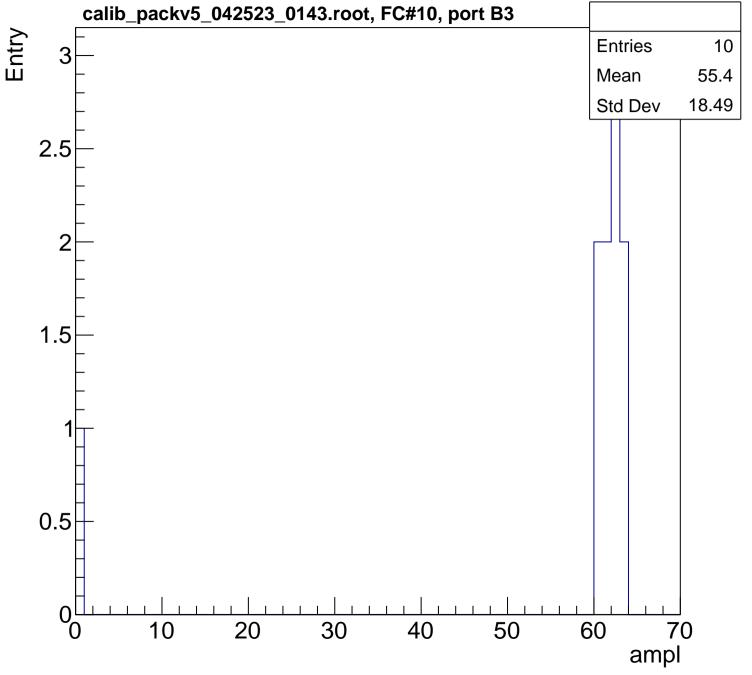


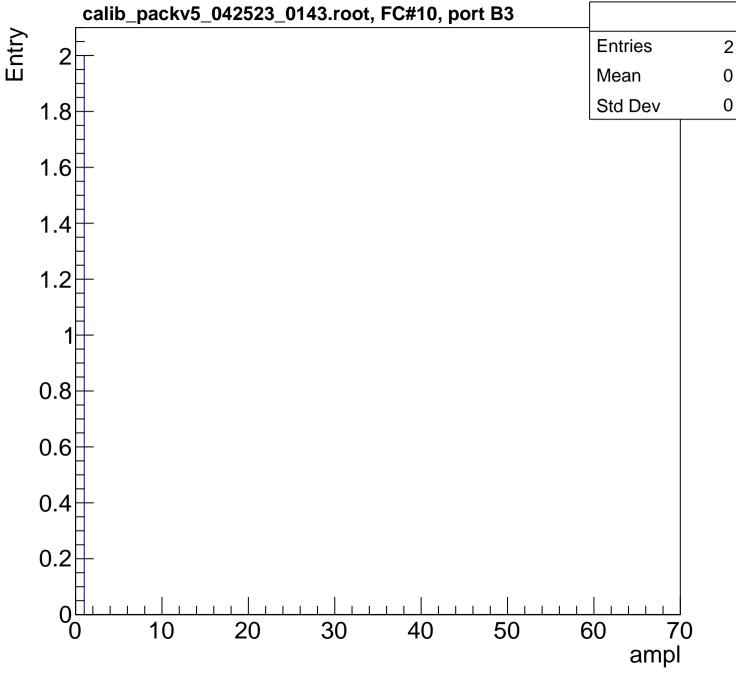


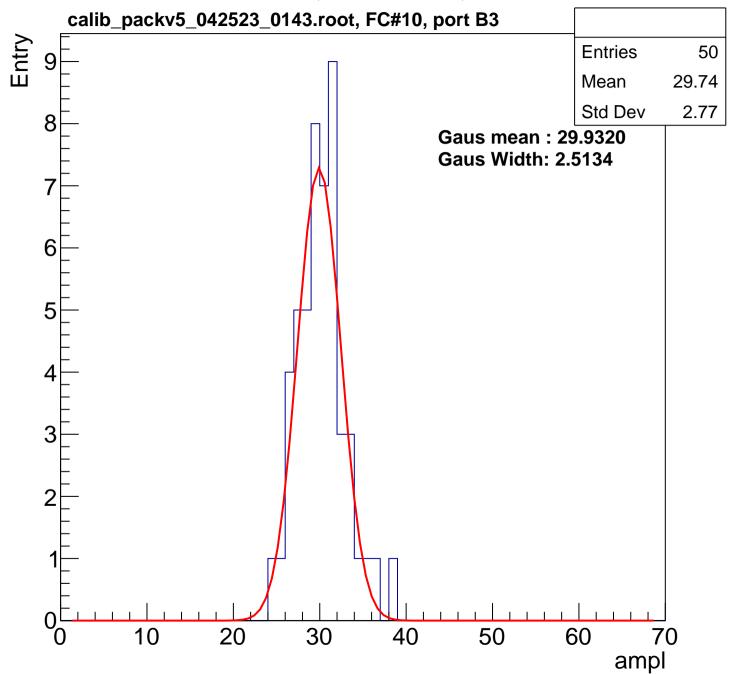


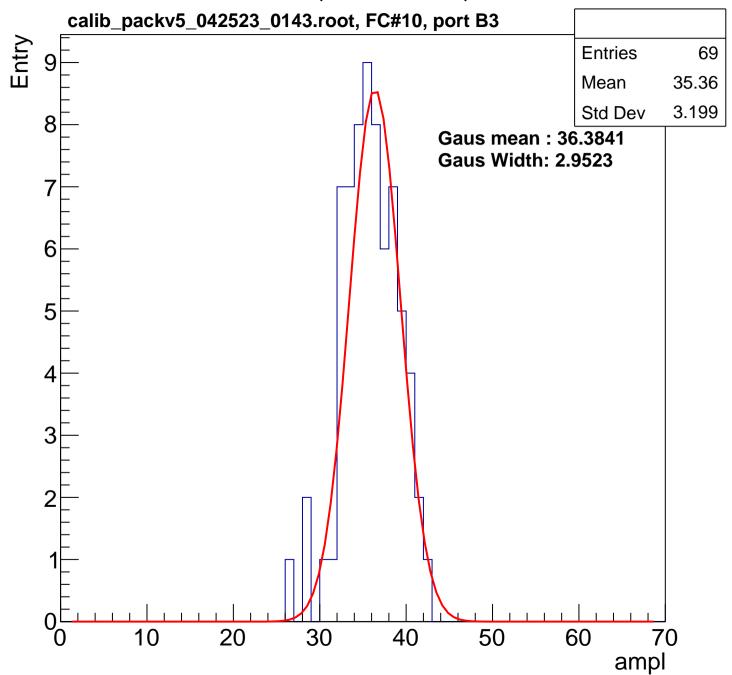


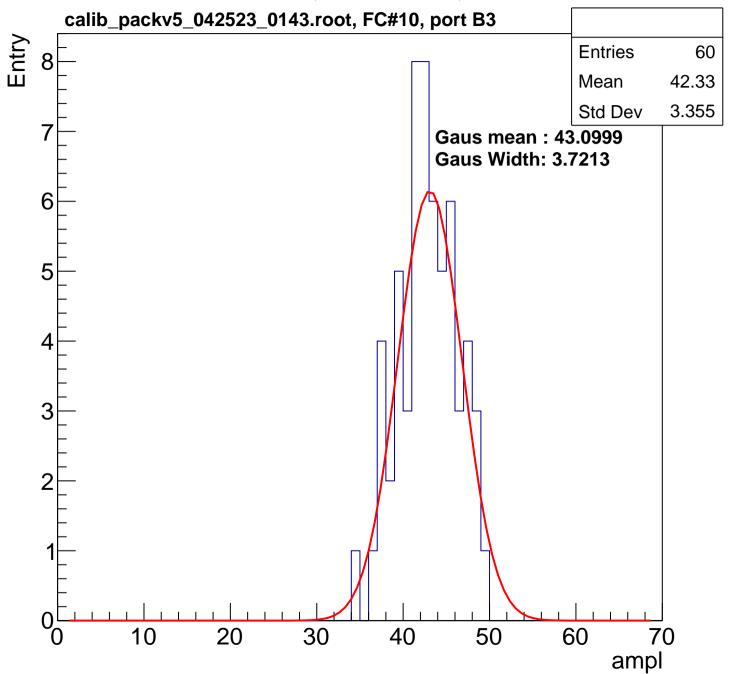


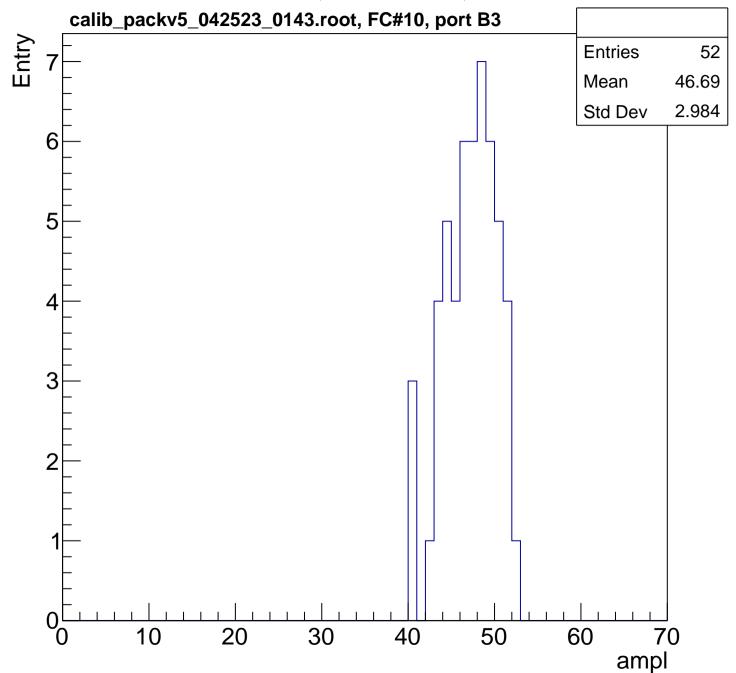


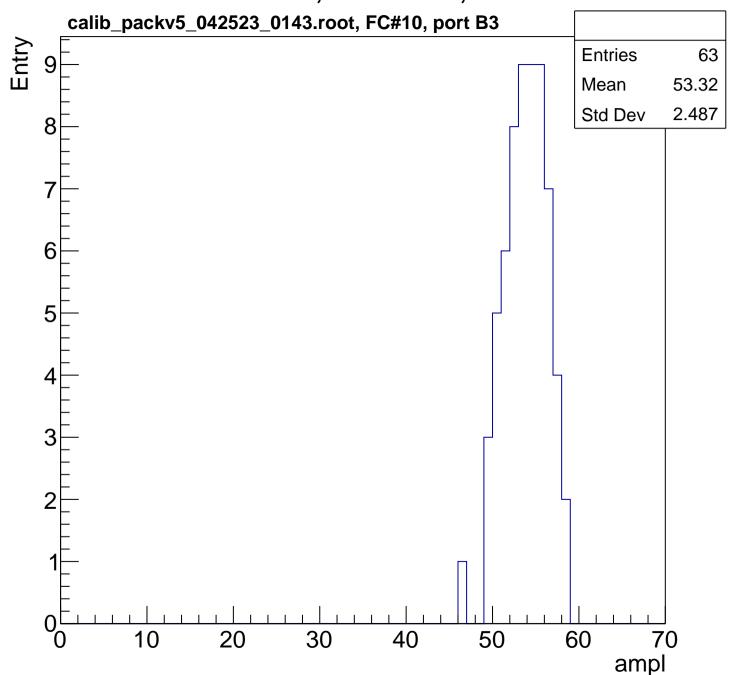


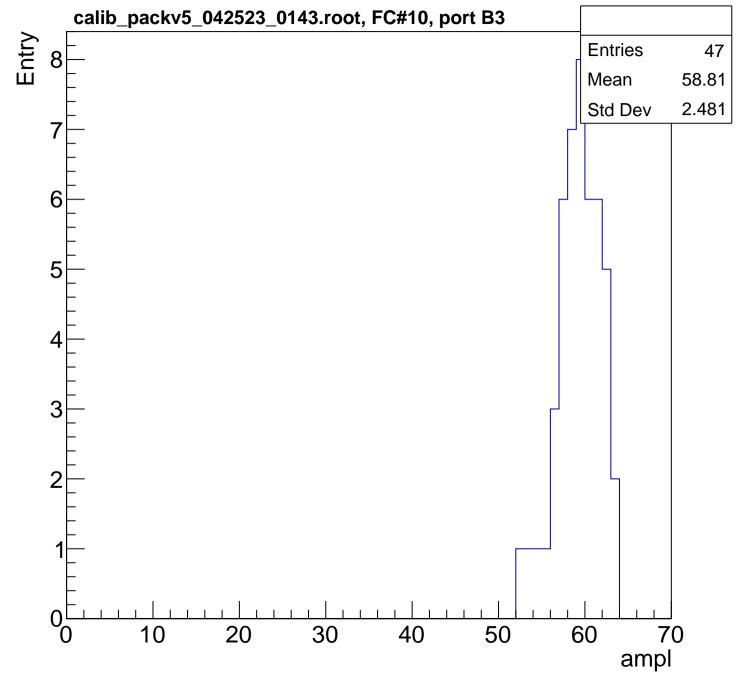


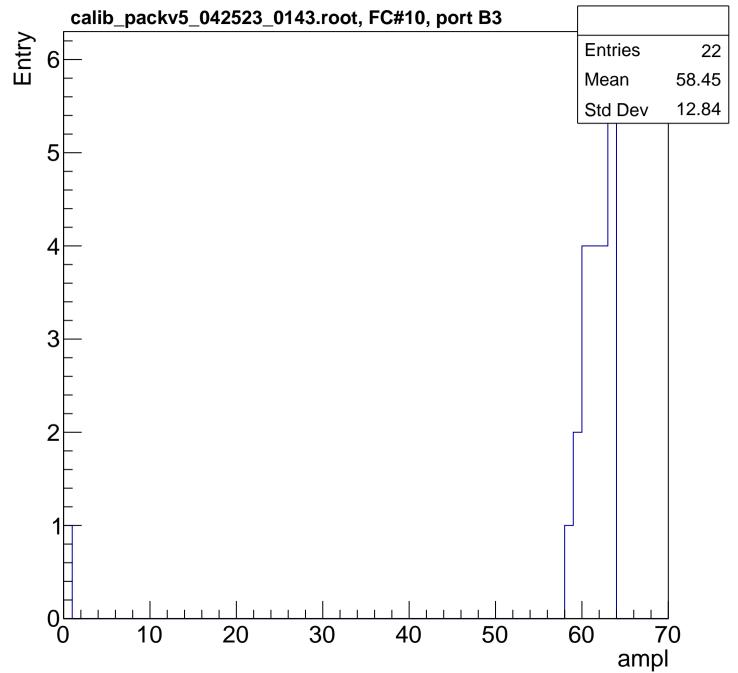


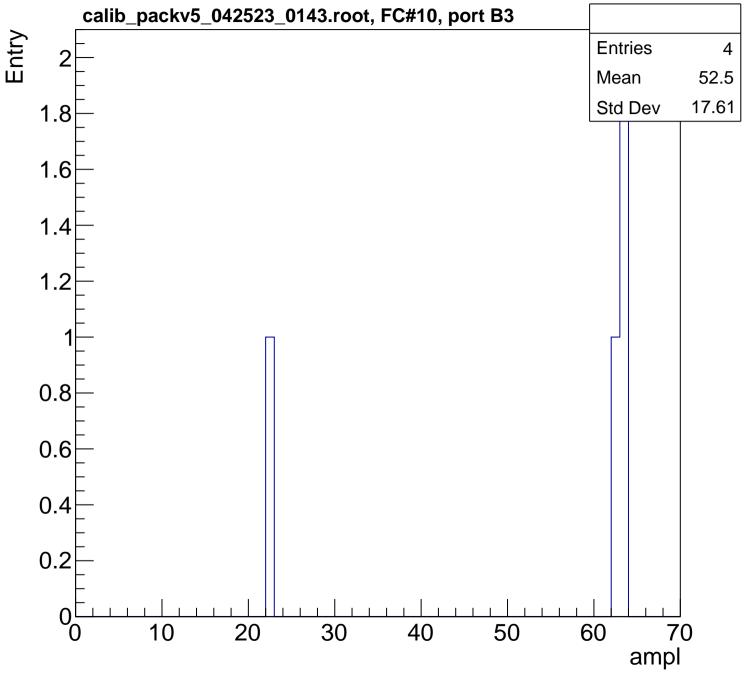


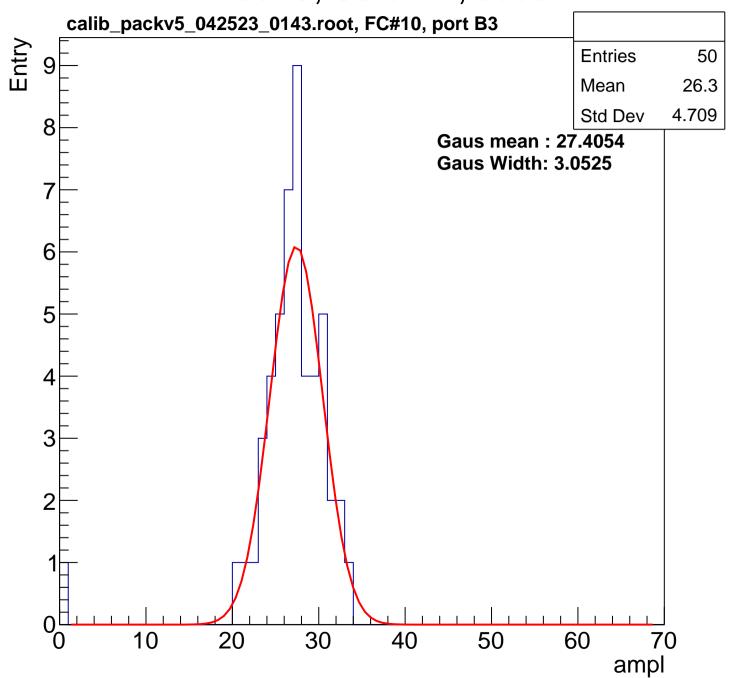


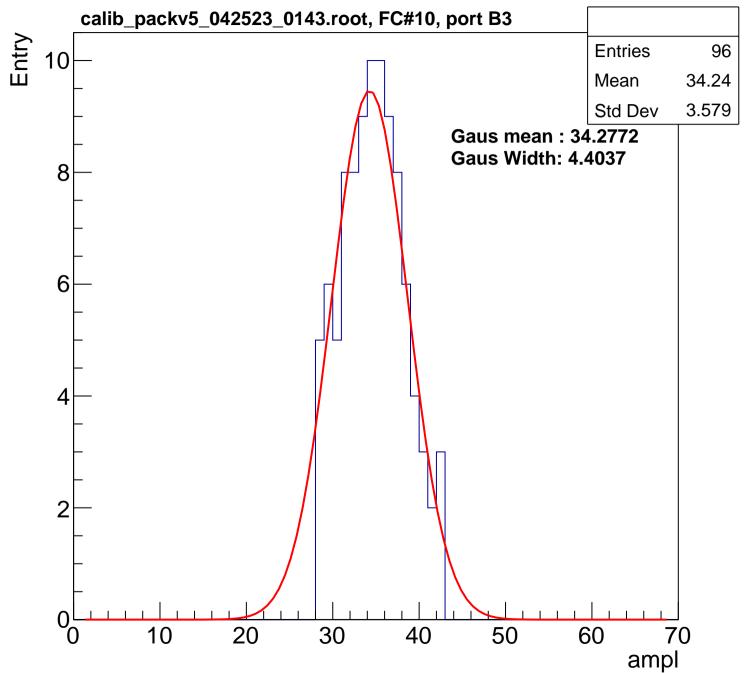


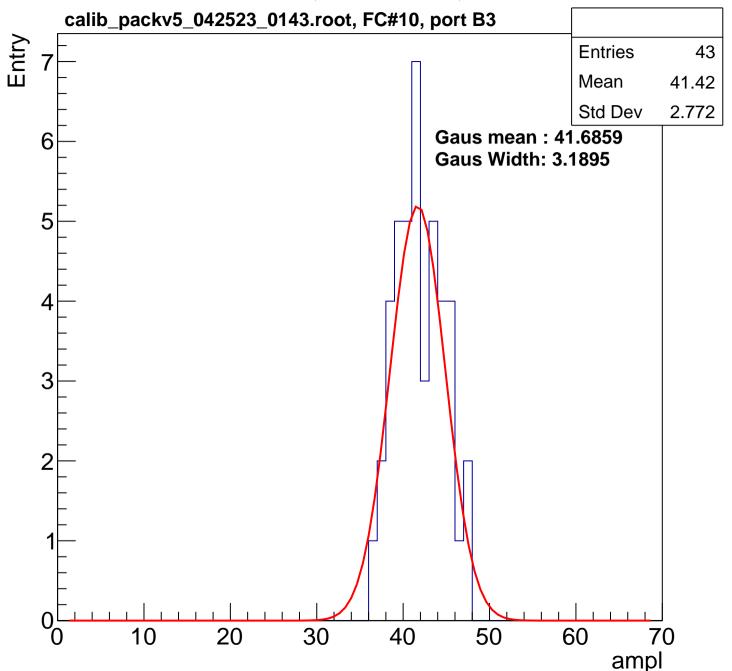


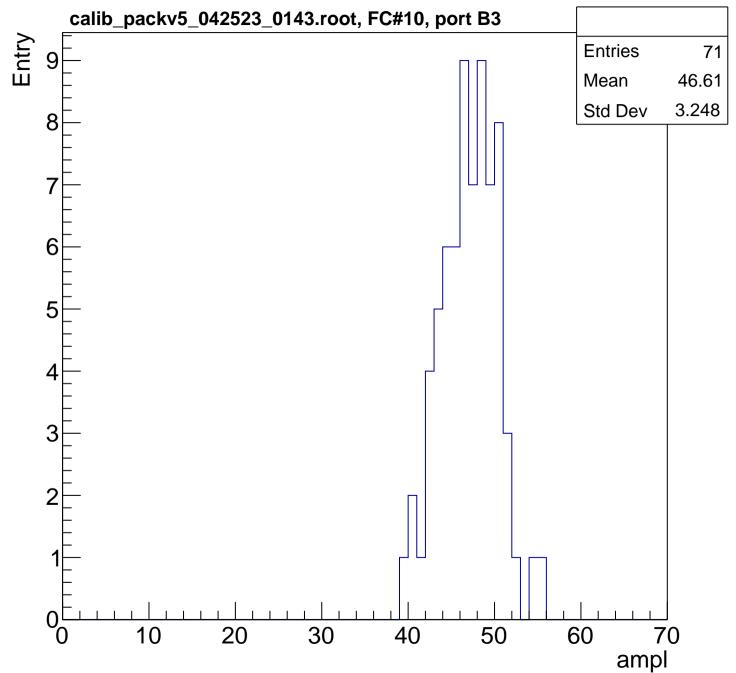


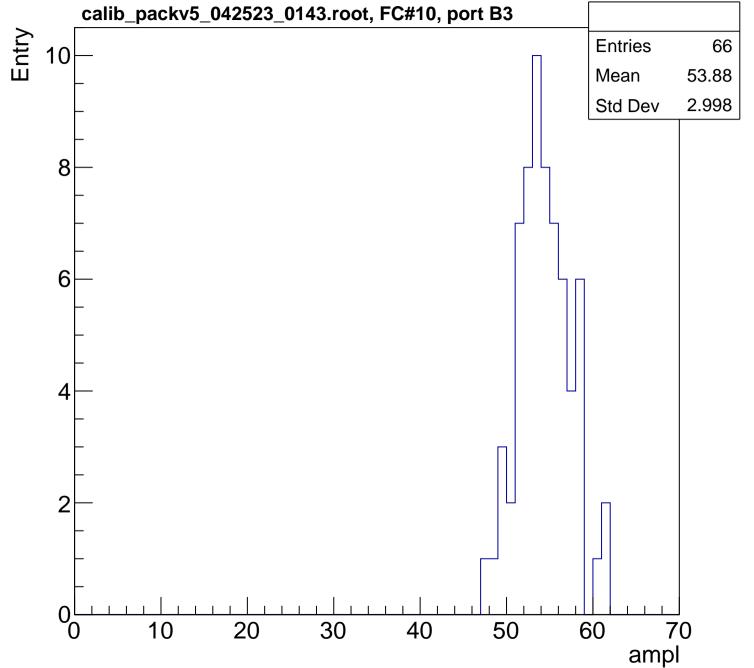


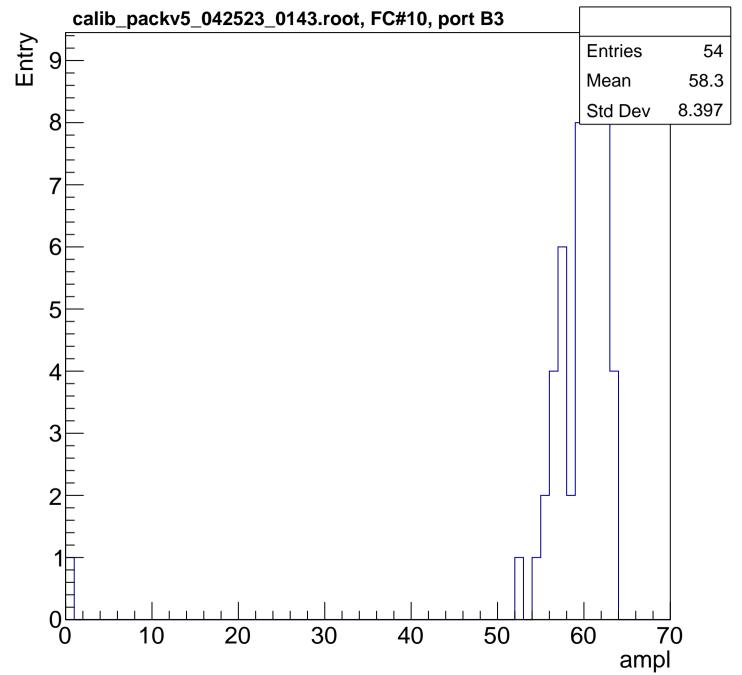


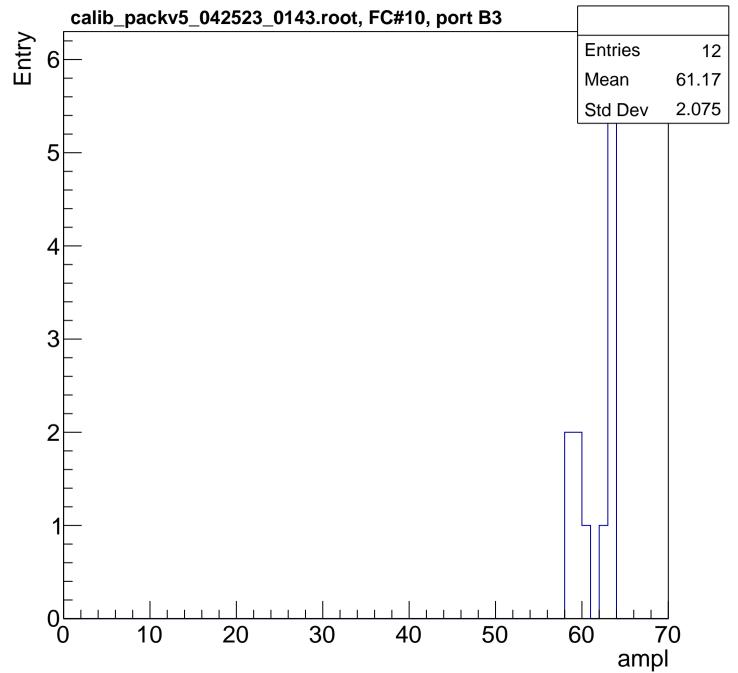


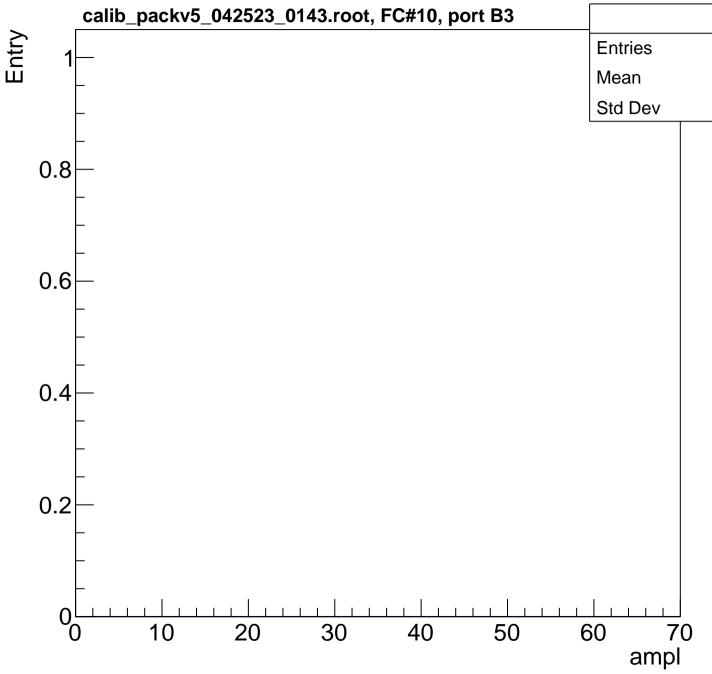


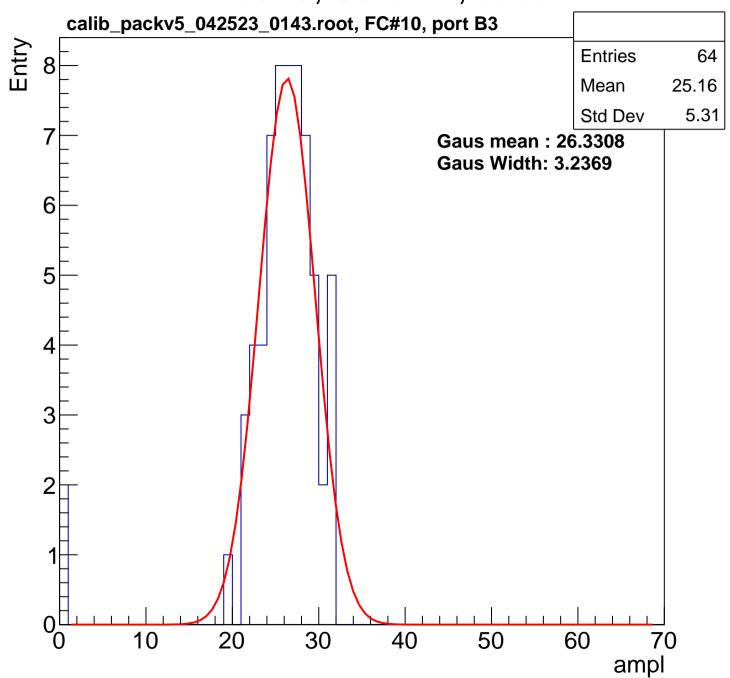


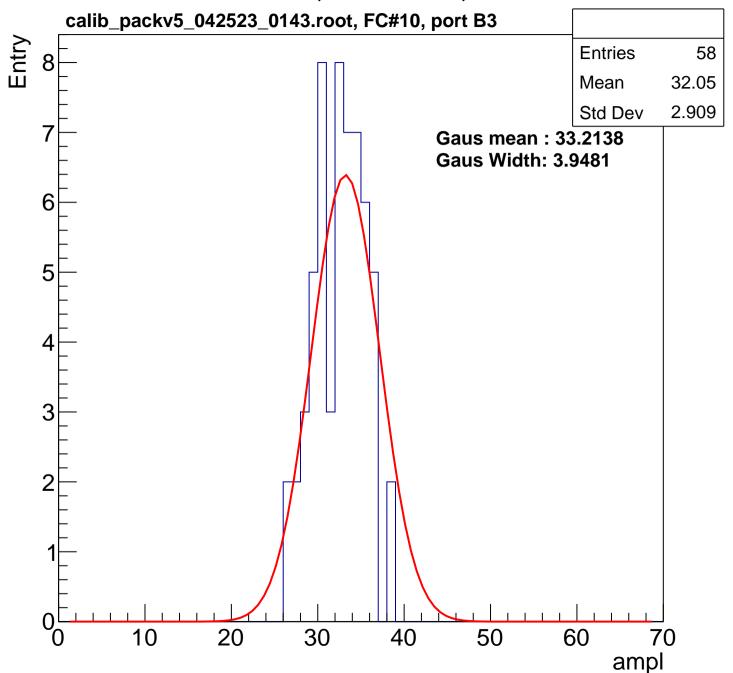


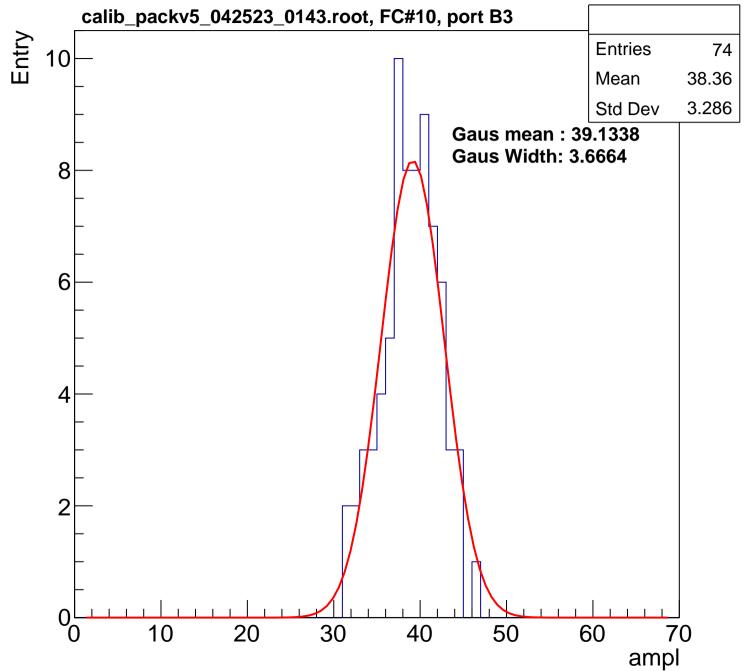


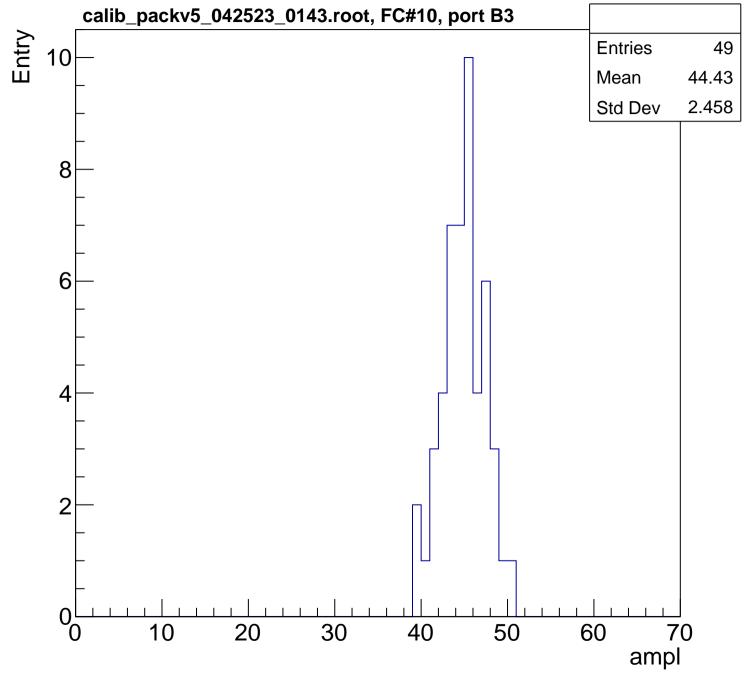


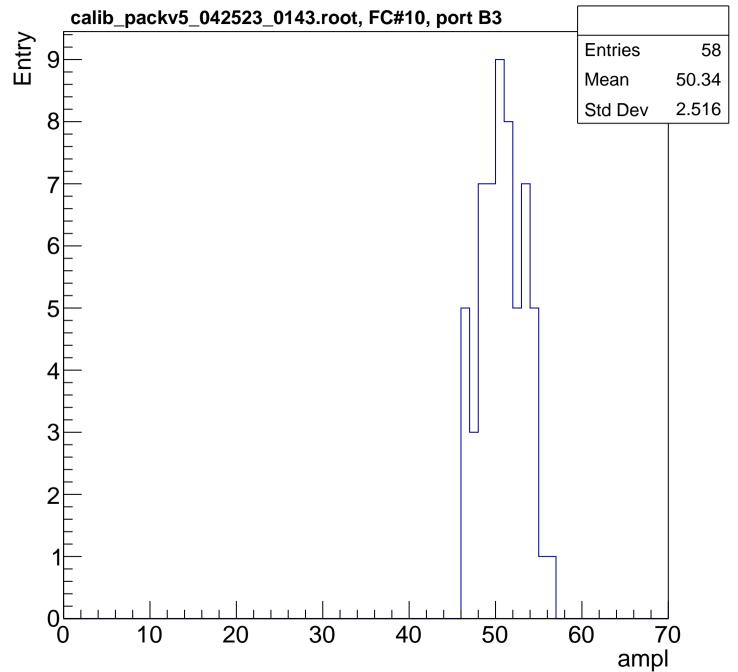


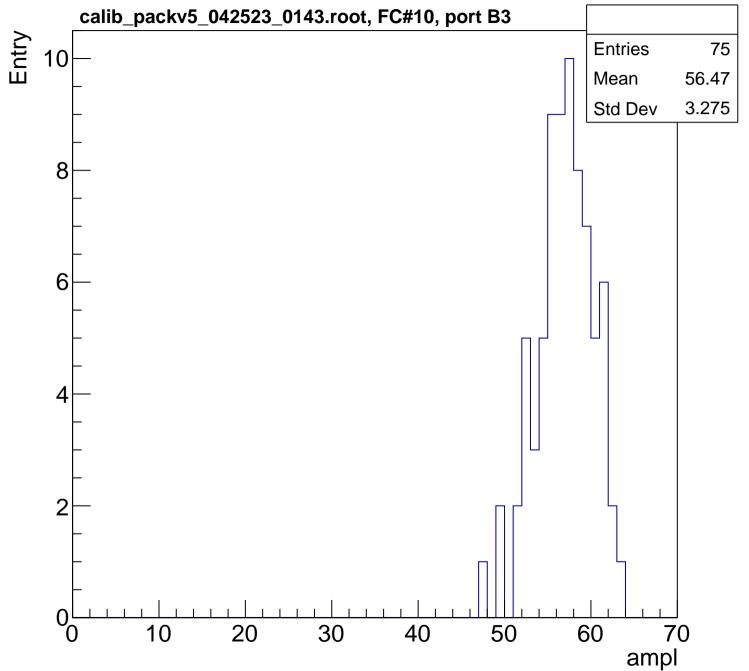


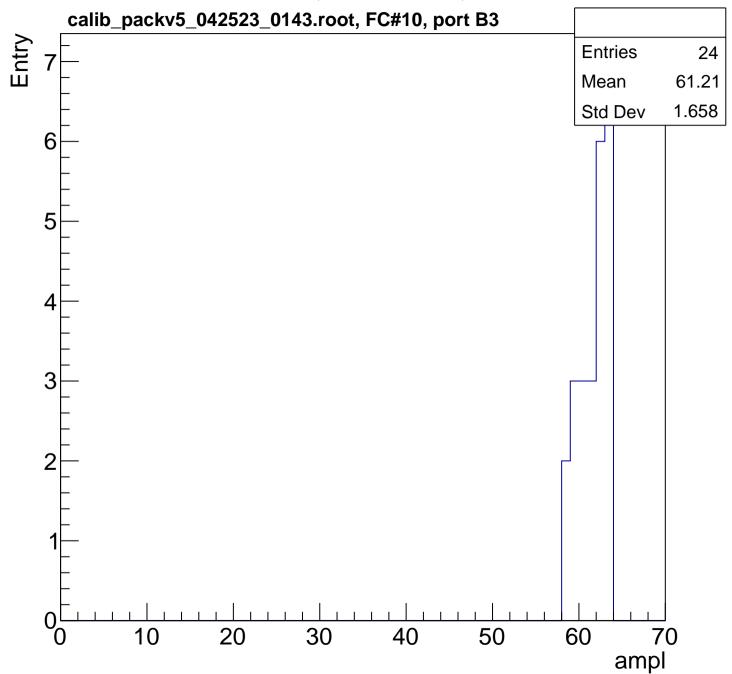


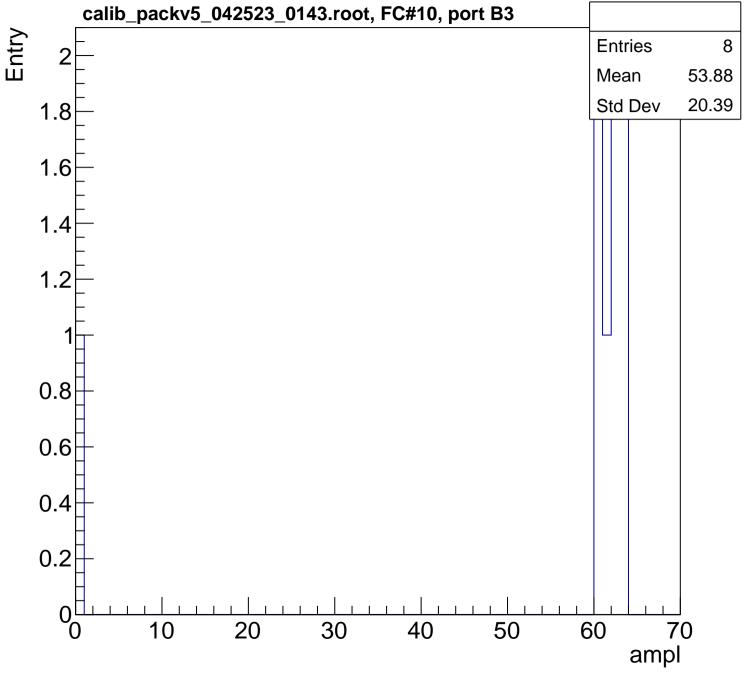


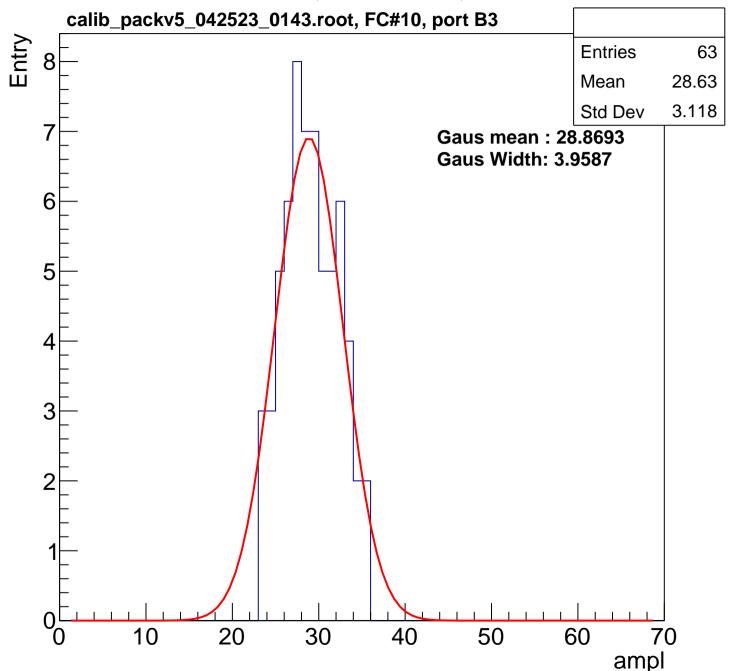


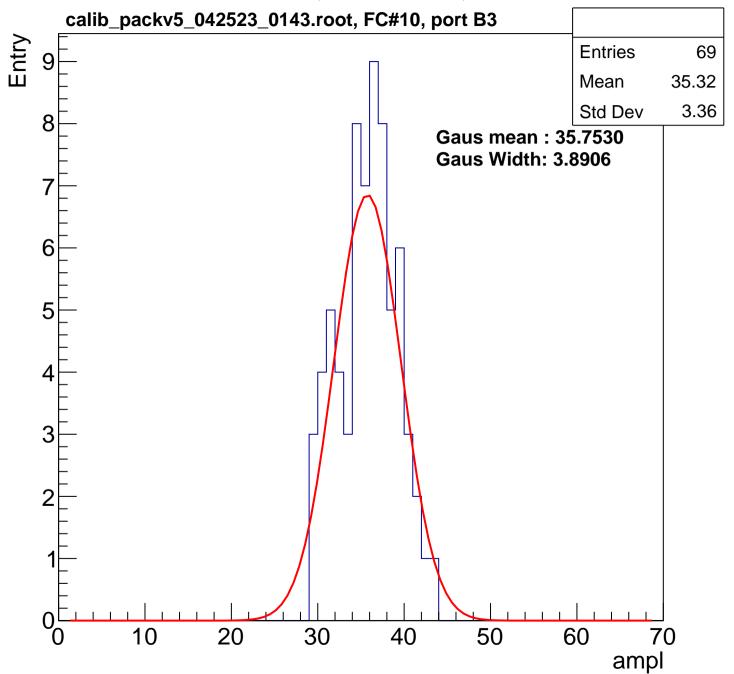


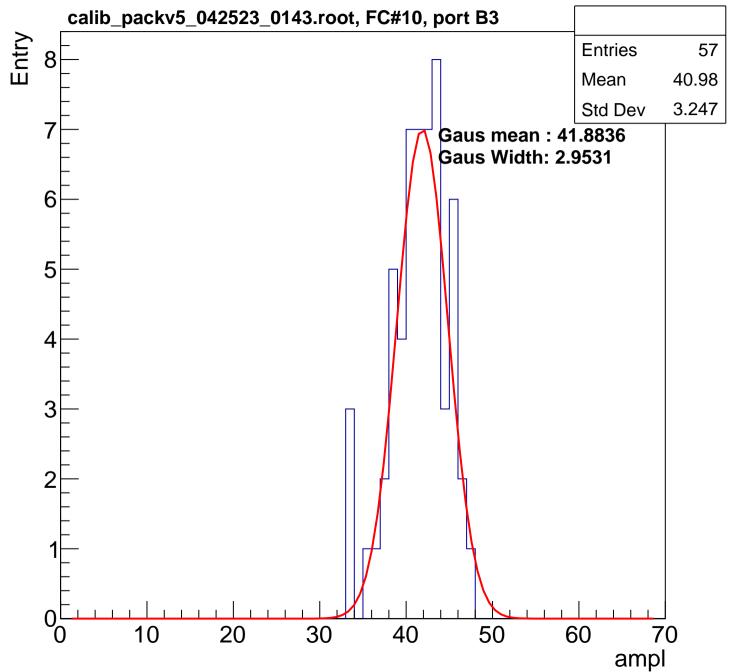


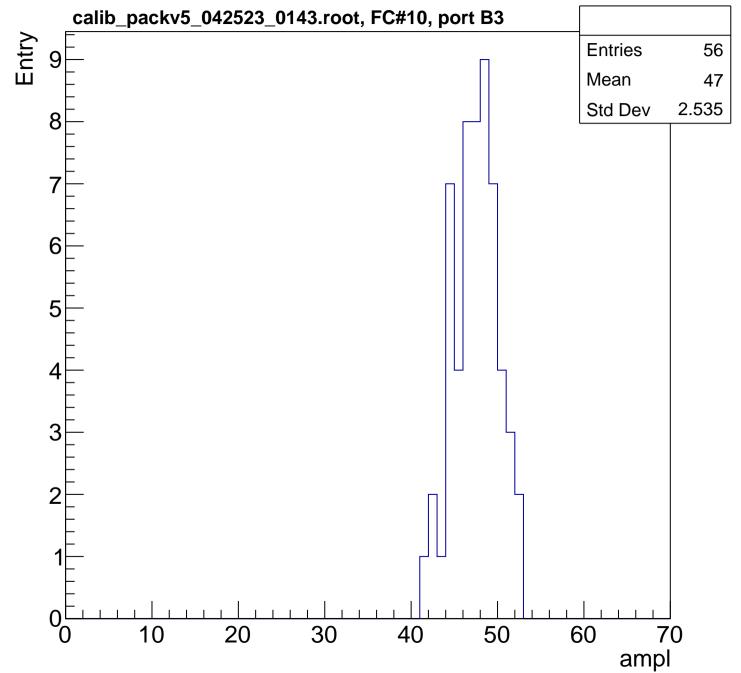


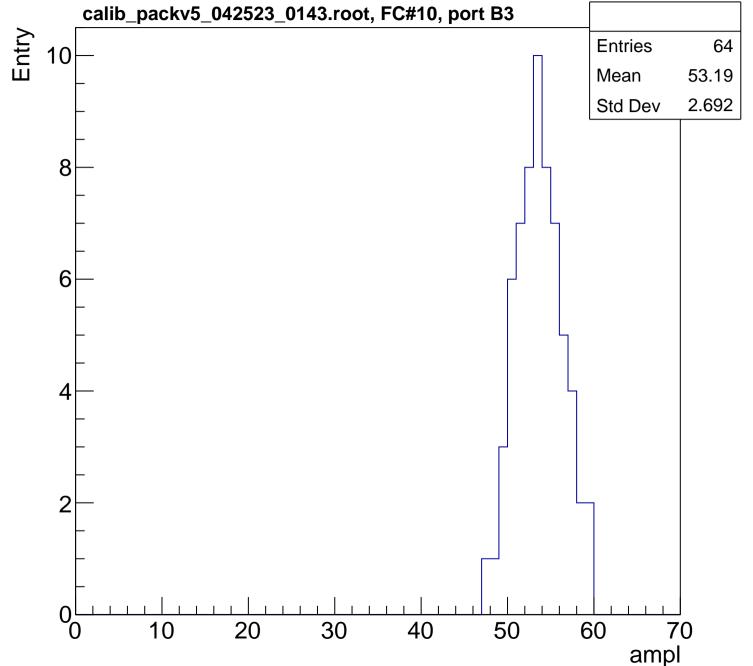


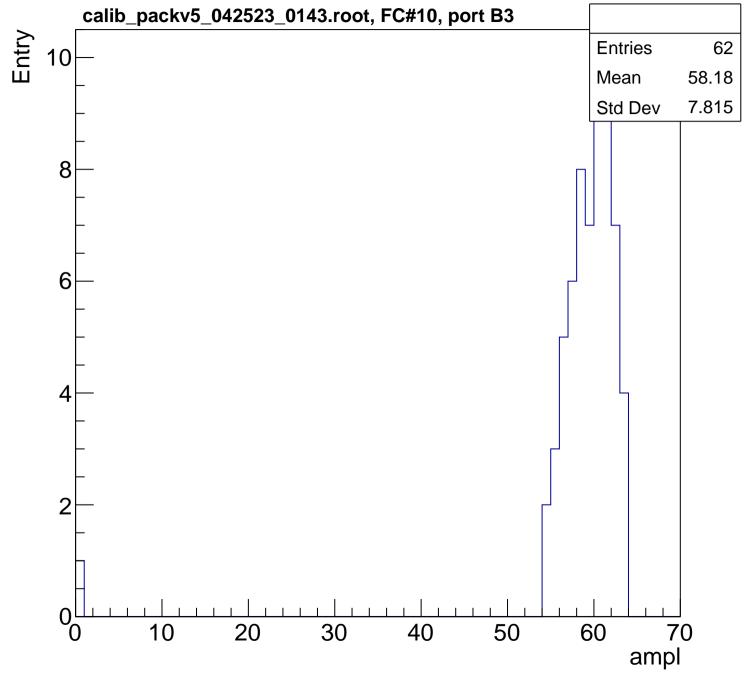


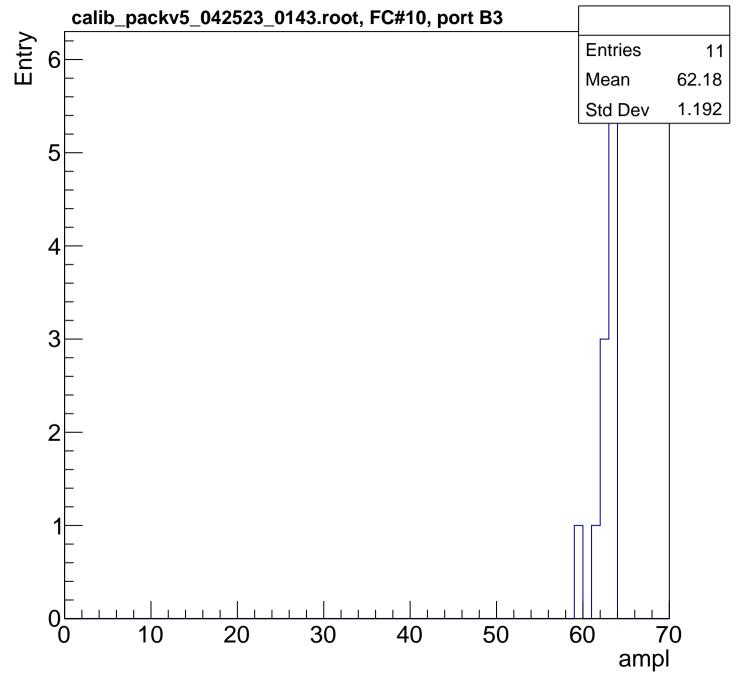




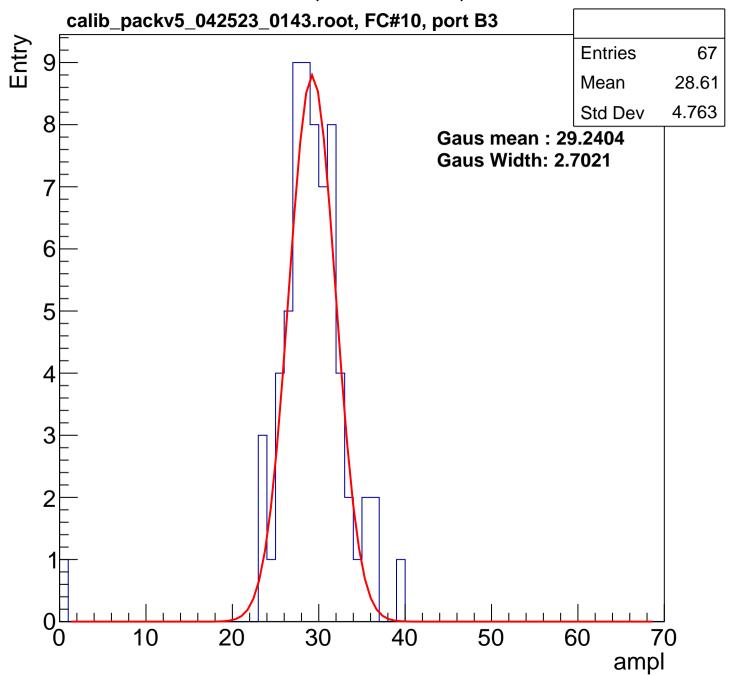


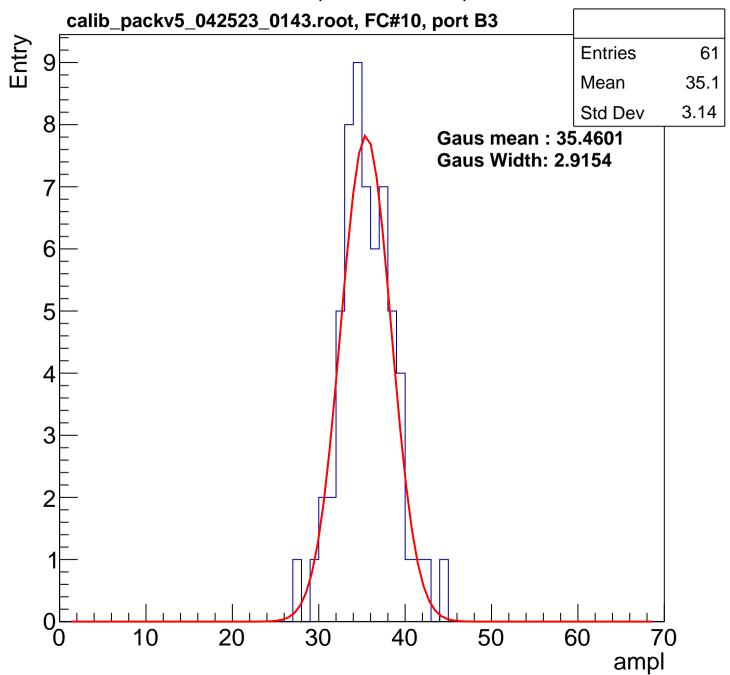


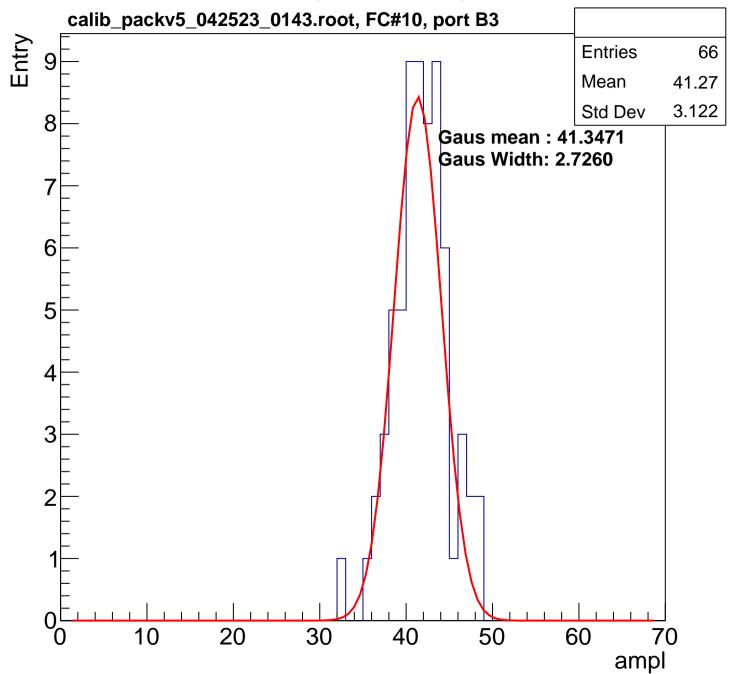


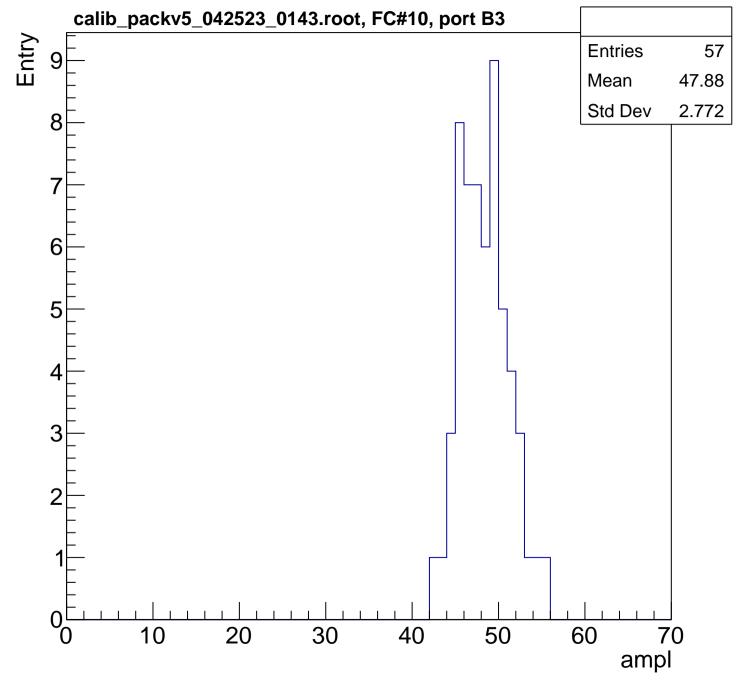


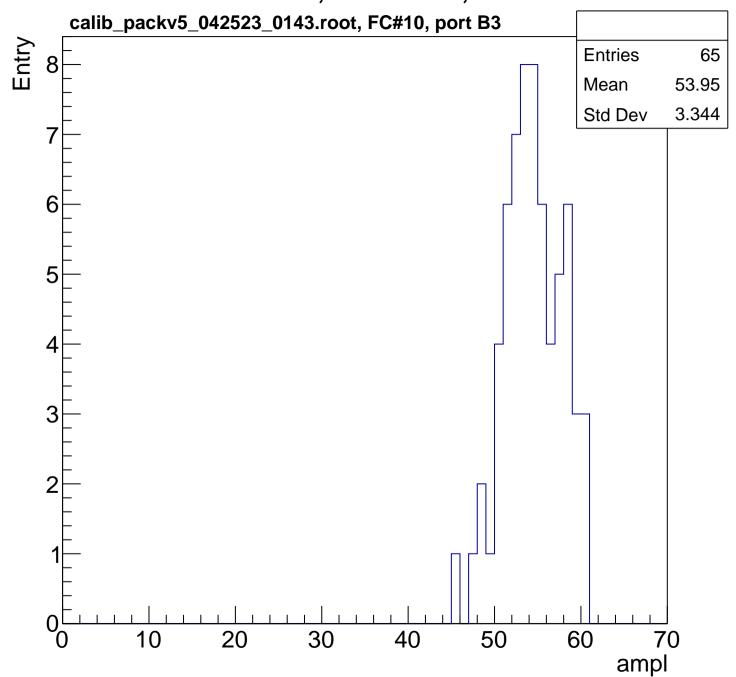


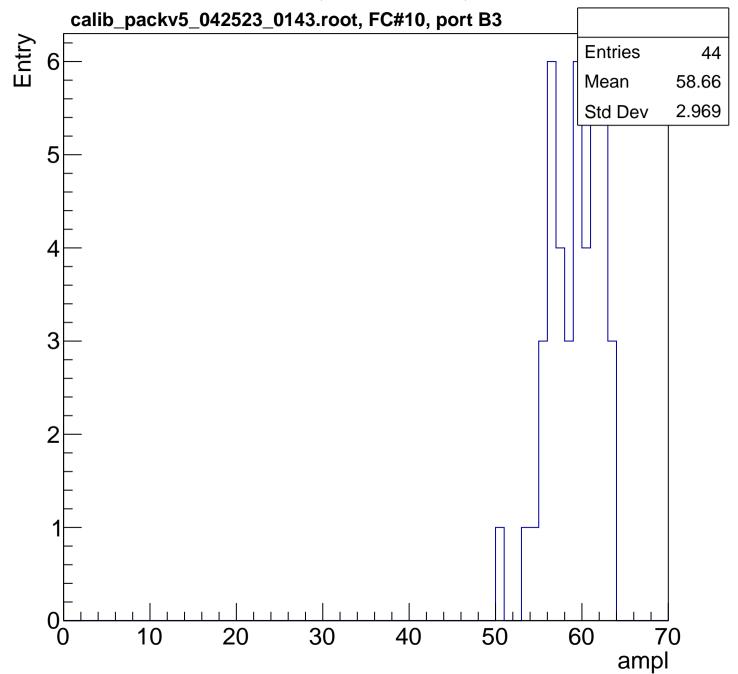


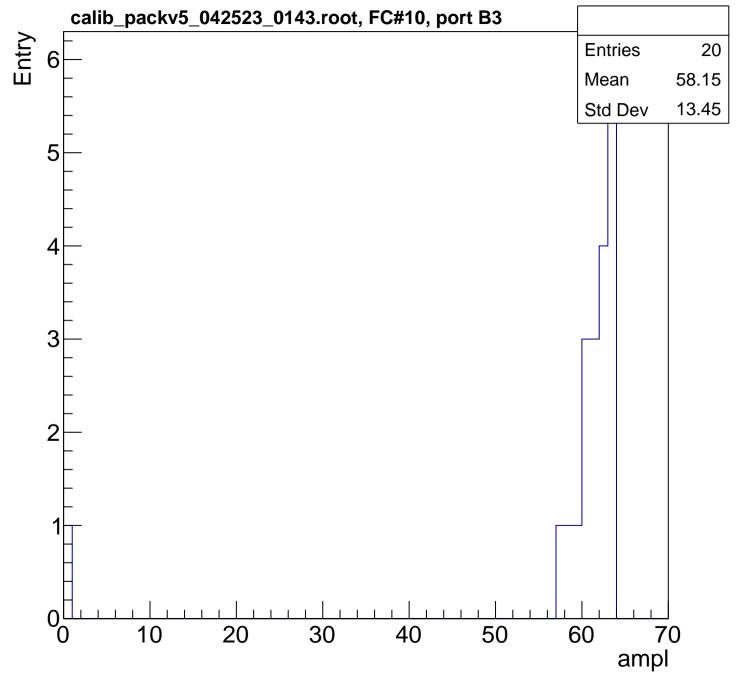


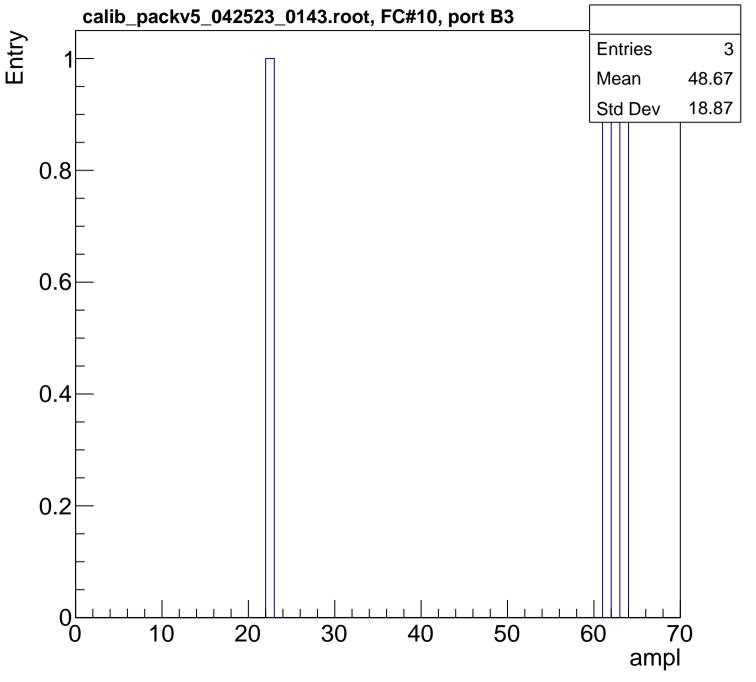


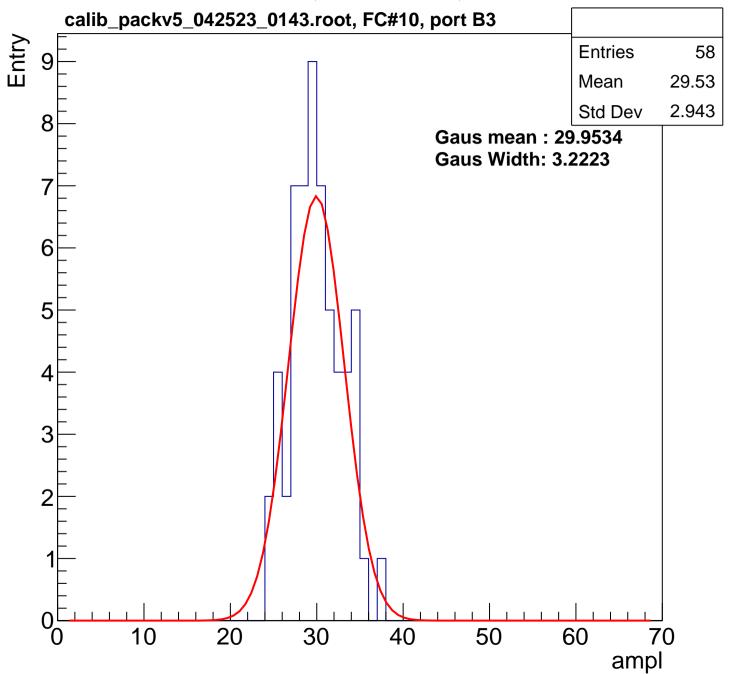


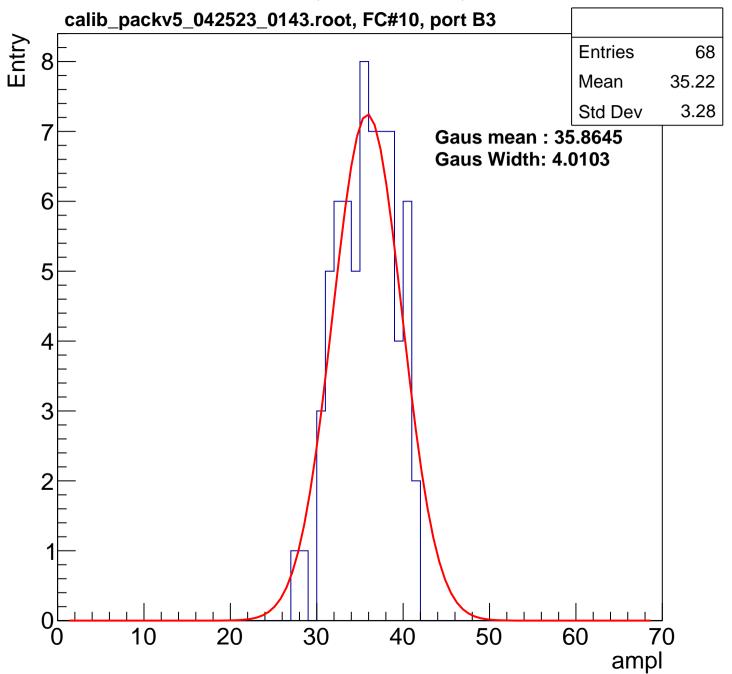


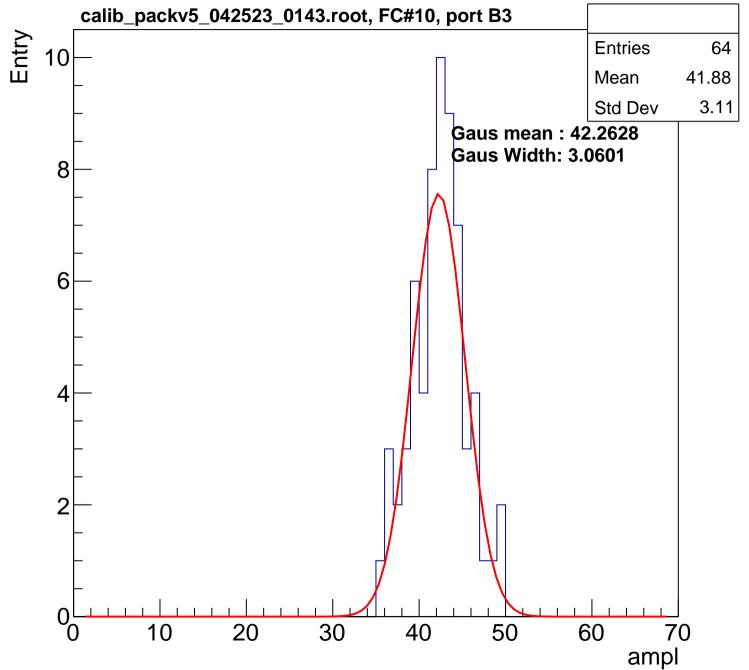


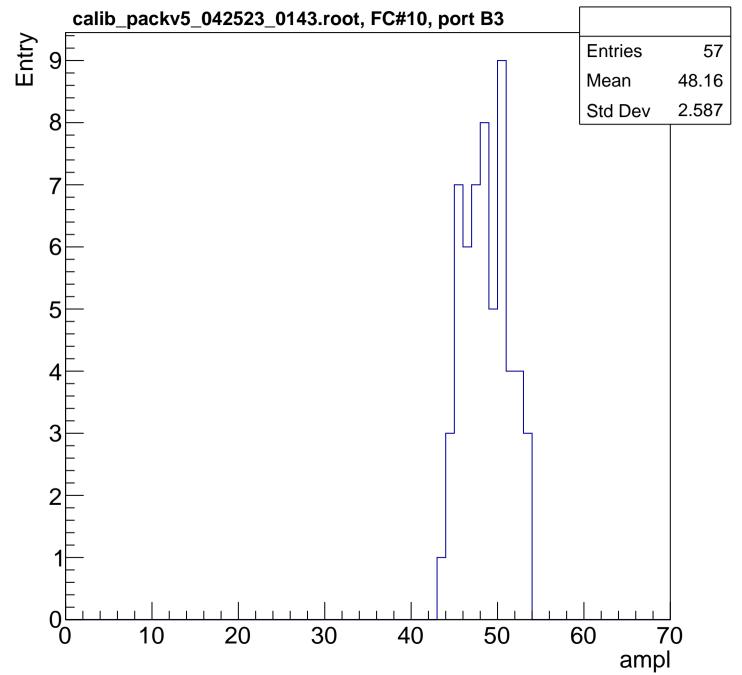


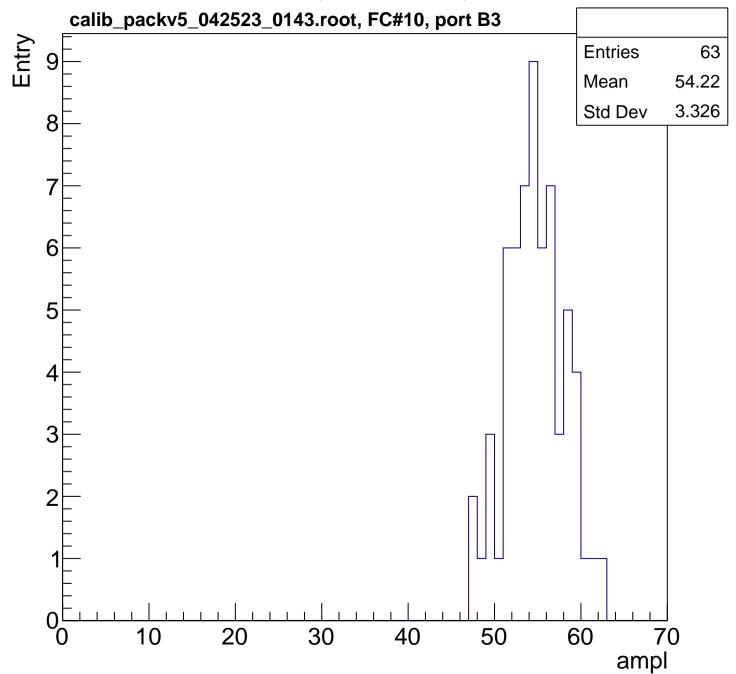


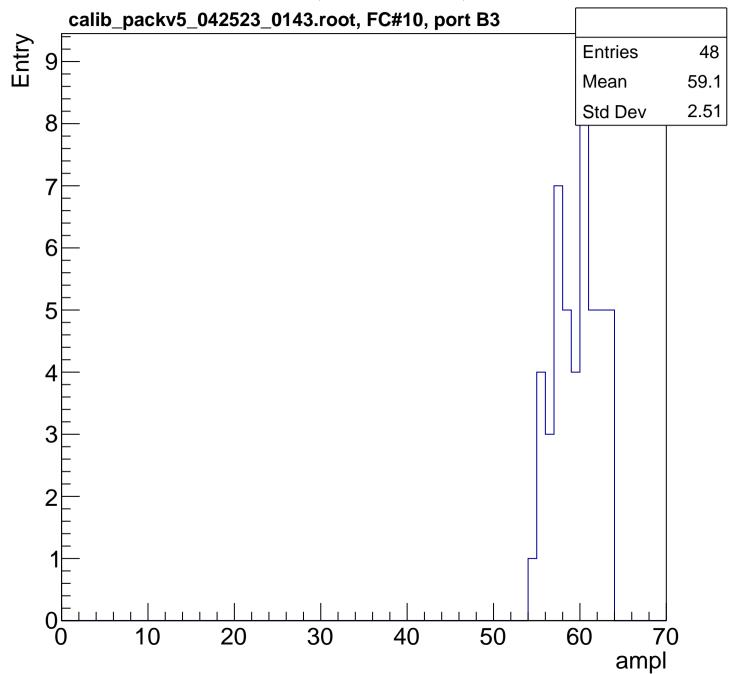


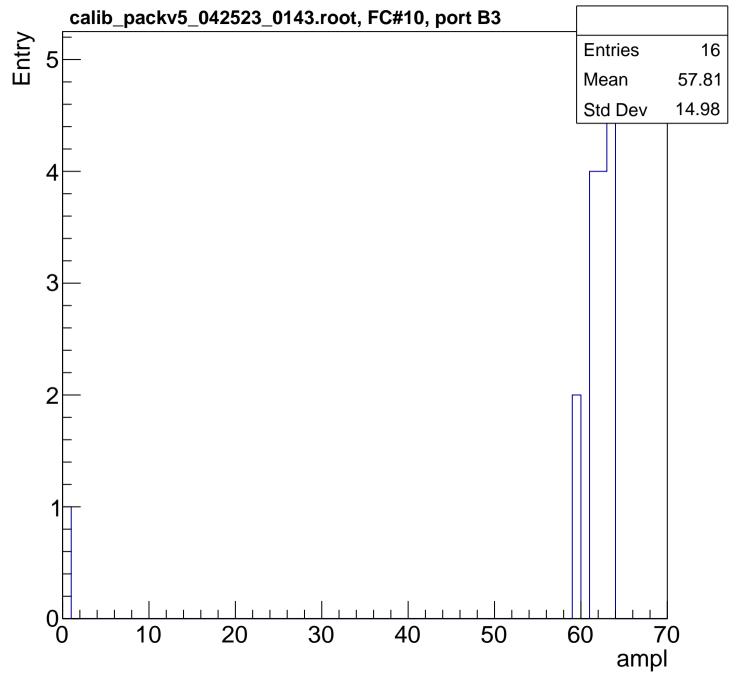


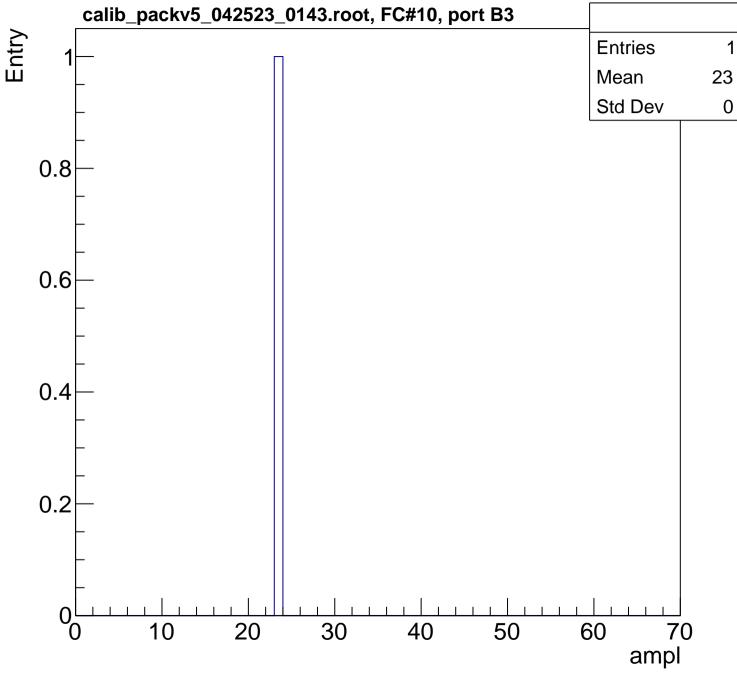


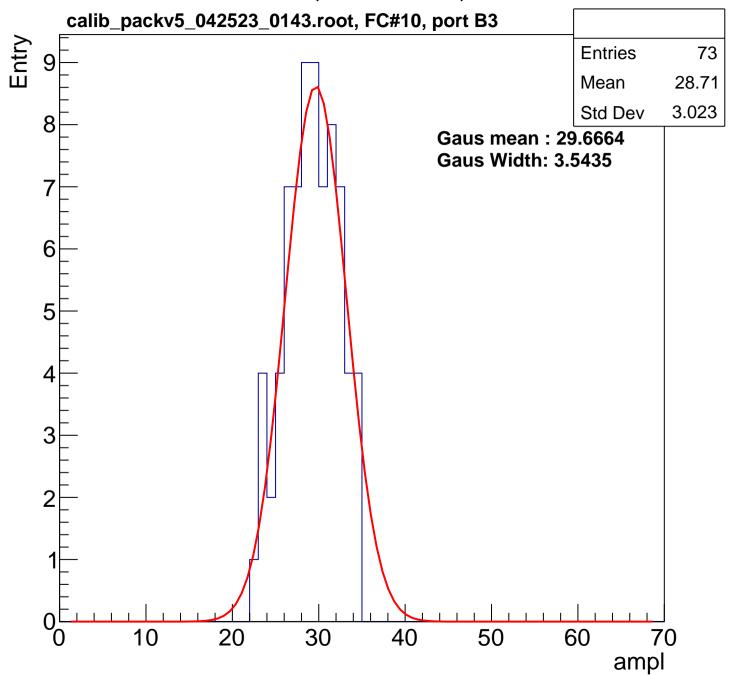


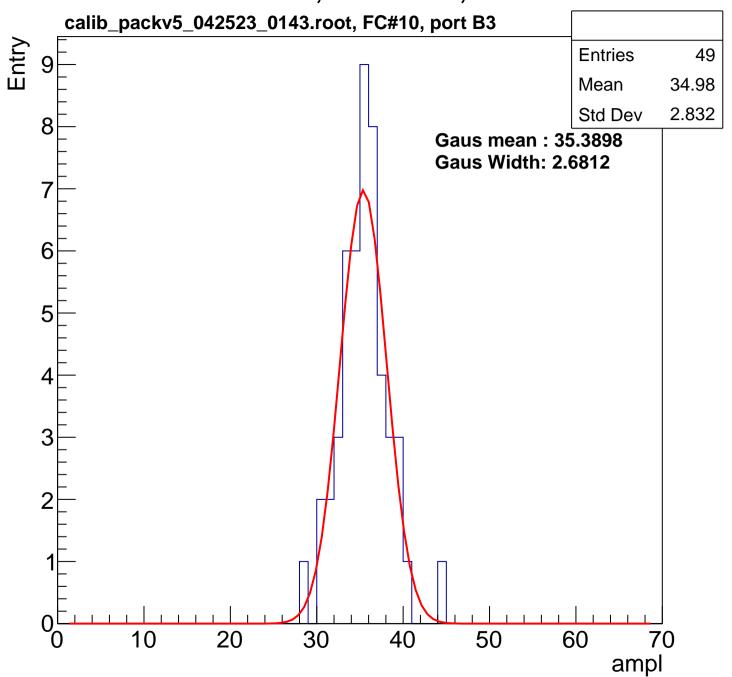


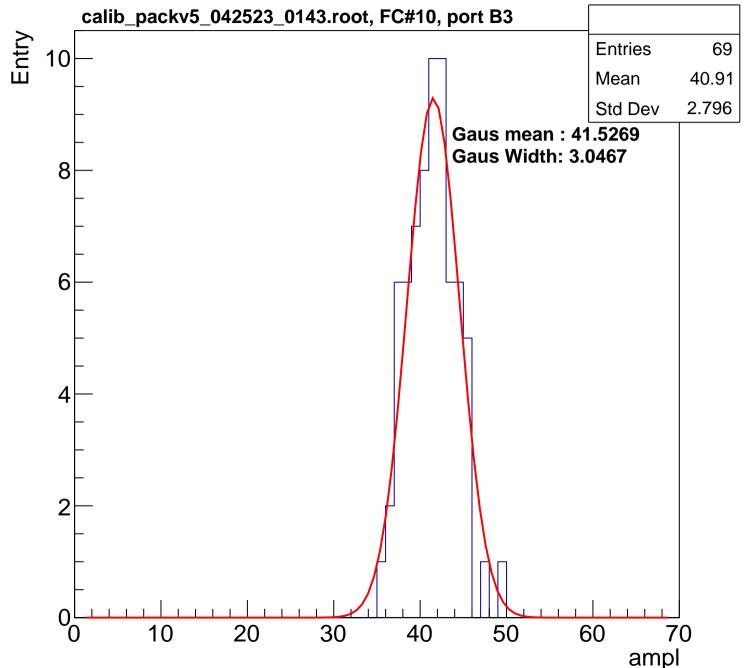


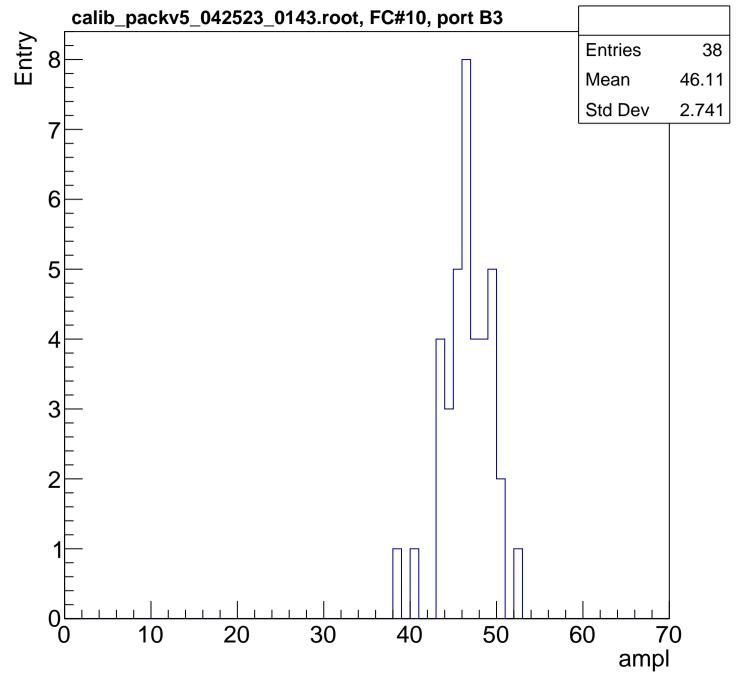


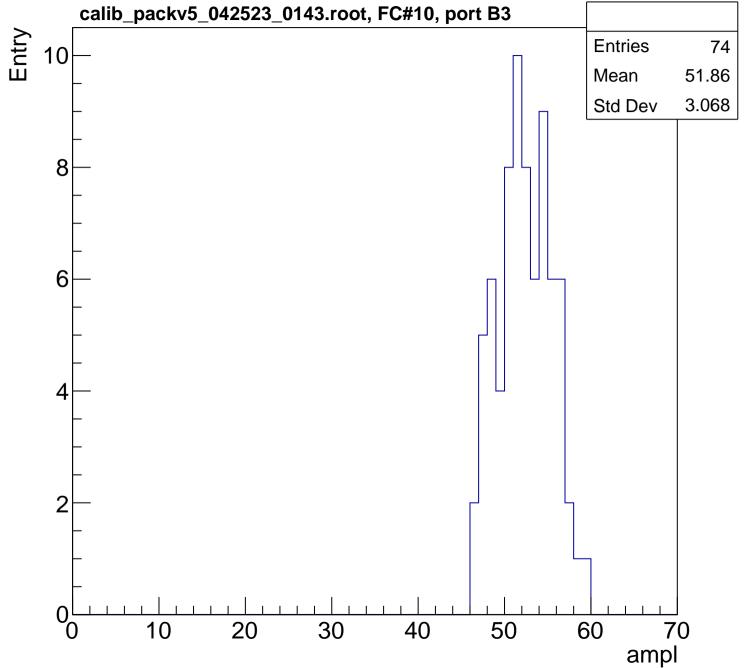


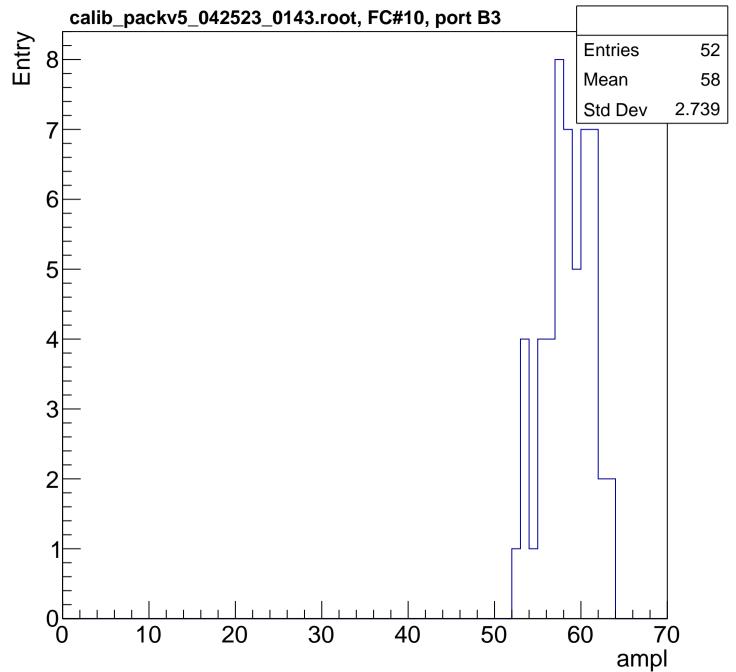


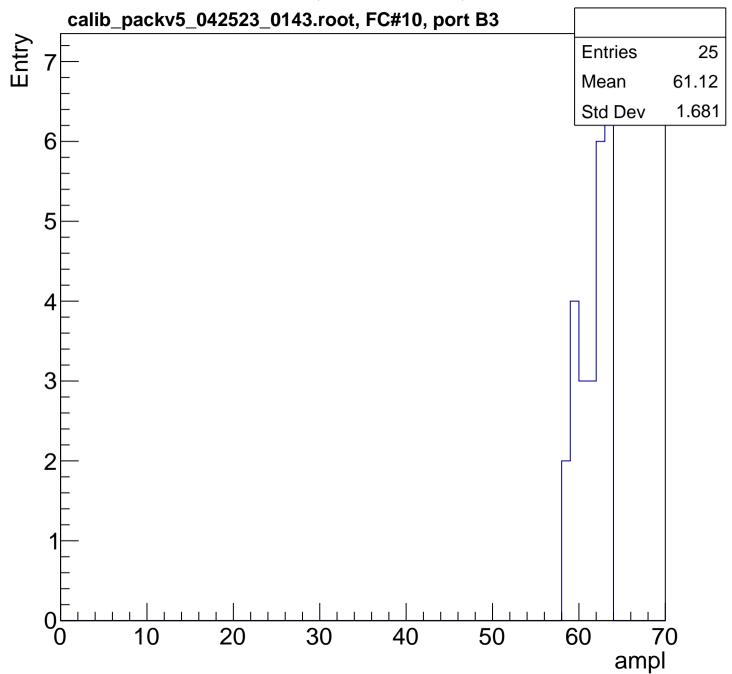


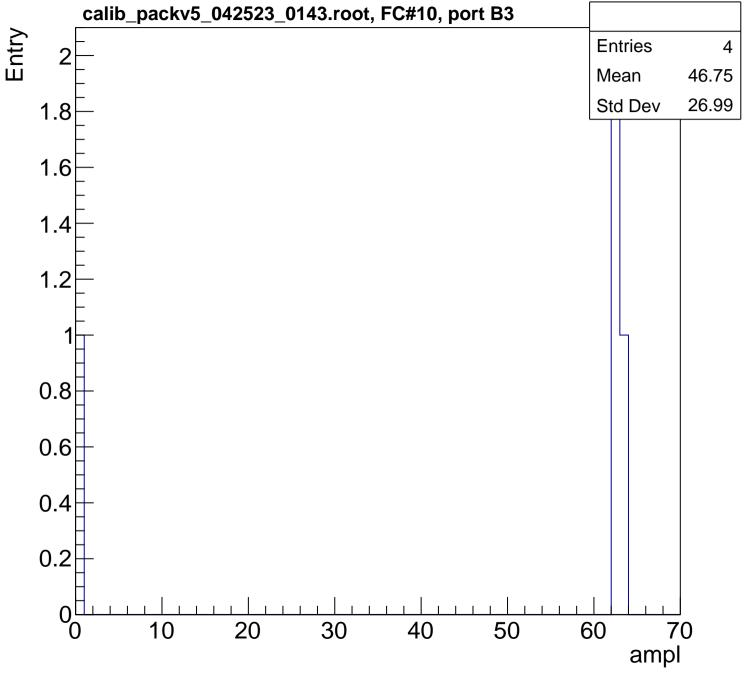


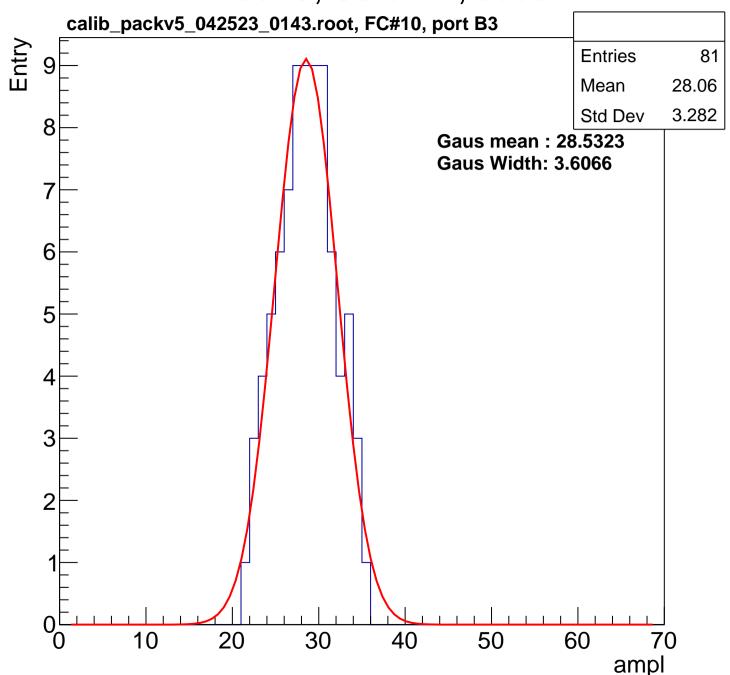


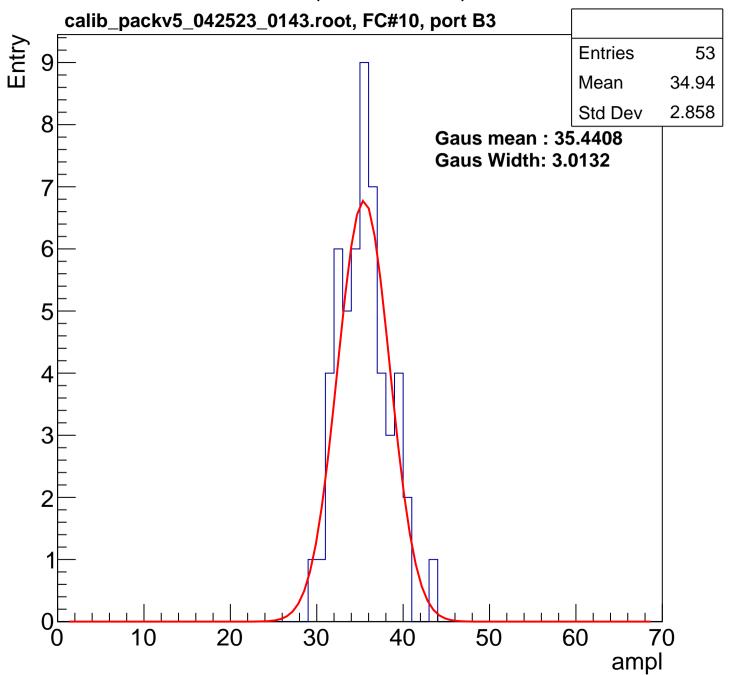


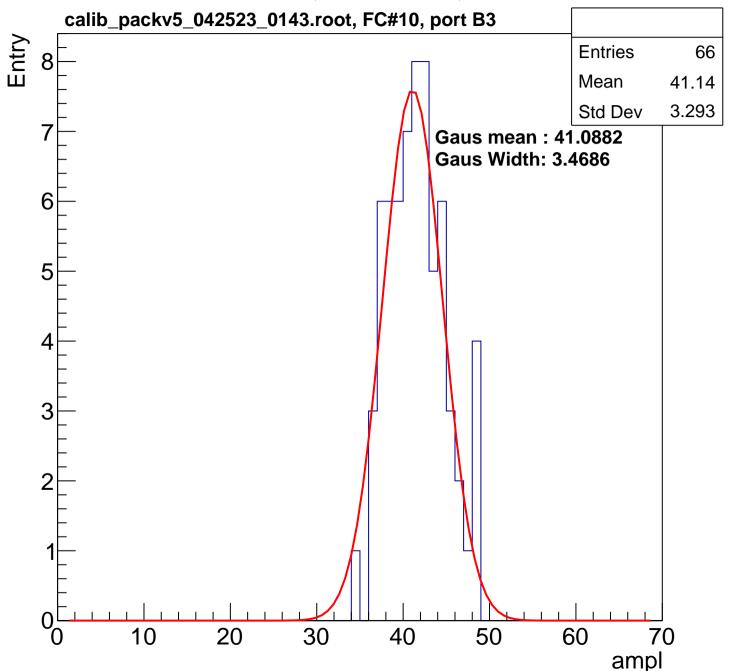


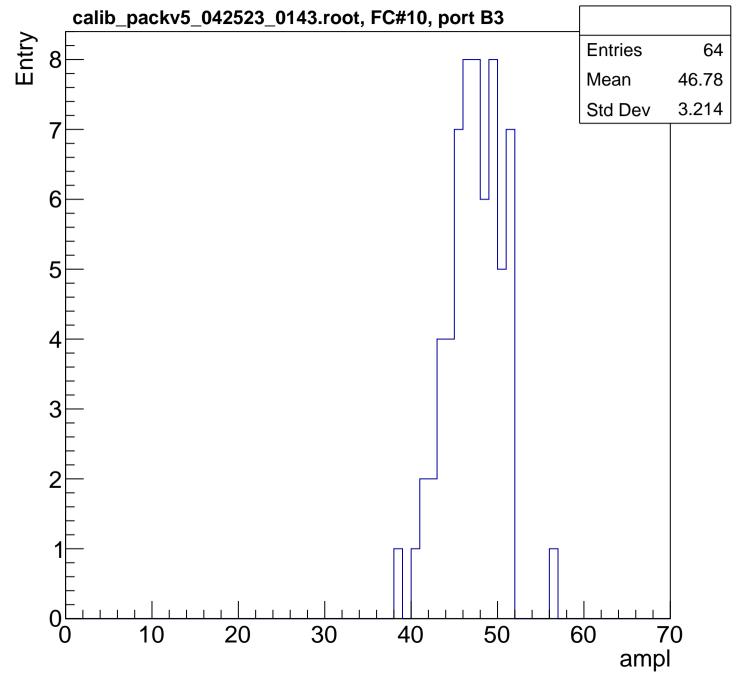


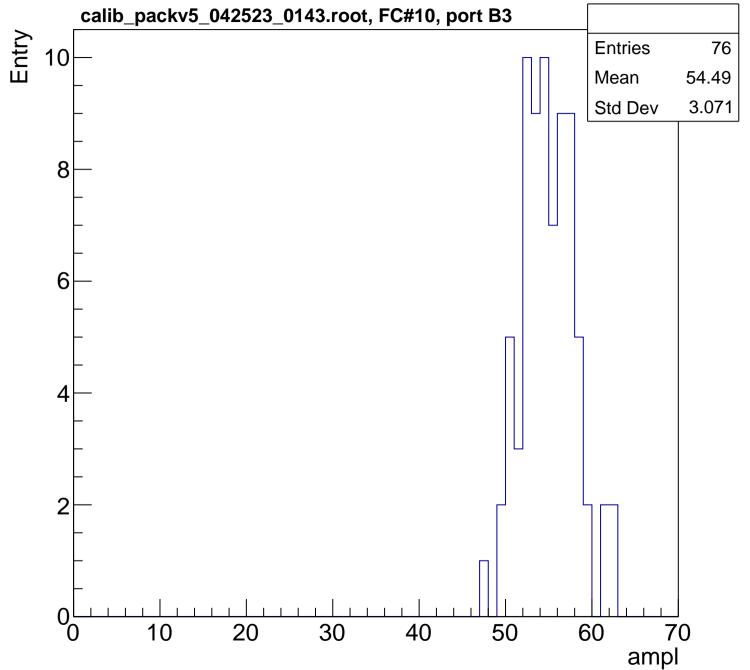


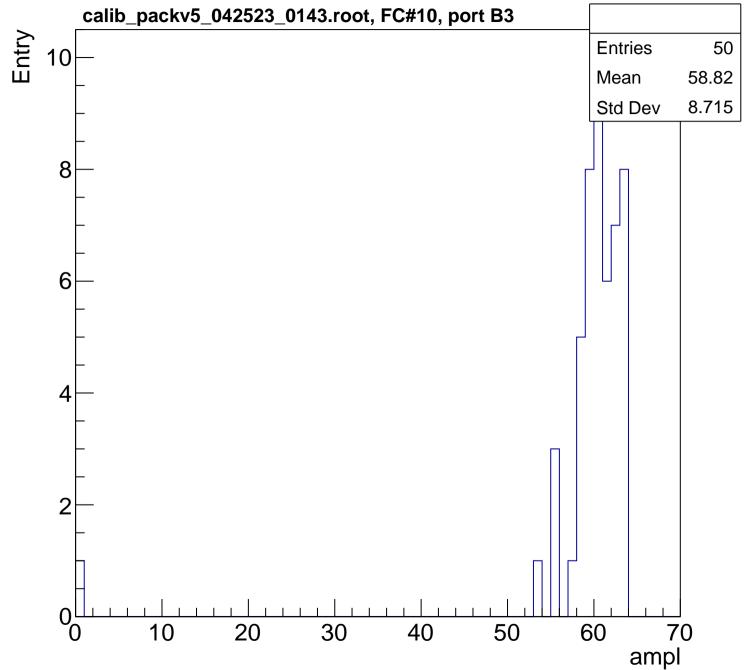


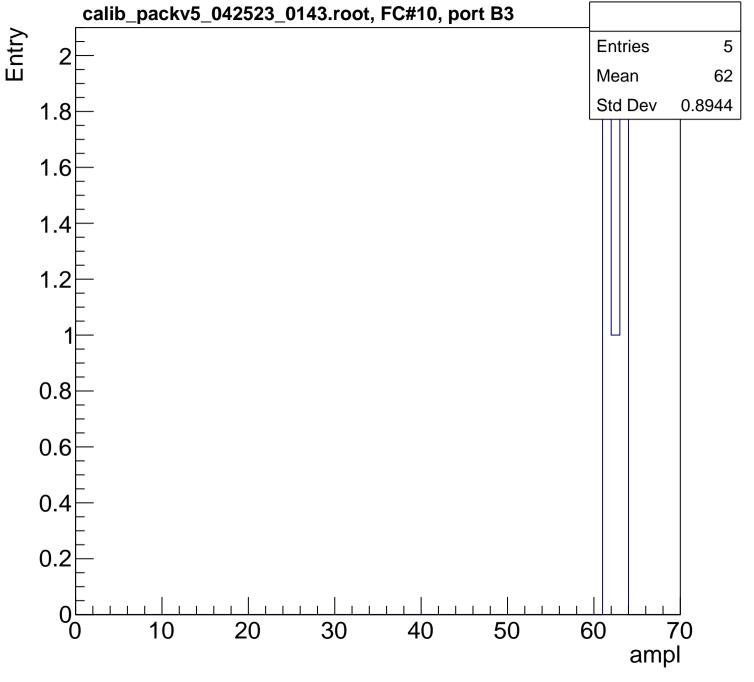


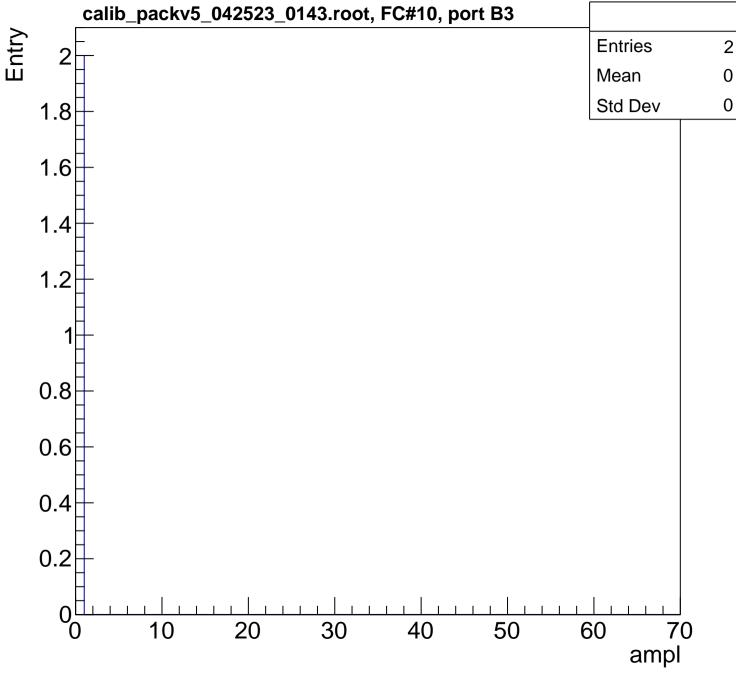


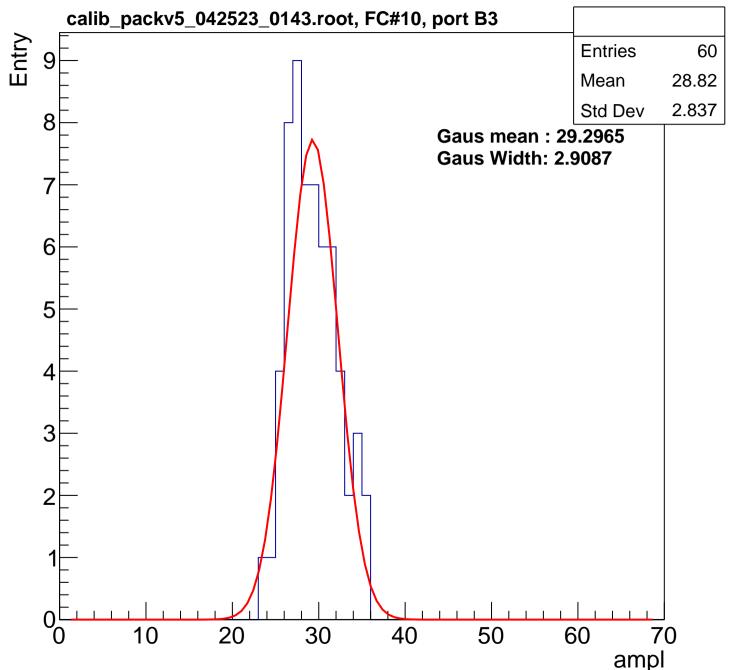


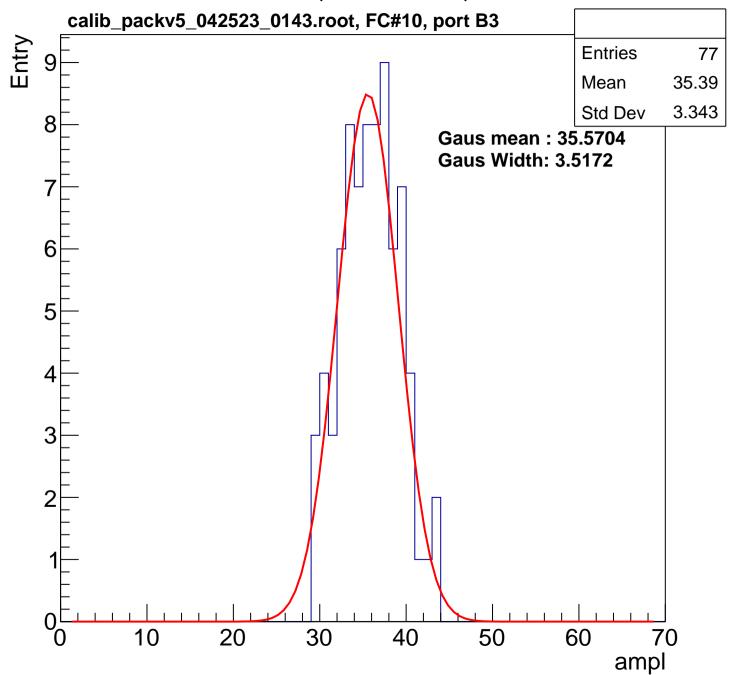


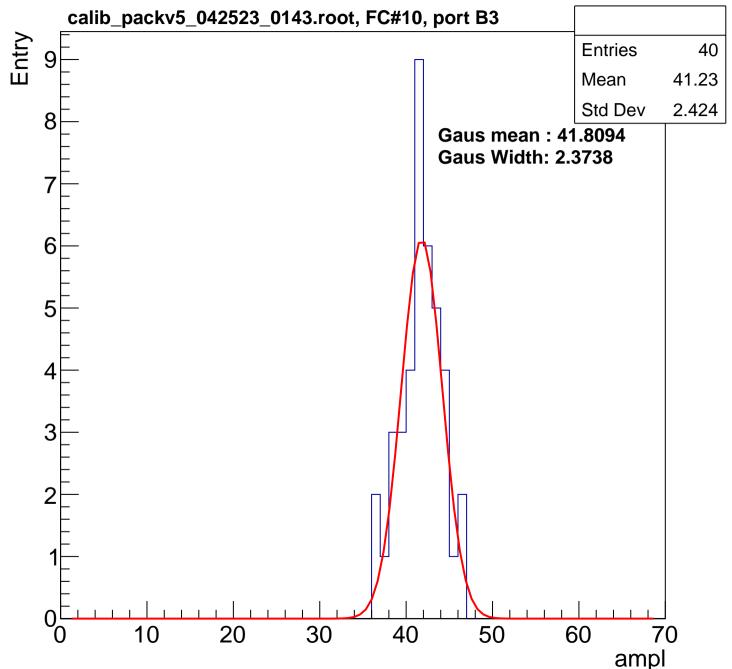


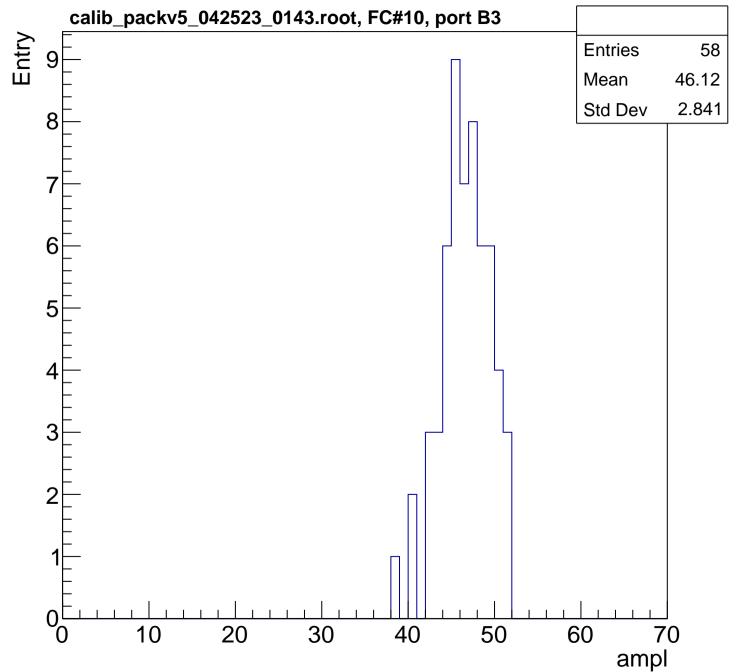


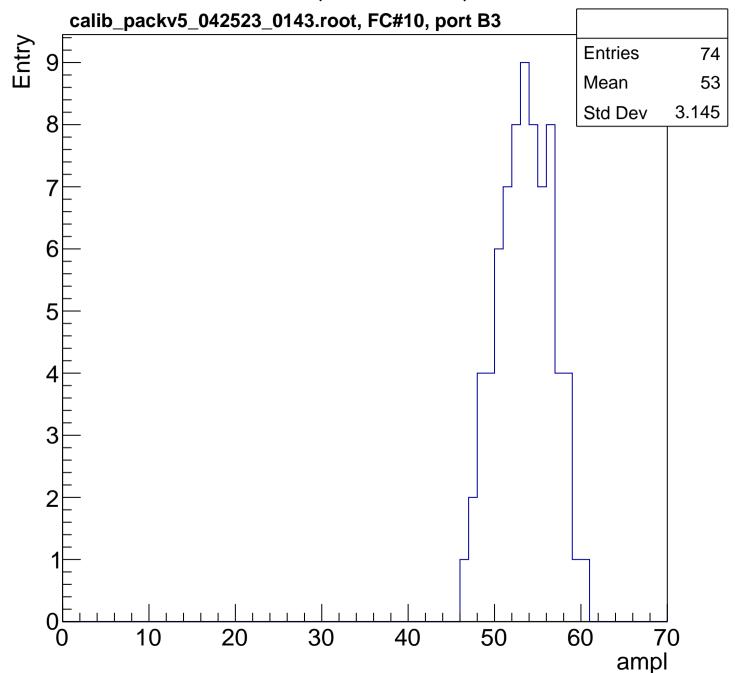


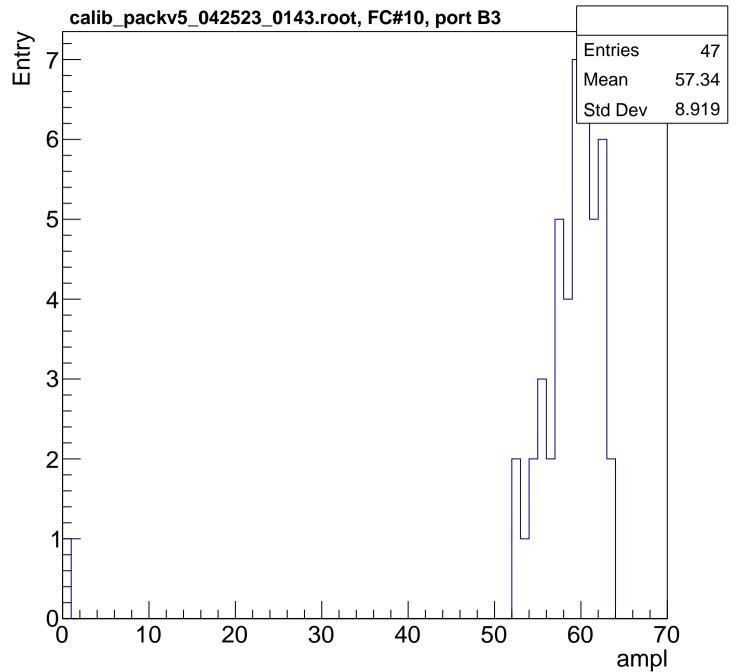


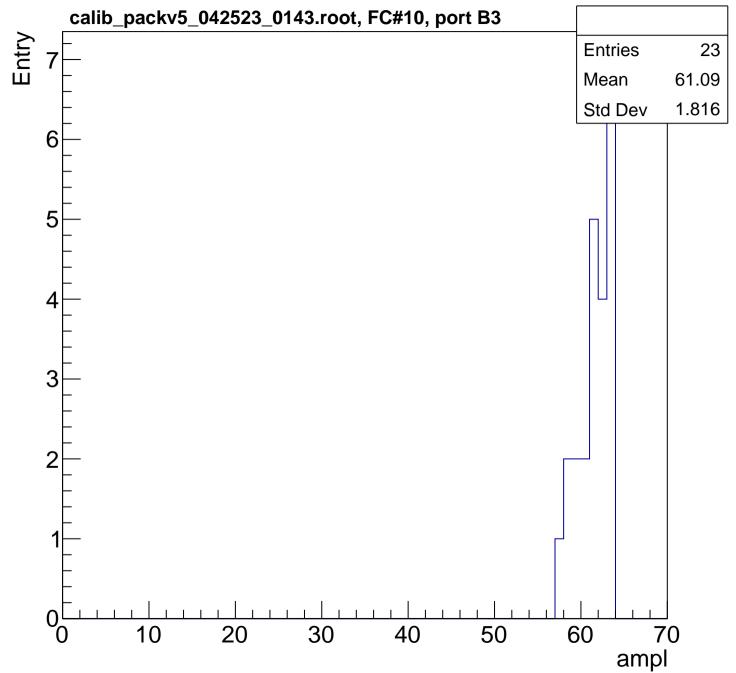




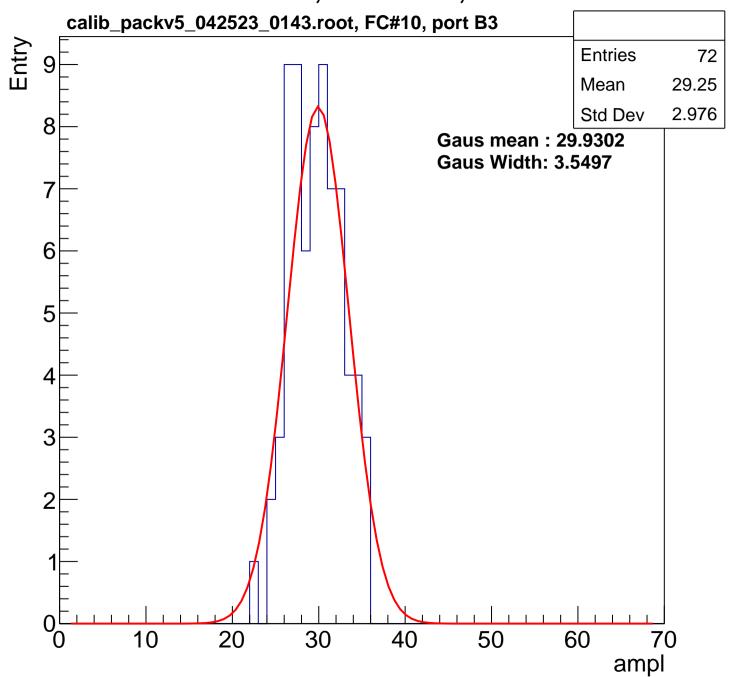


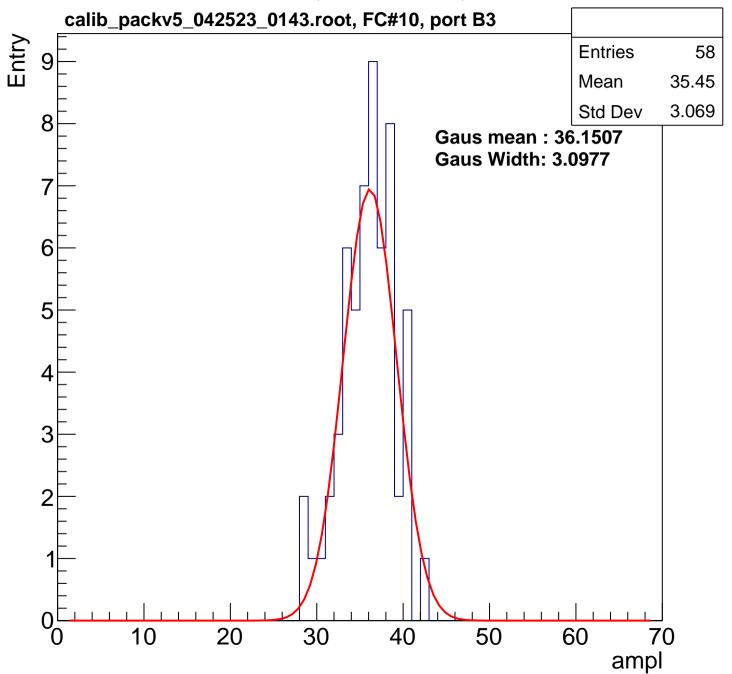


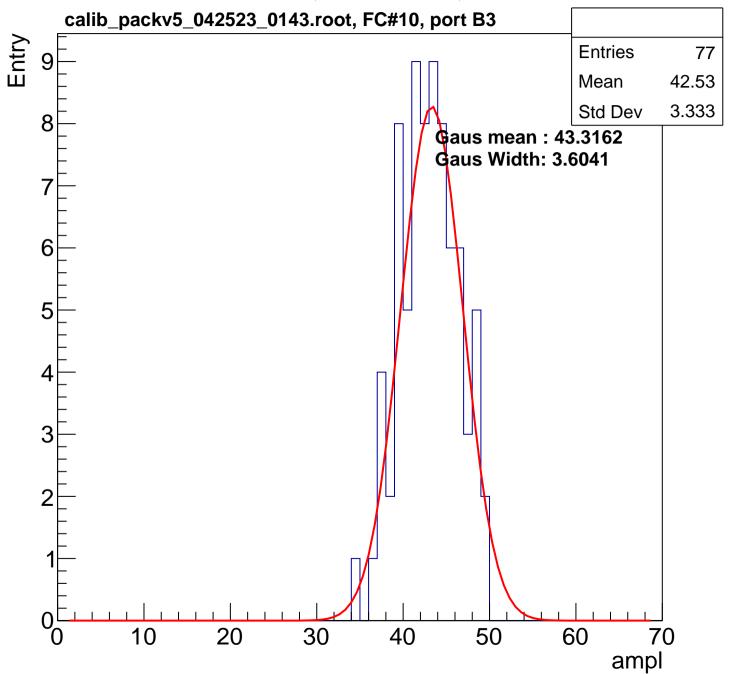


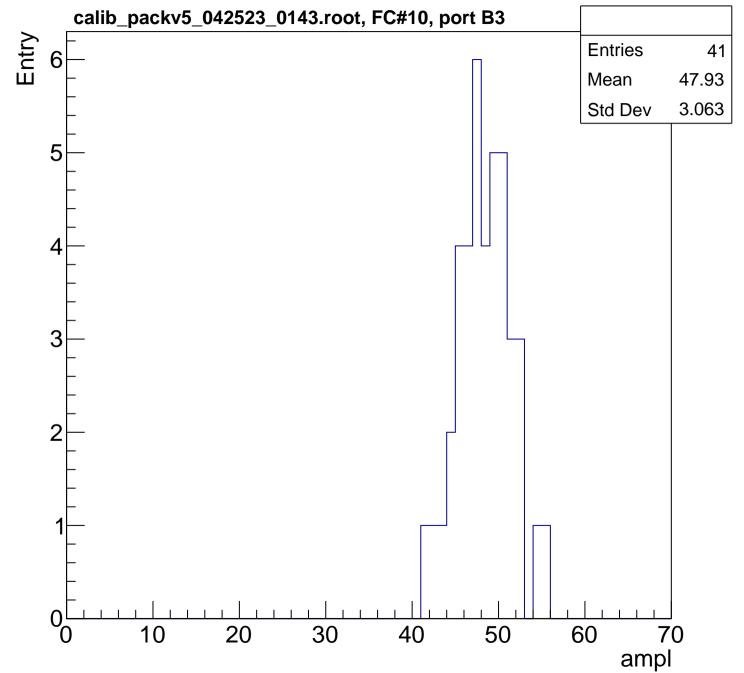


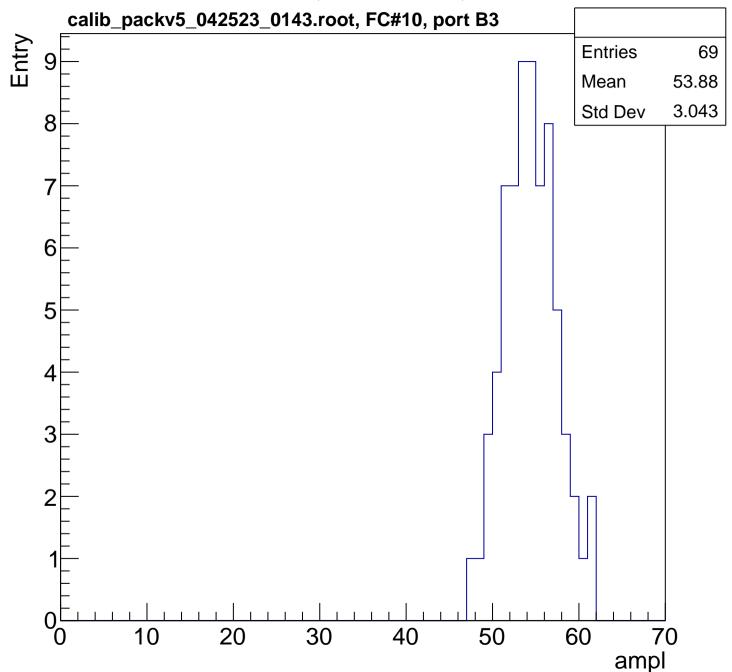


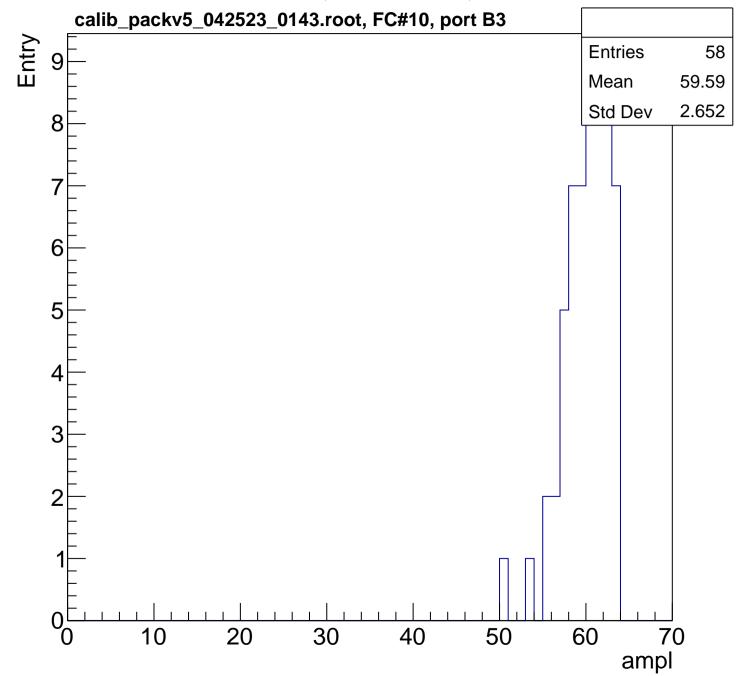


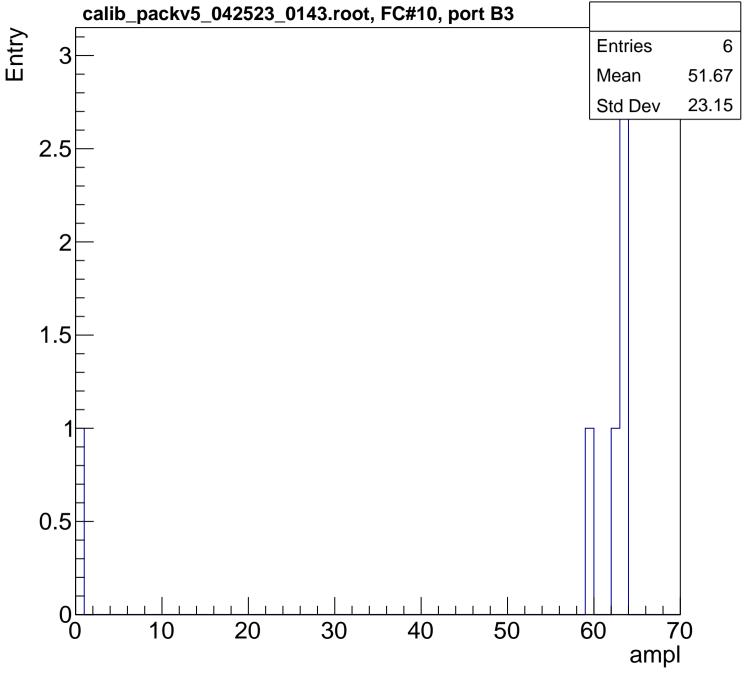




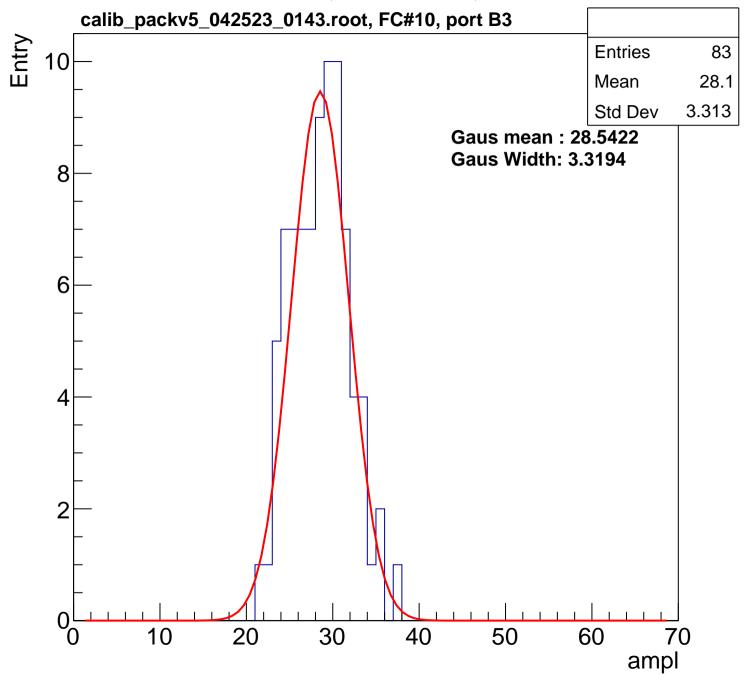


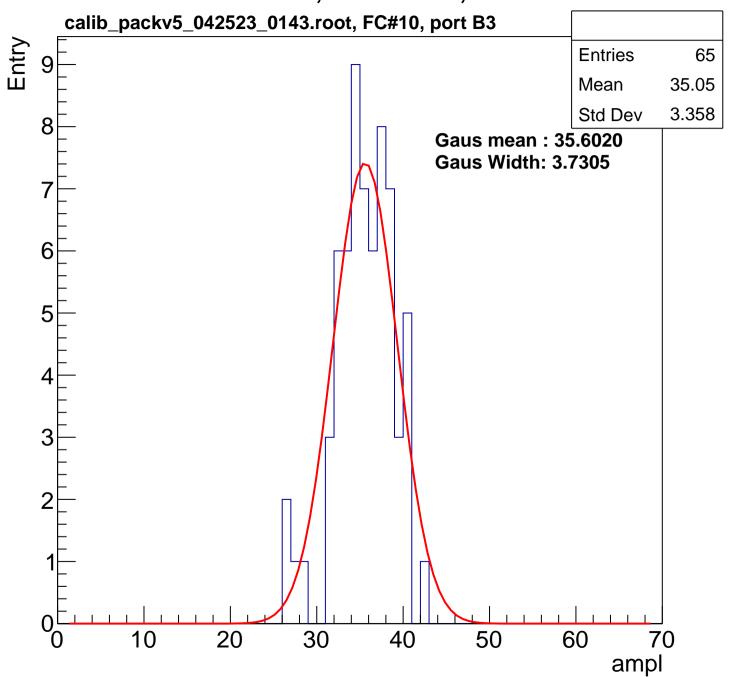


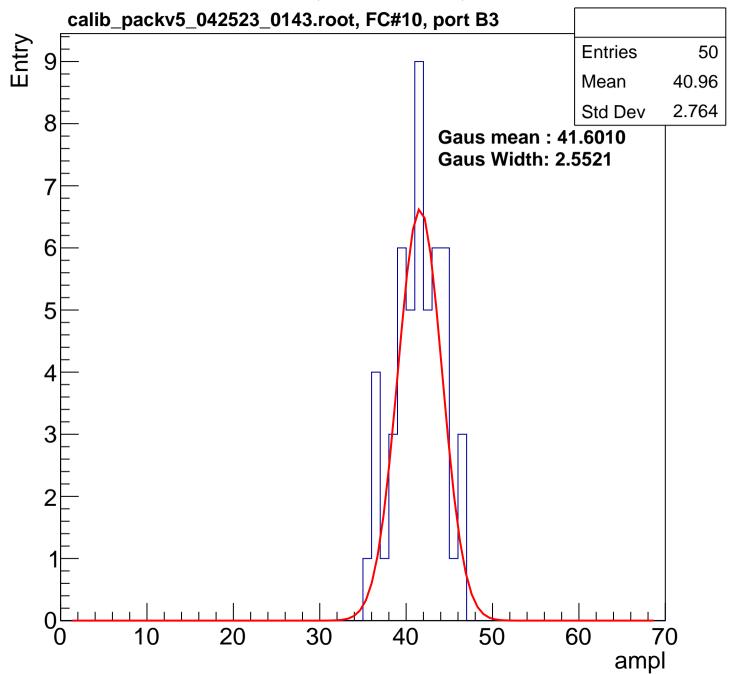


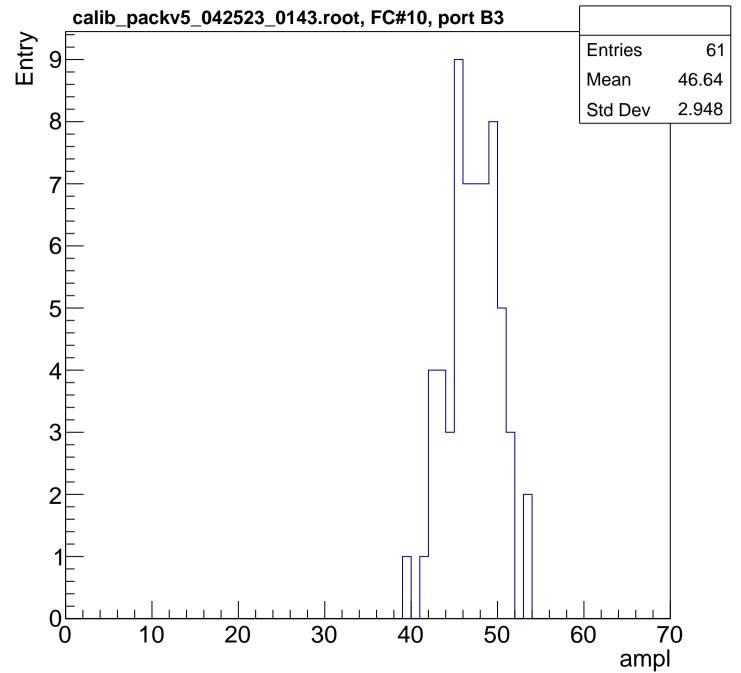


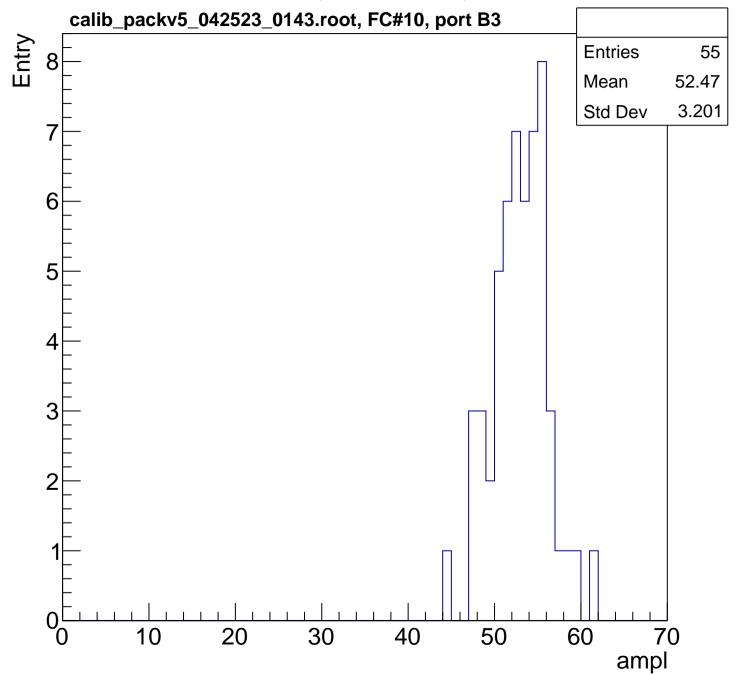


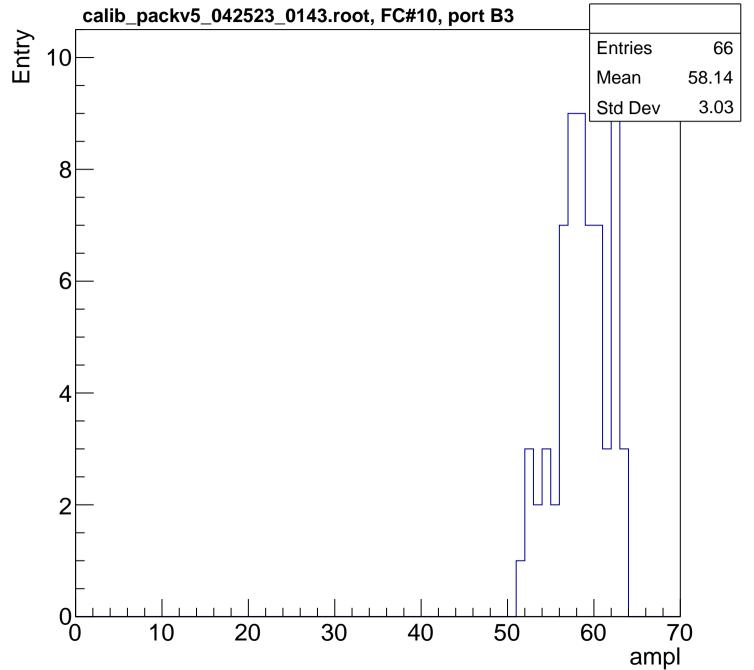


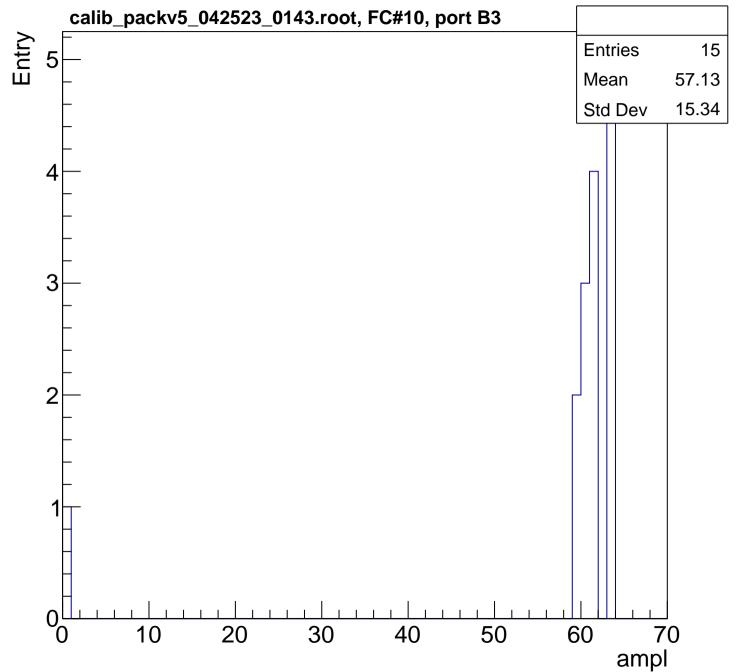


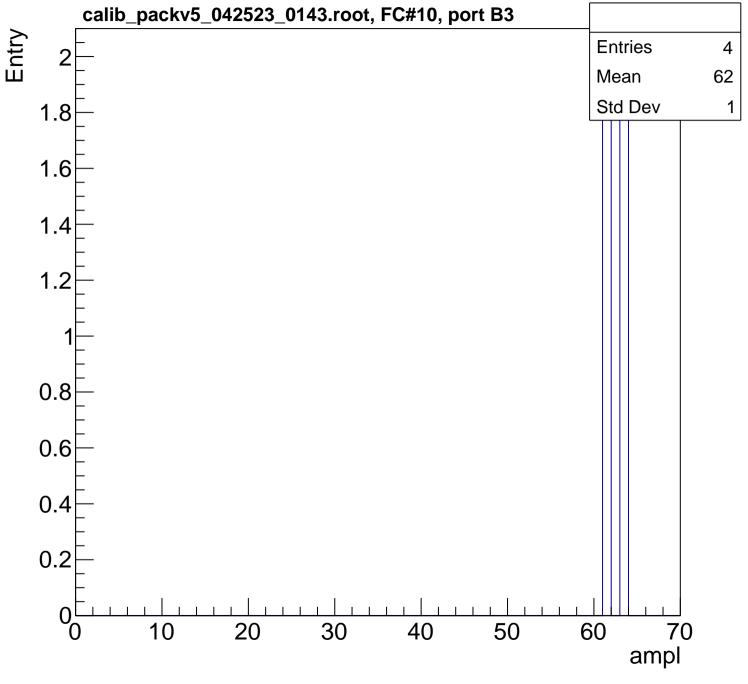


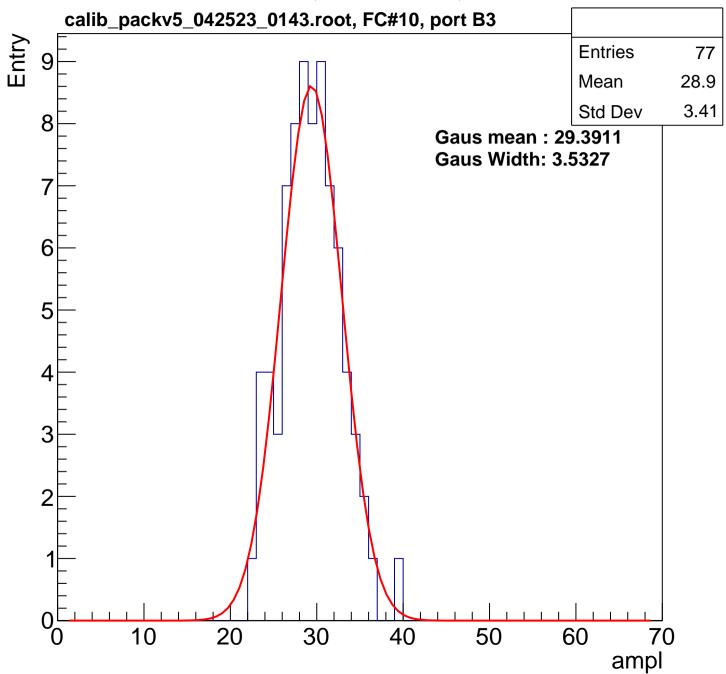


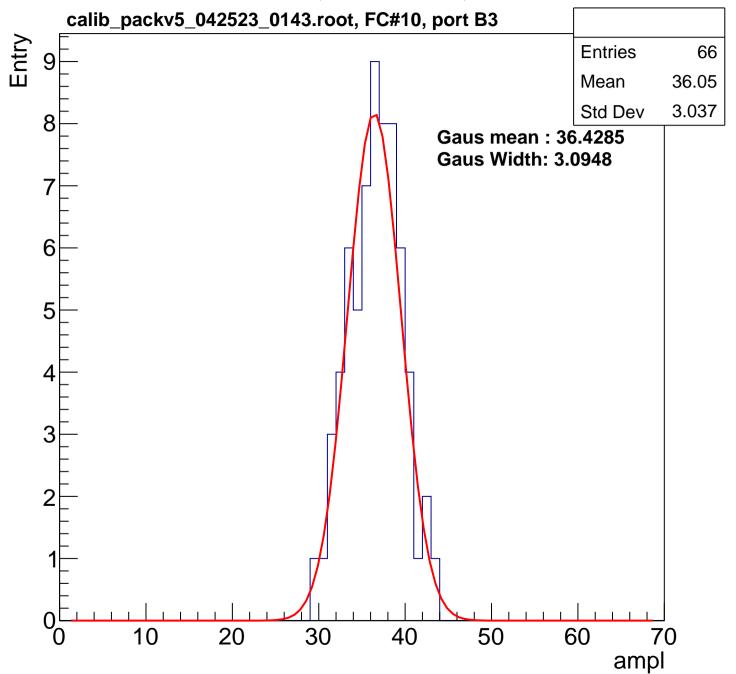


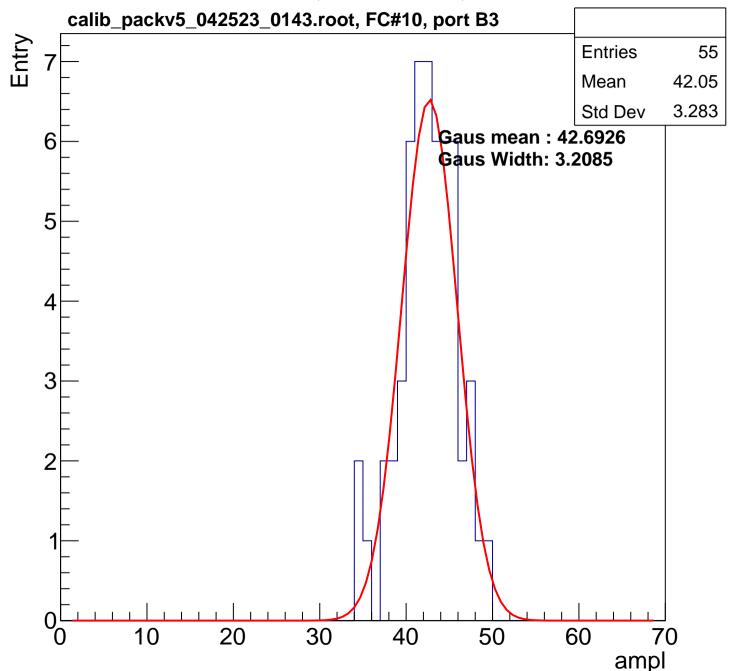


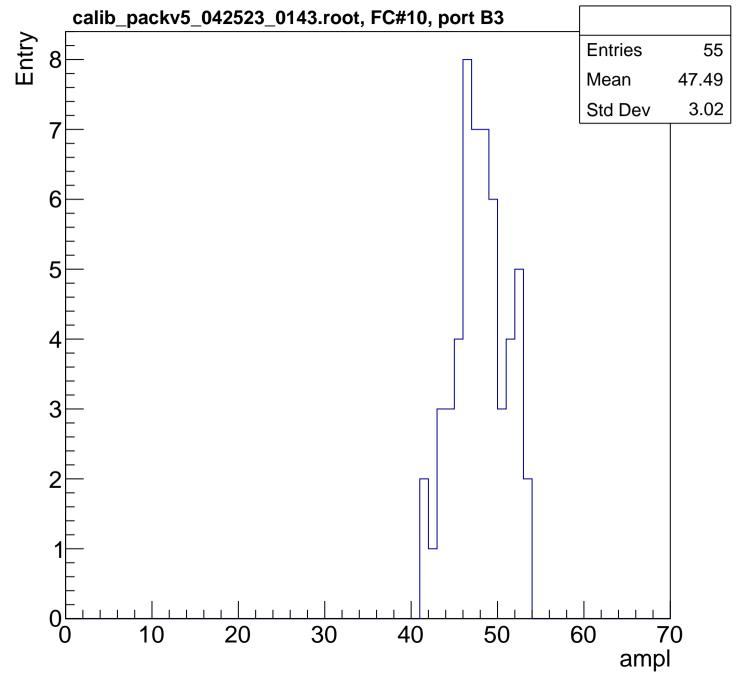


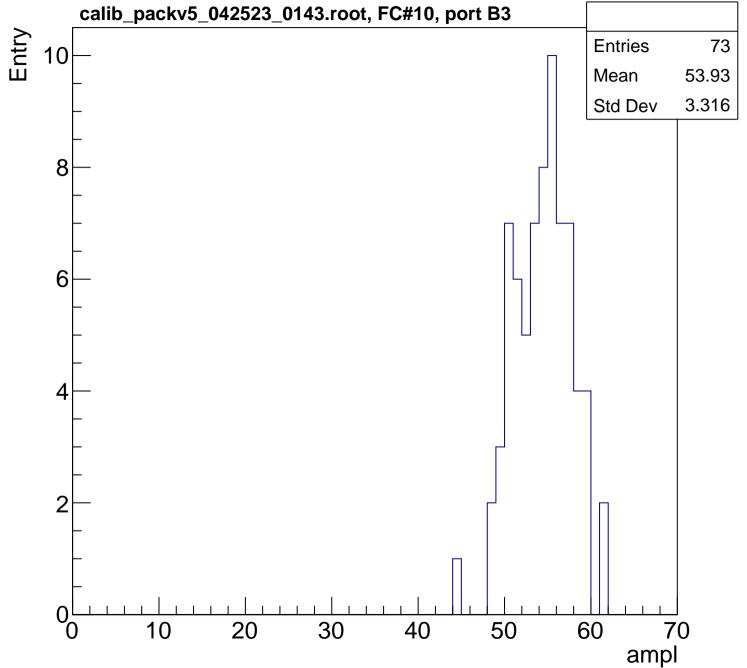


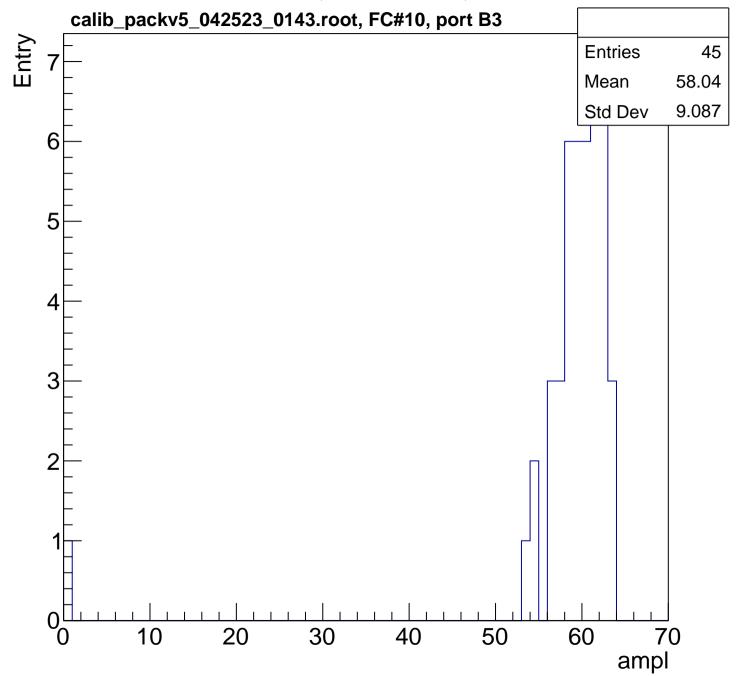


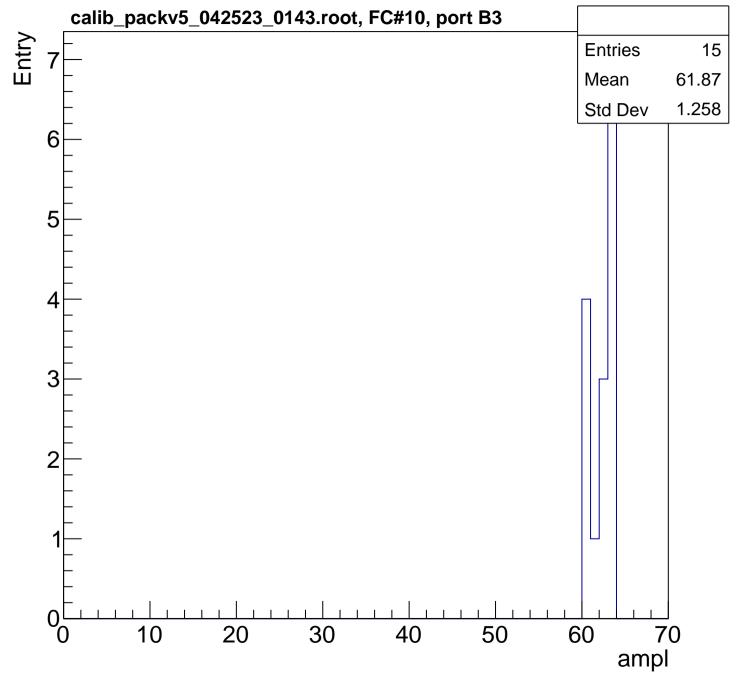


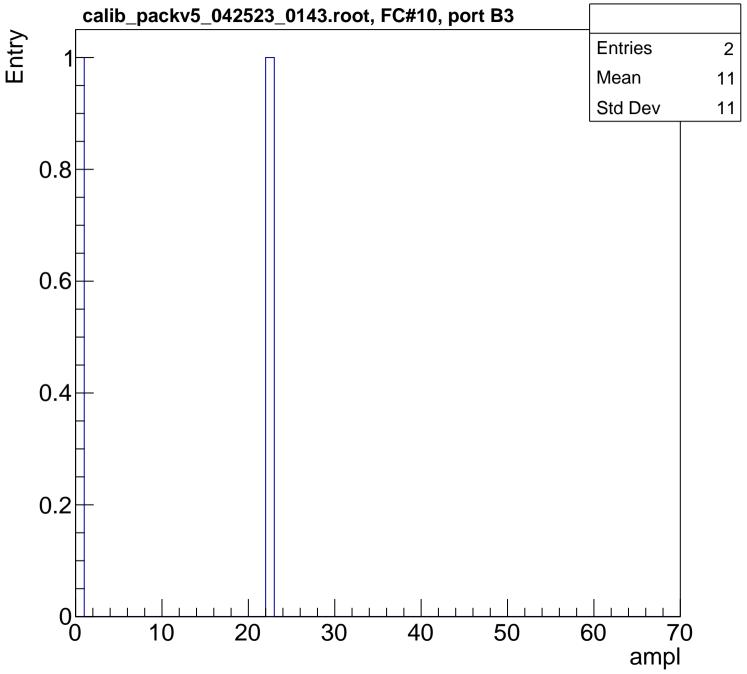


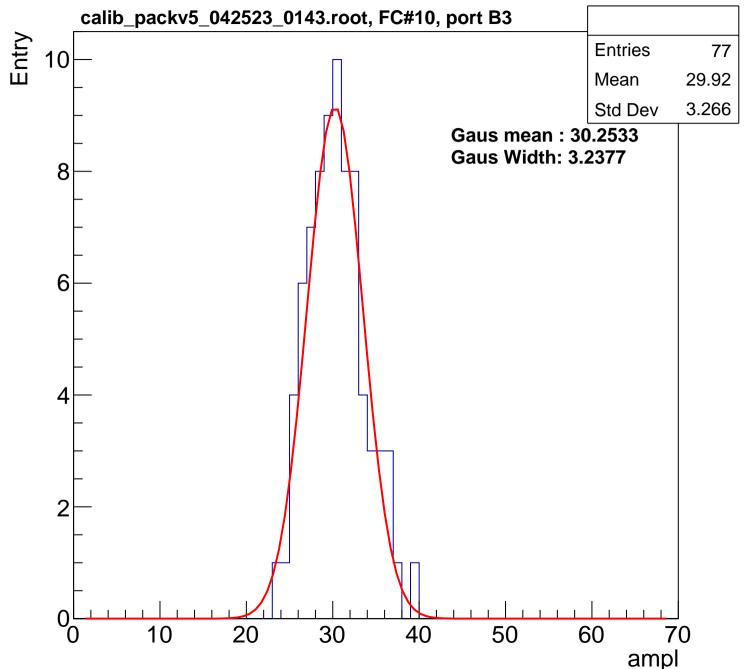


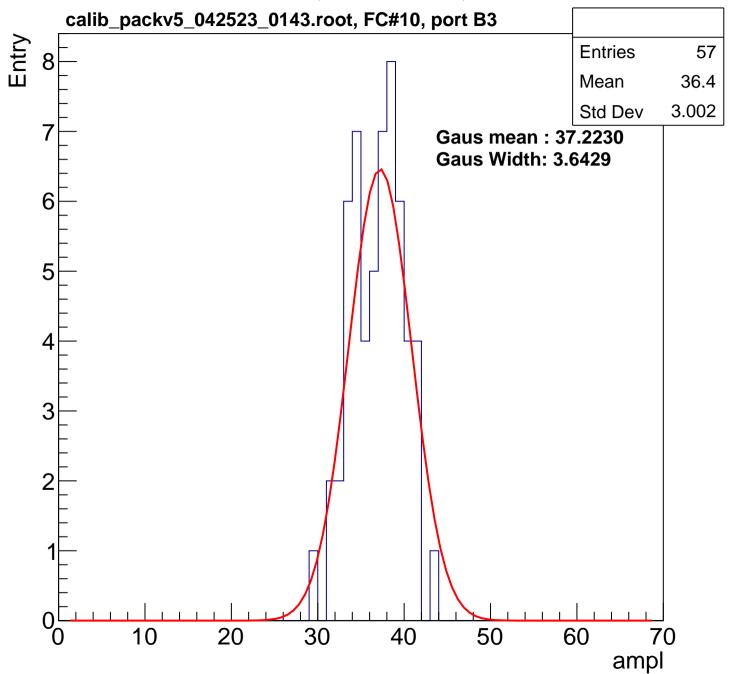


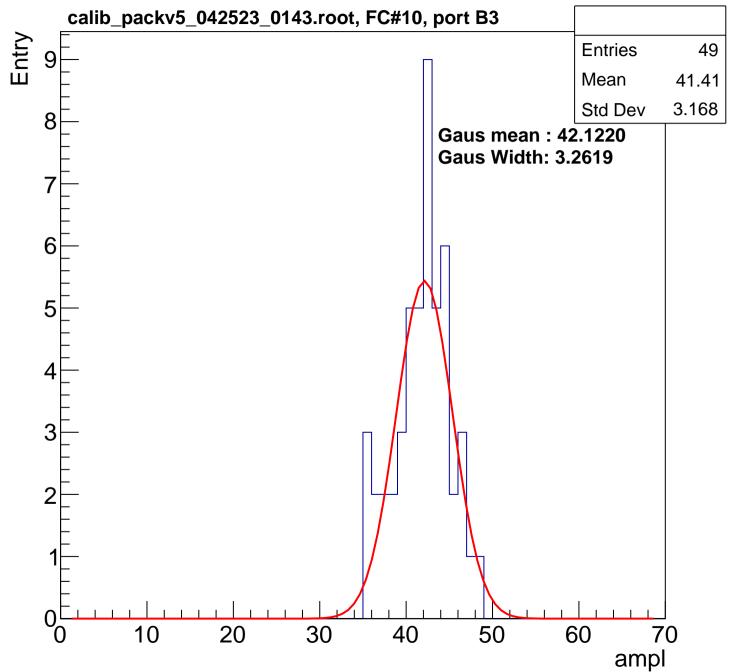


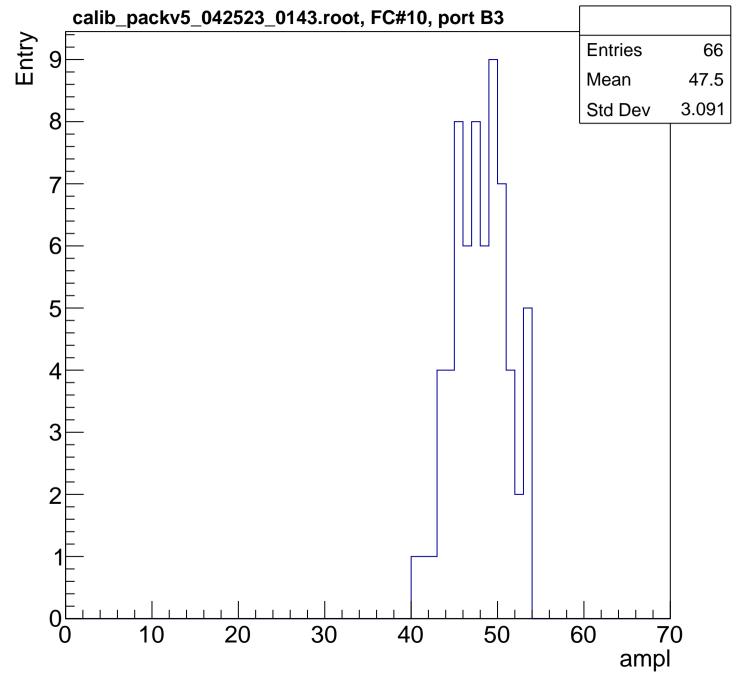


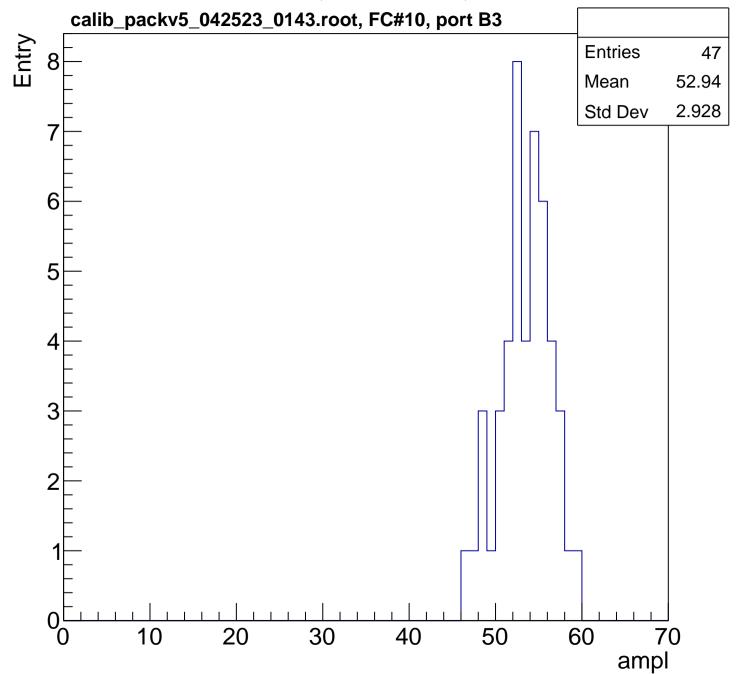


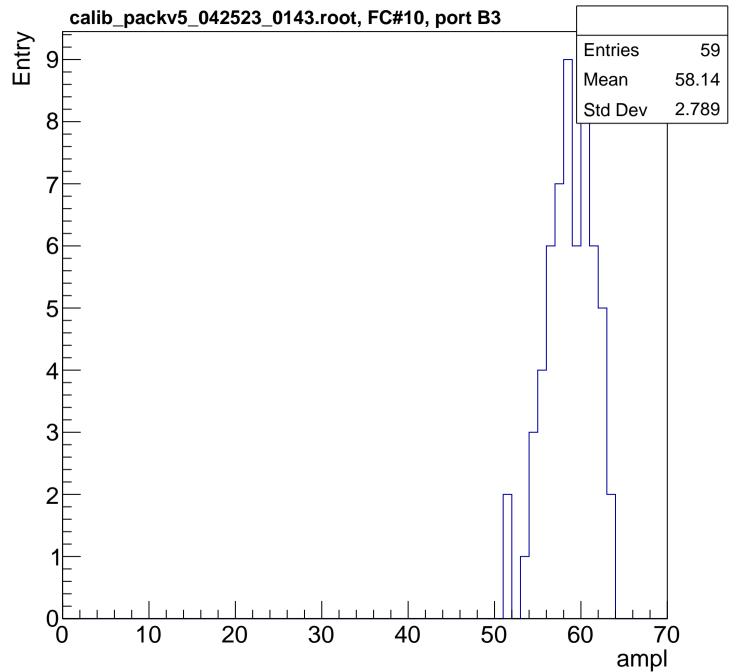


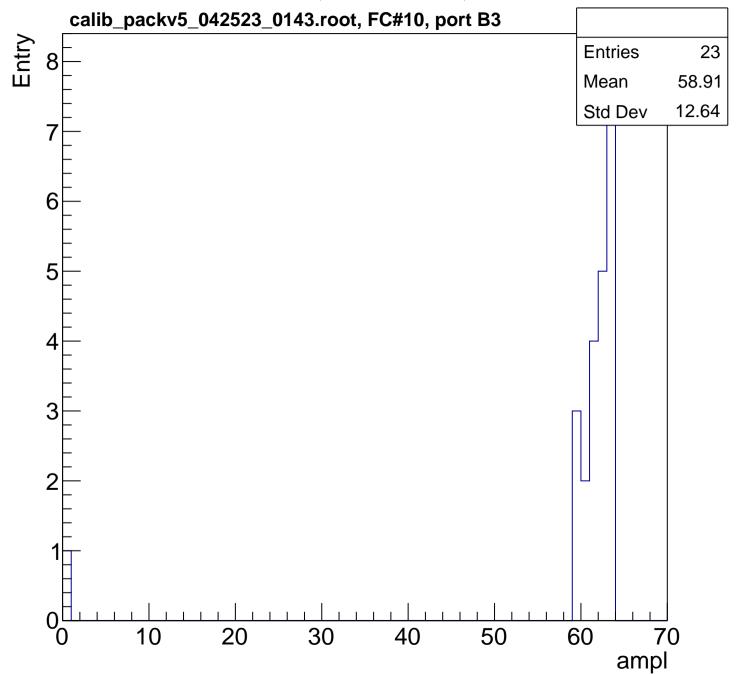




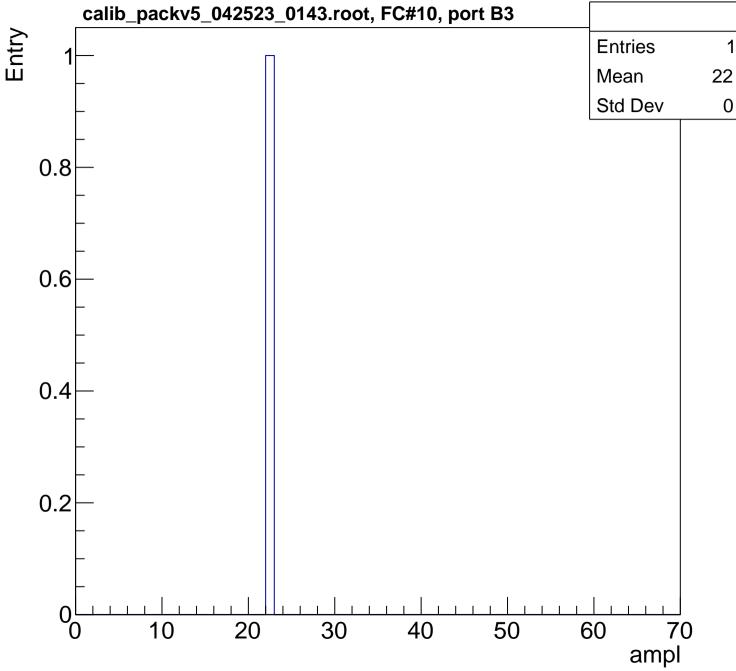


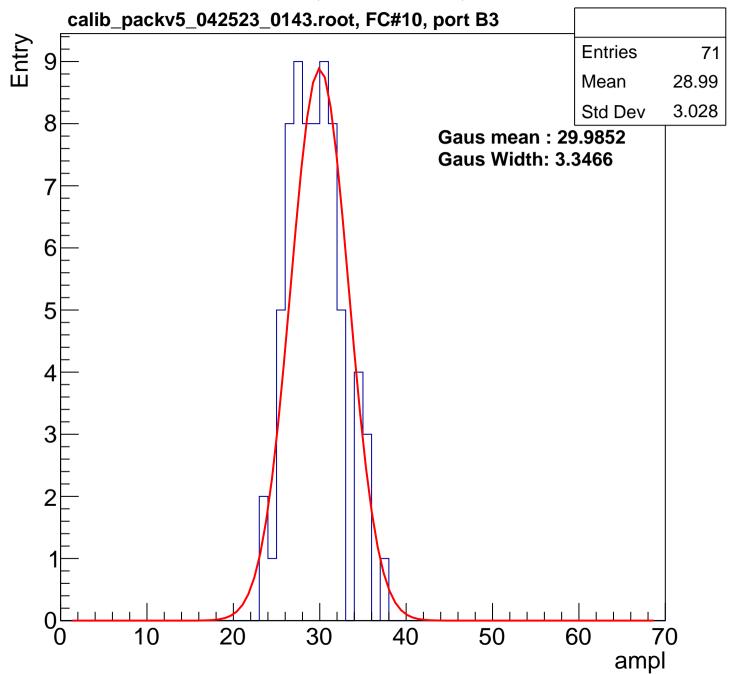


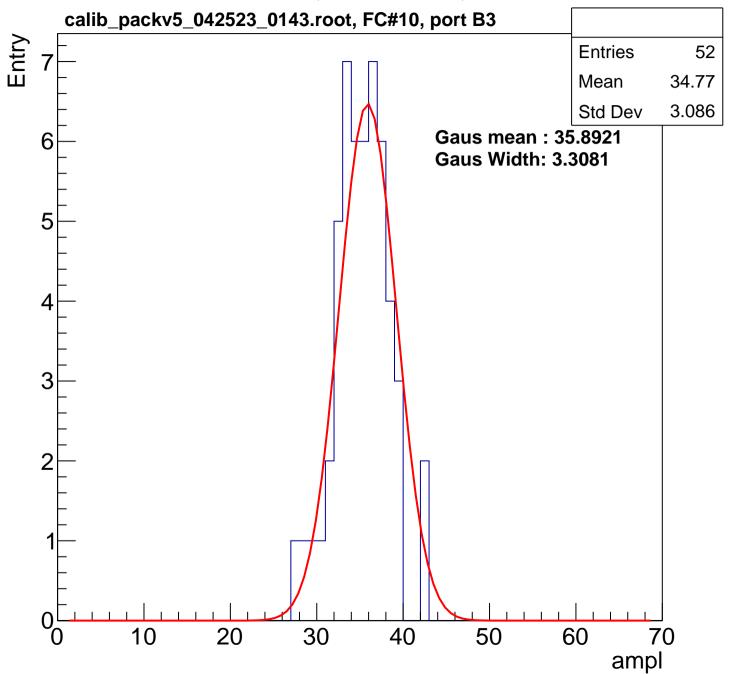


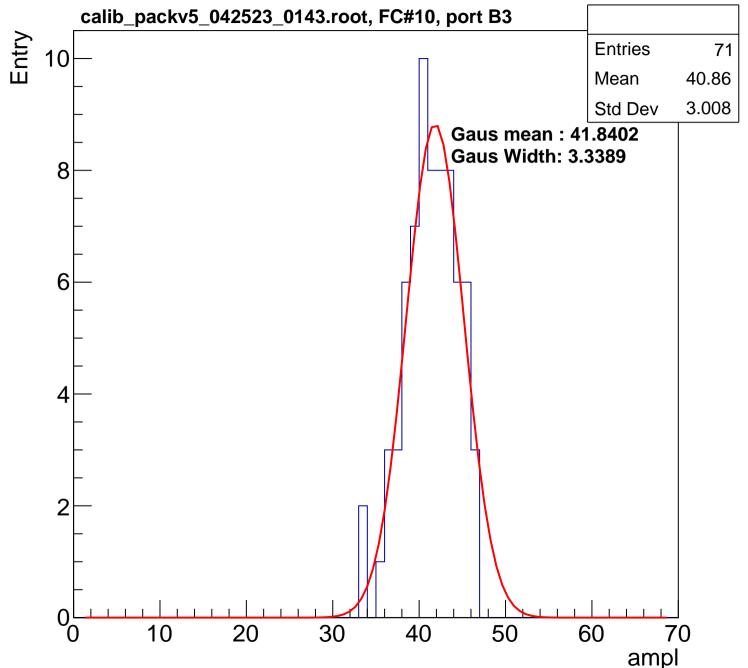


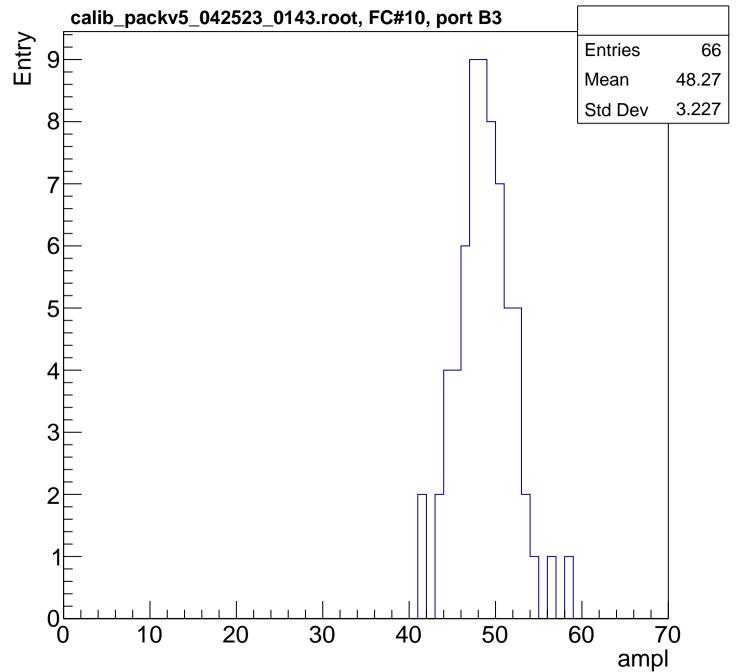
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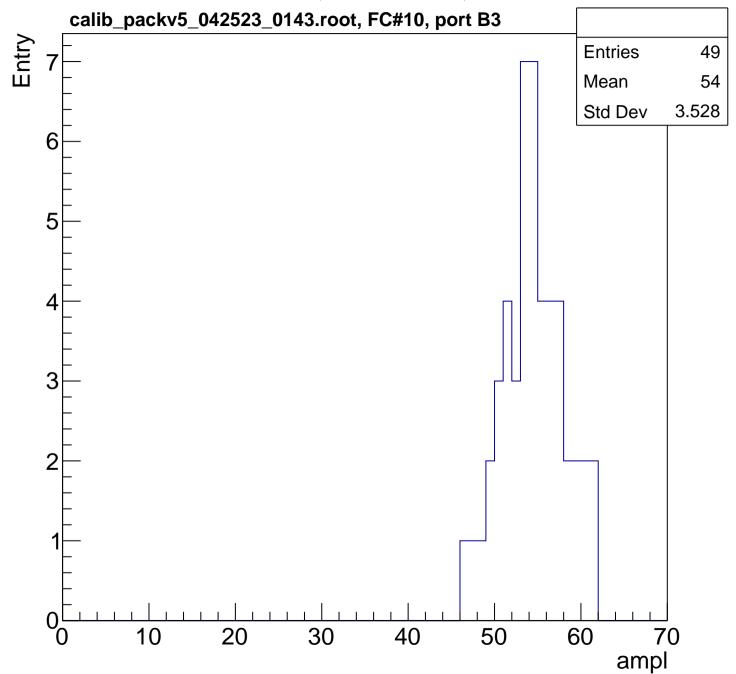


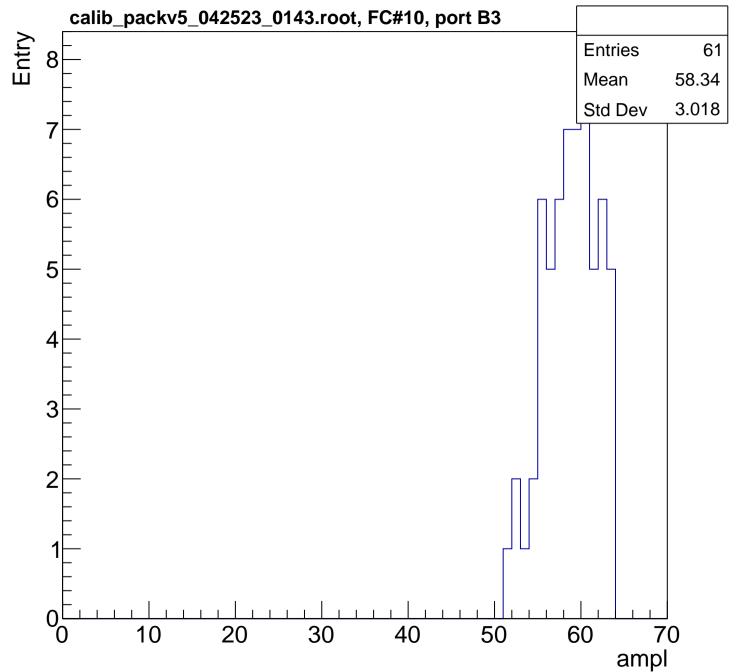


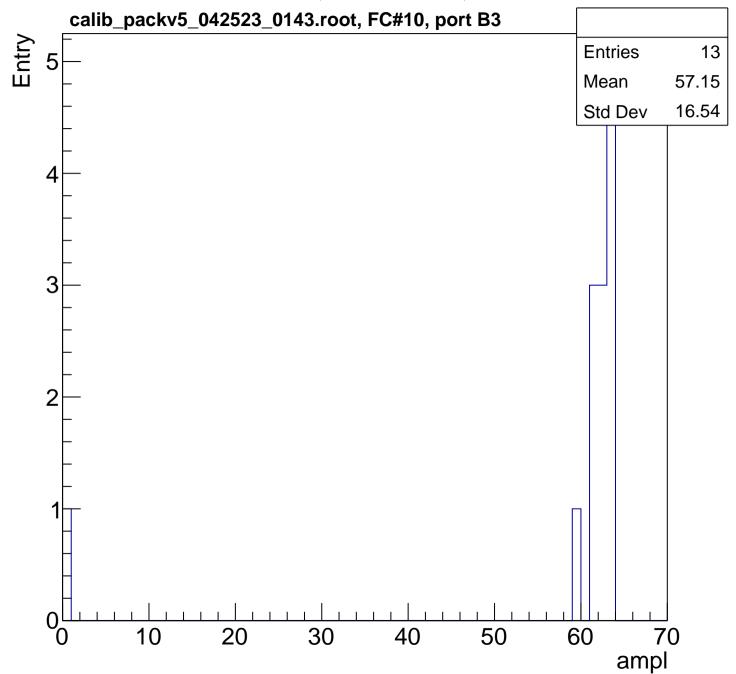


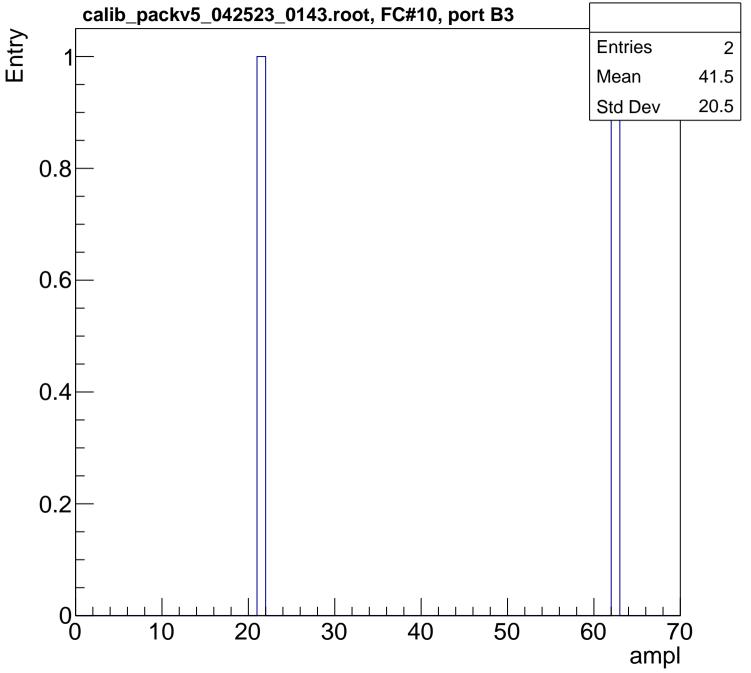


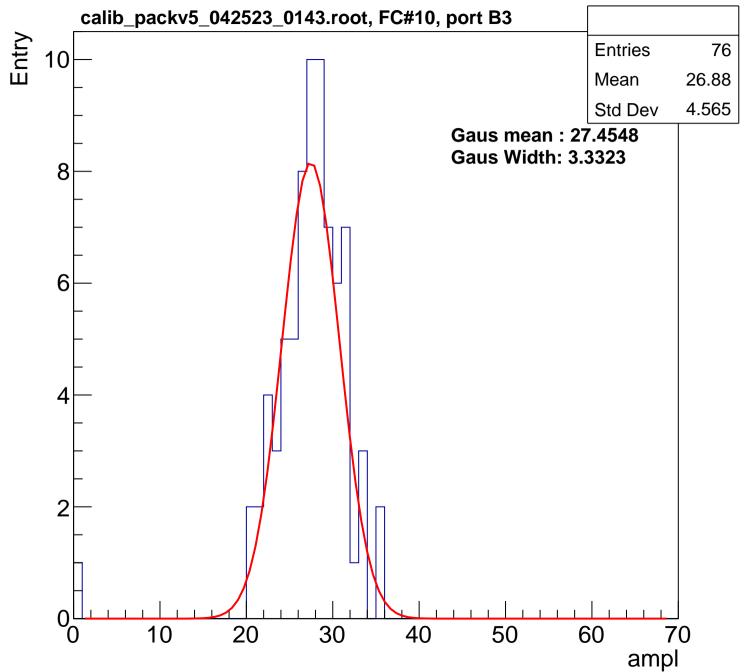


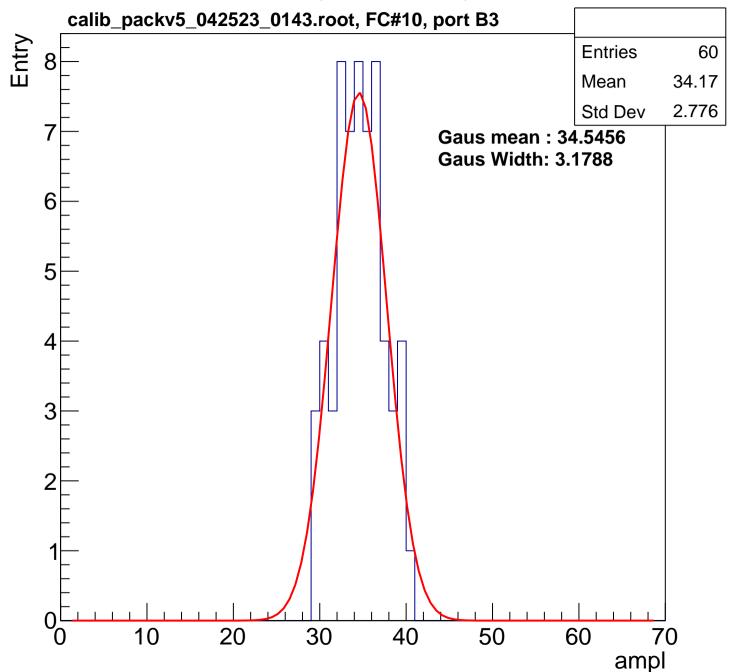


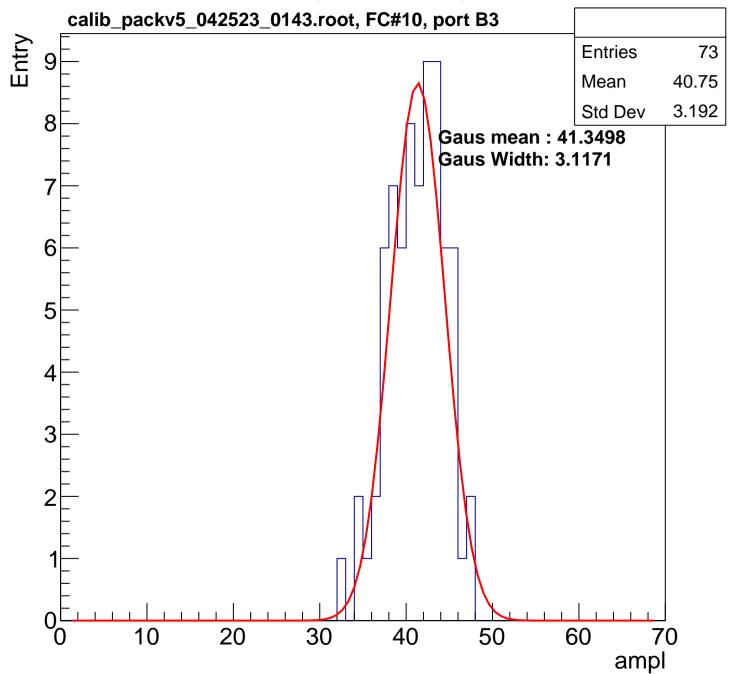


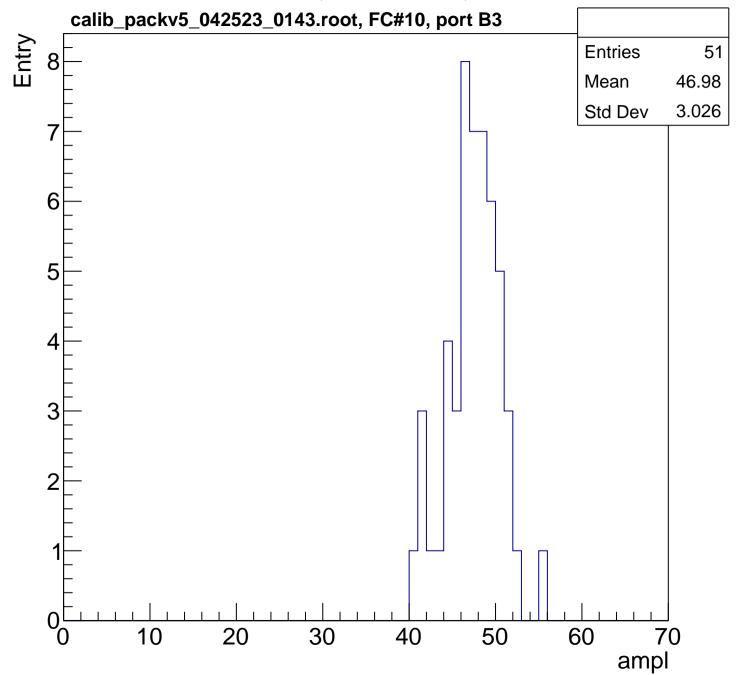


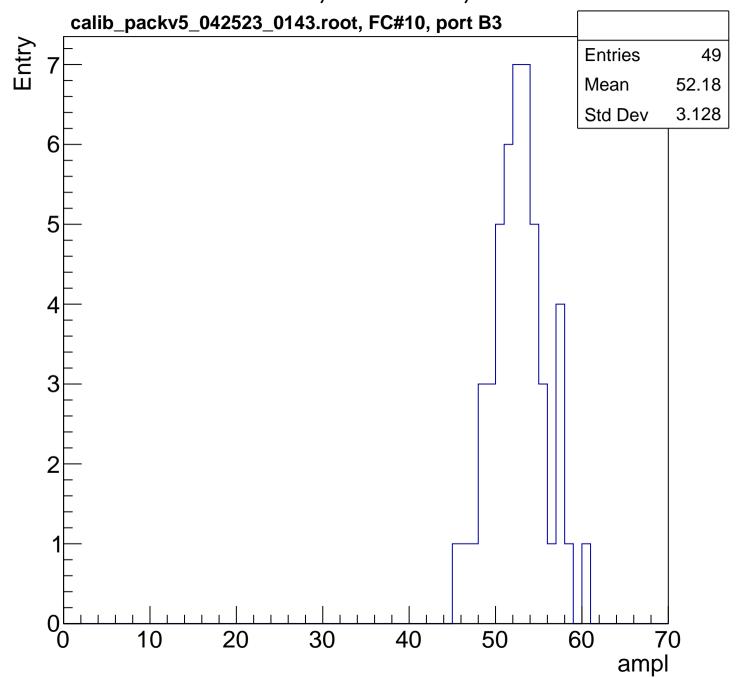


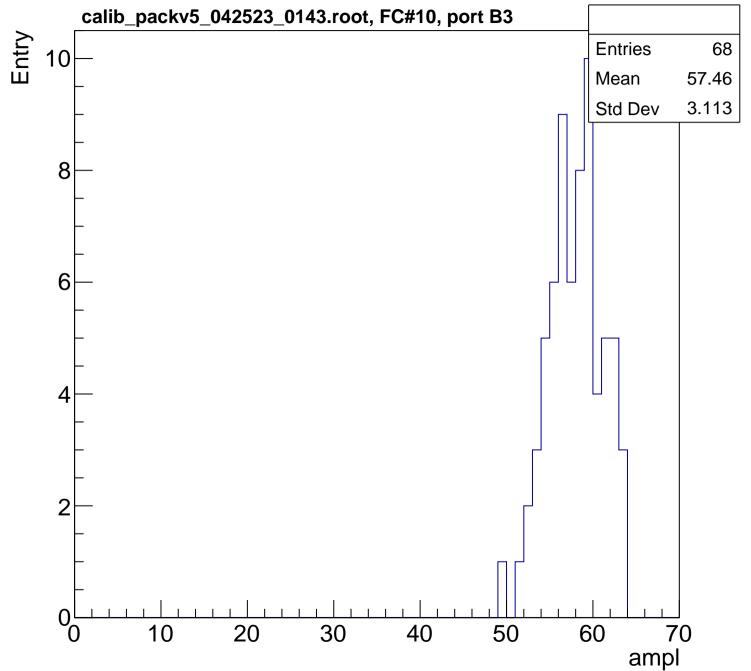


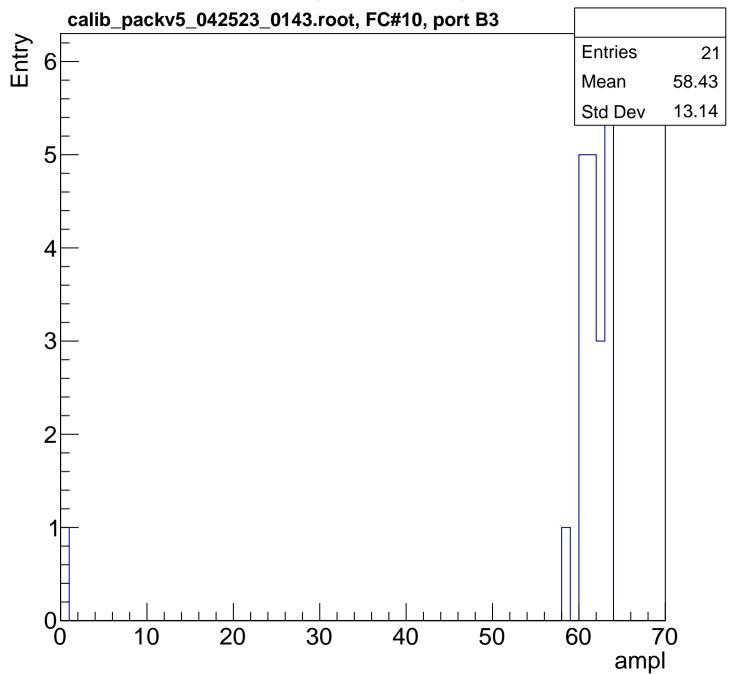


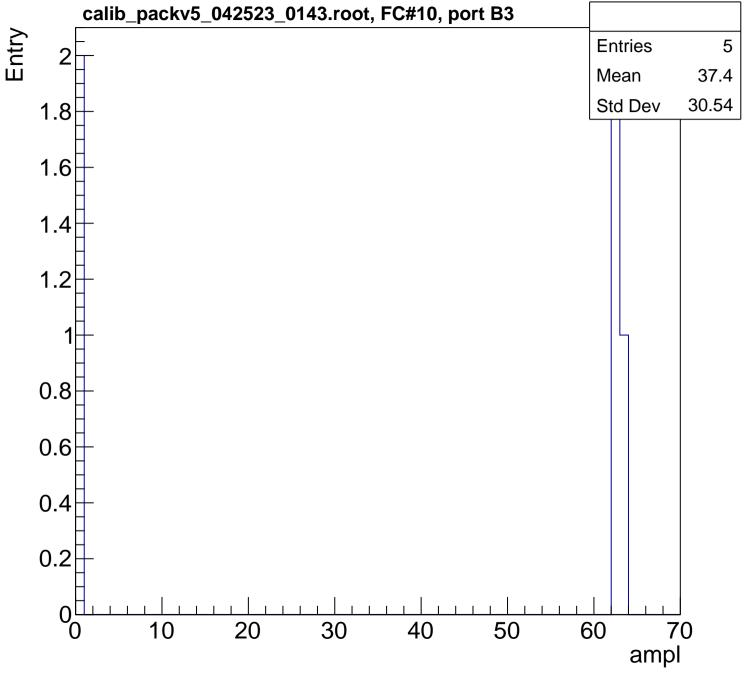


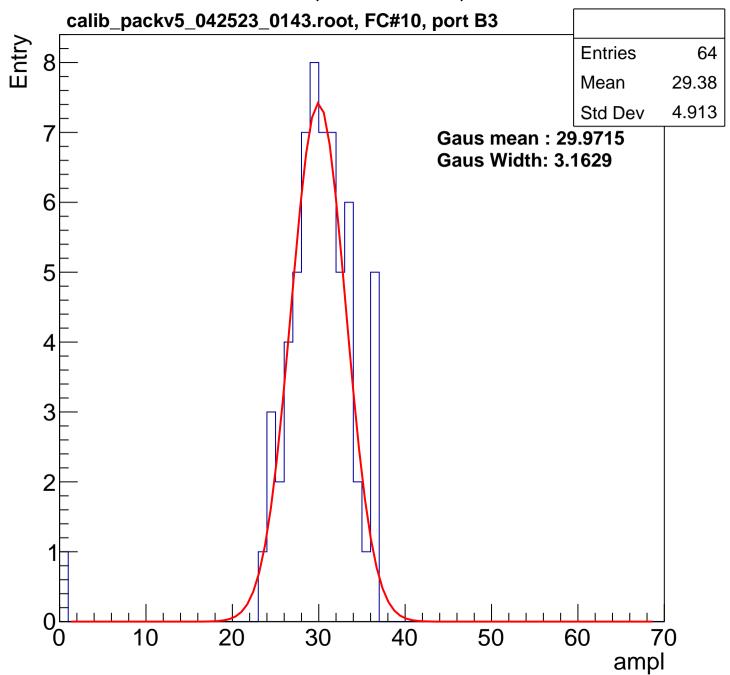


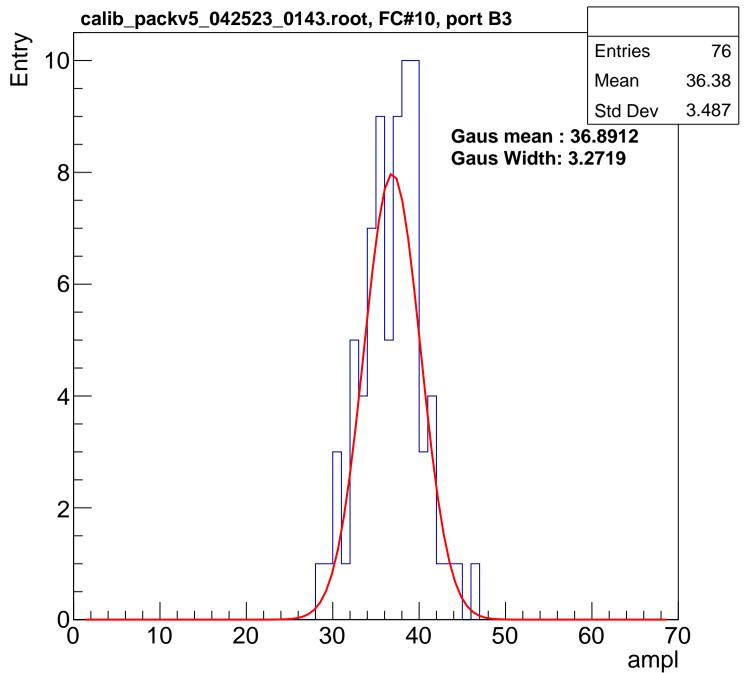


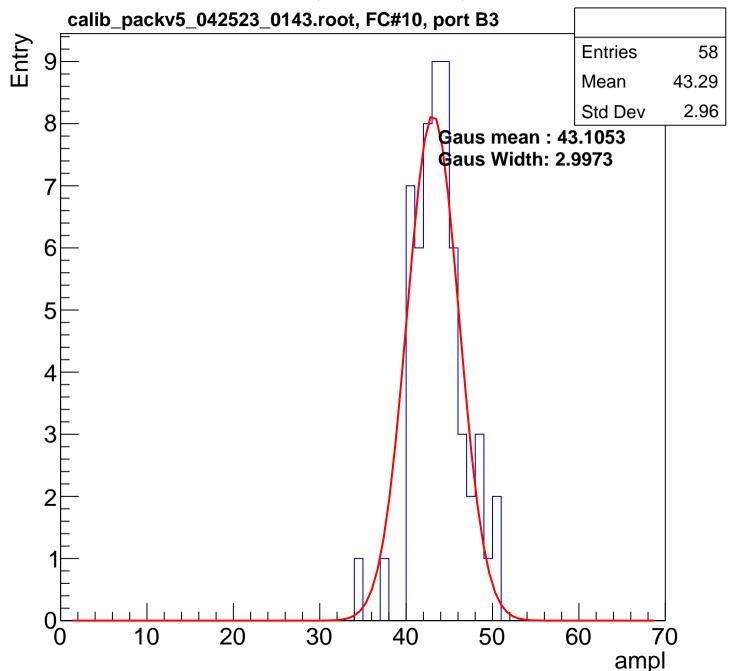


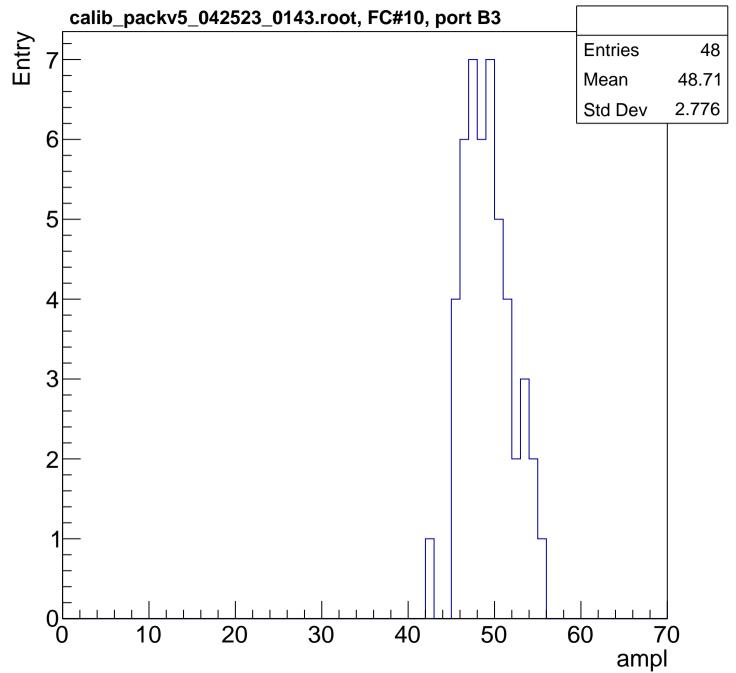


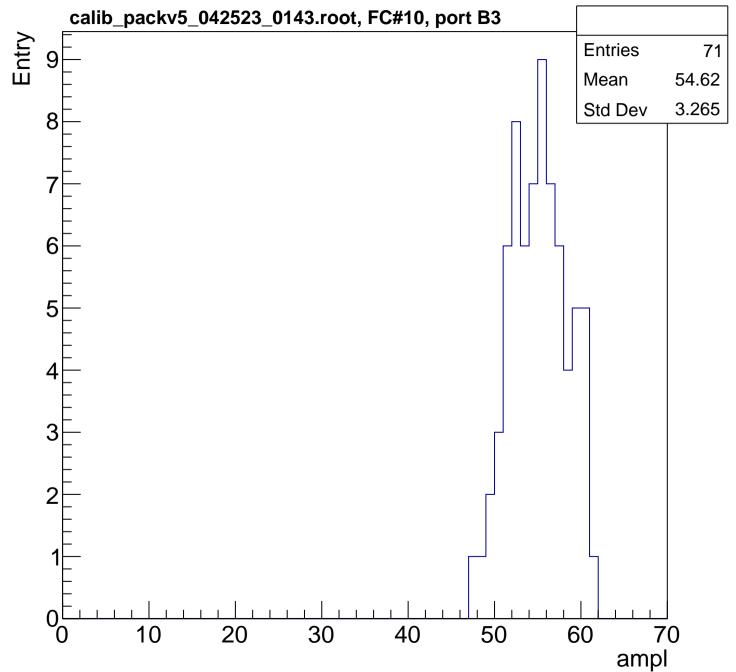


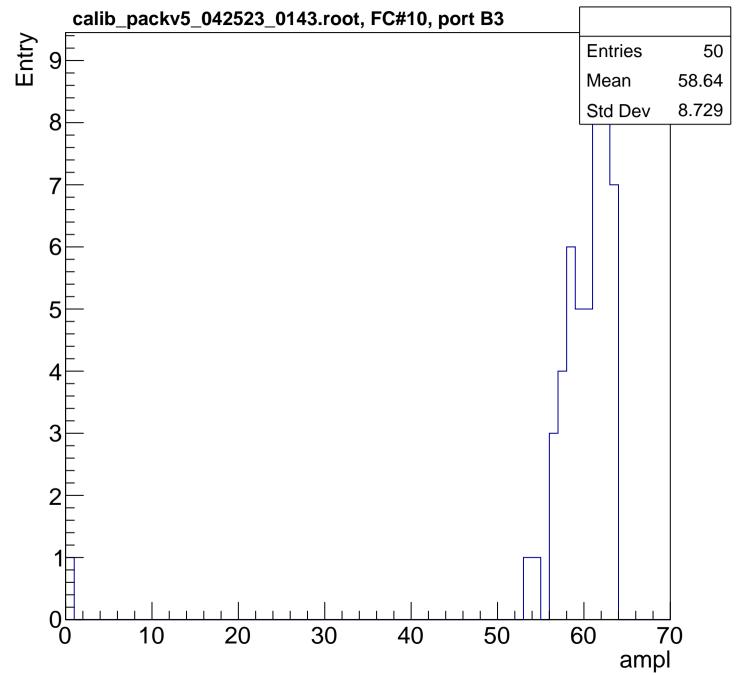


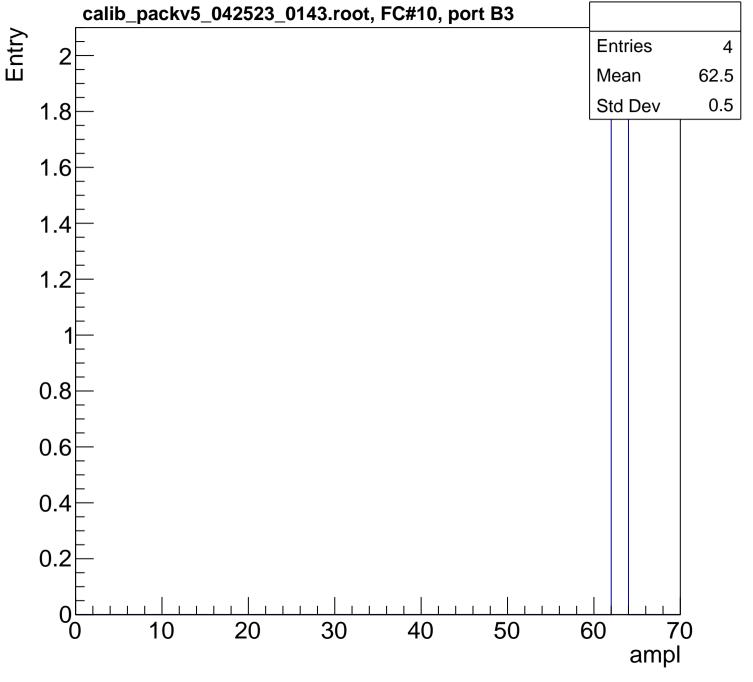




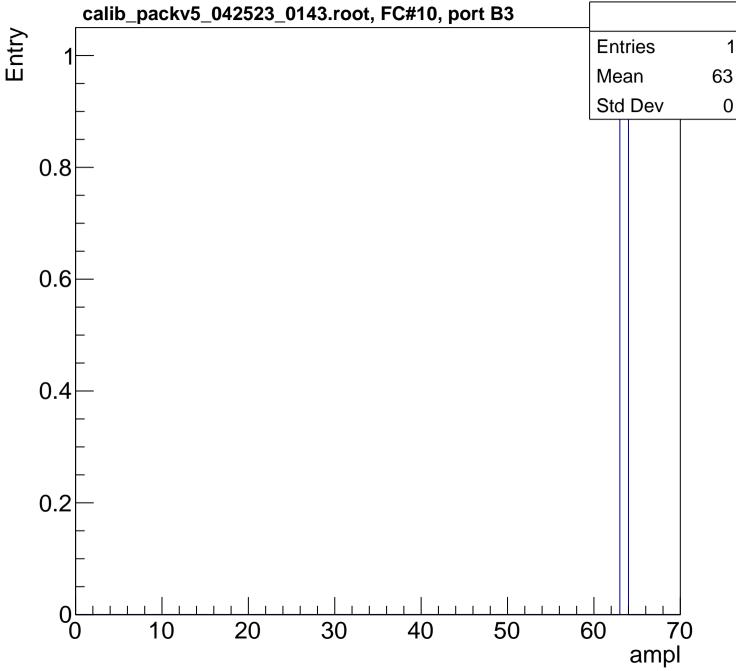


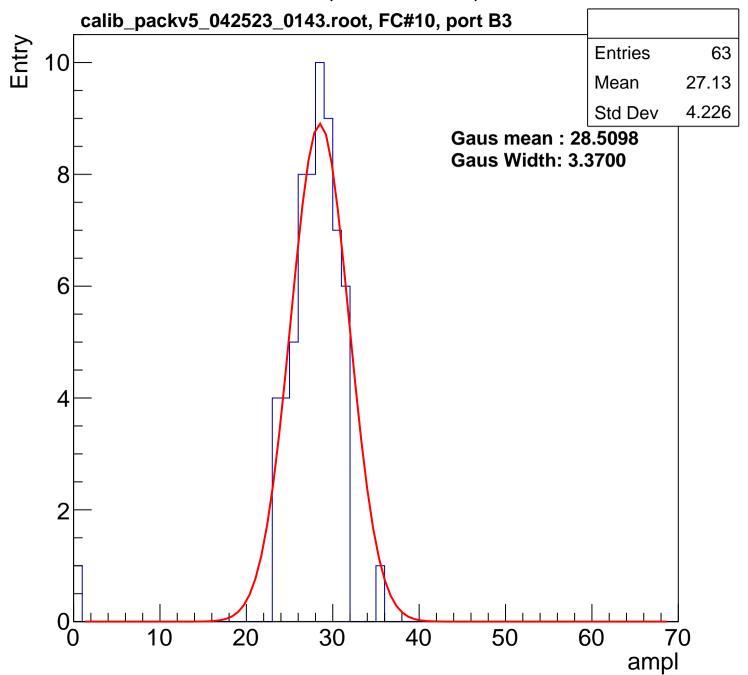


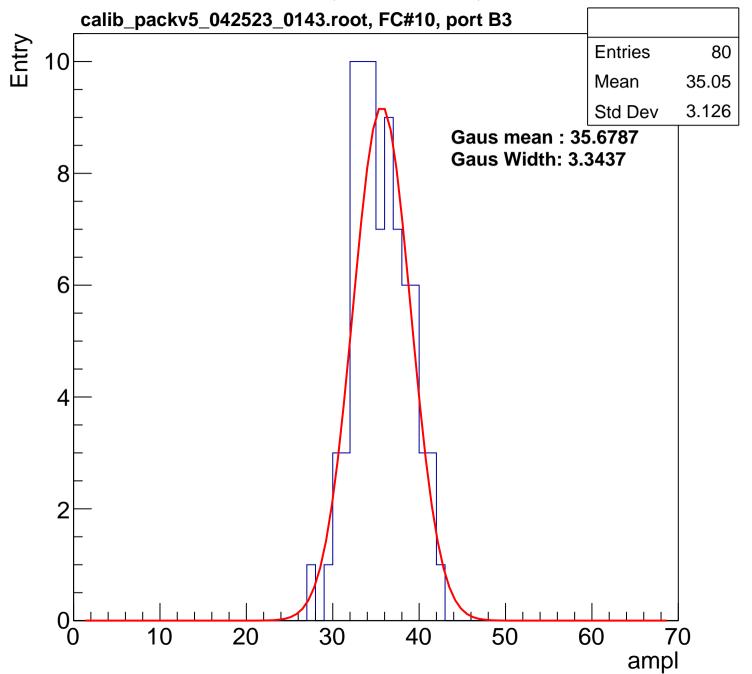


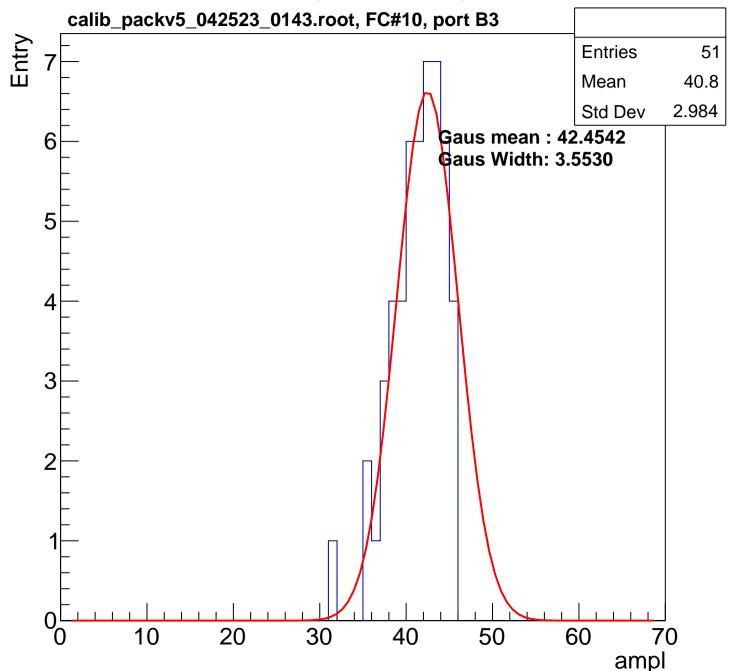


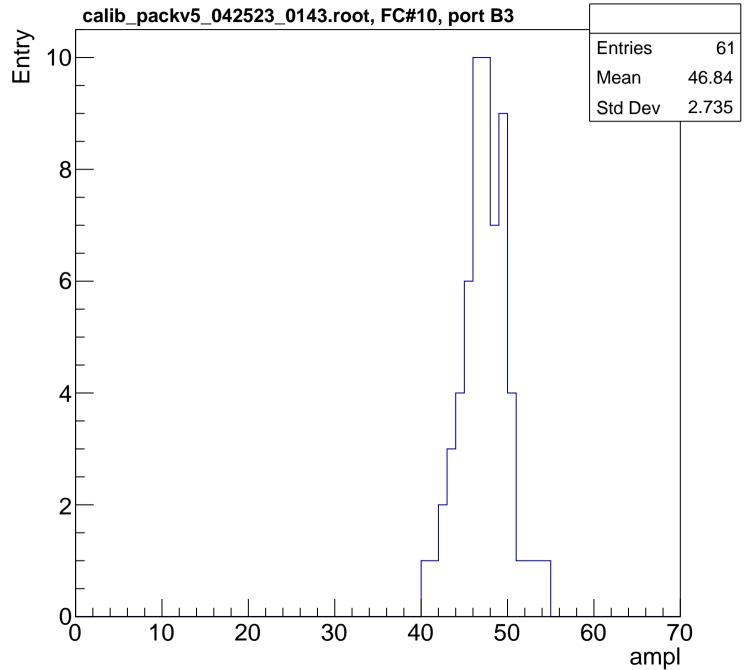
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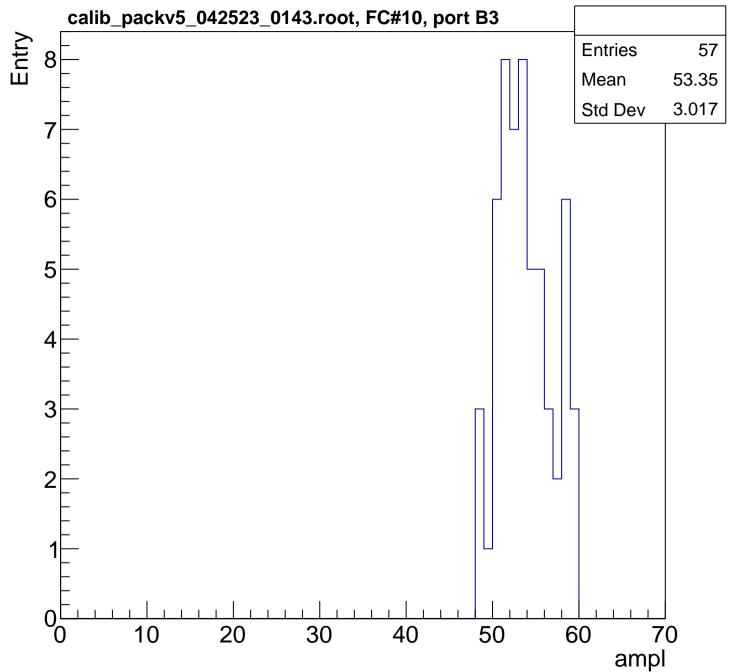


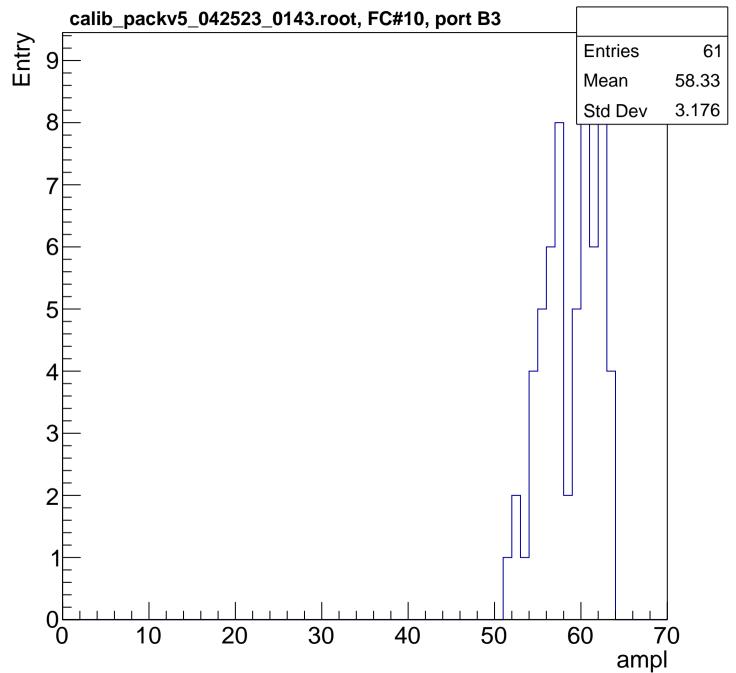


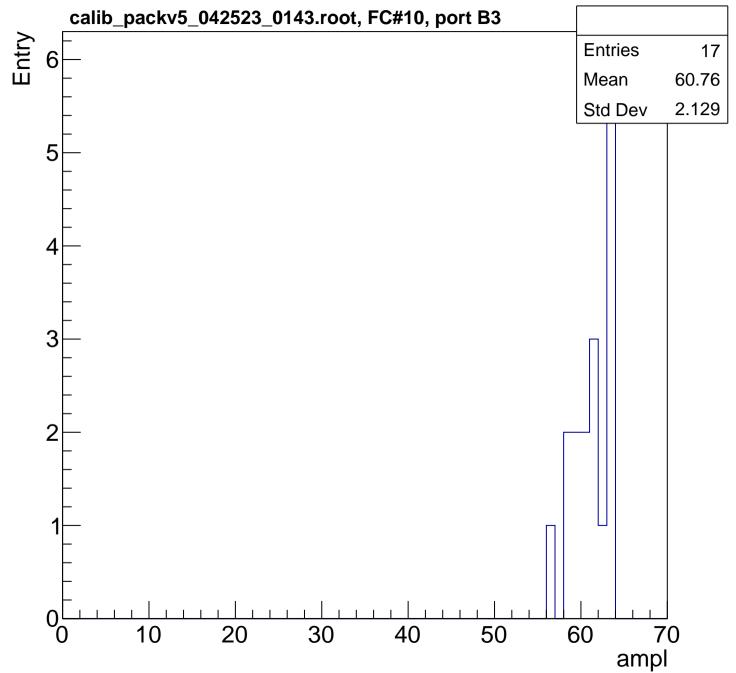


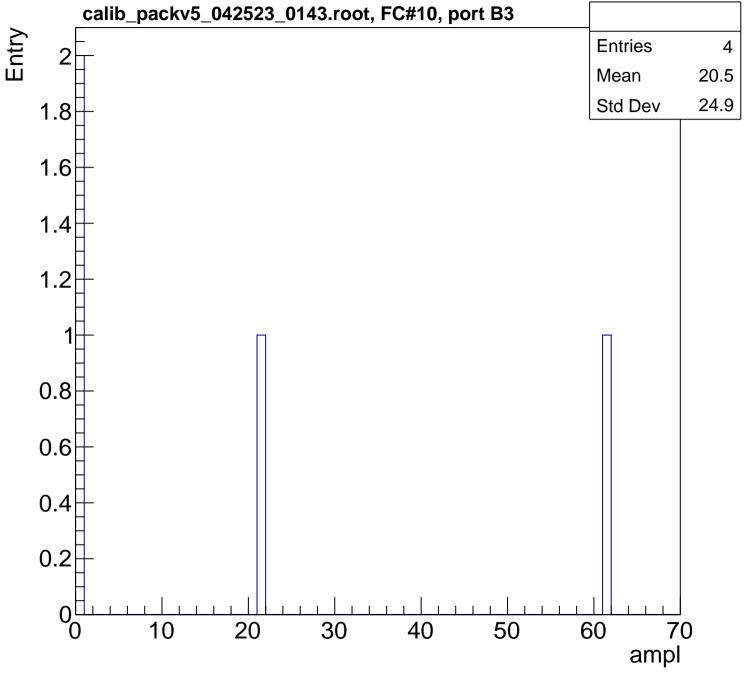


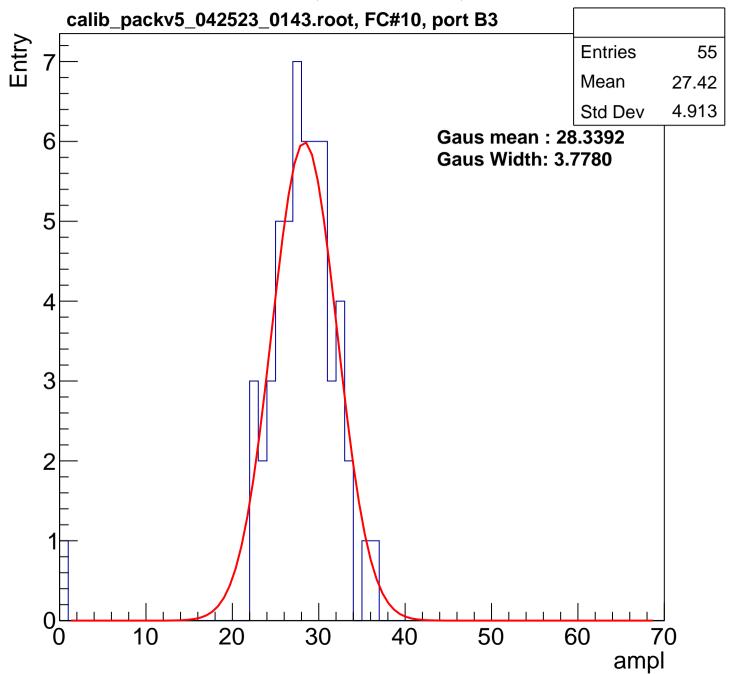


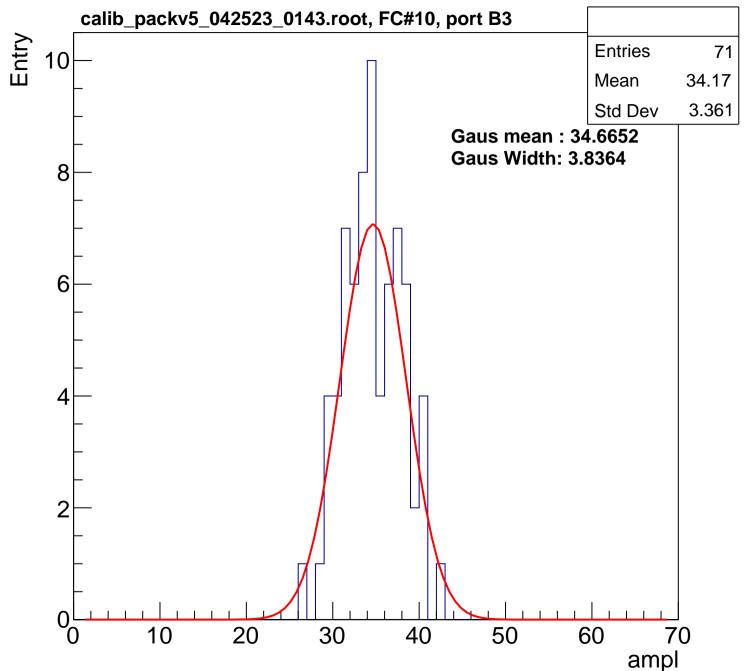


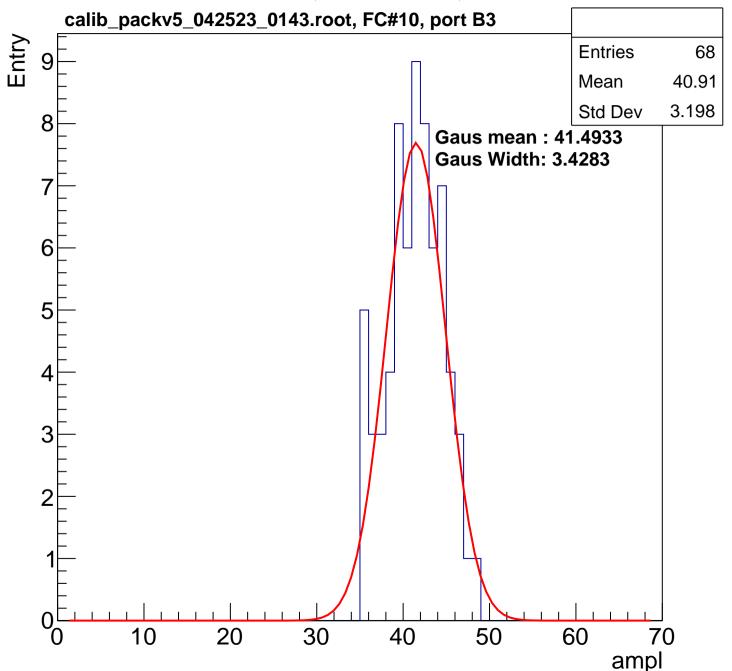


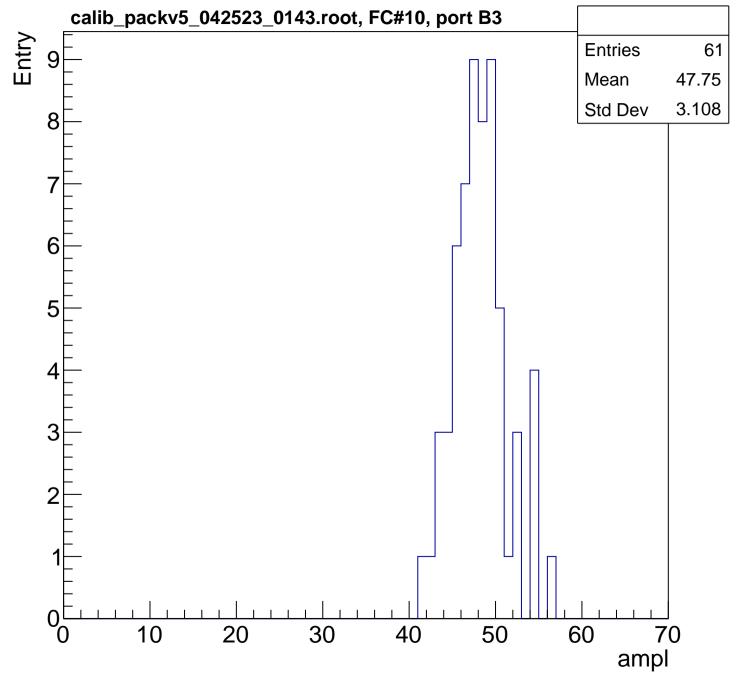


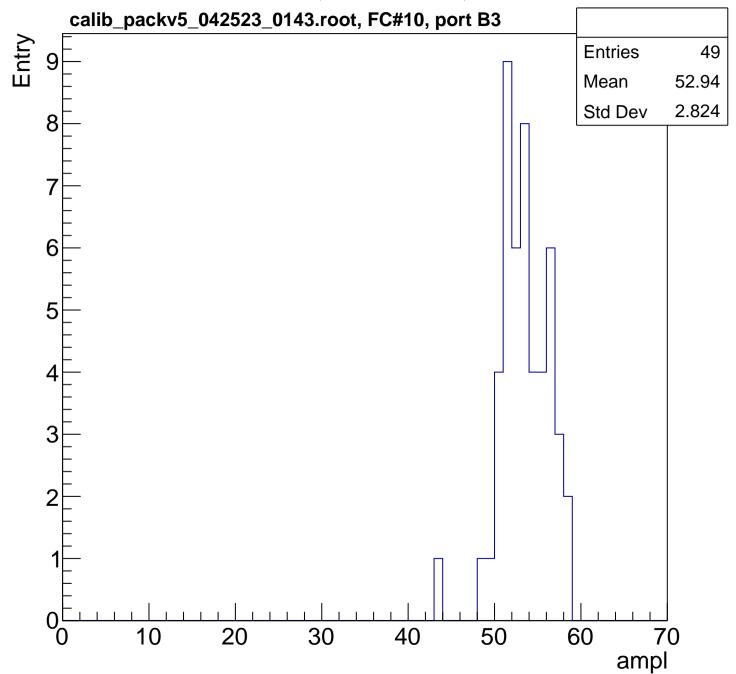


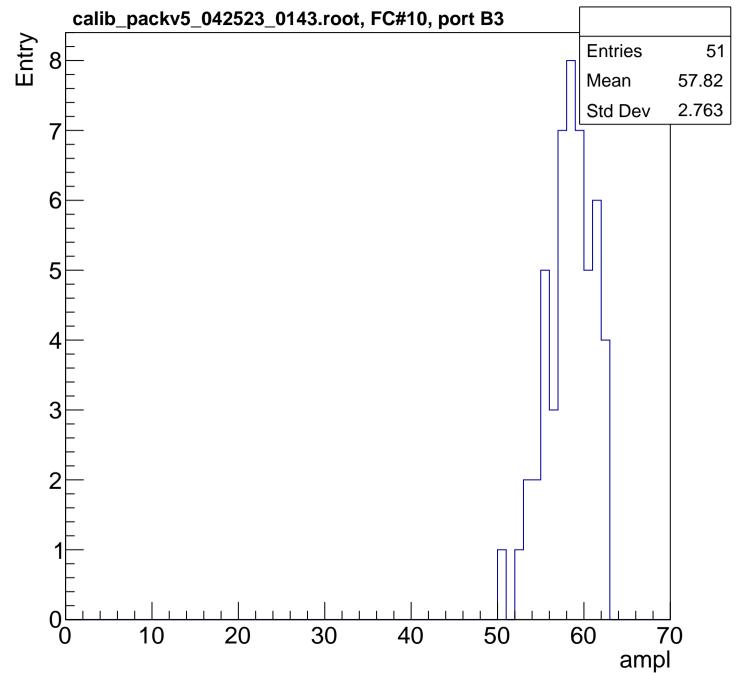


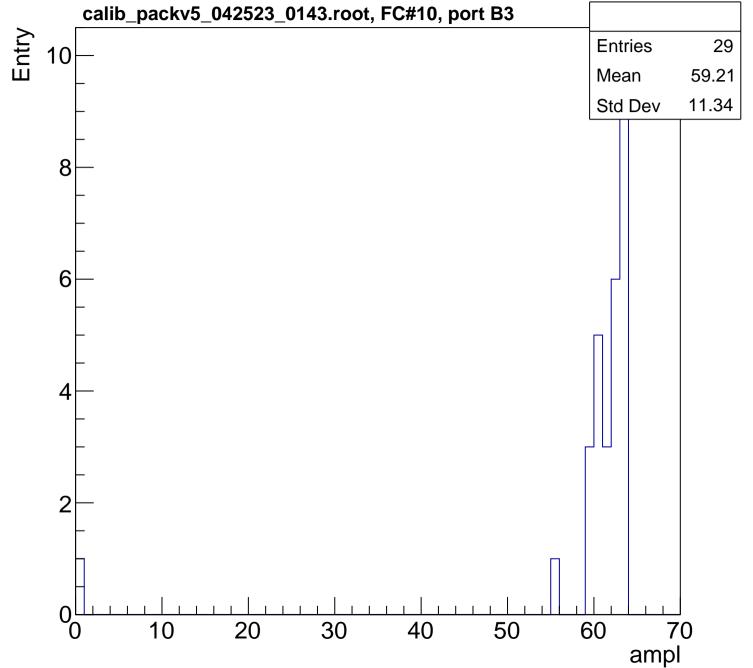


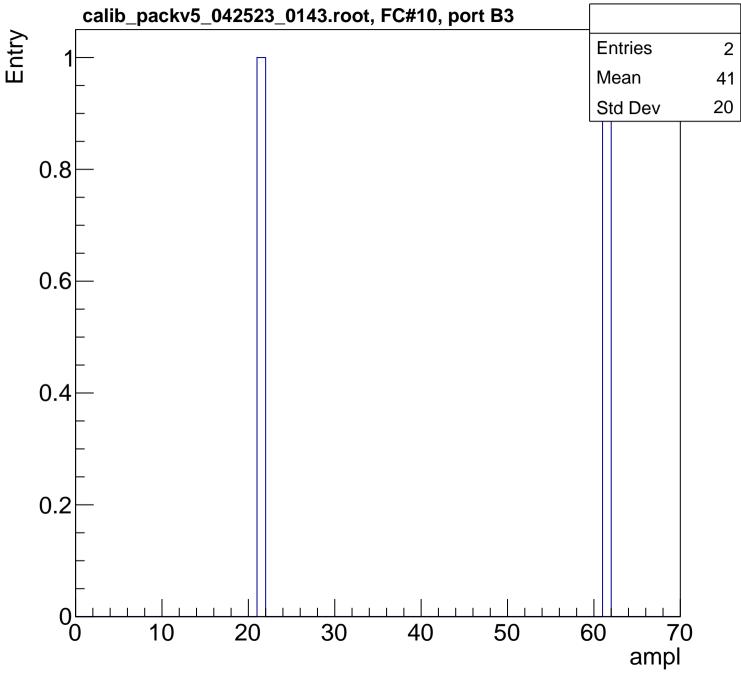


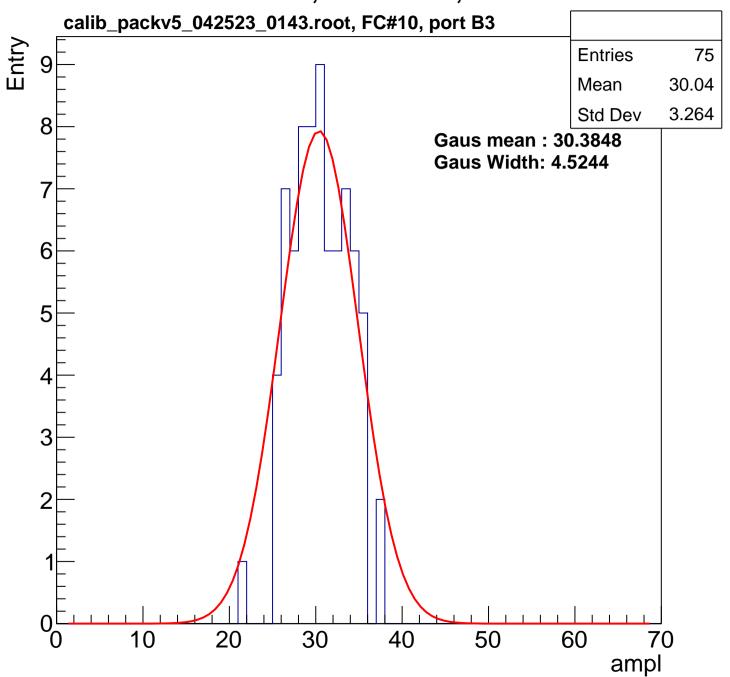


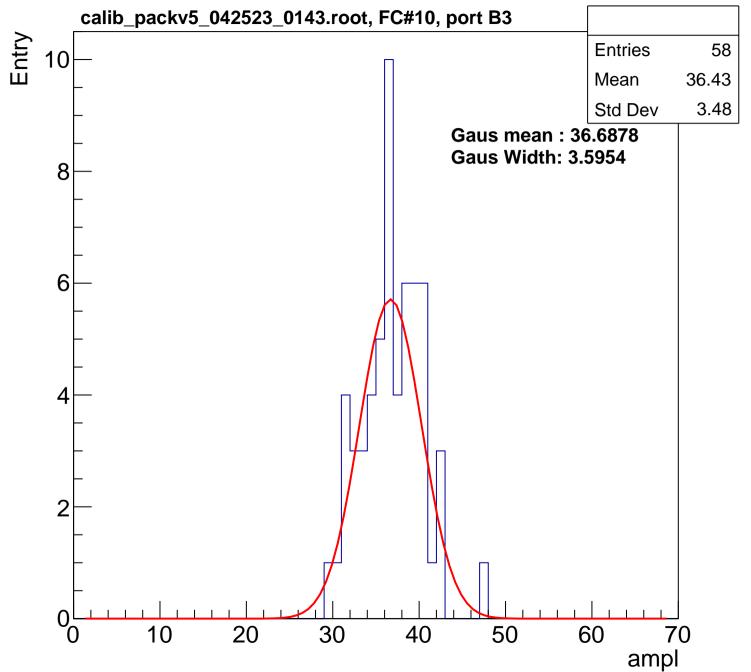


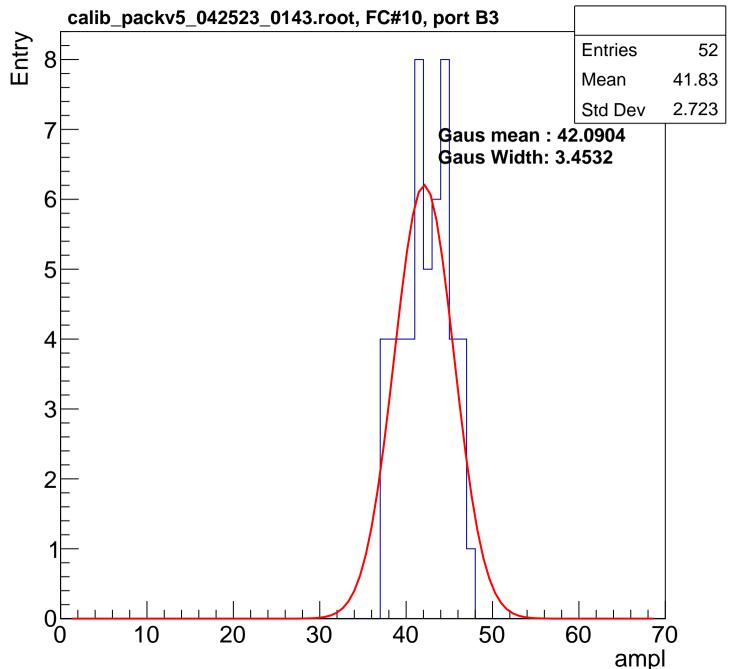


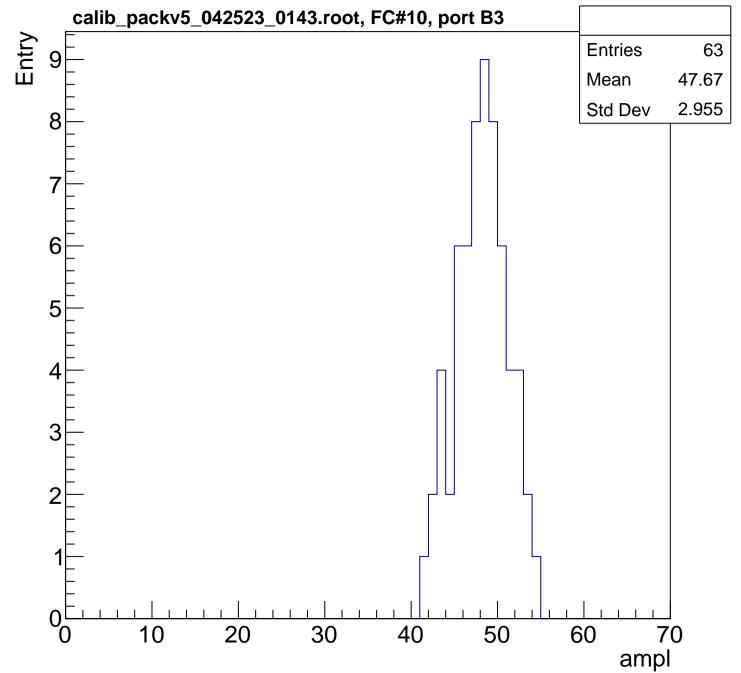


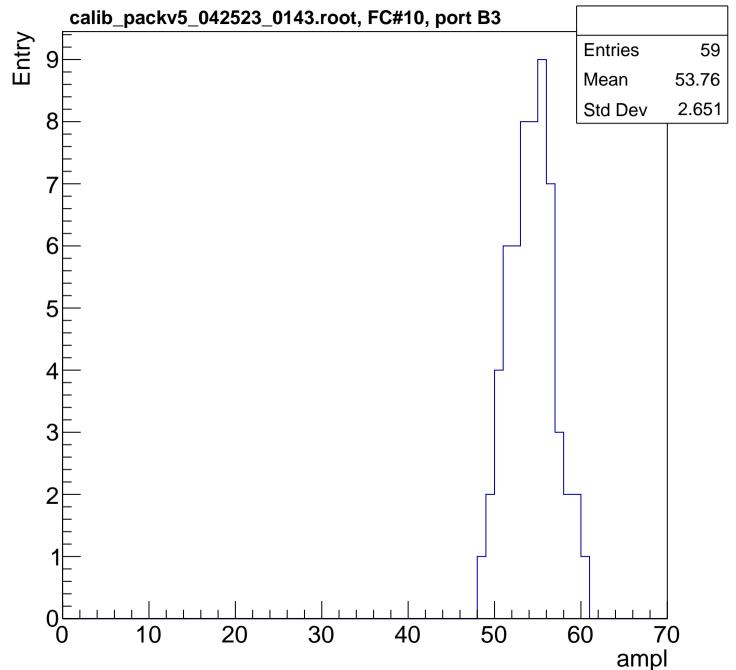


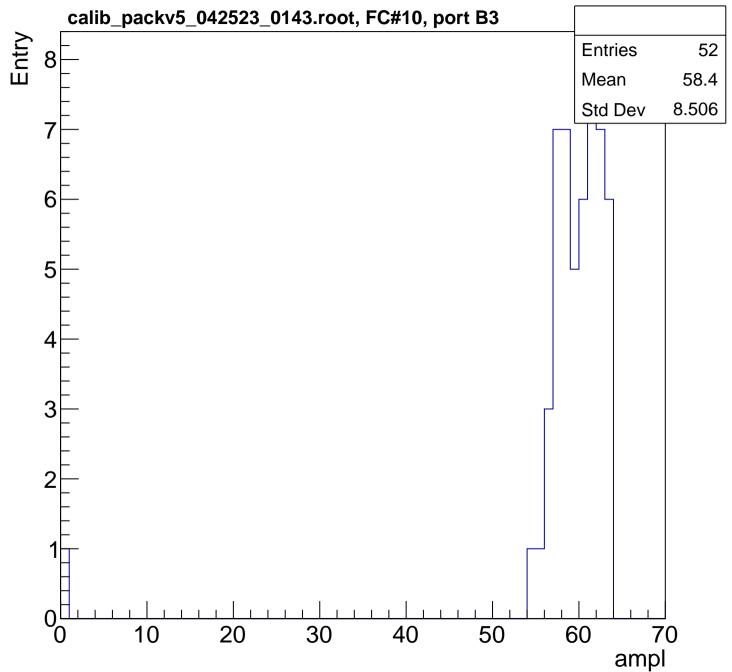


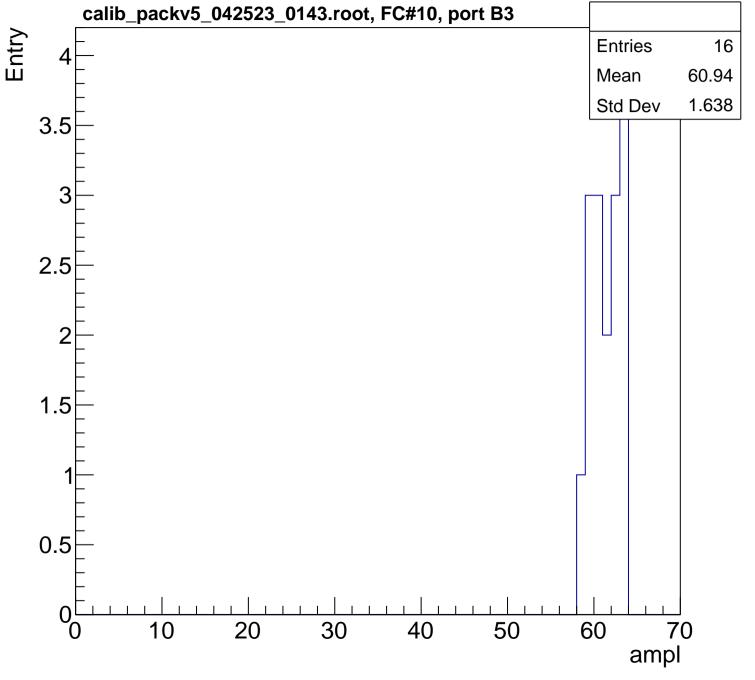




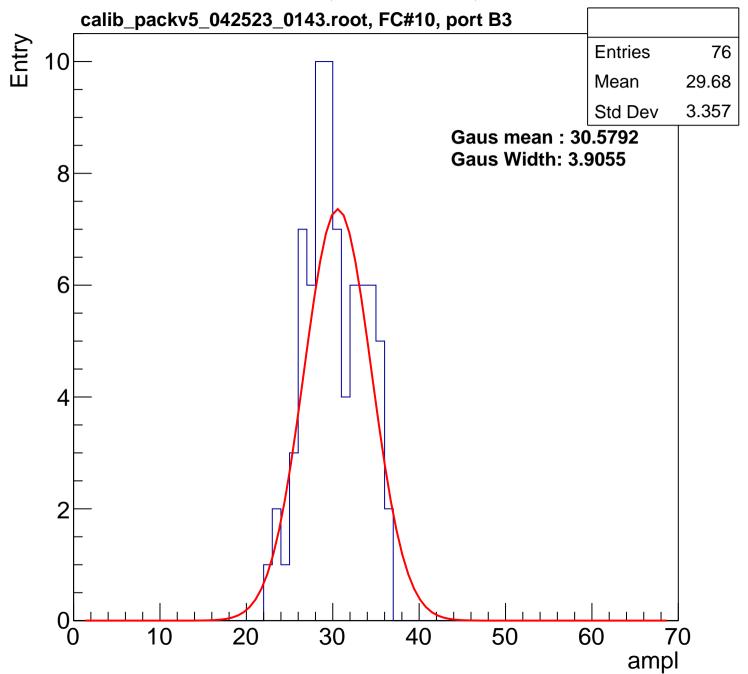


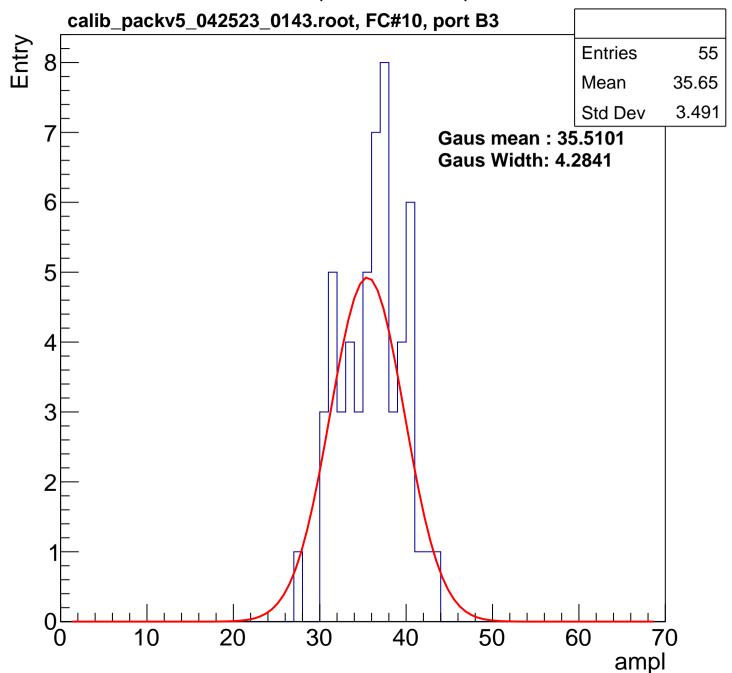


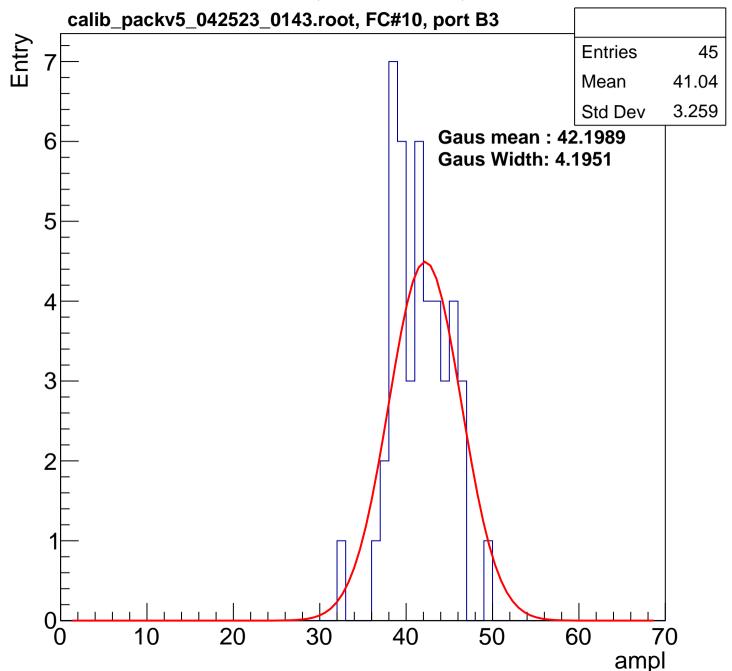


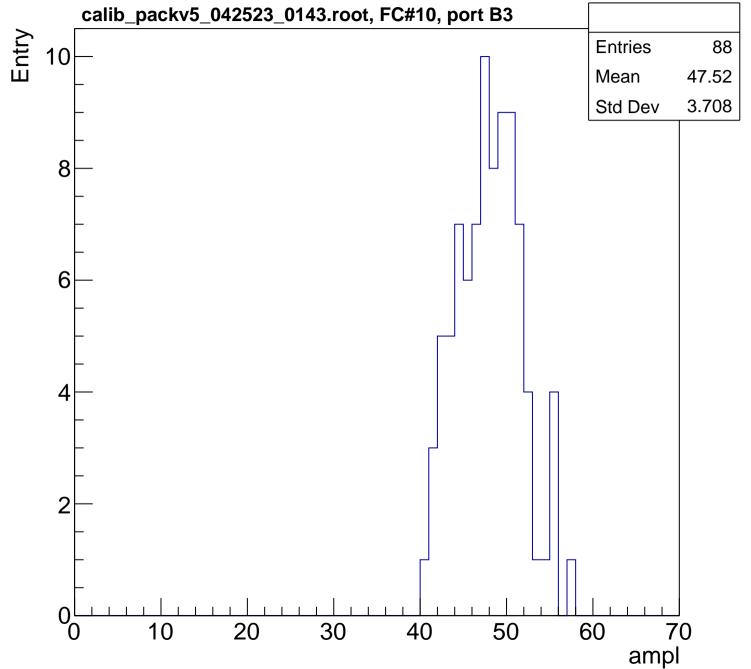


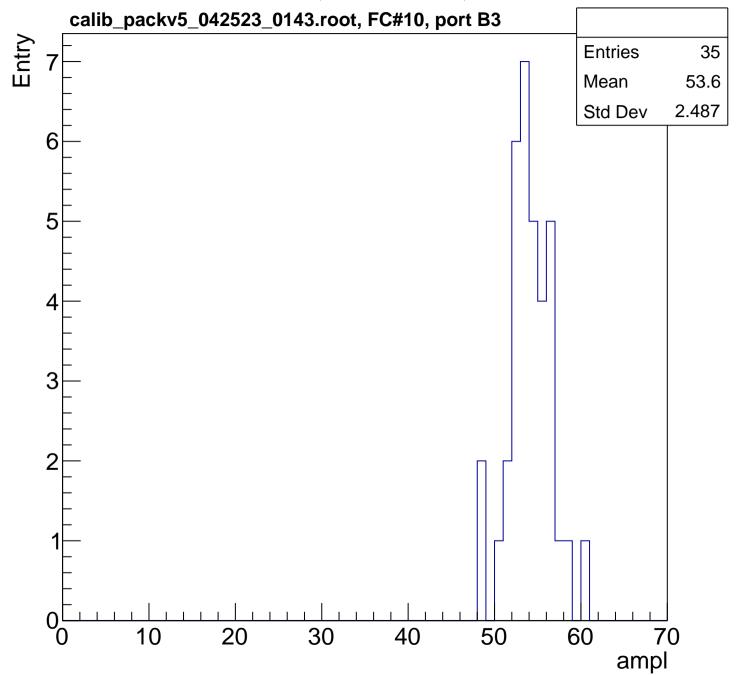
B1L002S, U9-ch38, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

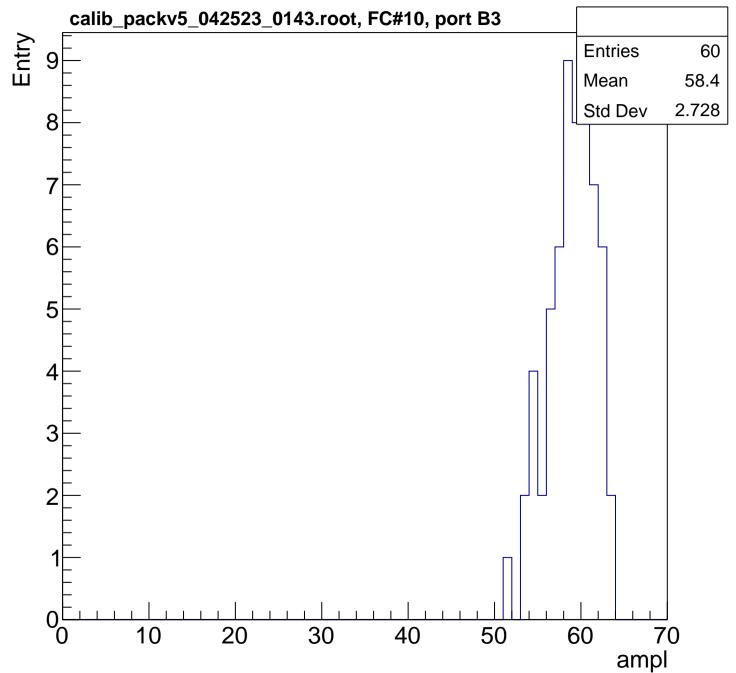


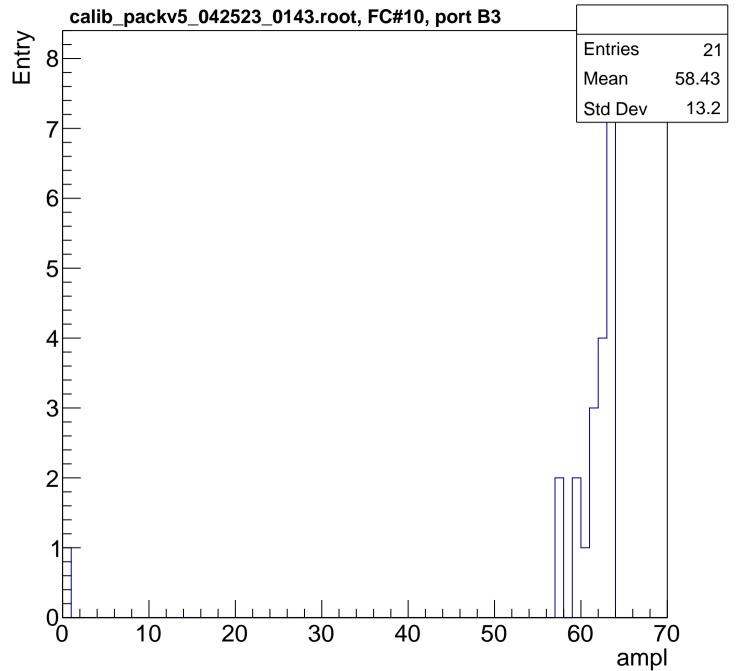






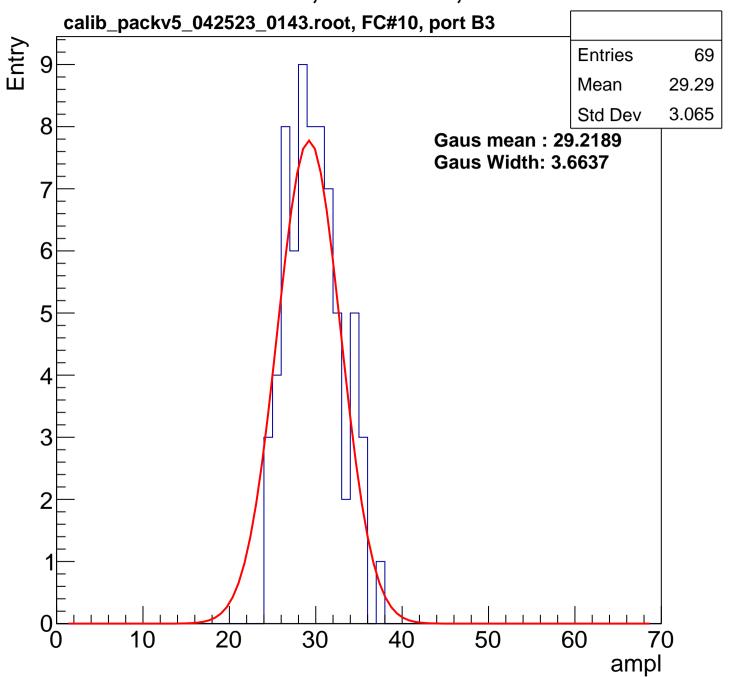


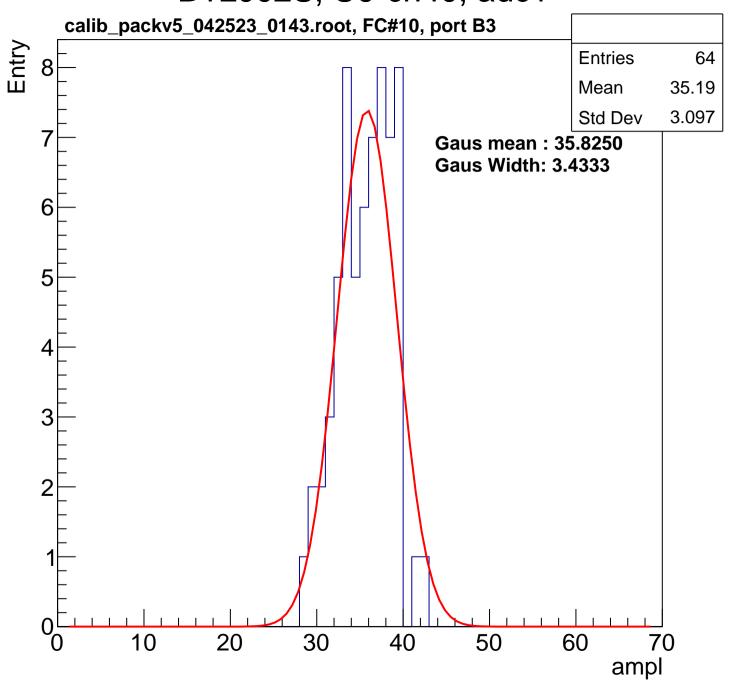


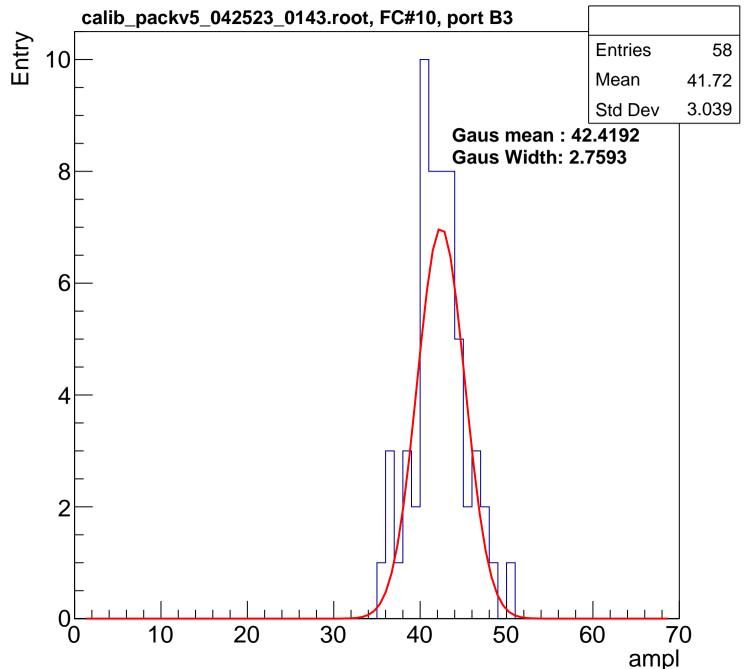


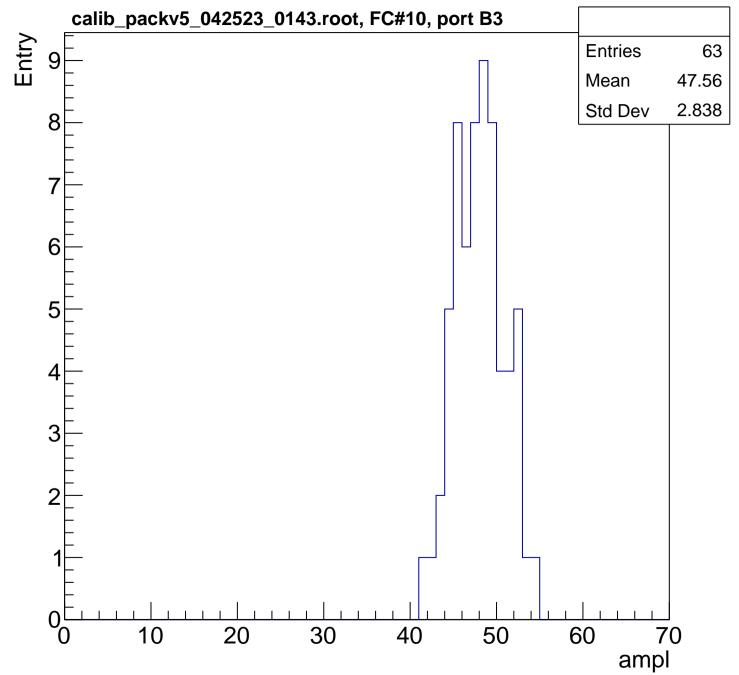
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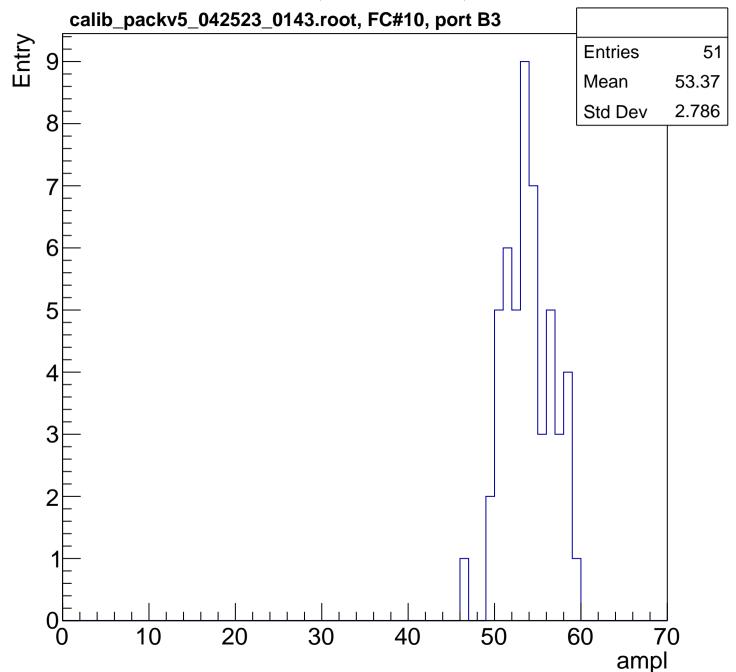
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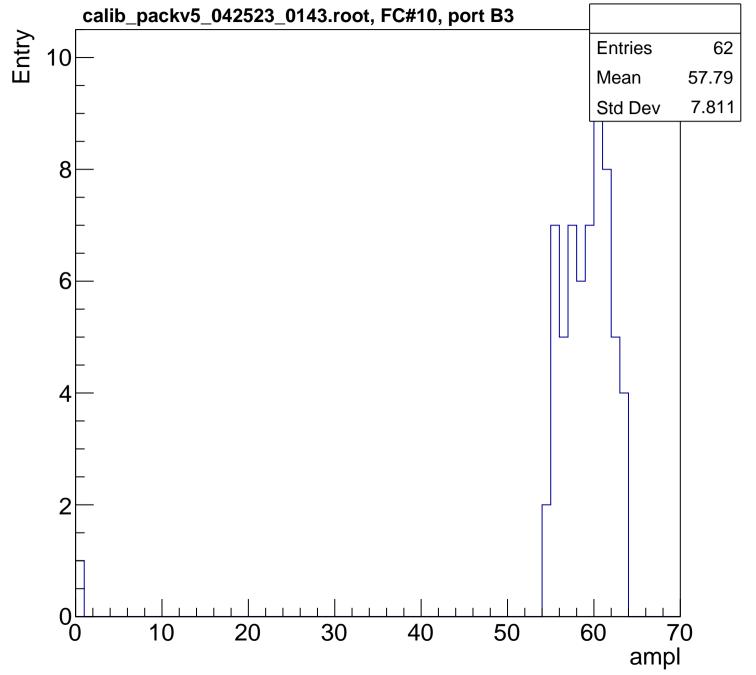


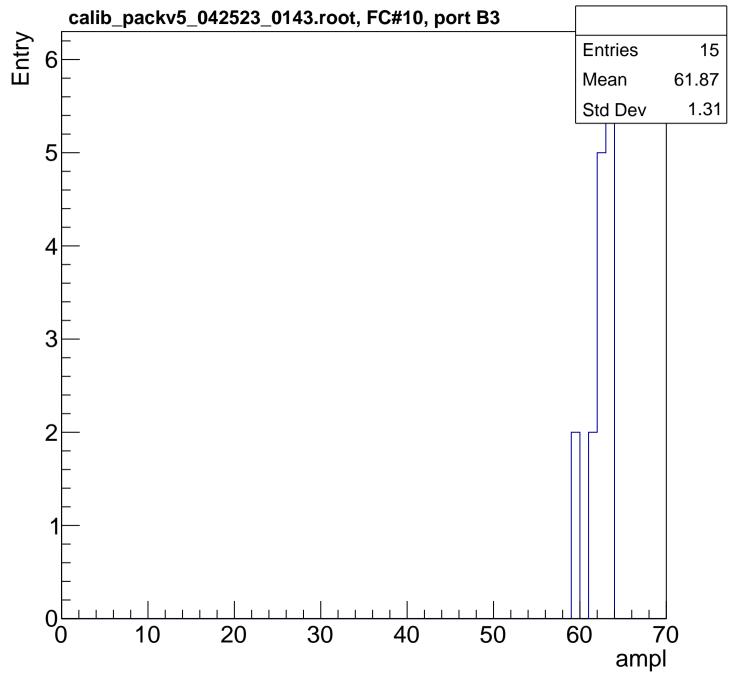




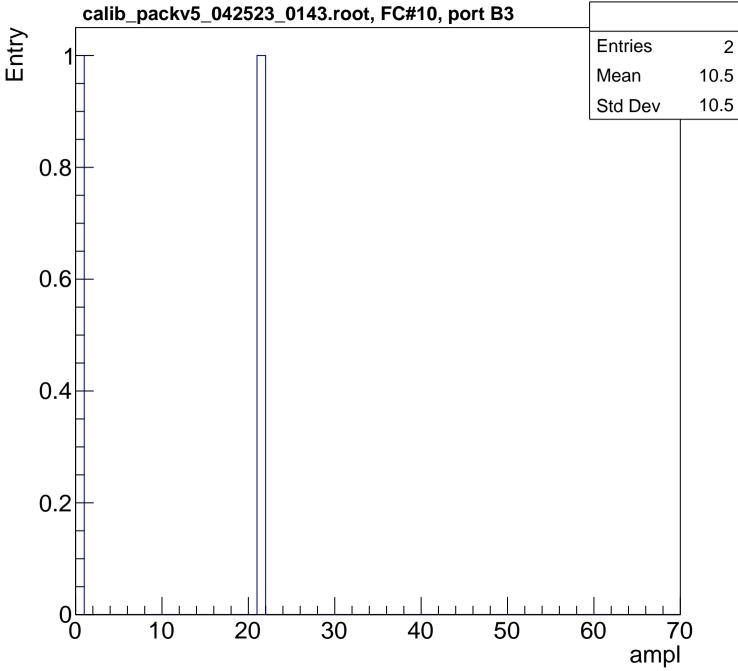


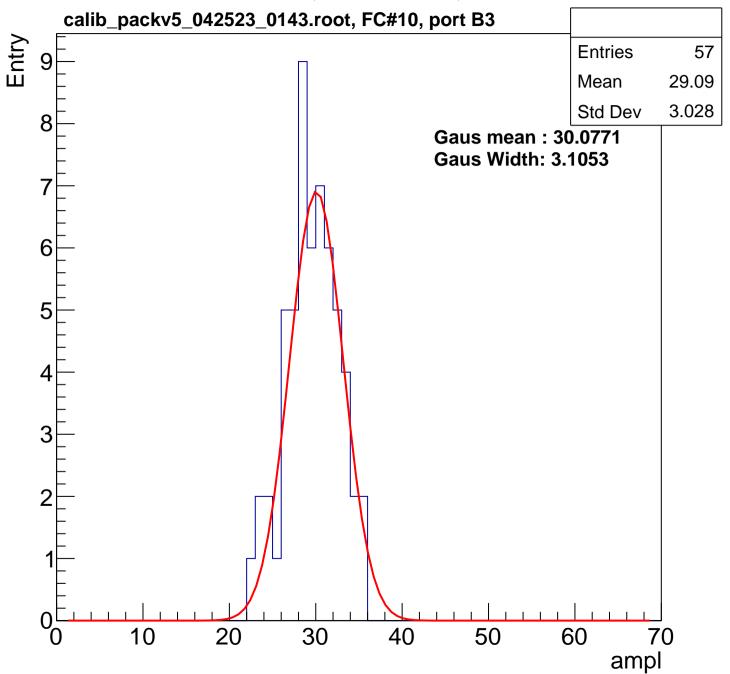


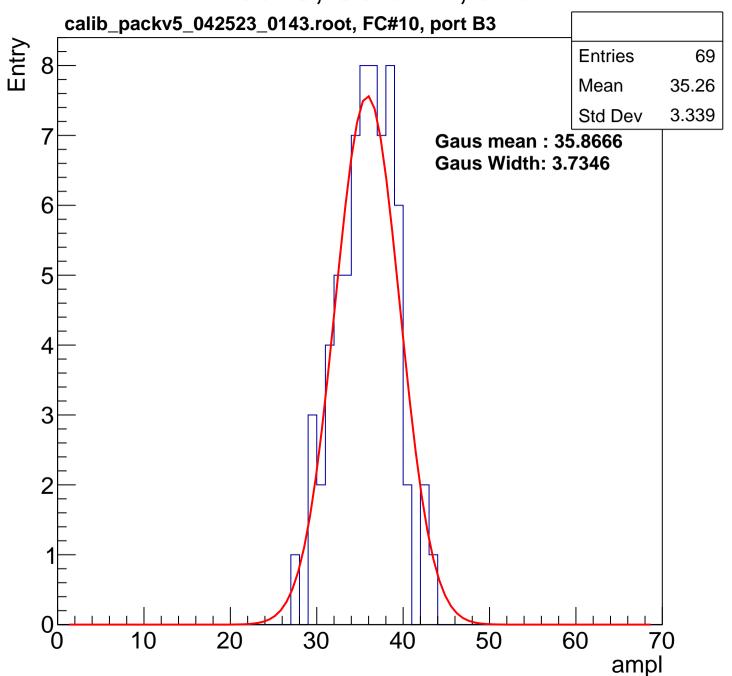


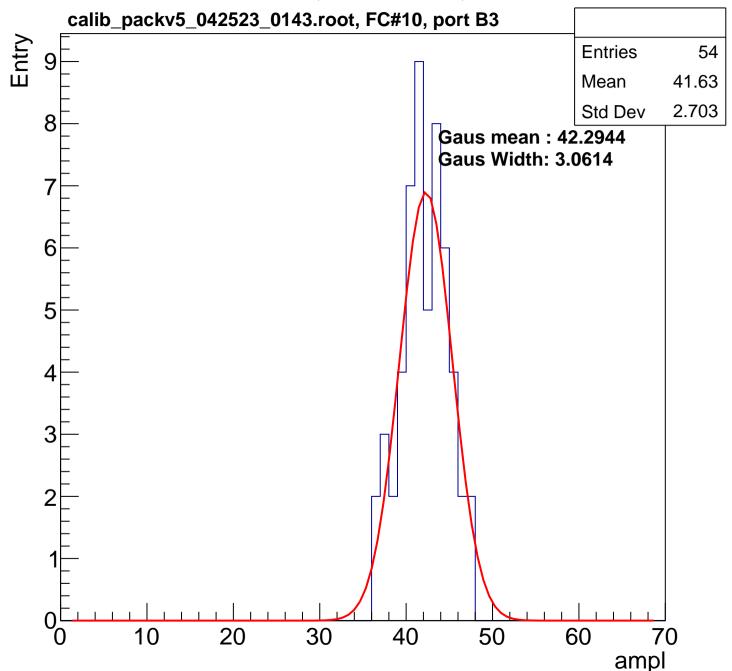


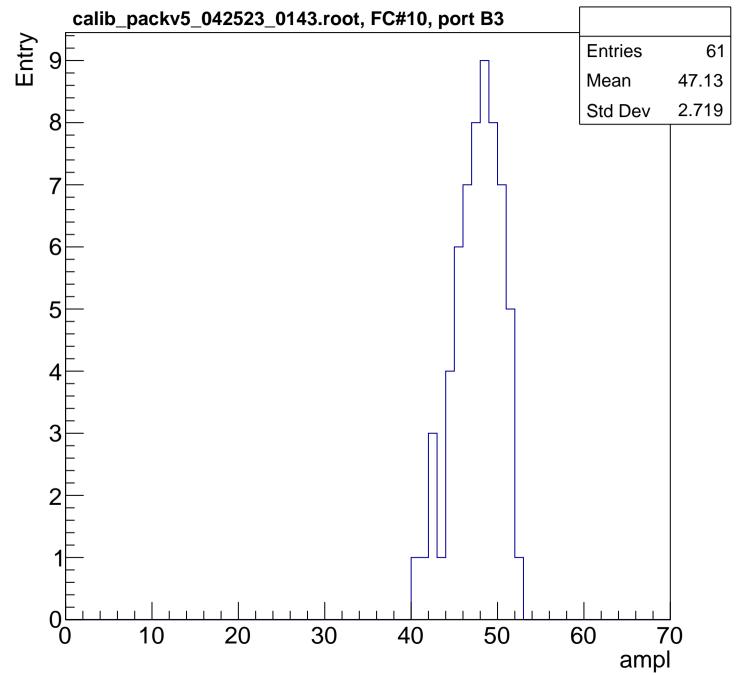
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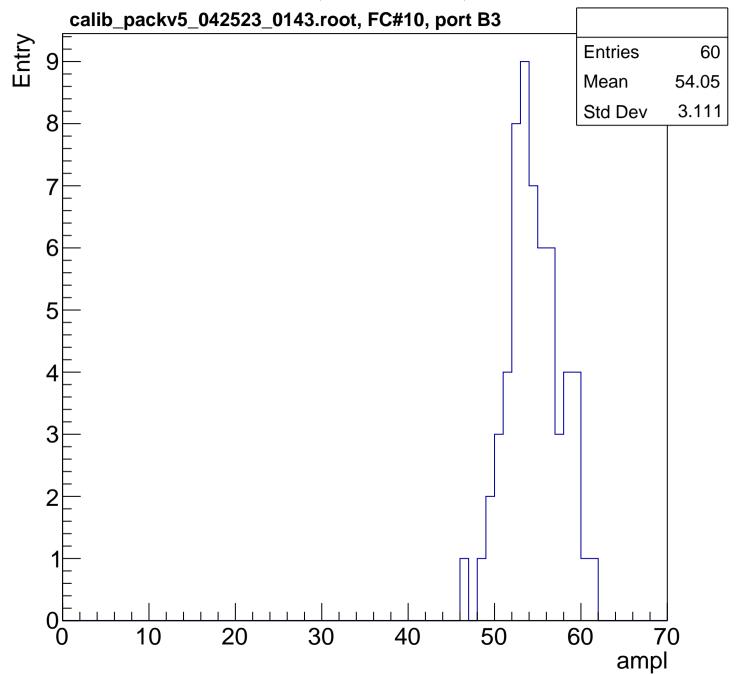


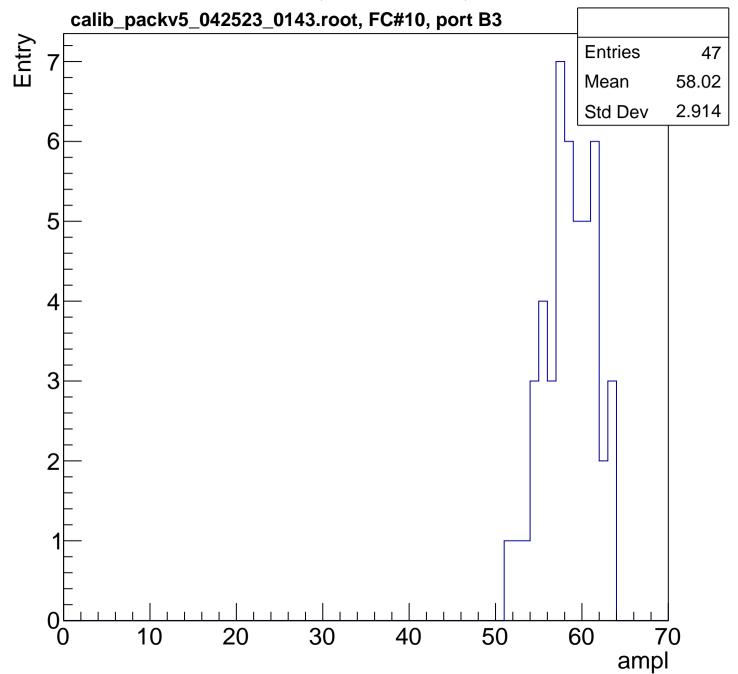


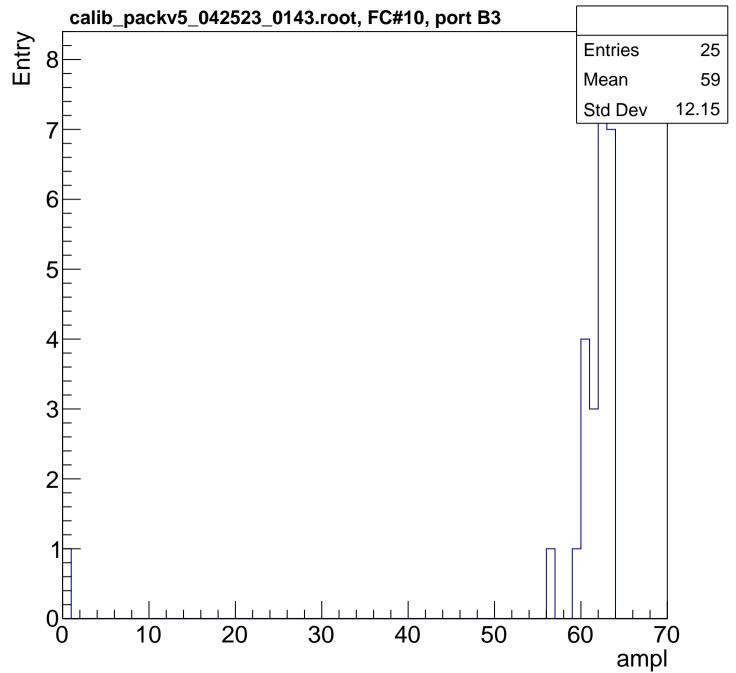


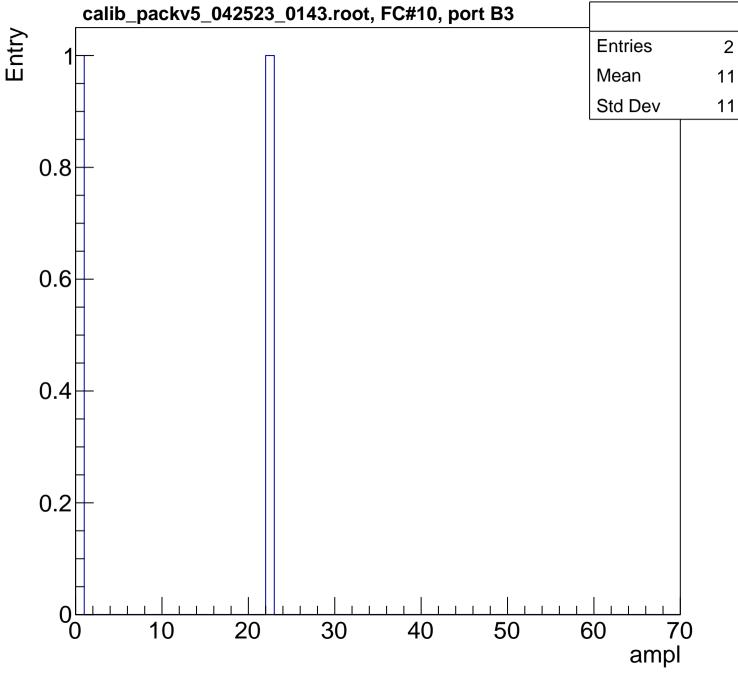


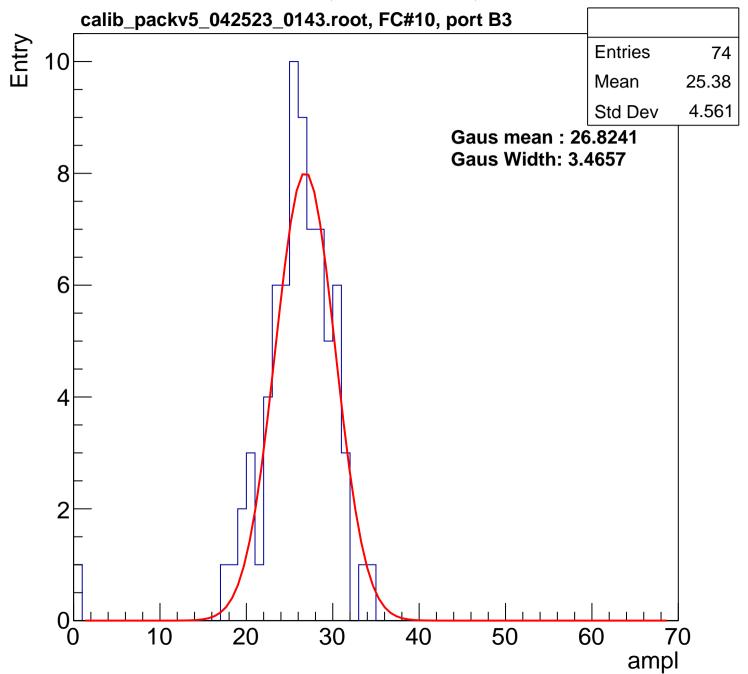


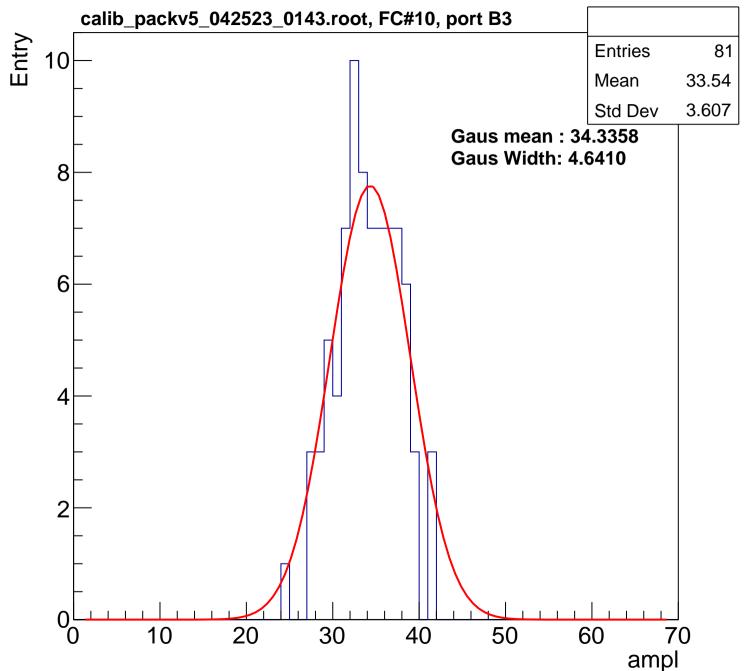


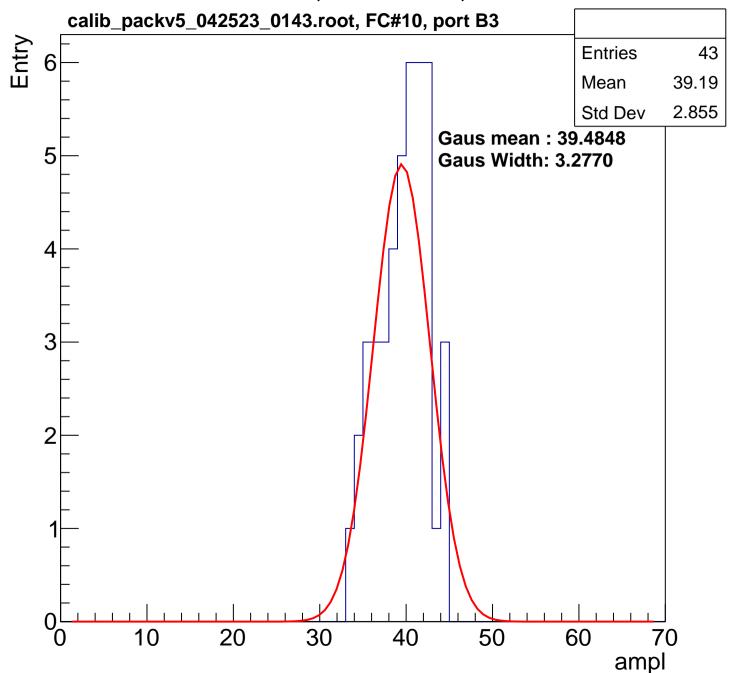


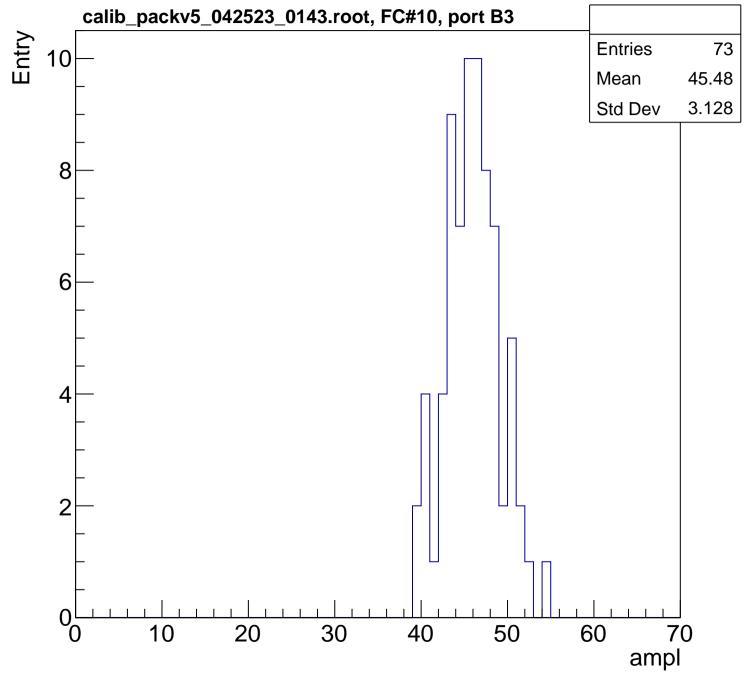


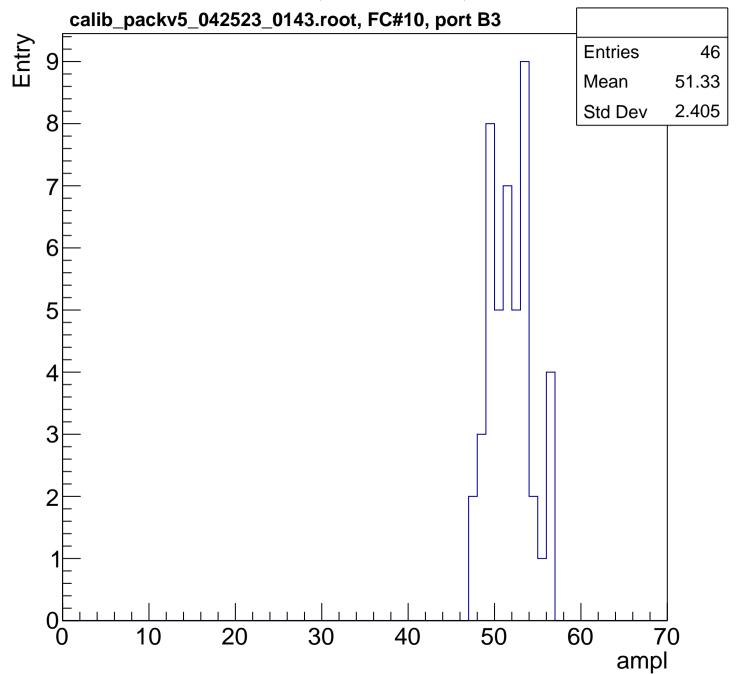


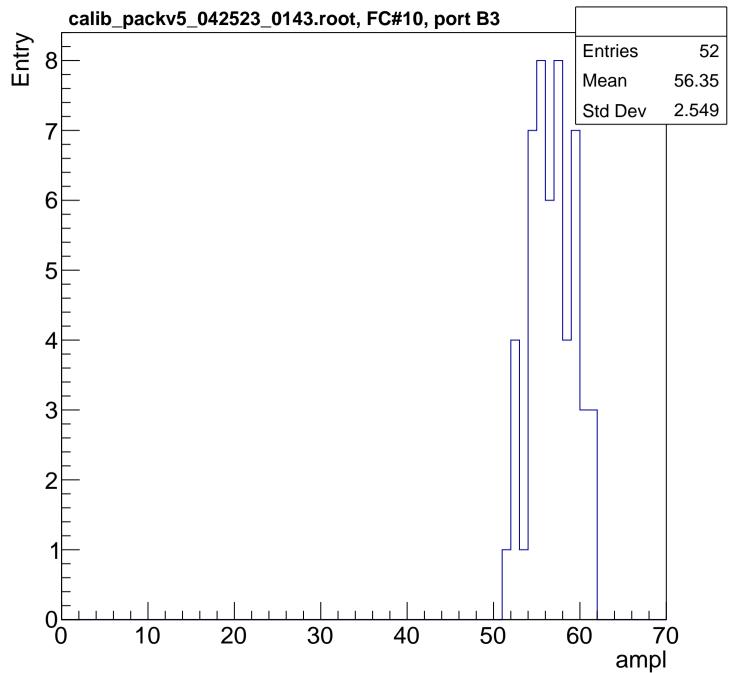


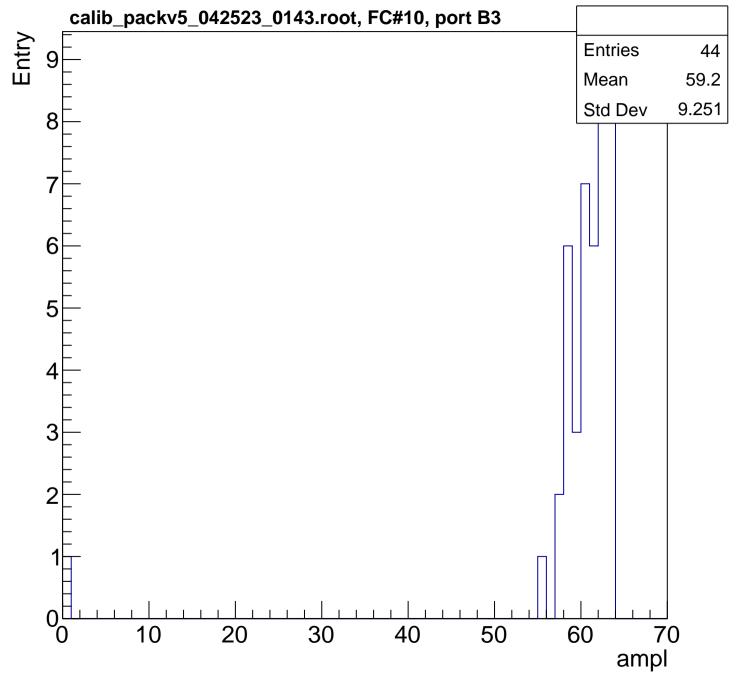


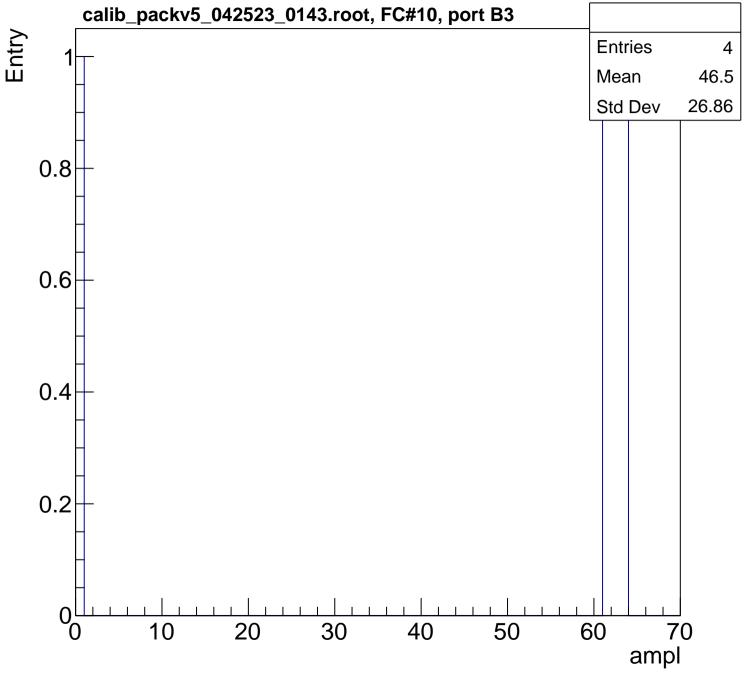


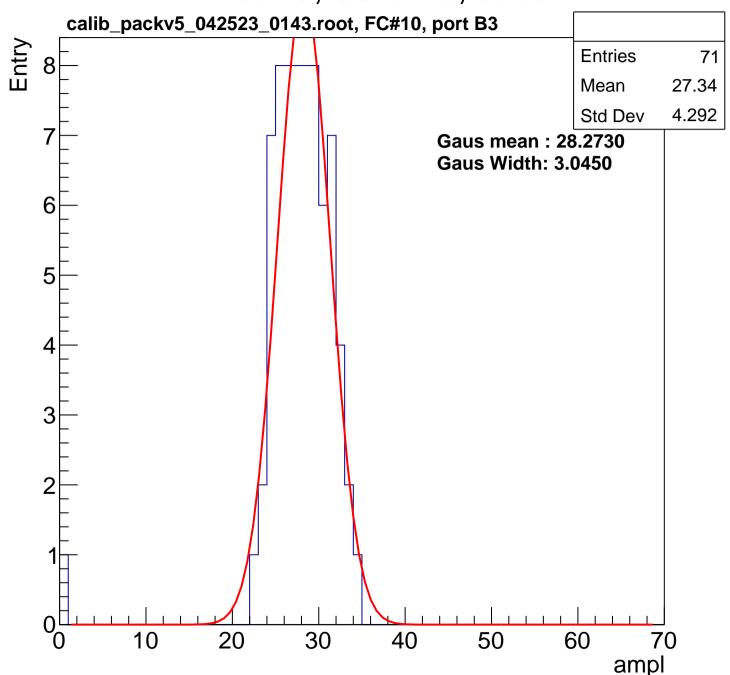


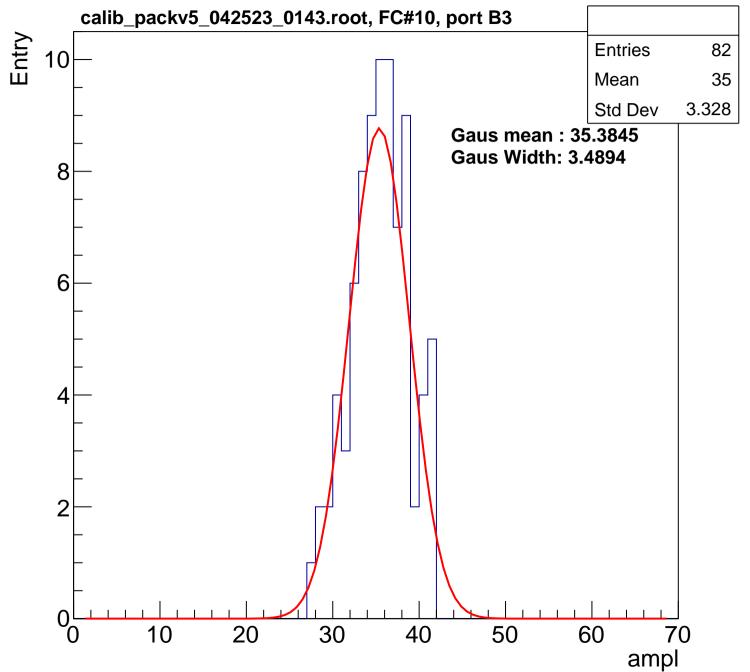


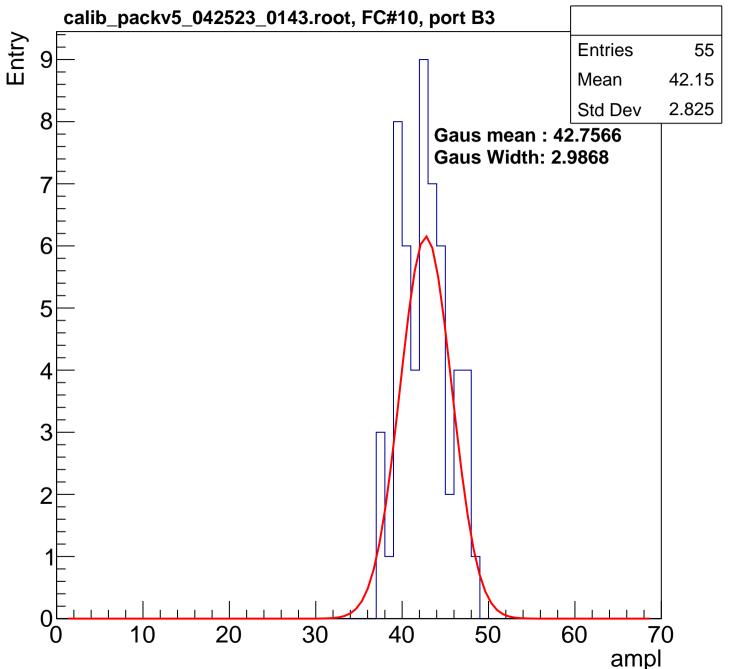


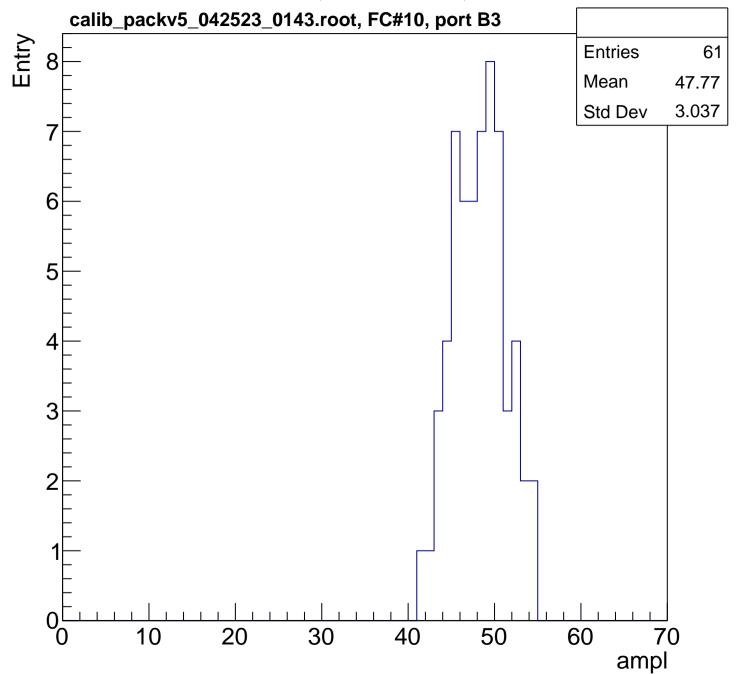


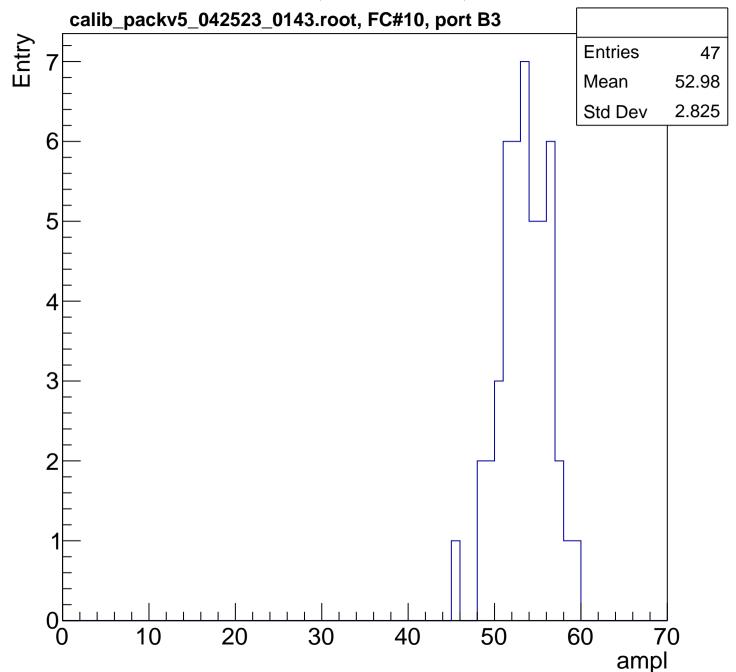


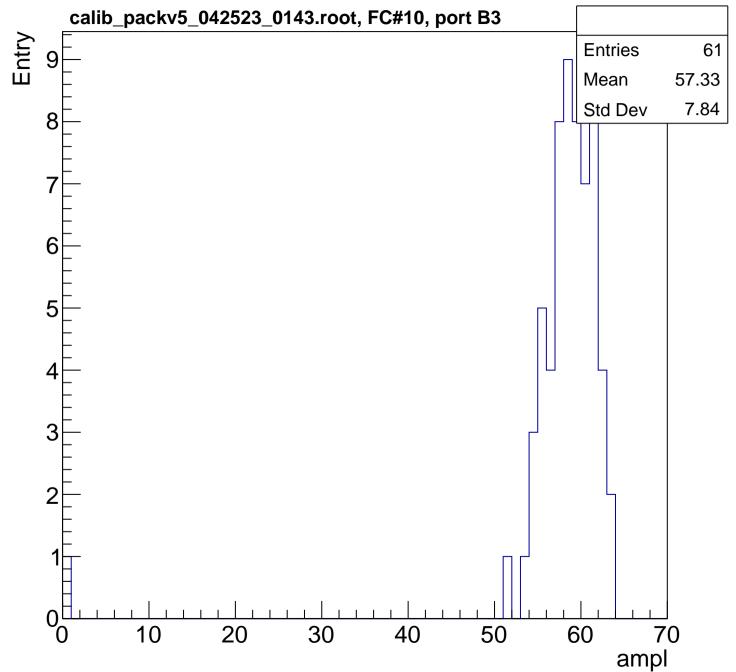


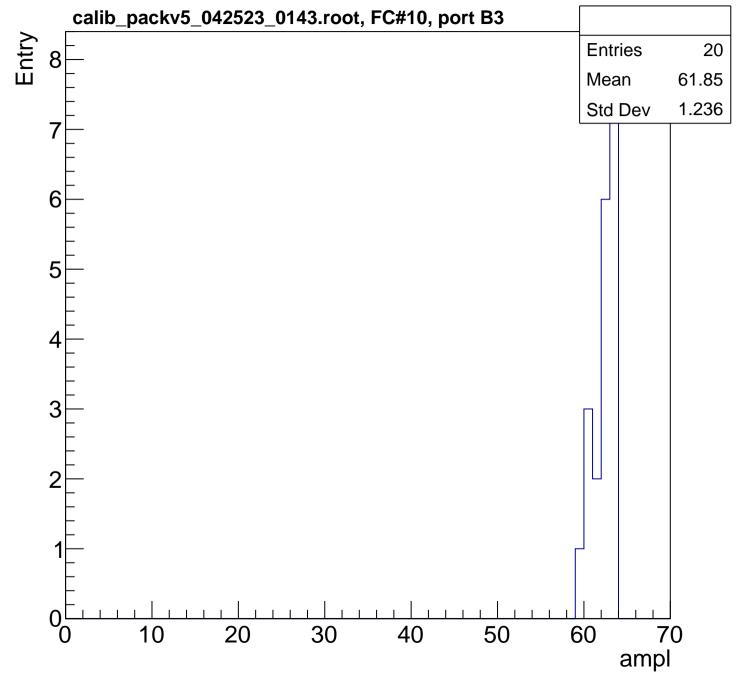


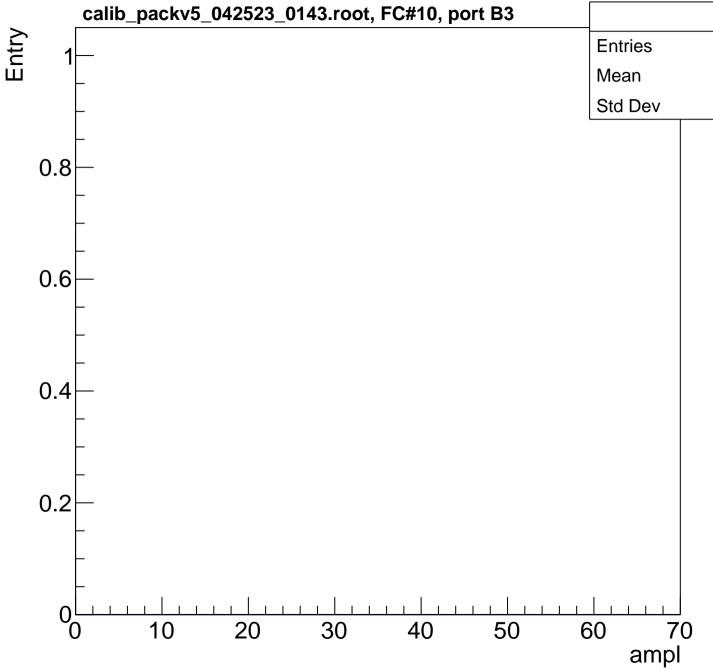


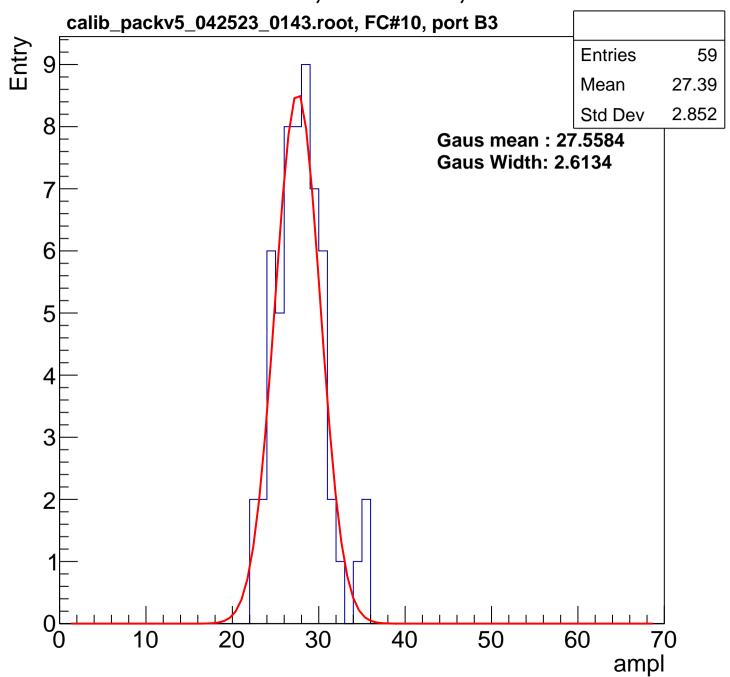


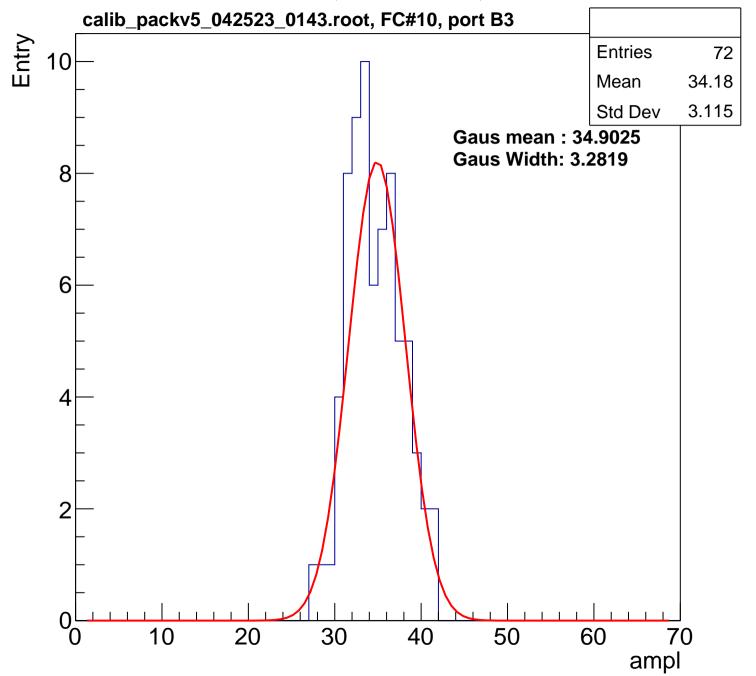


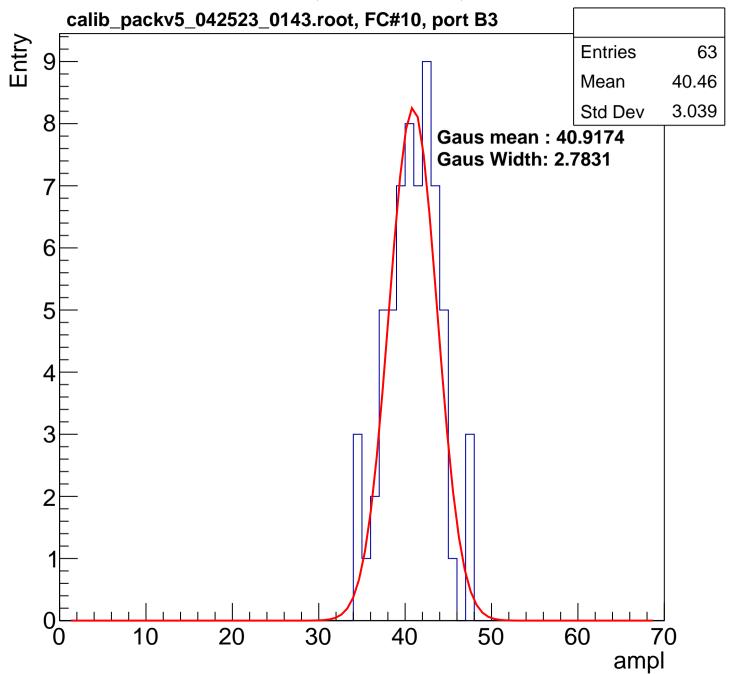


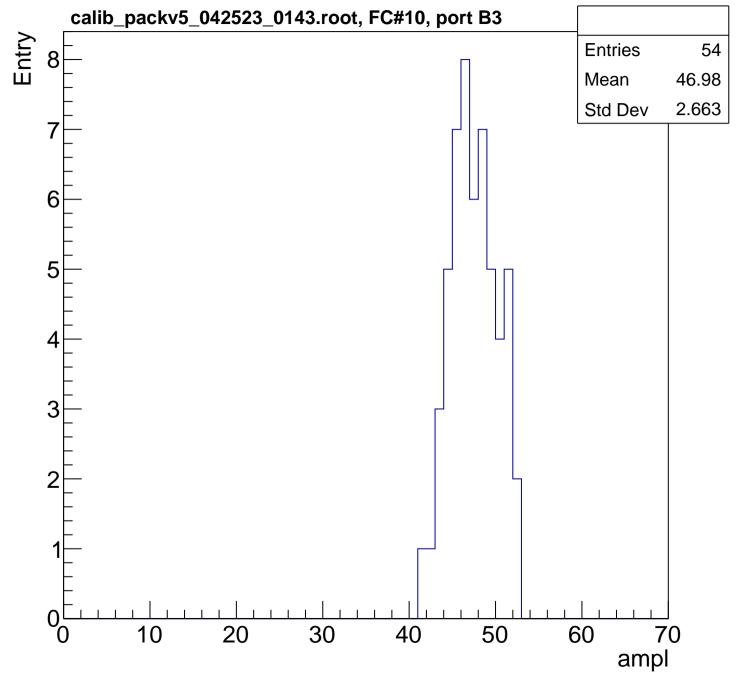


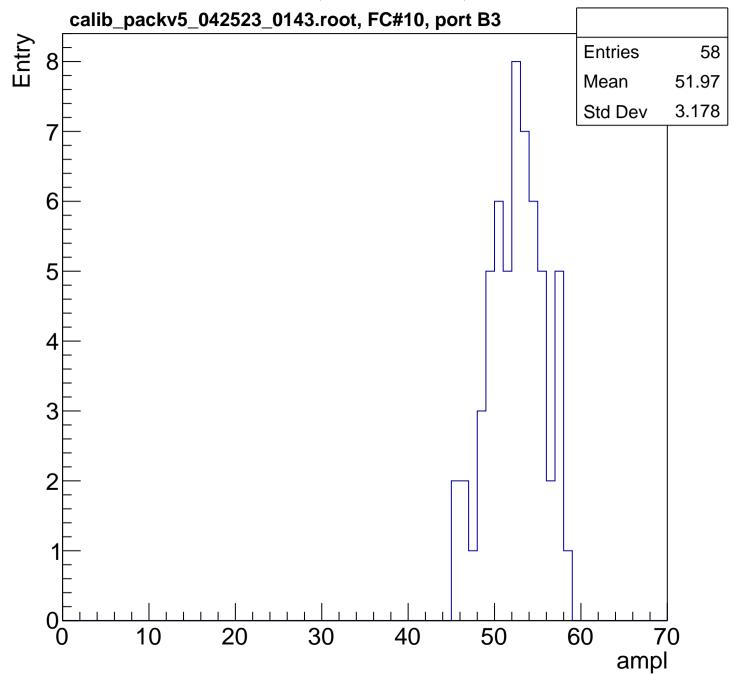


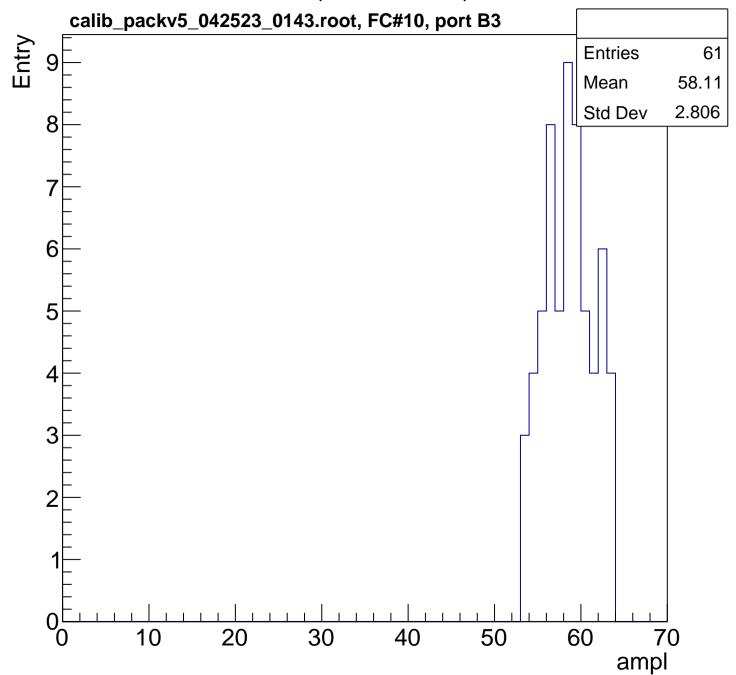


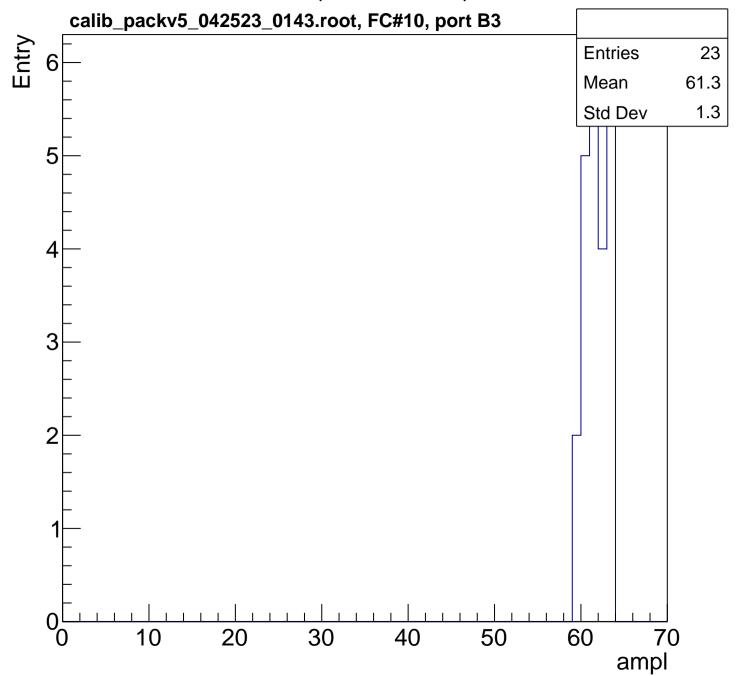


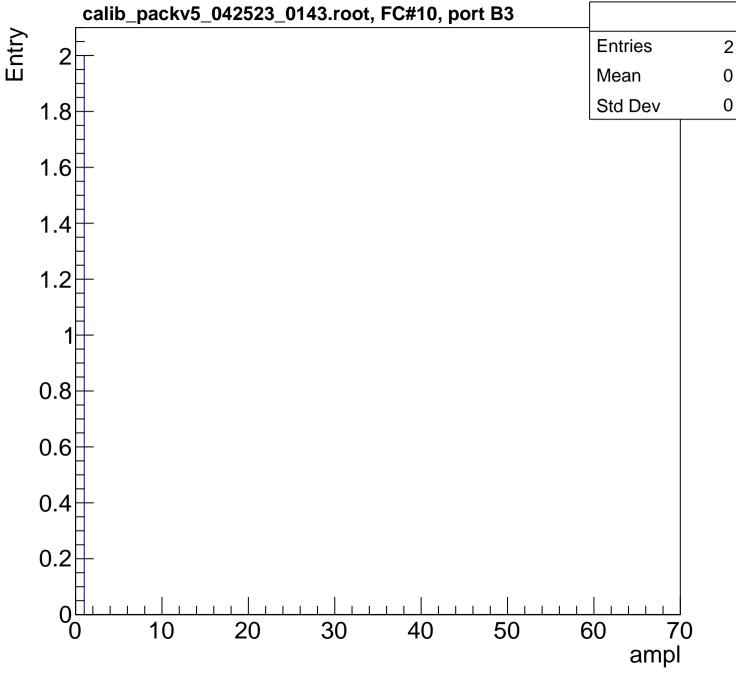


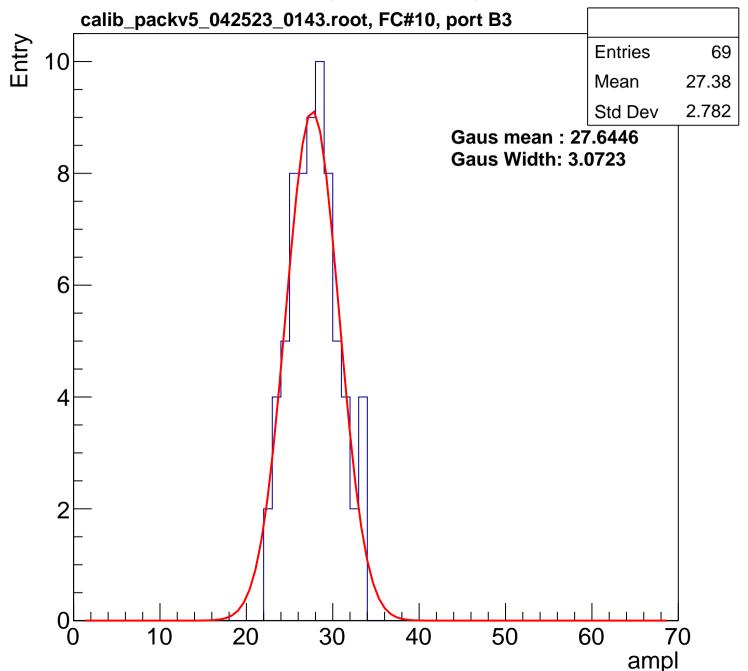


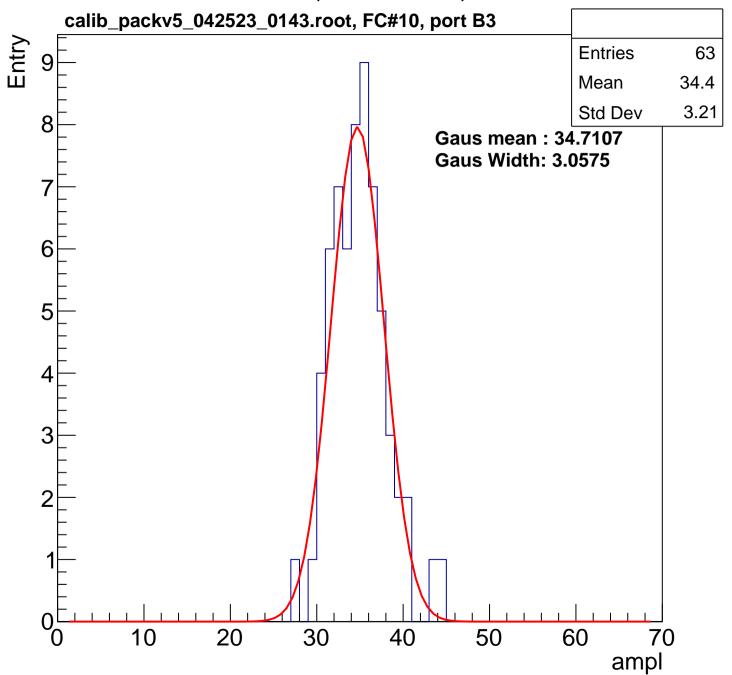


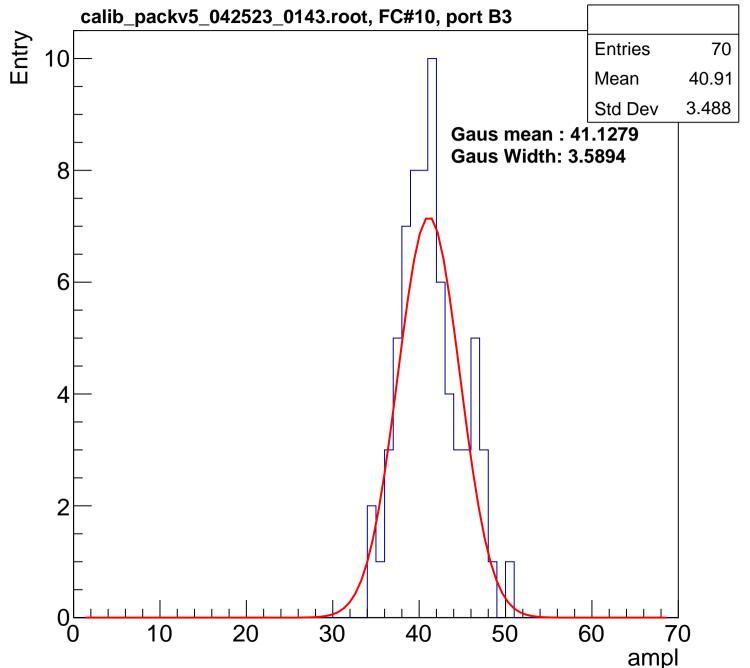


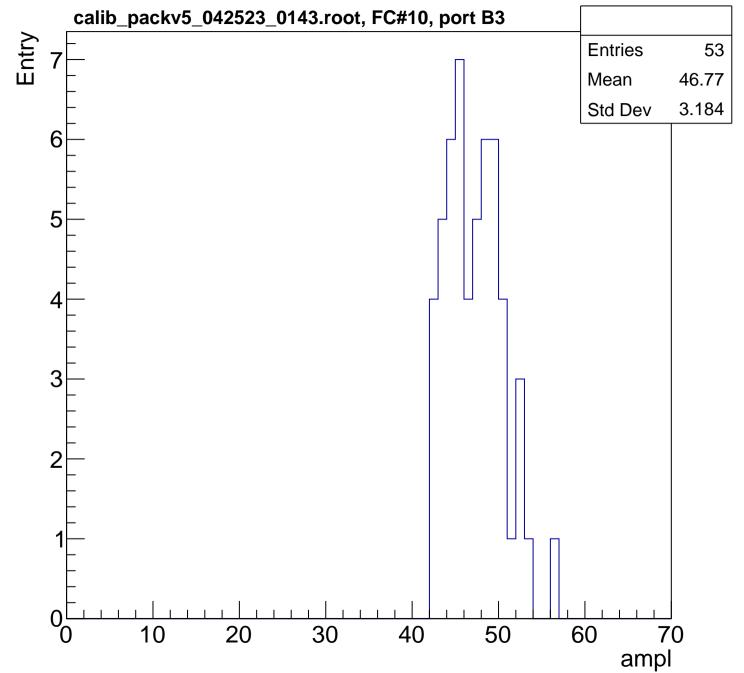


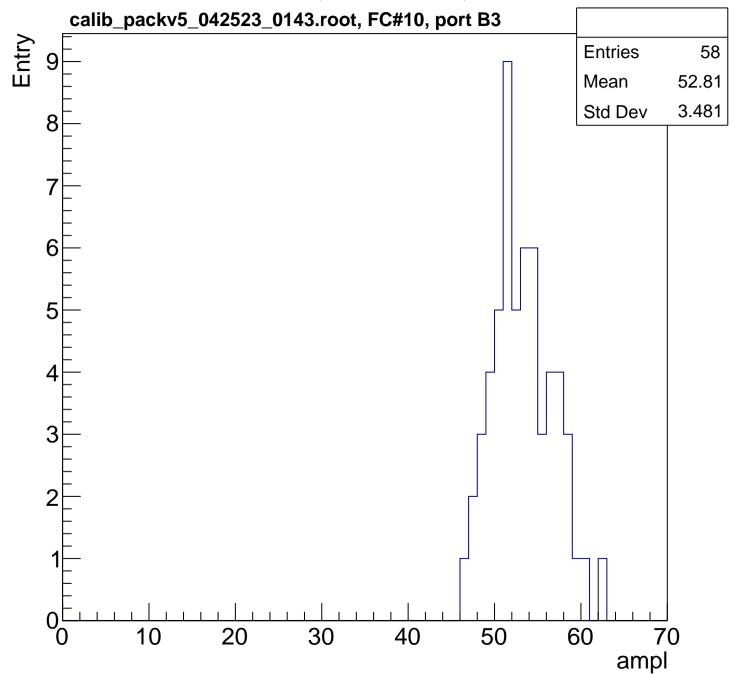


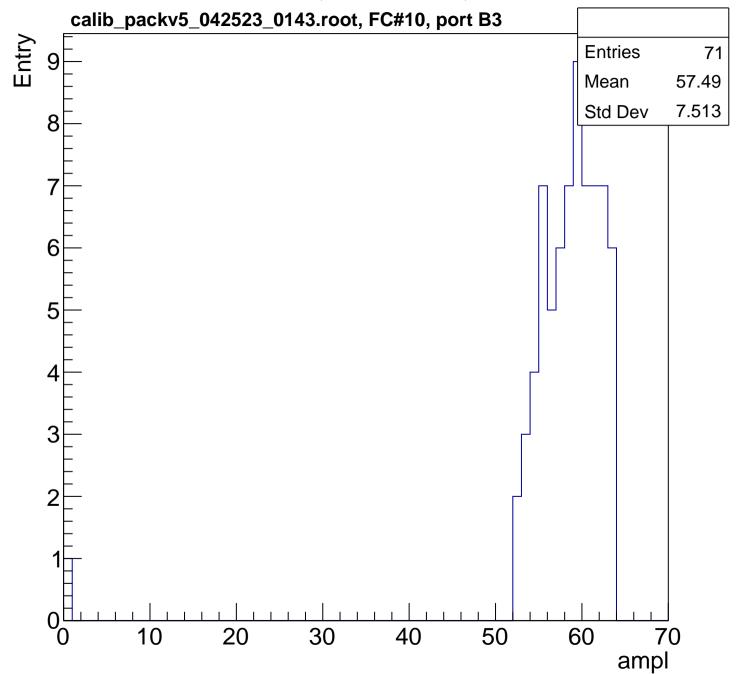


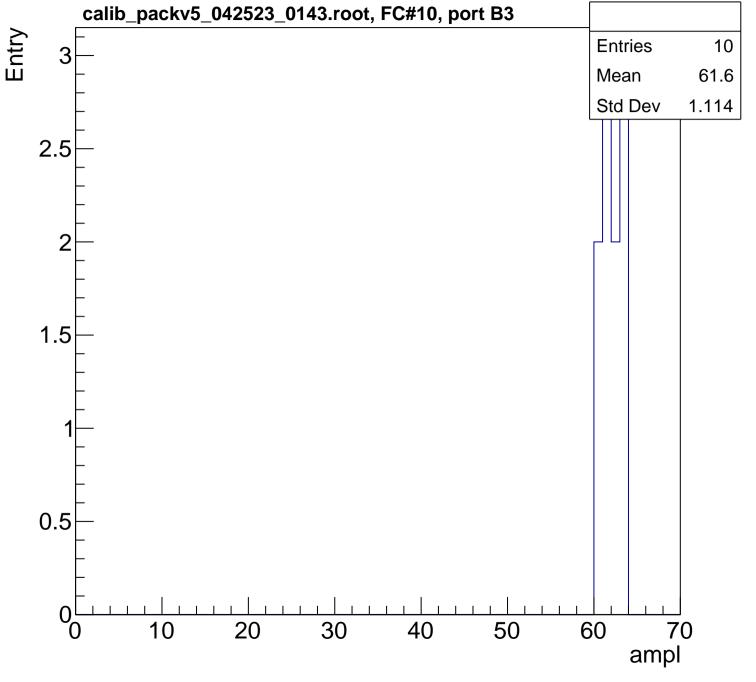


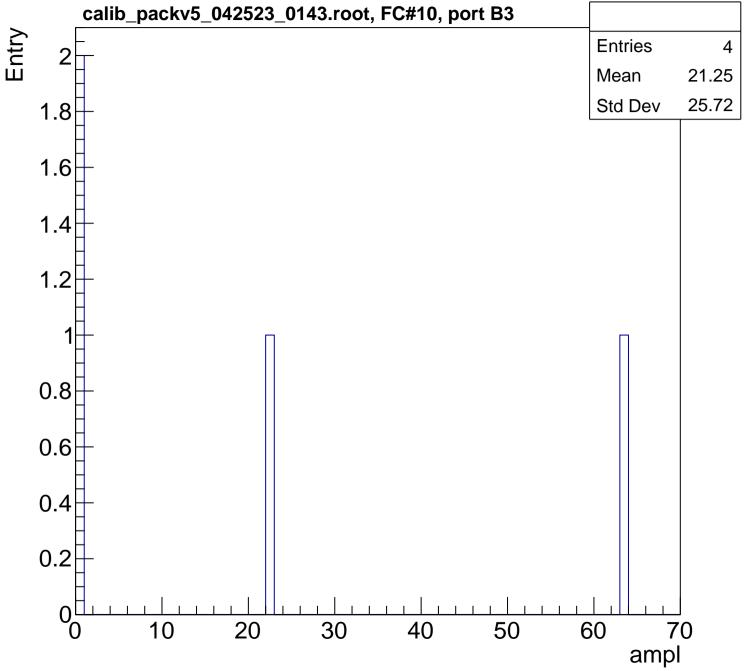


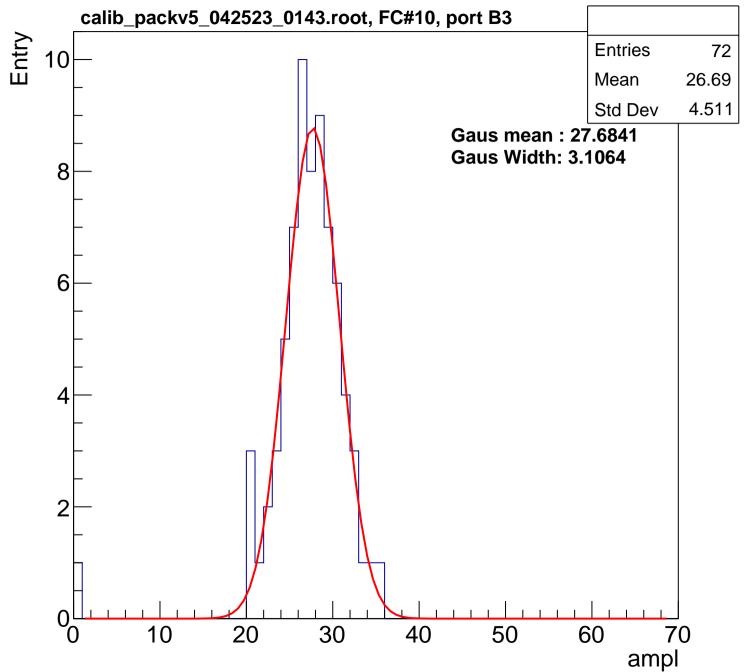


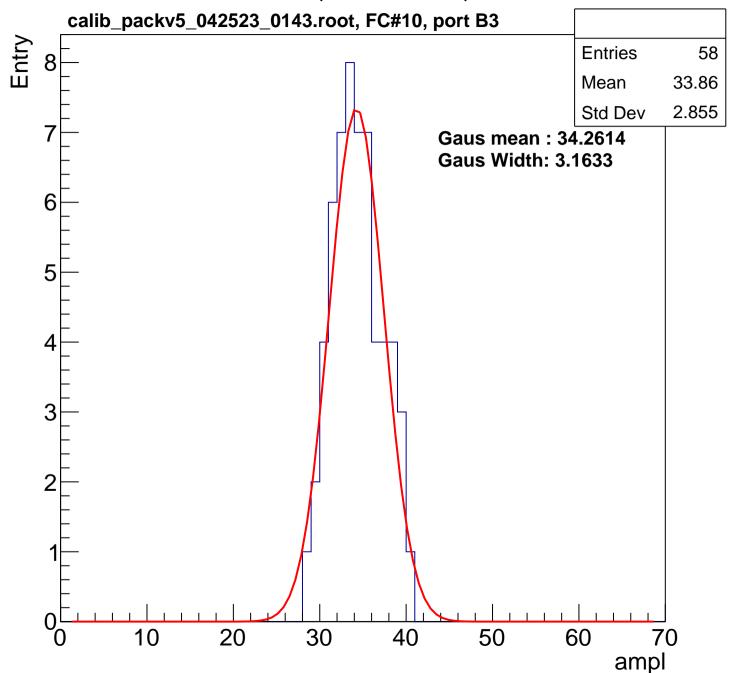


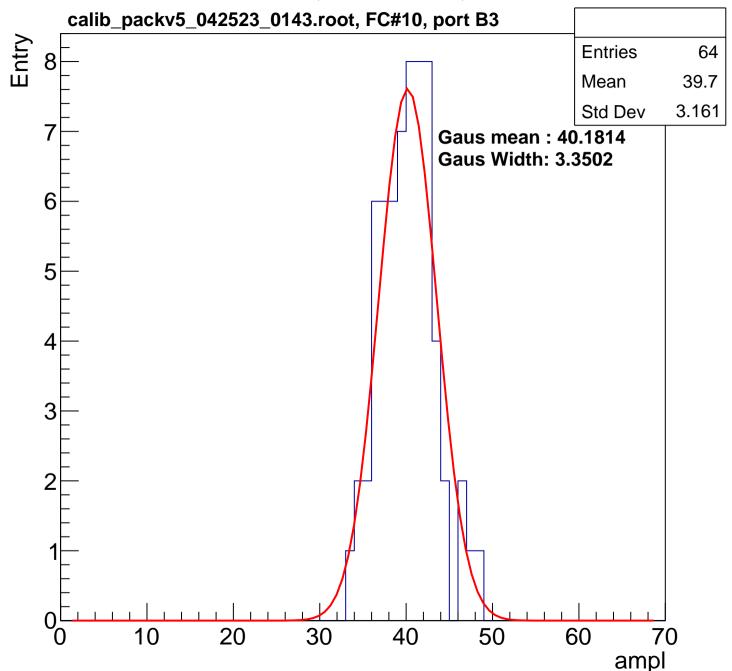


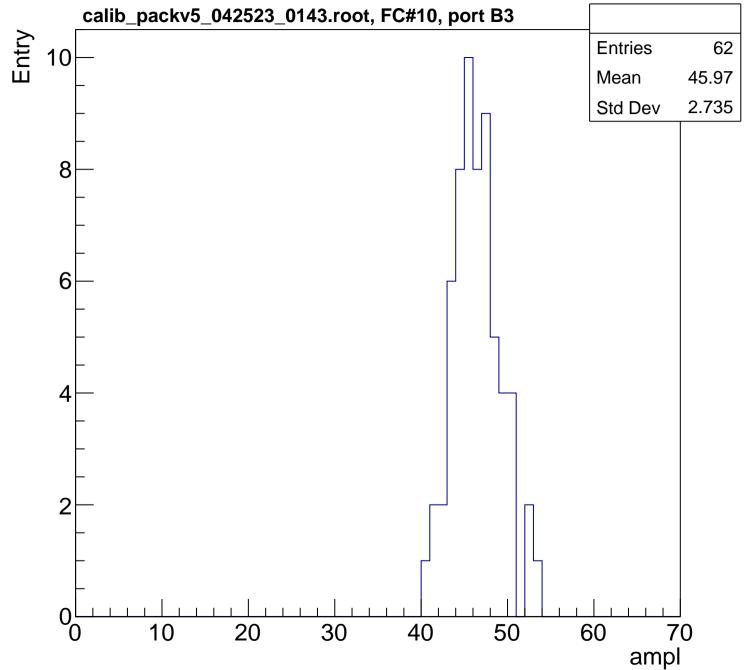


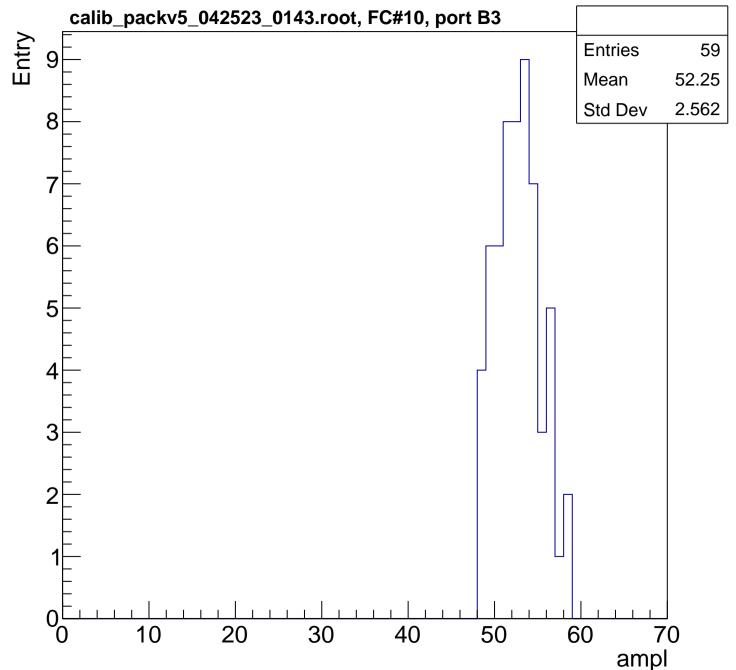


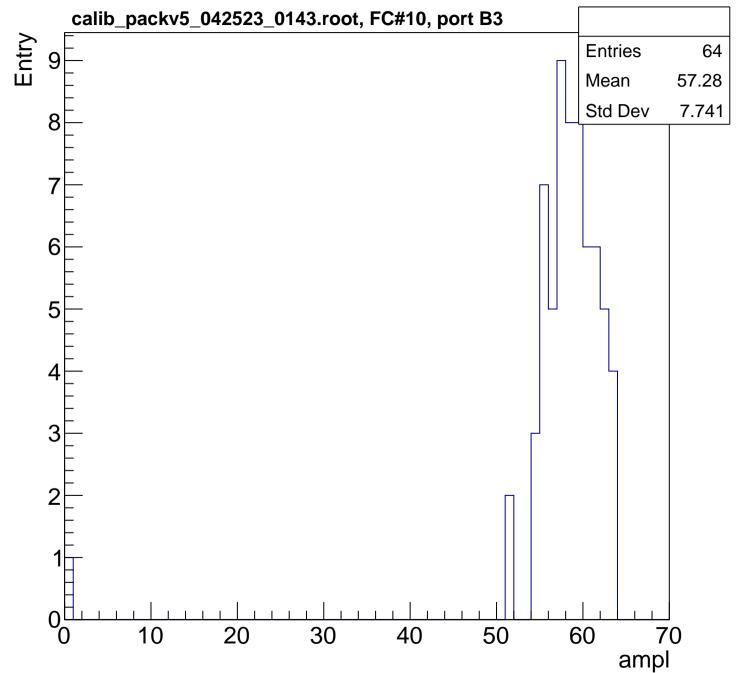


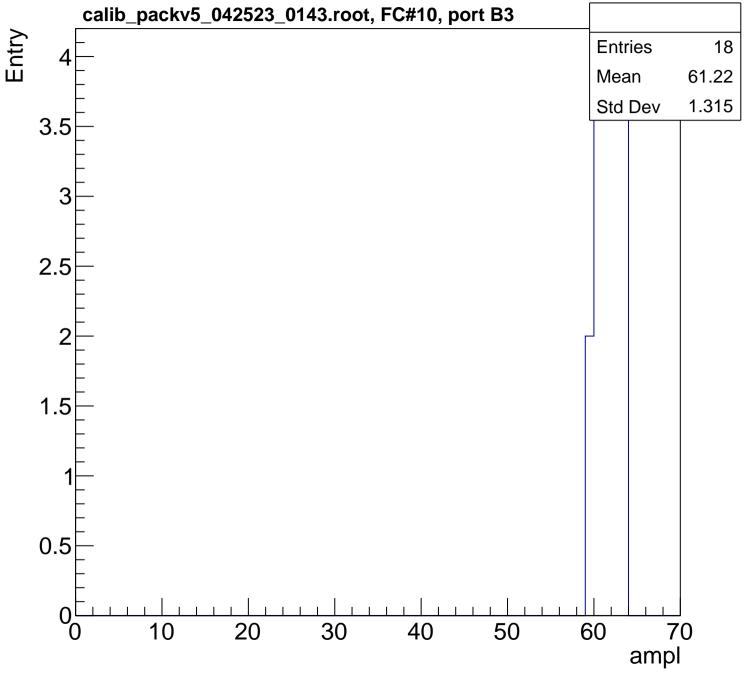


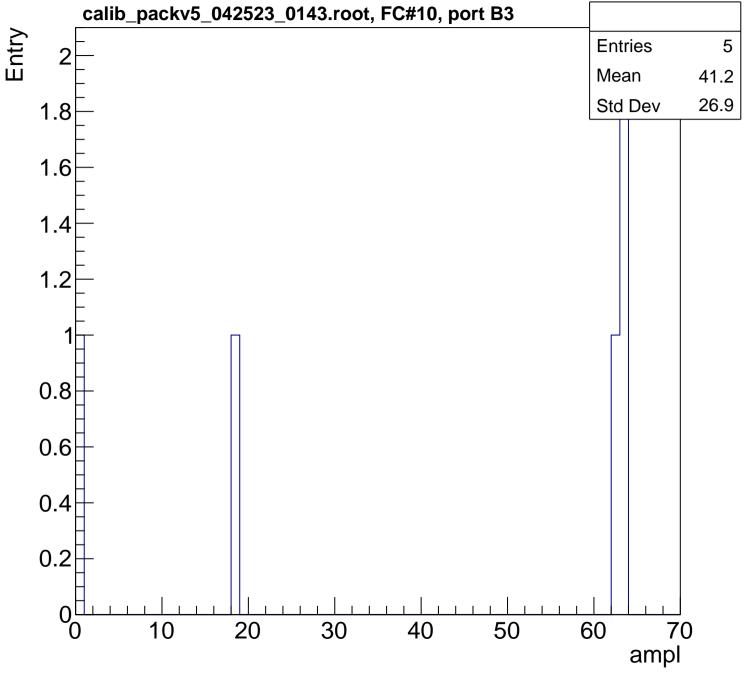


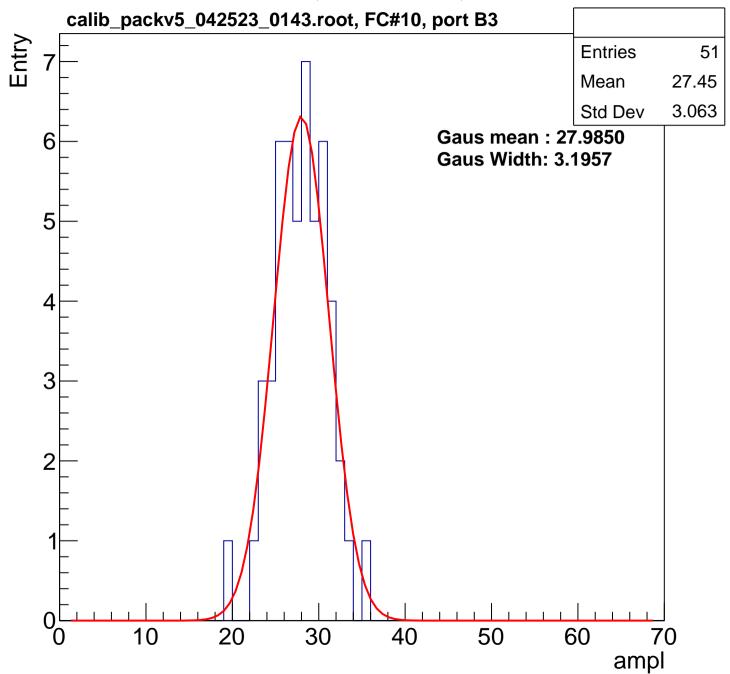


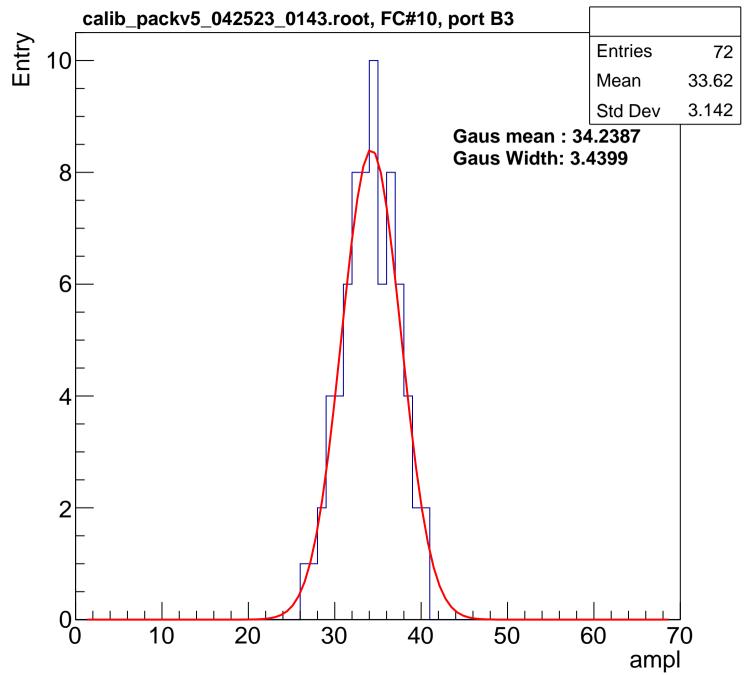


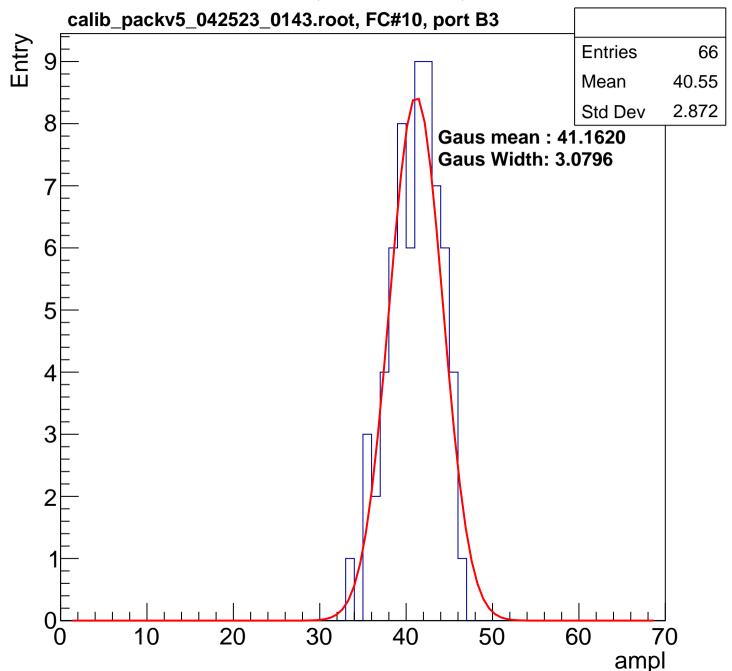


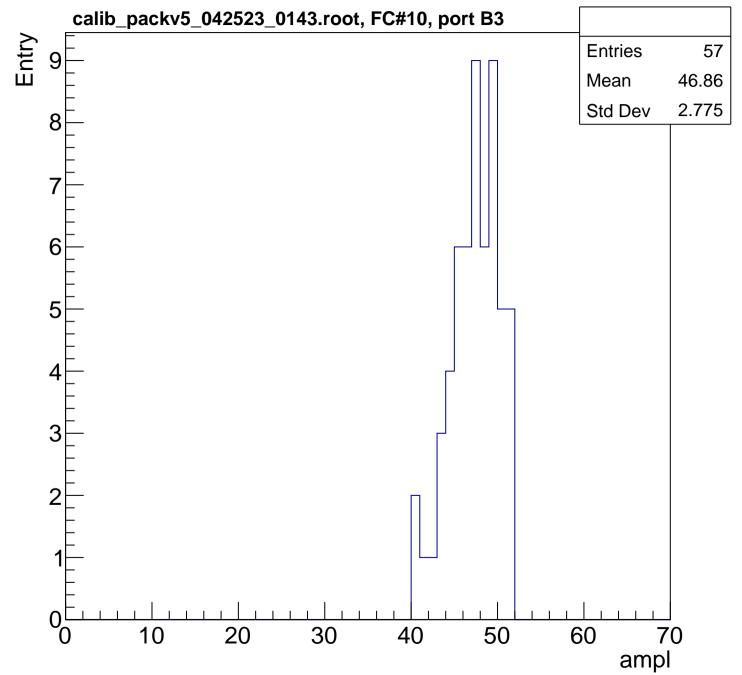


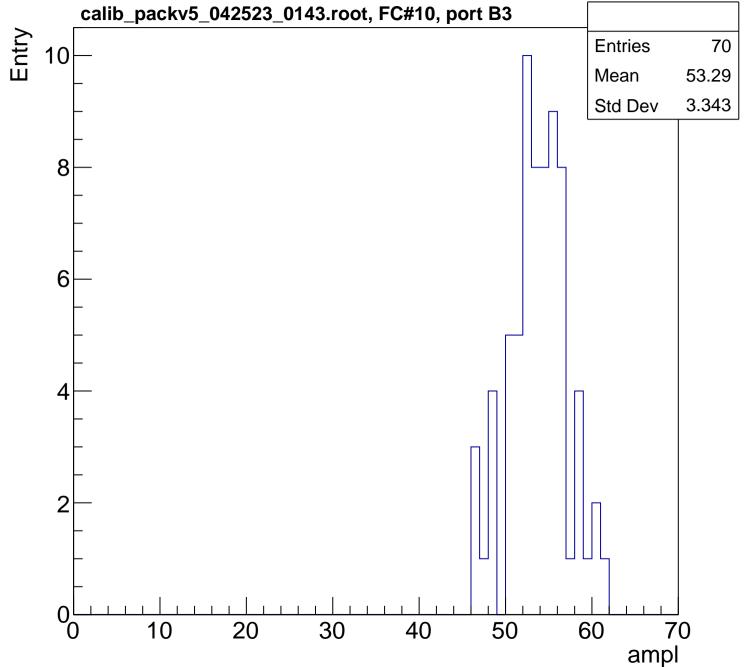


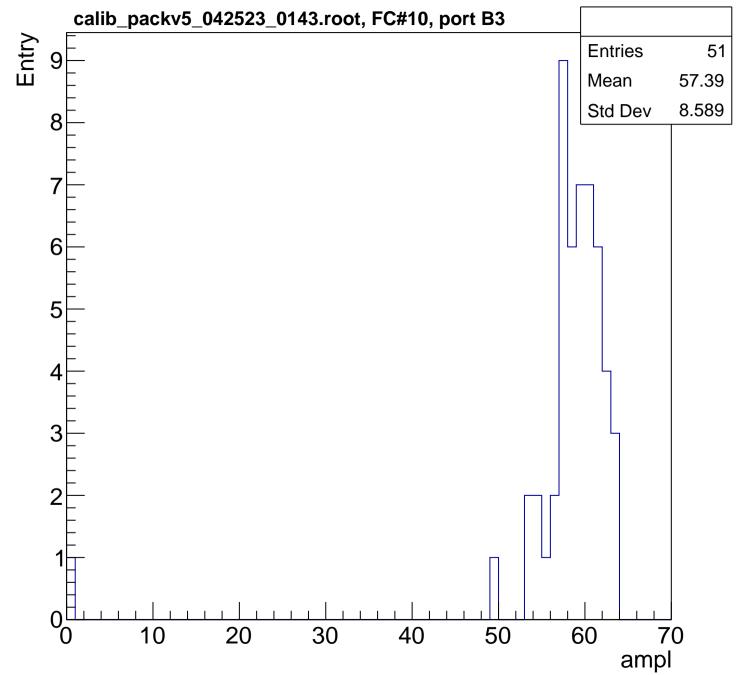


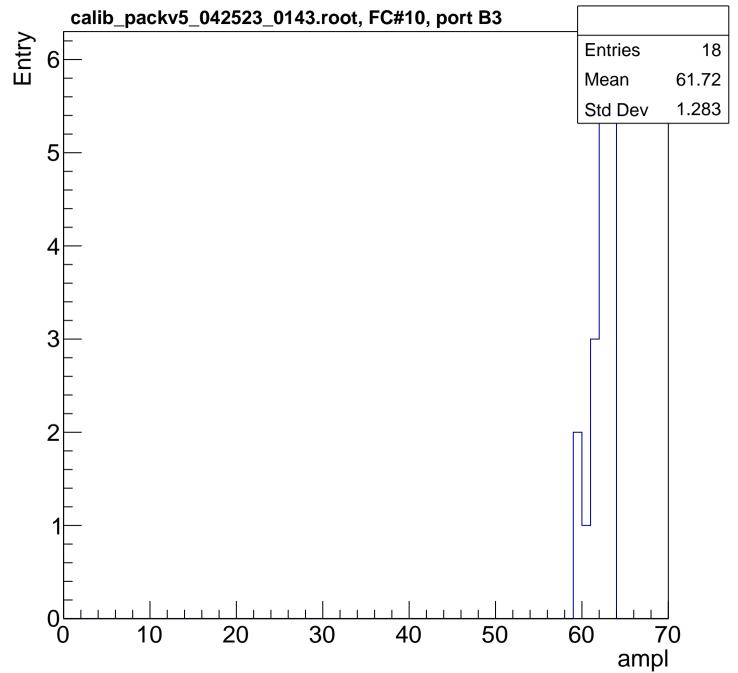


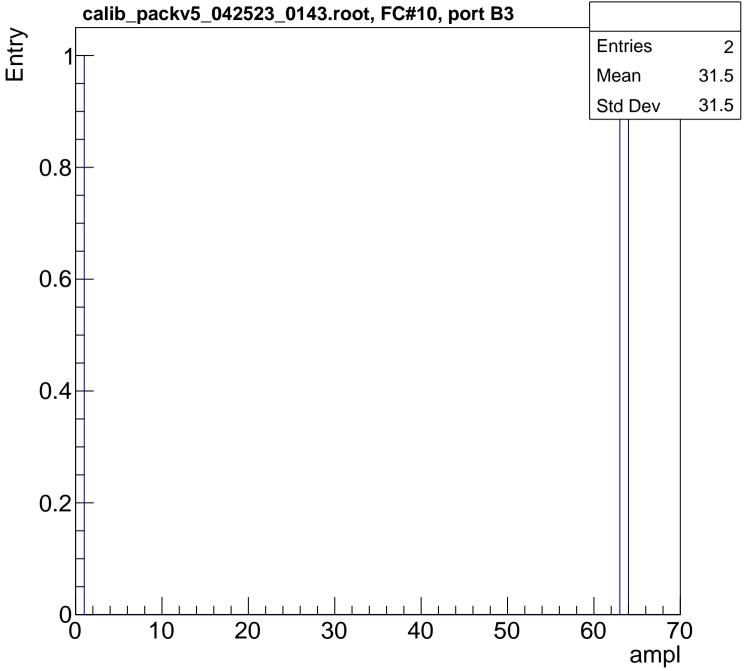


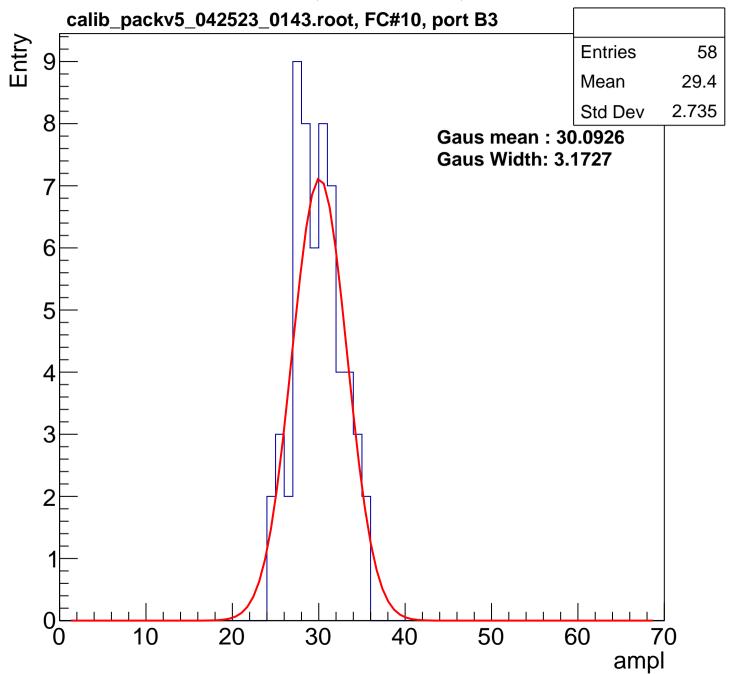


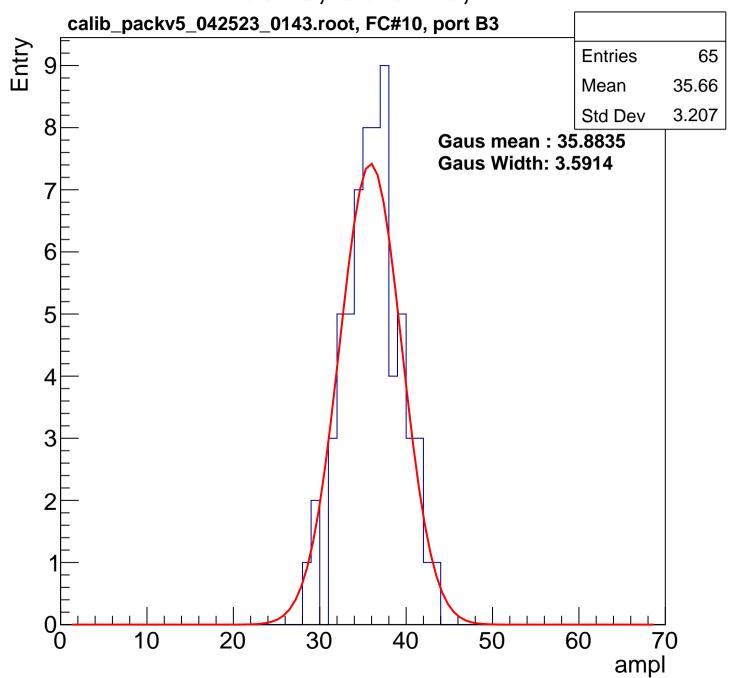


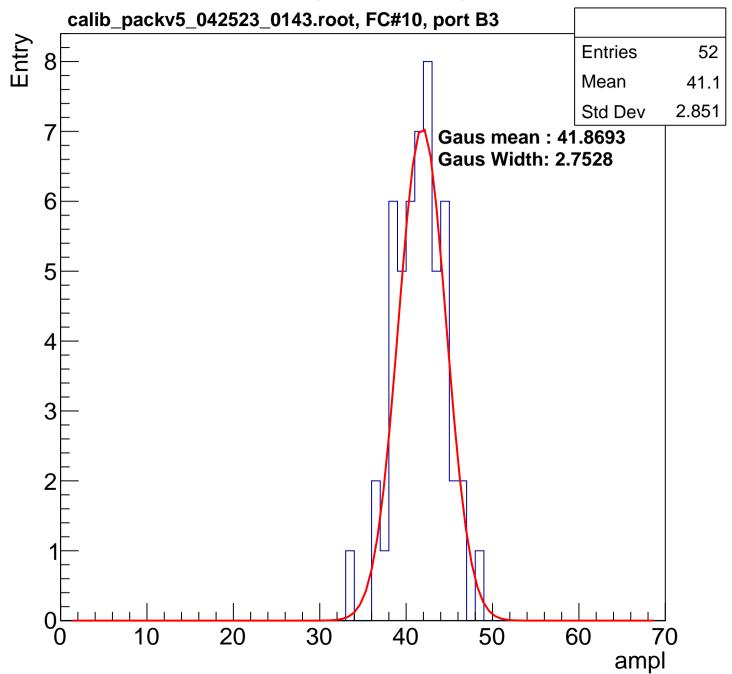


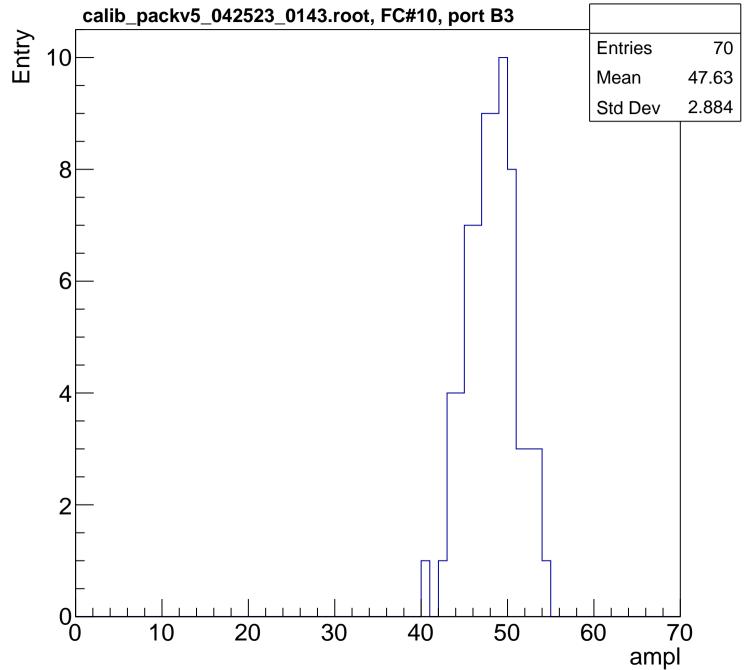


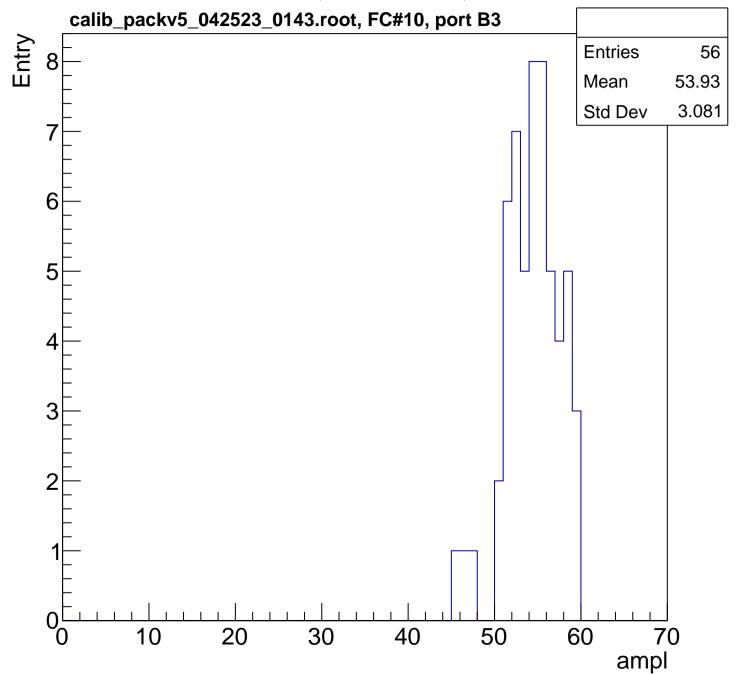


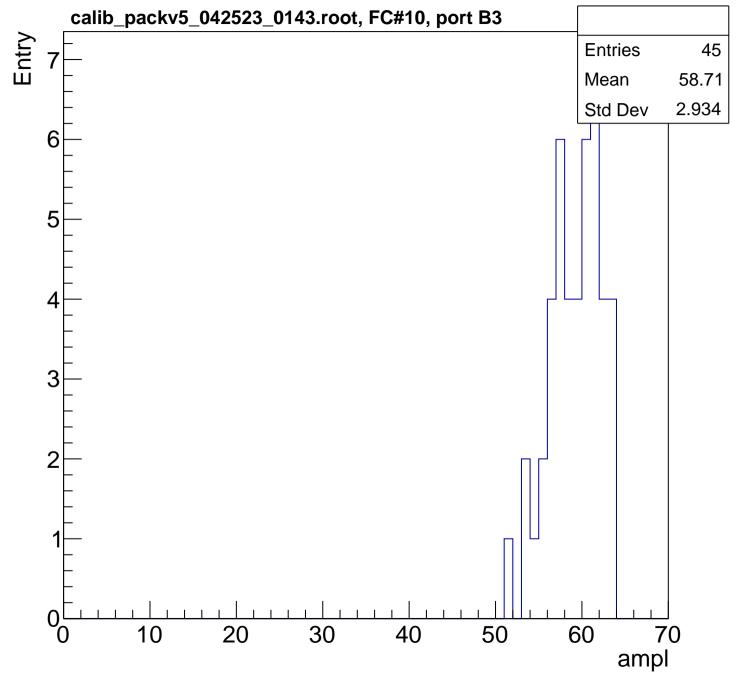


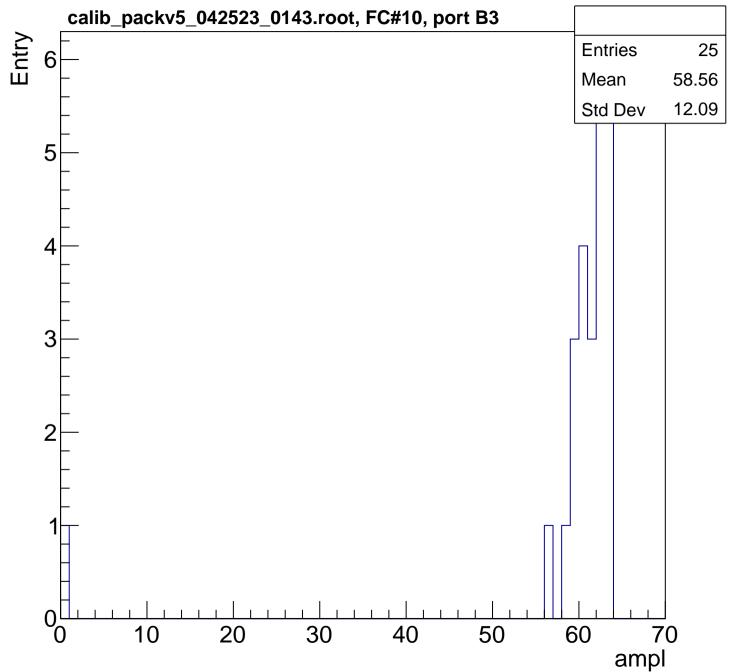


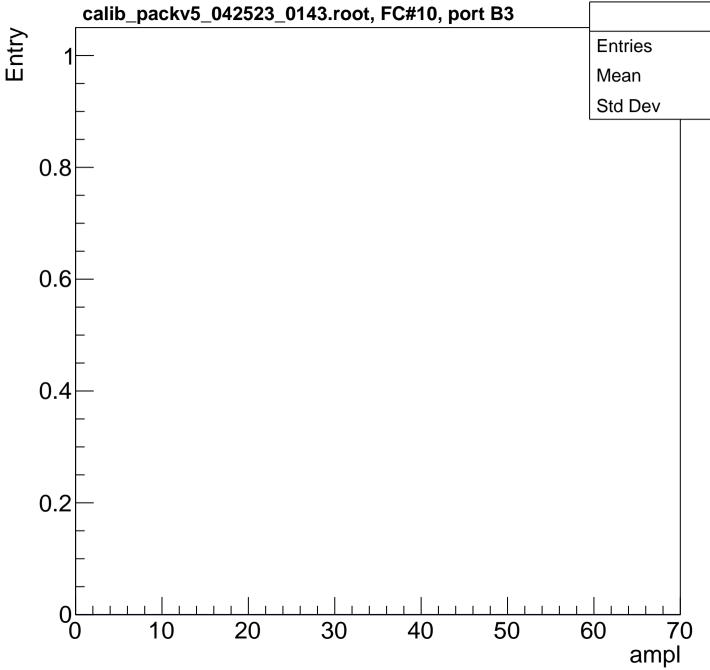


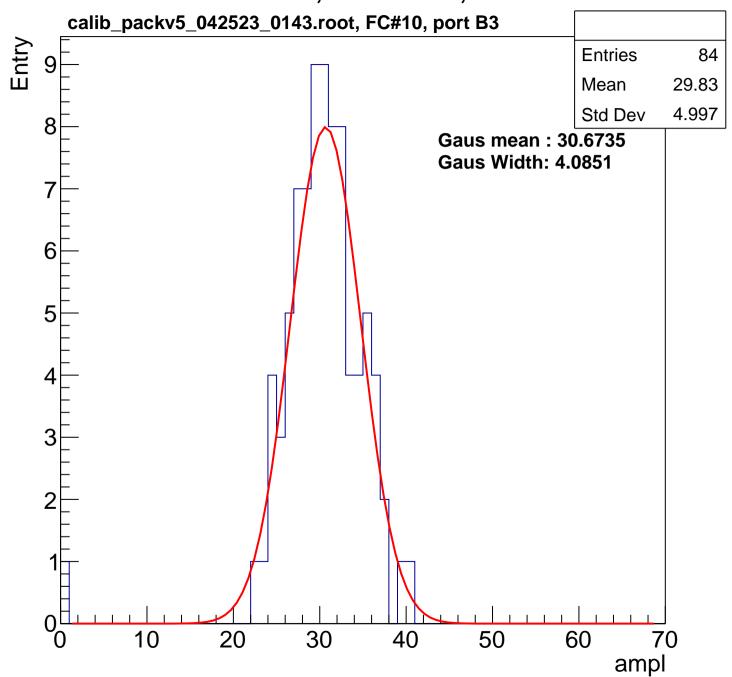


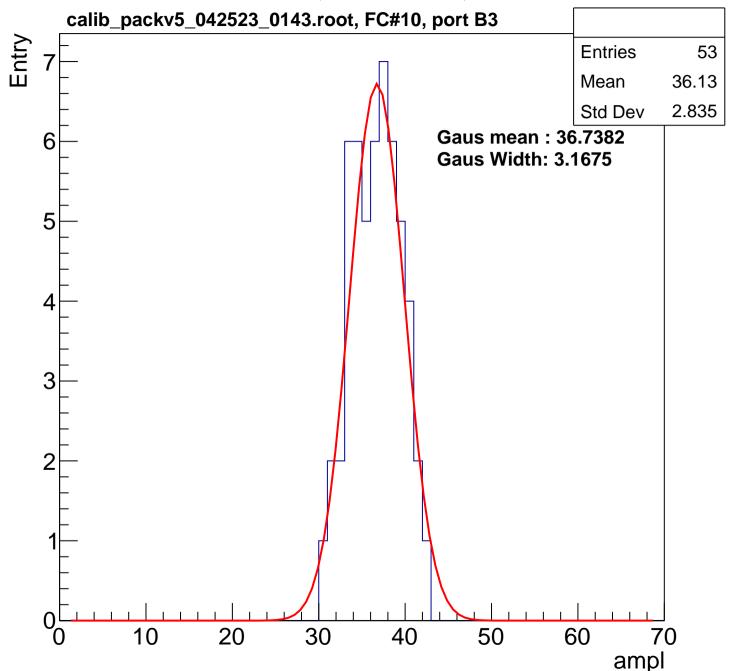


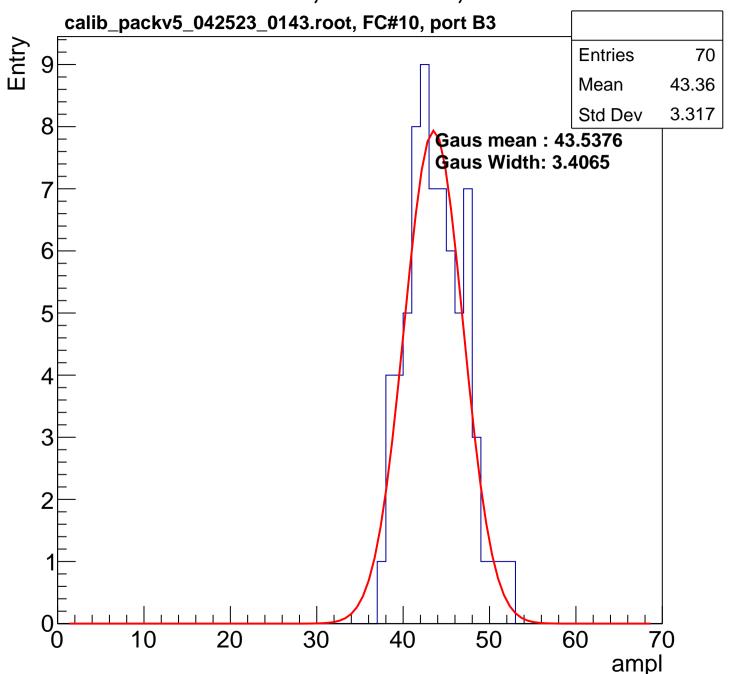


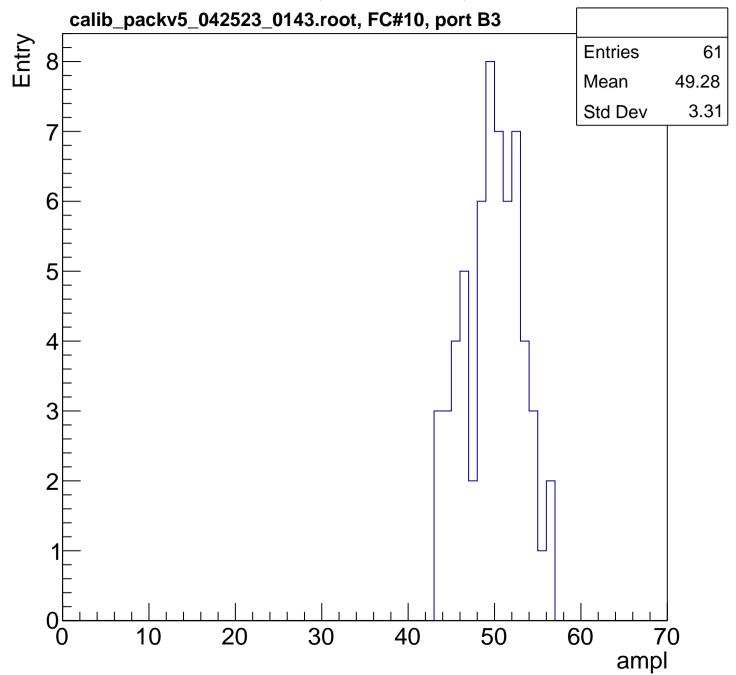


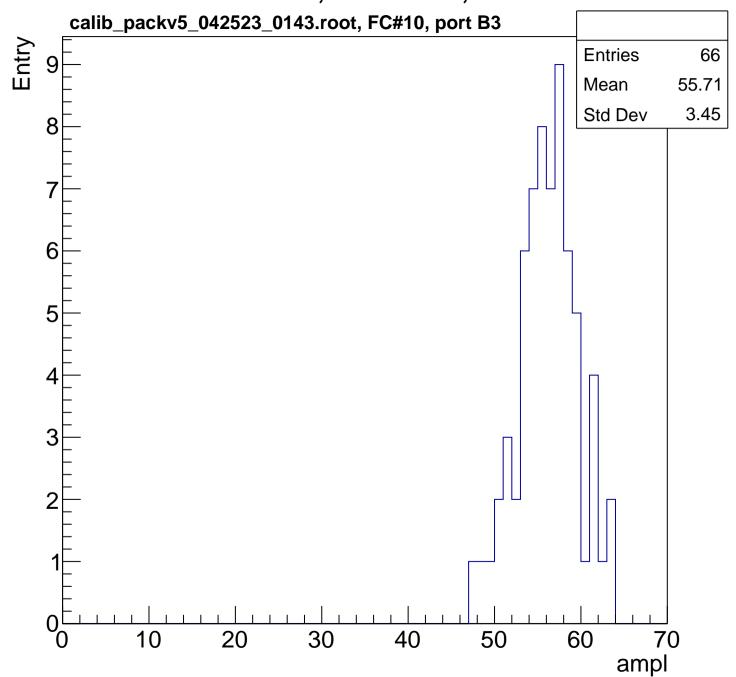


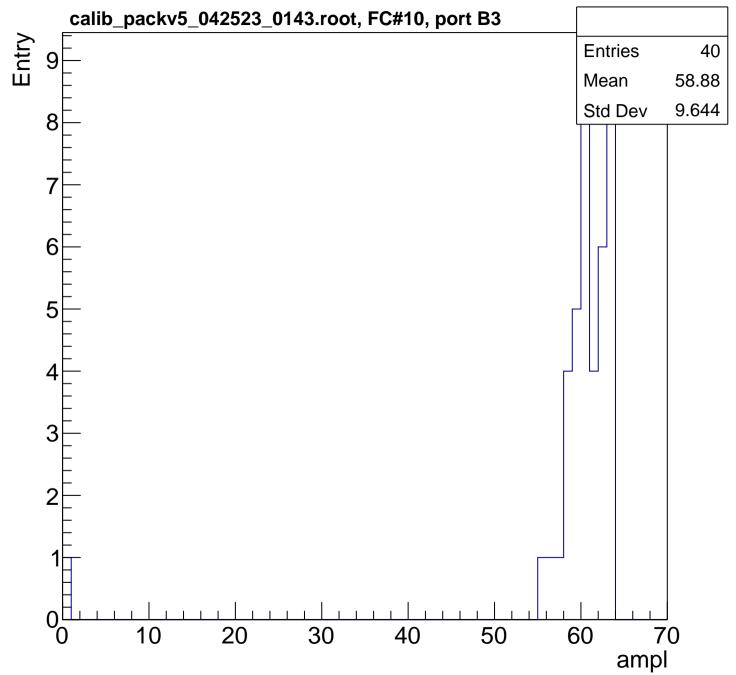


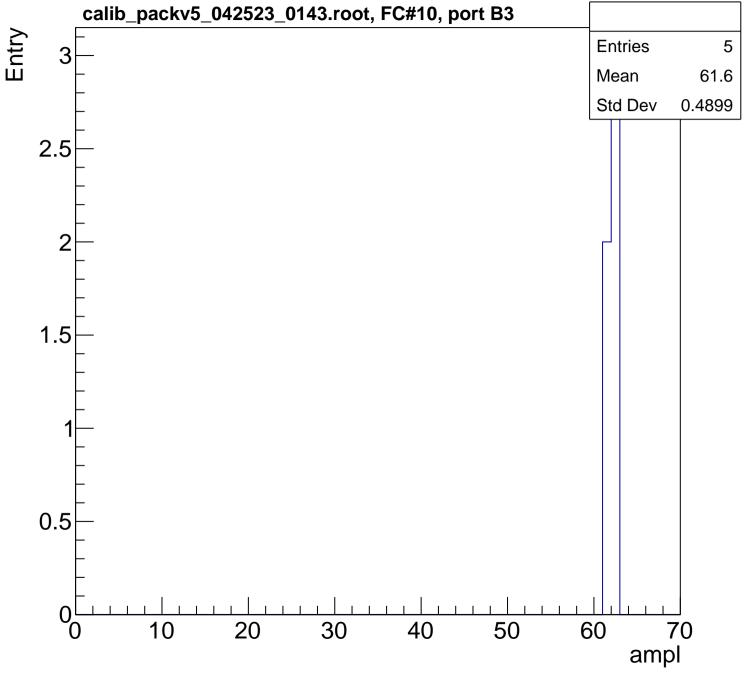


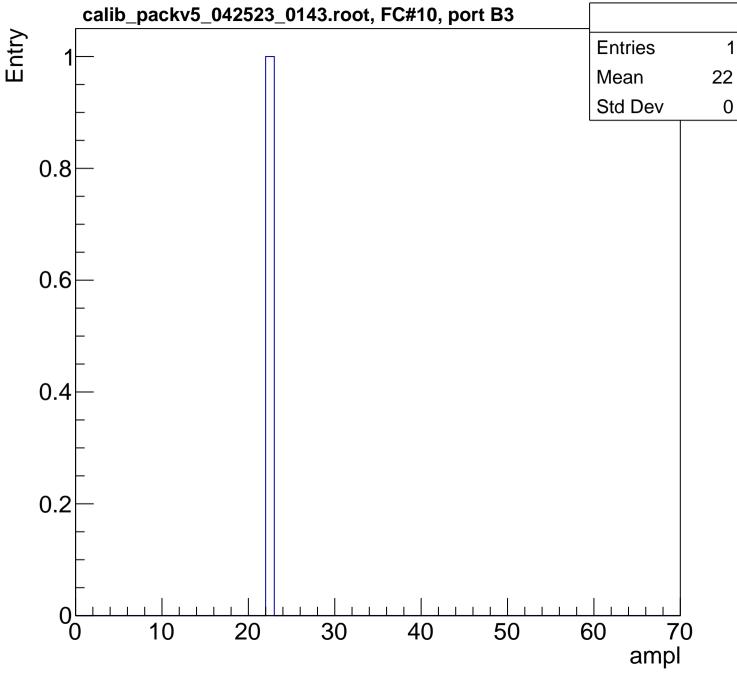


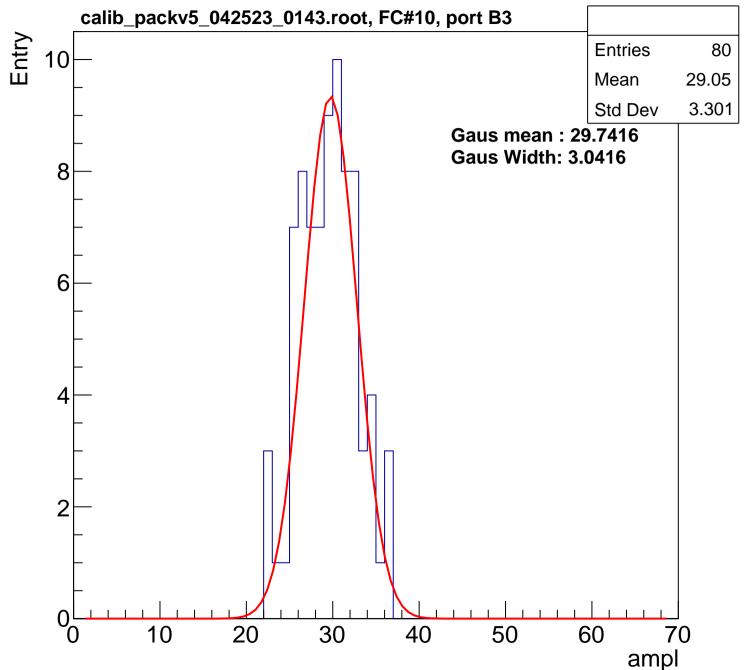


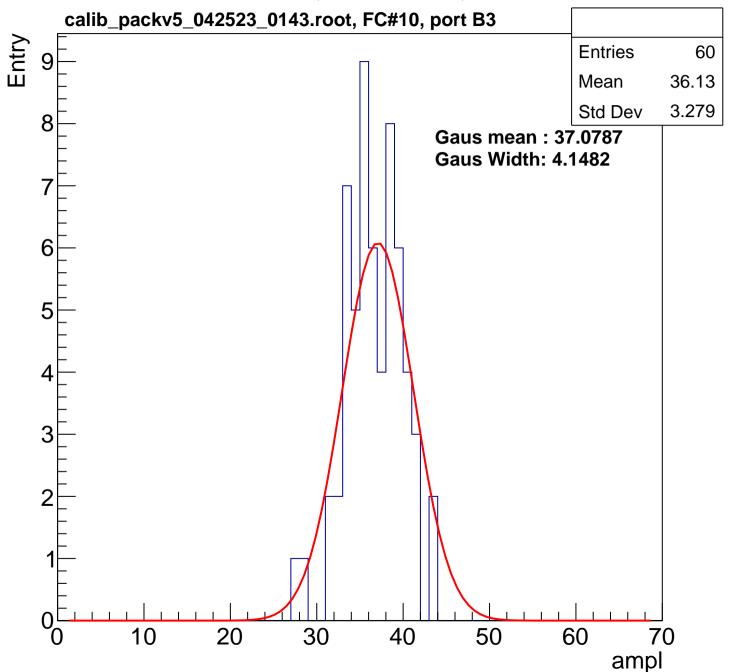


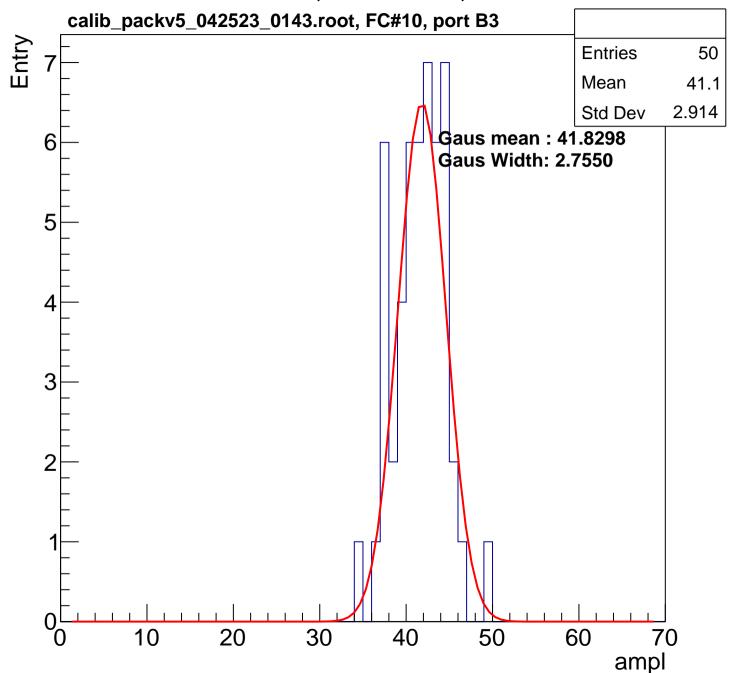


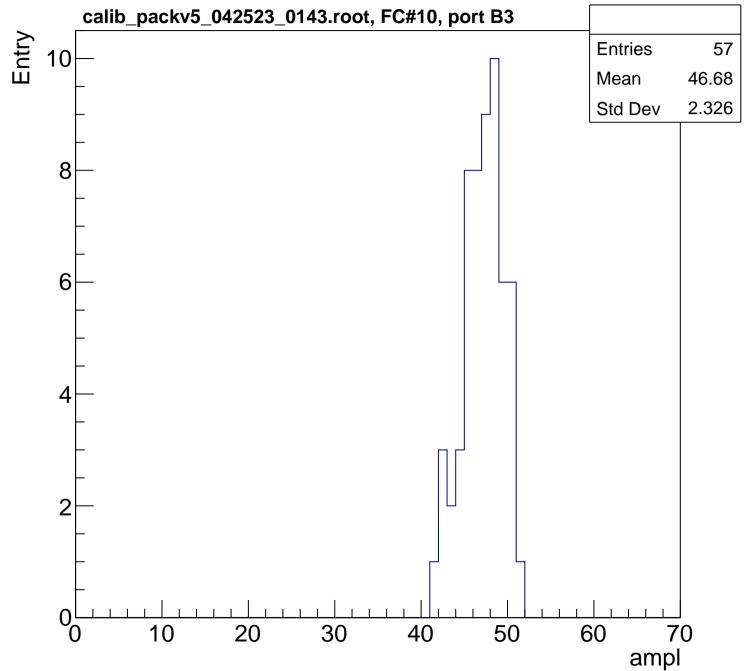


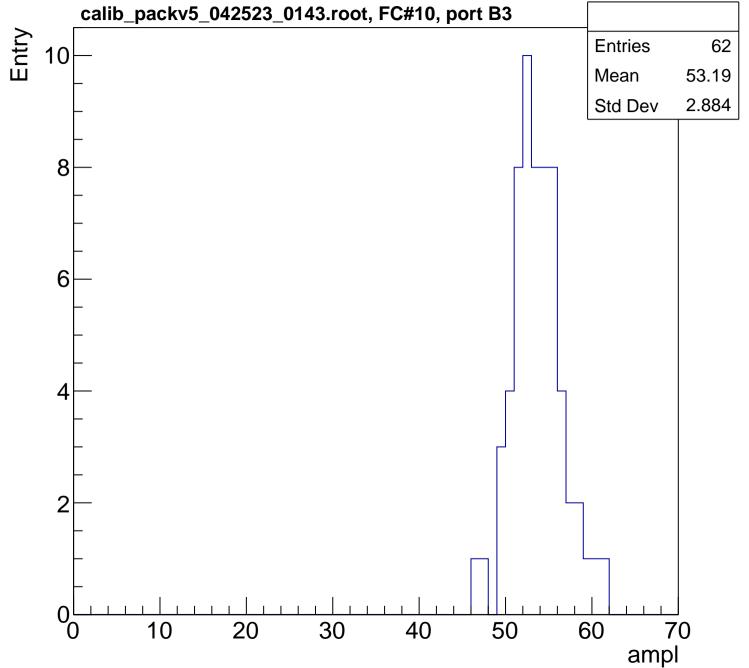


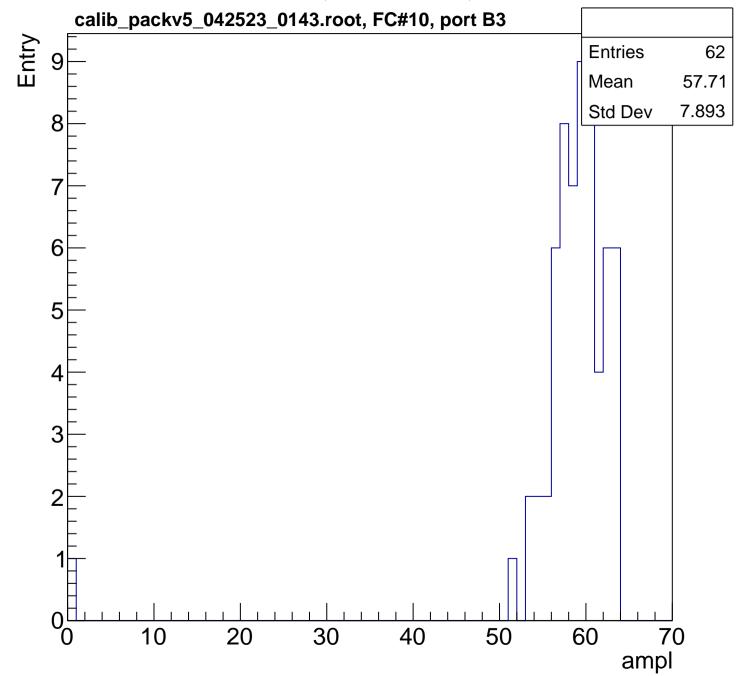


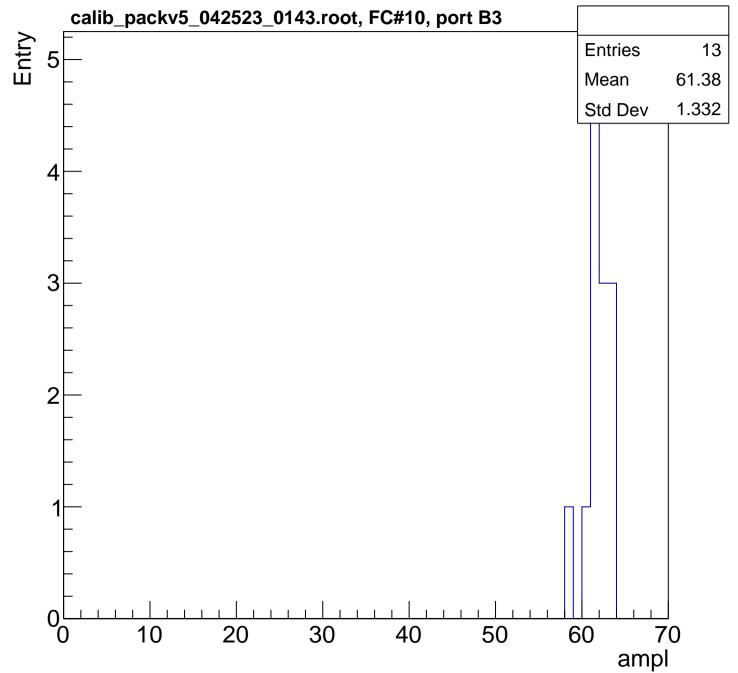


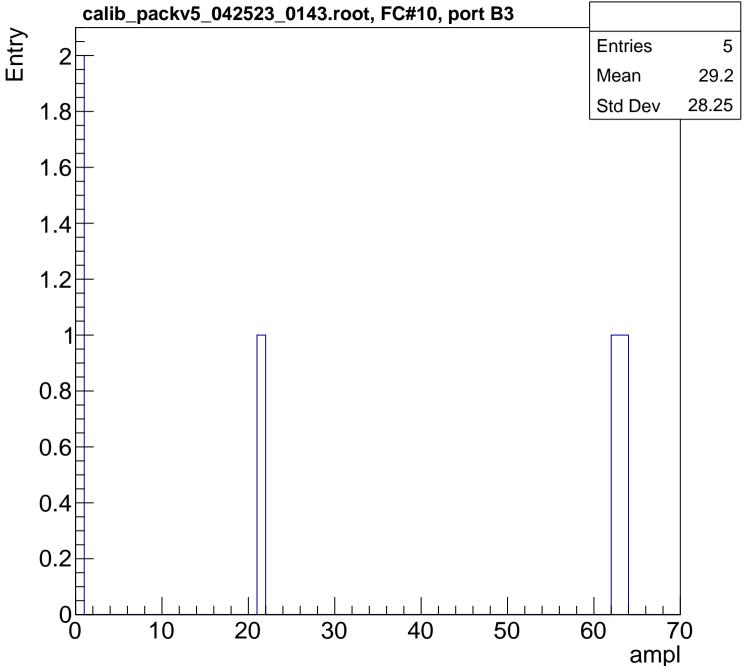


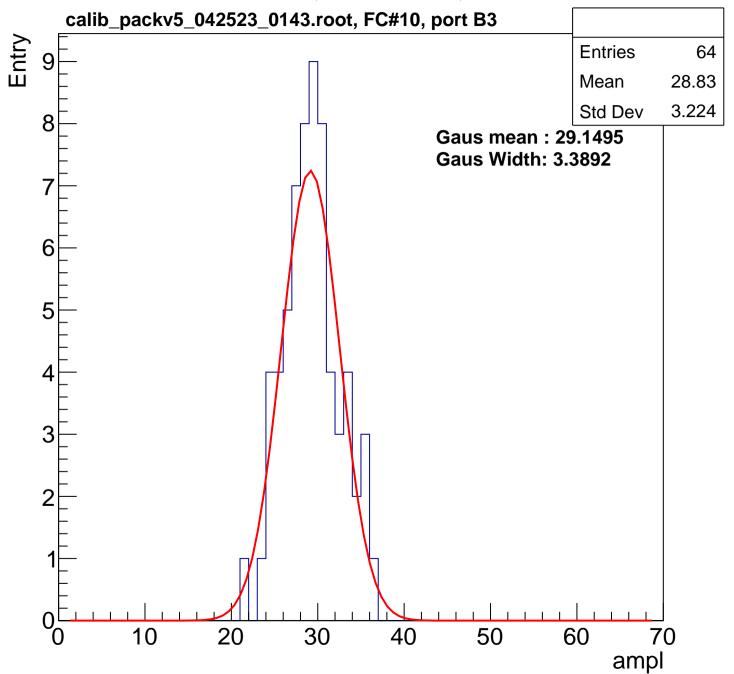


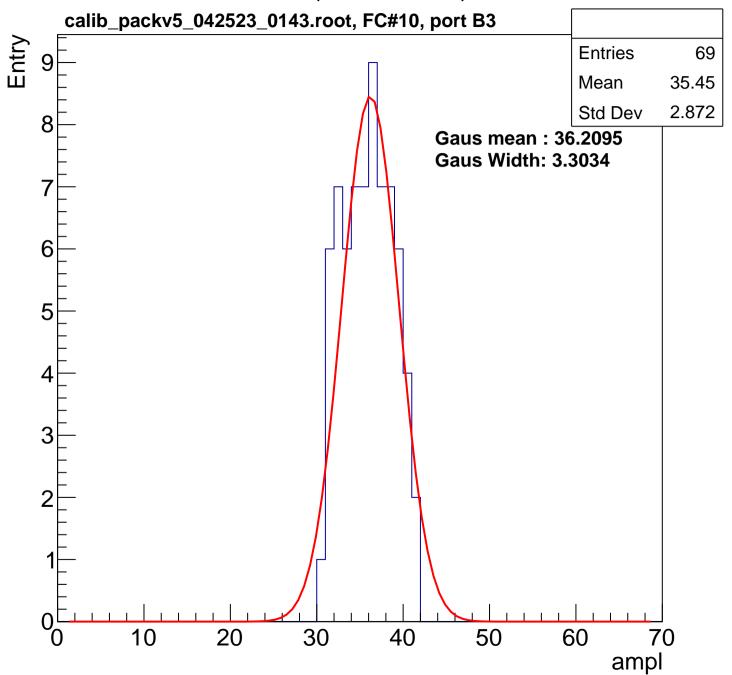


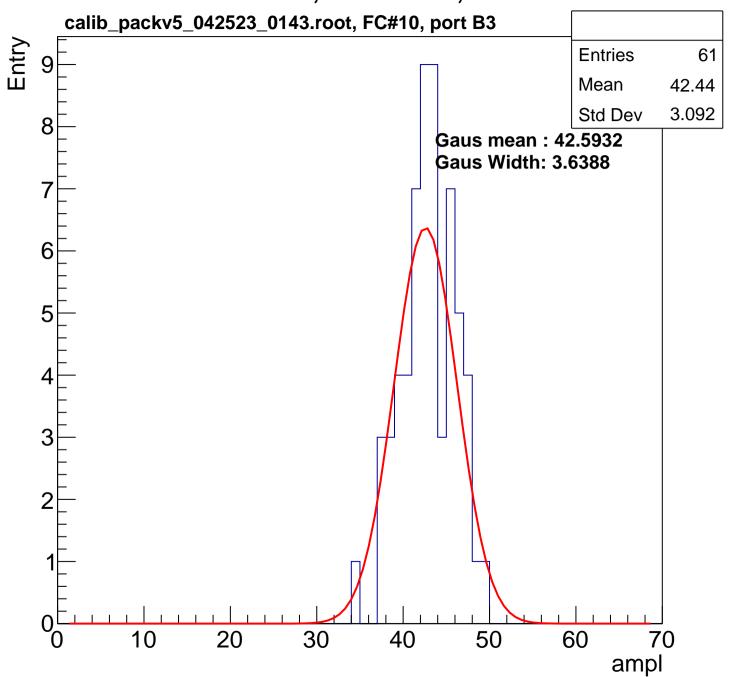


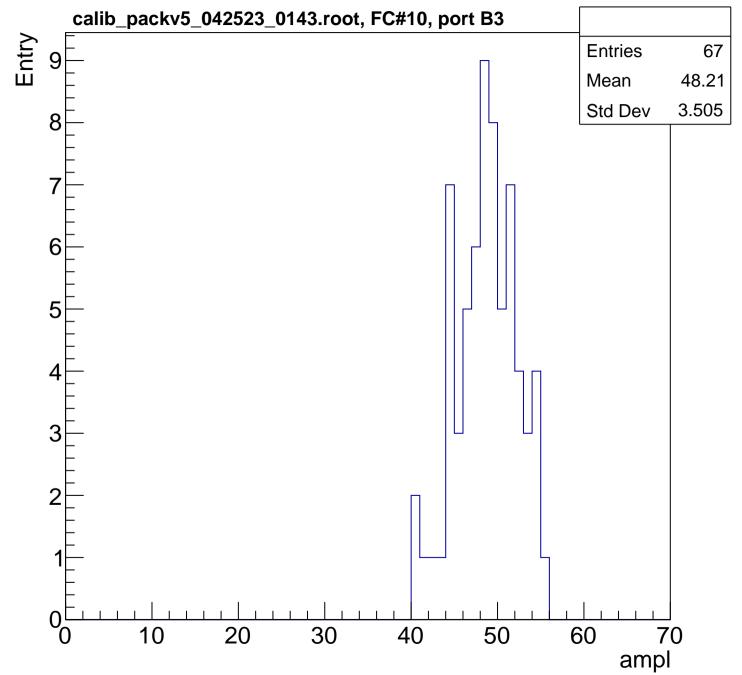


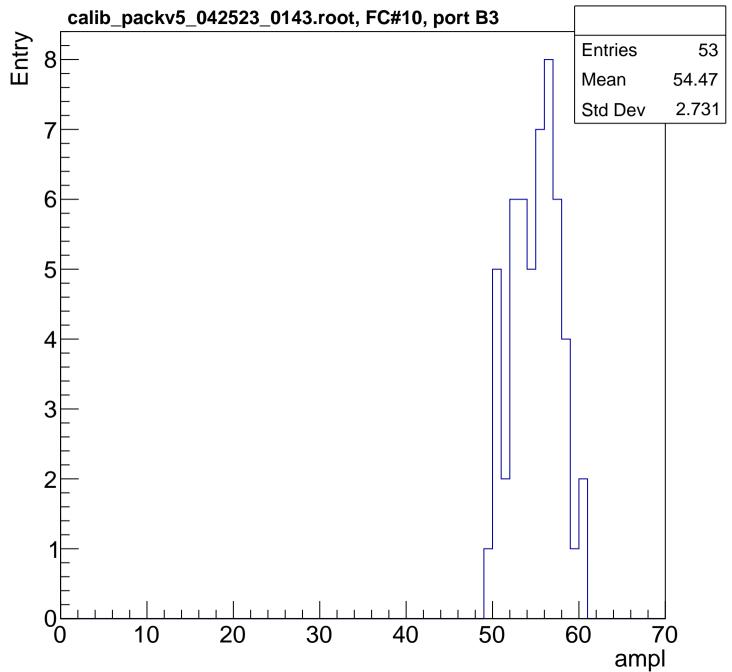


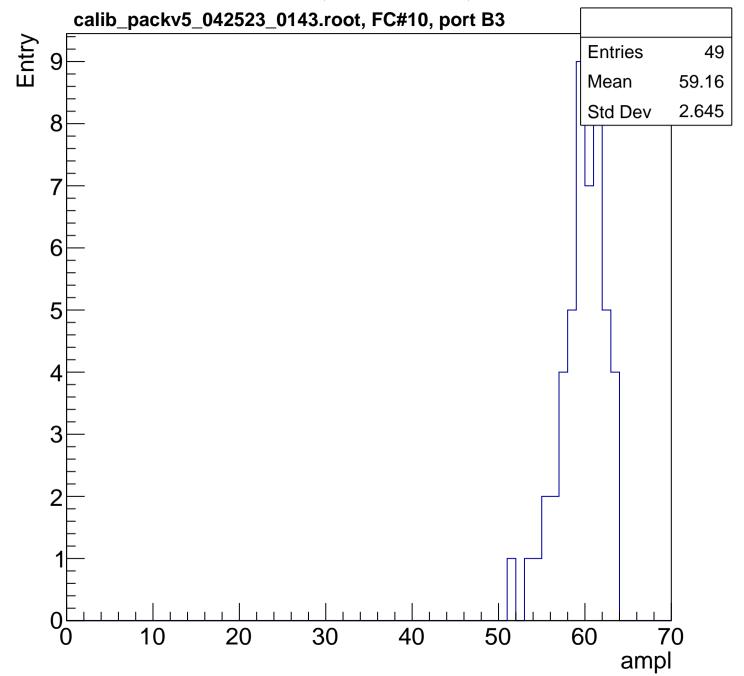


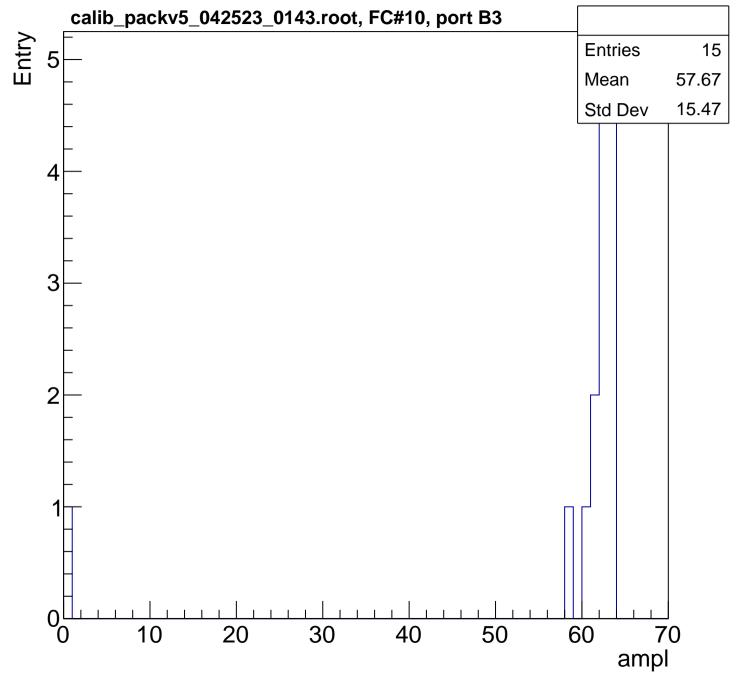


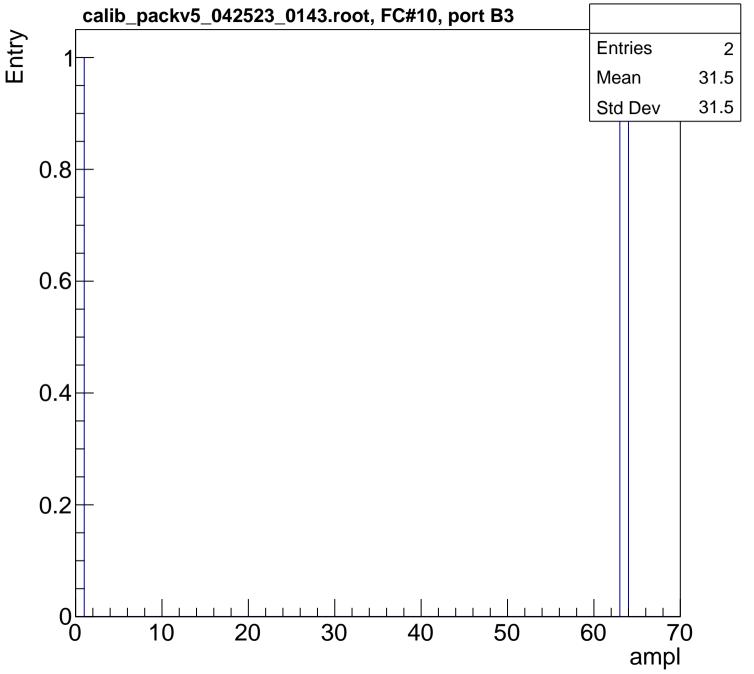


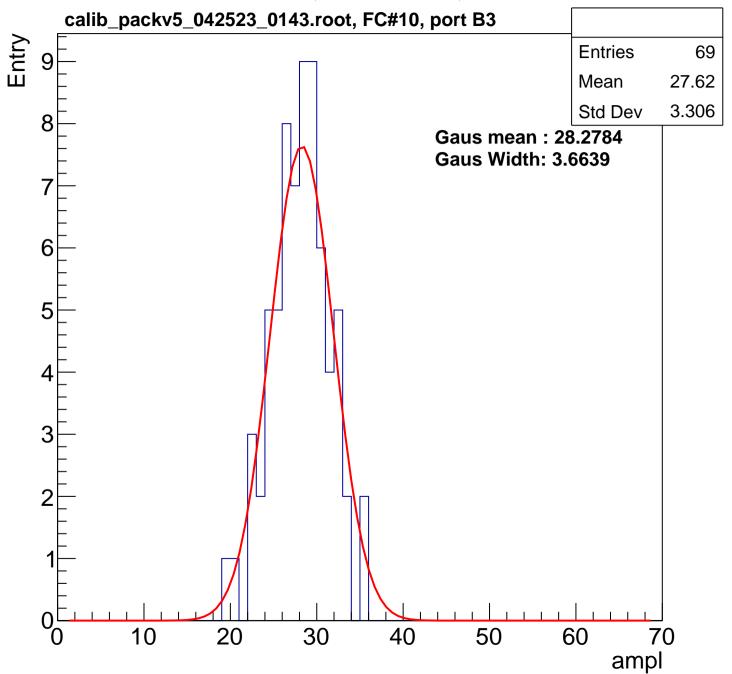


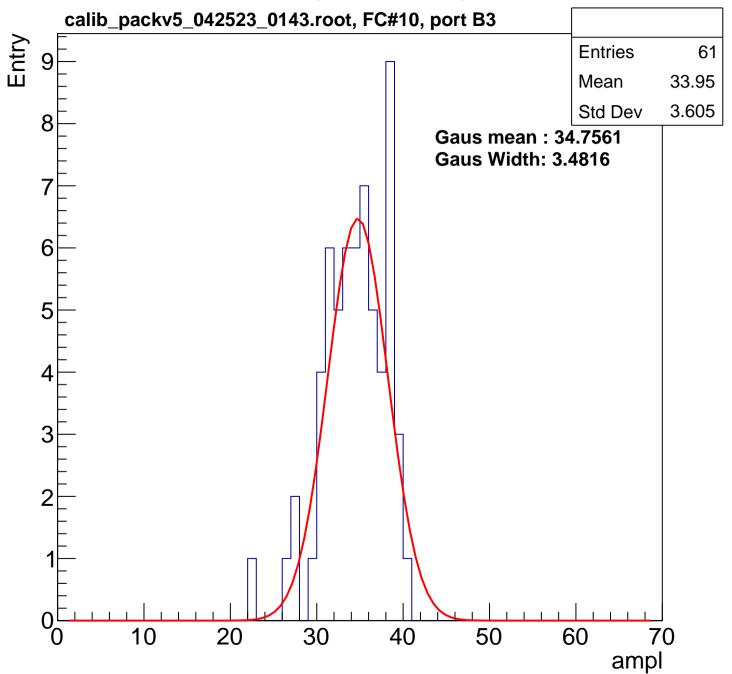


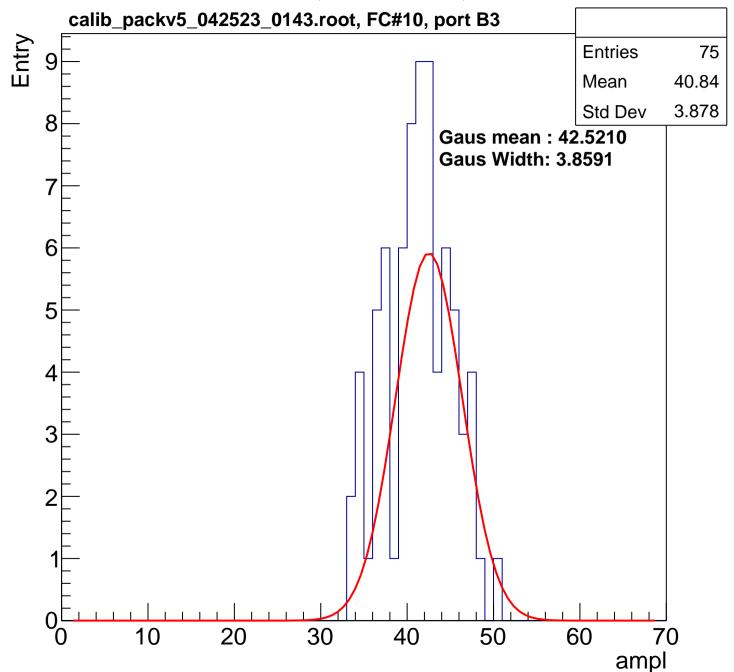


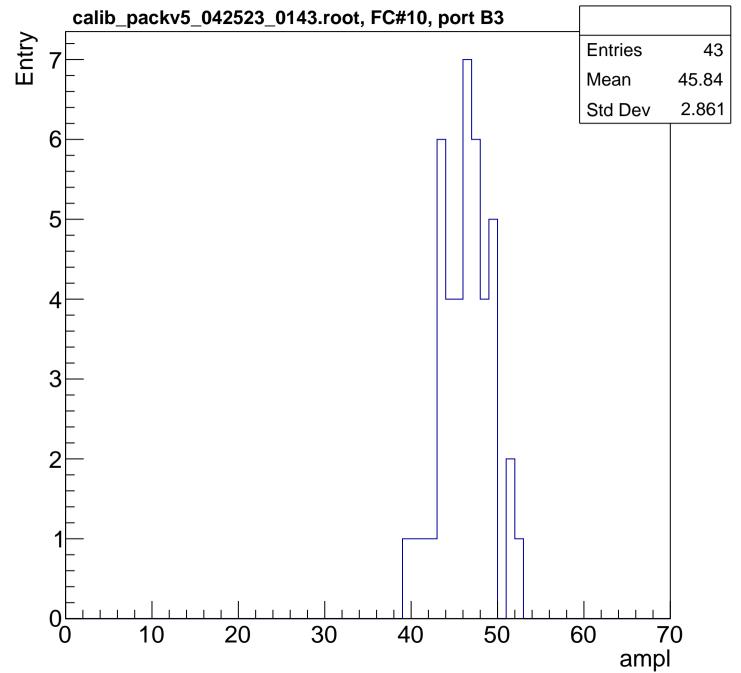


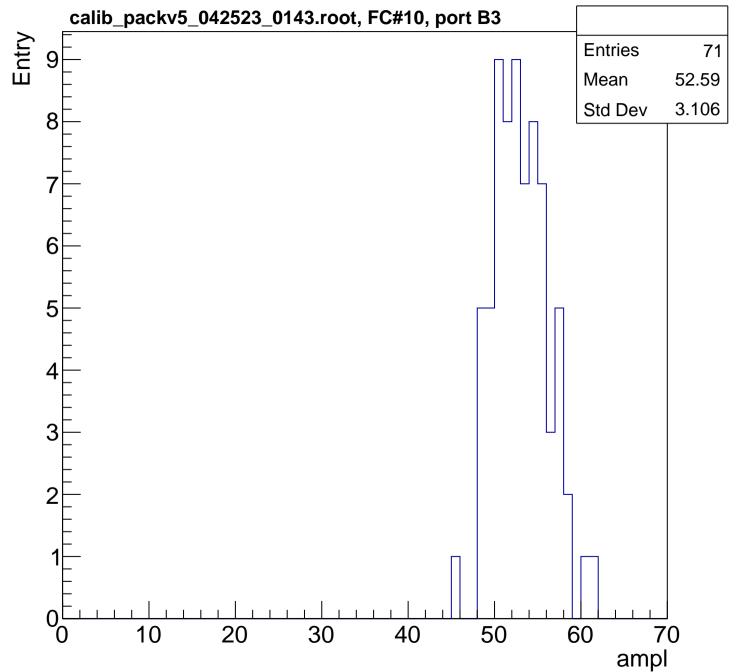


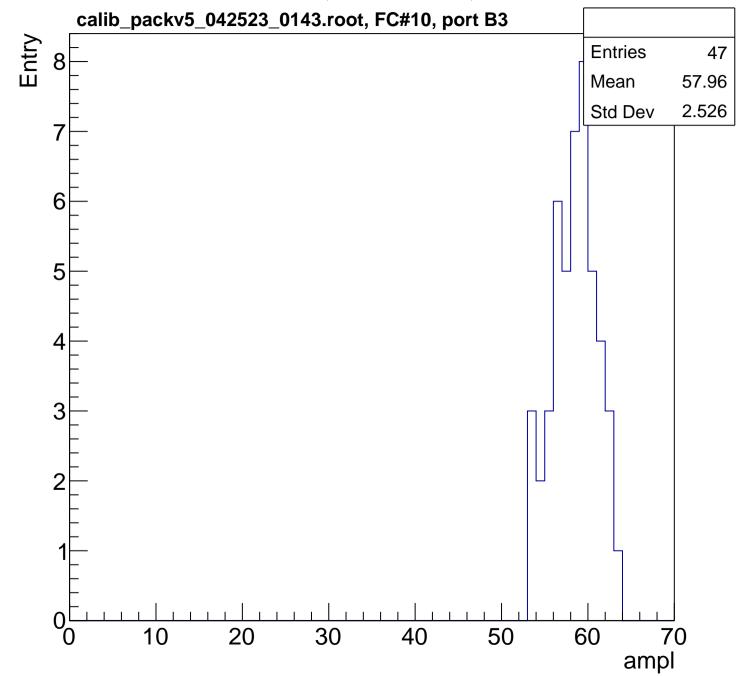


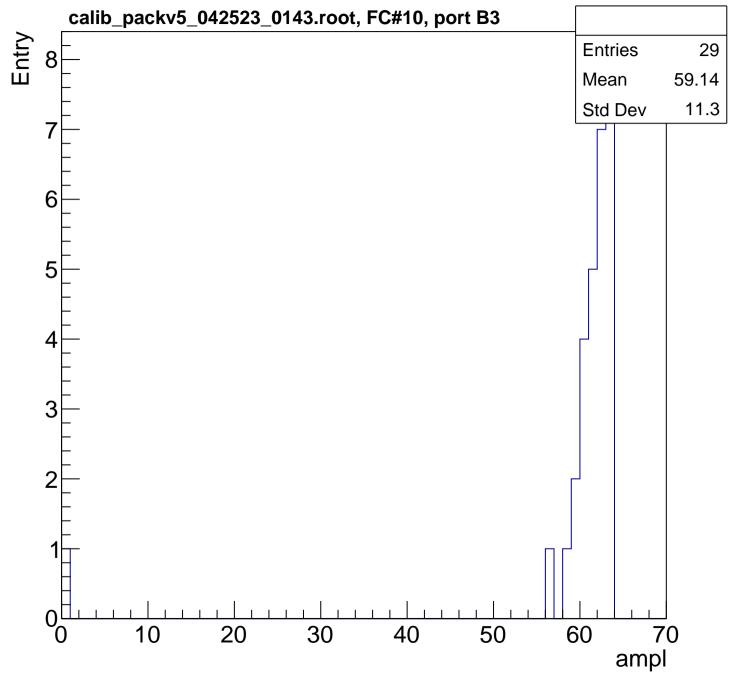


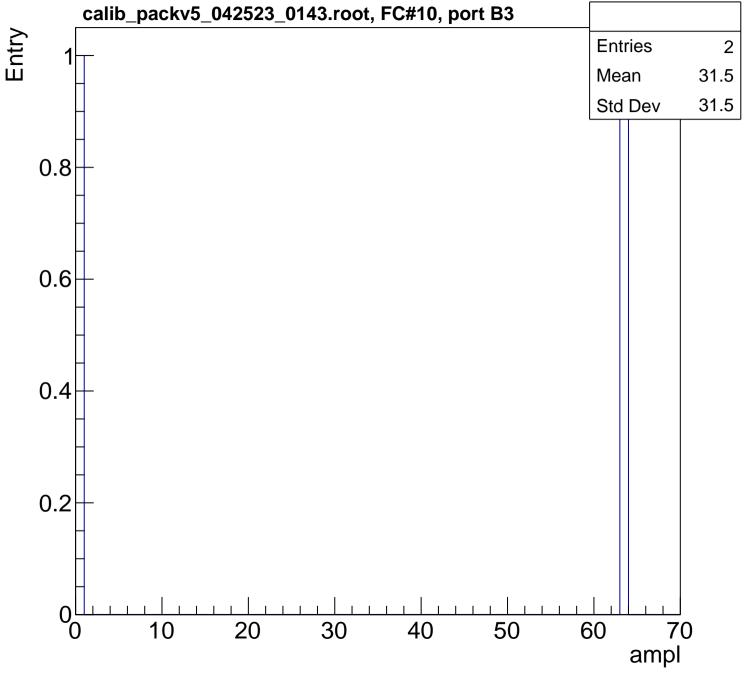


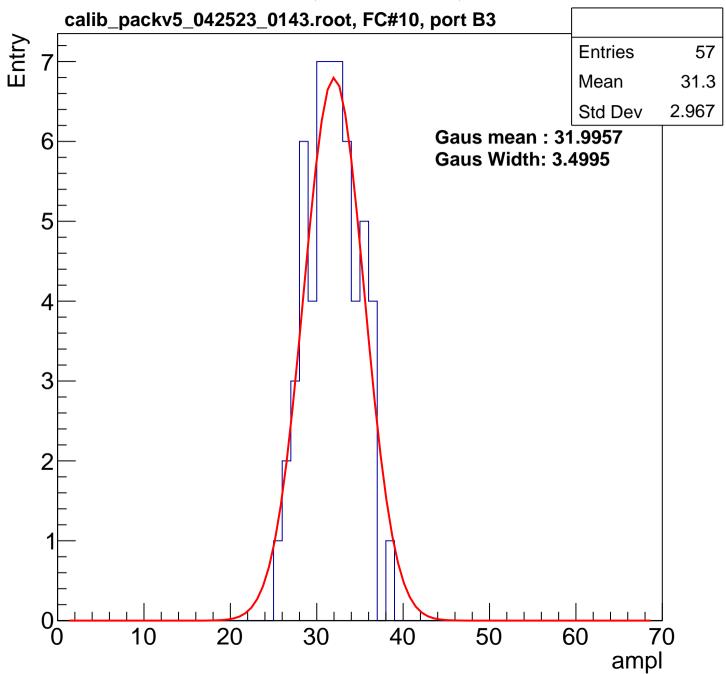


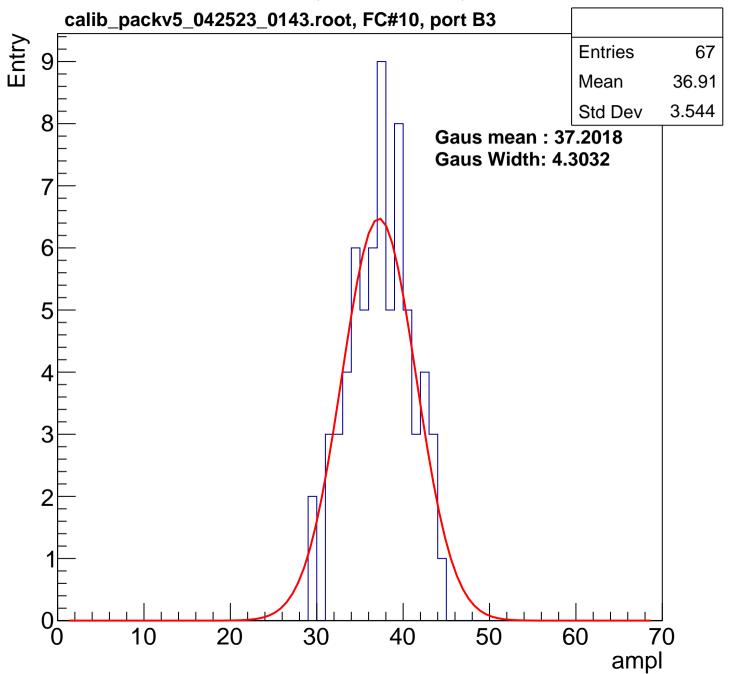


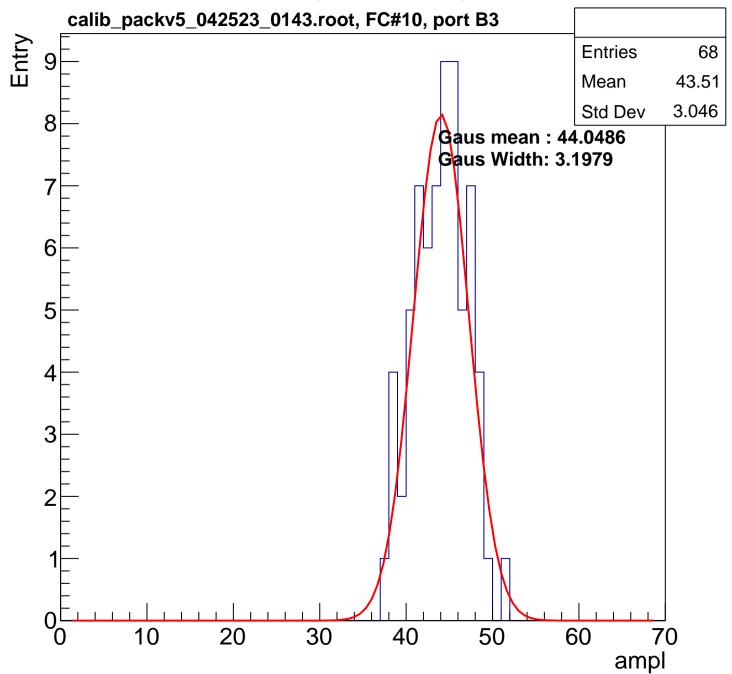


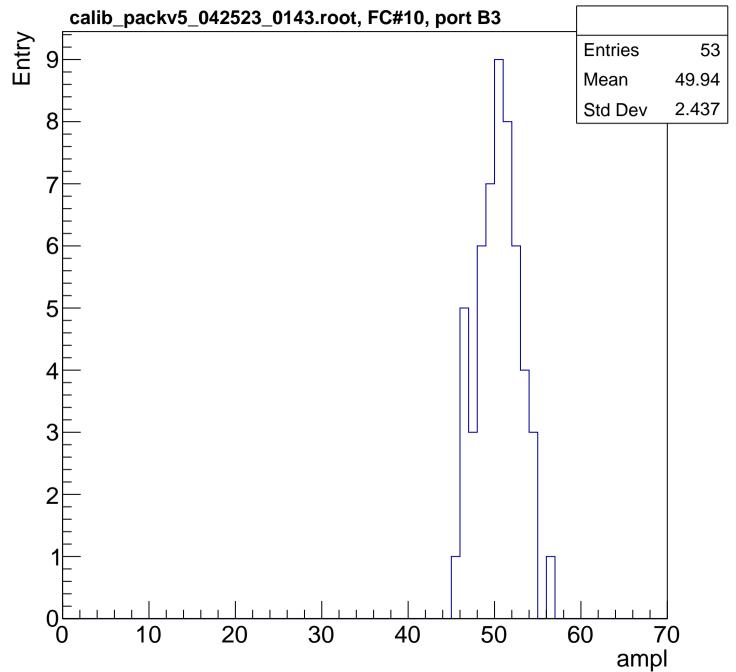


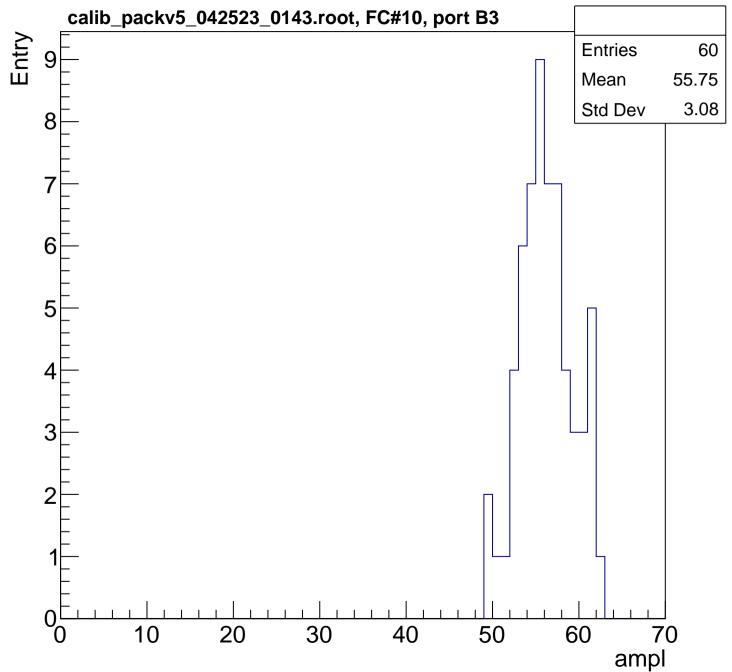


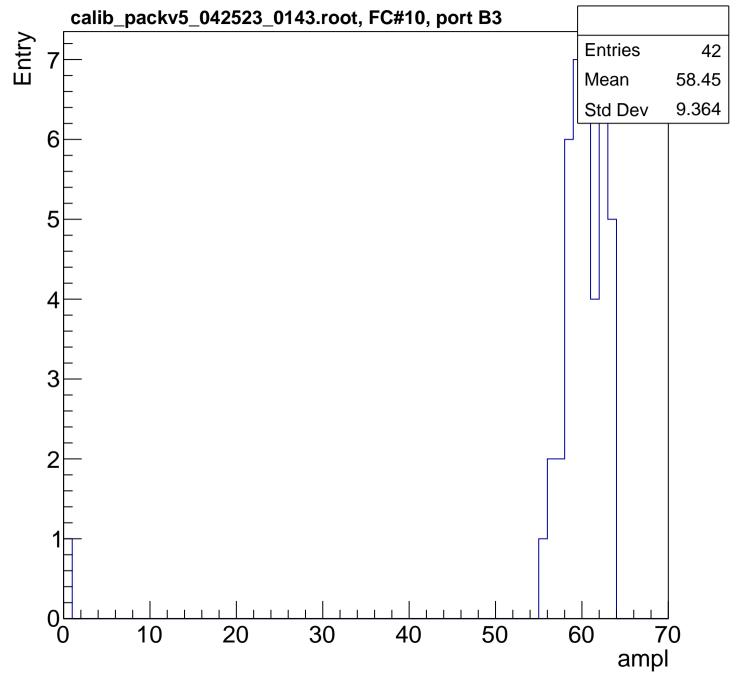


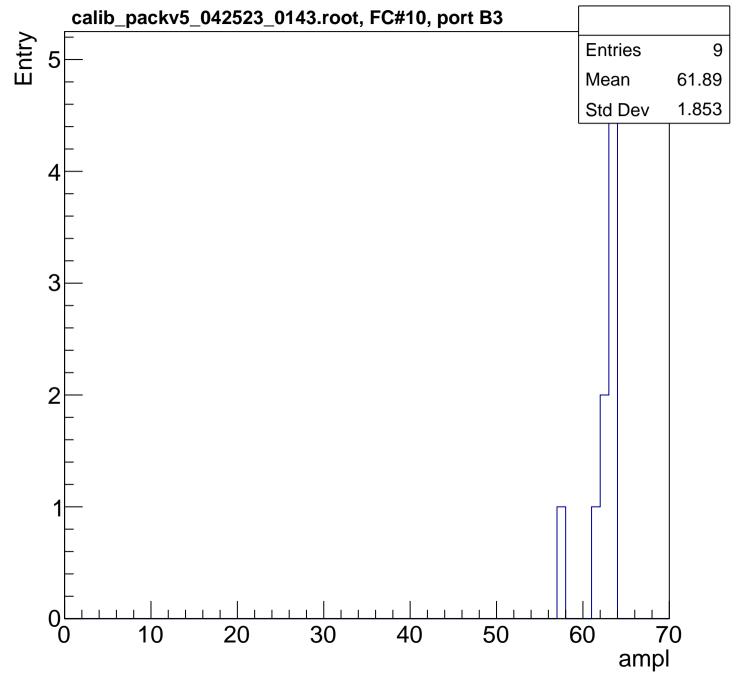












B1L002S, U9-ch53, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2

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40

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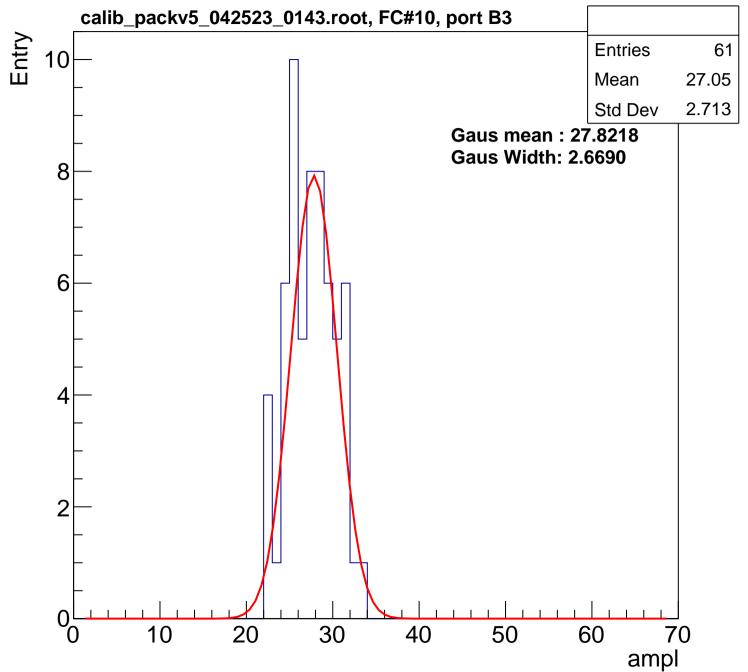
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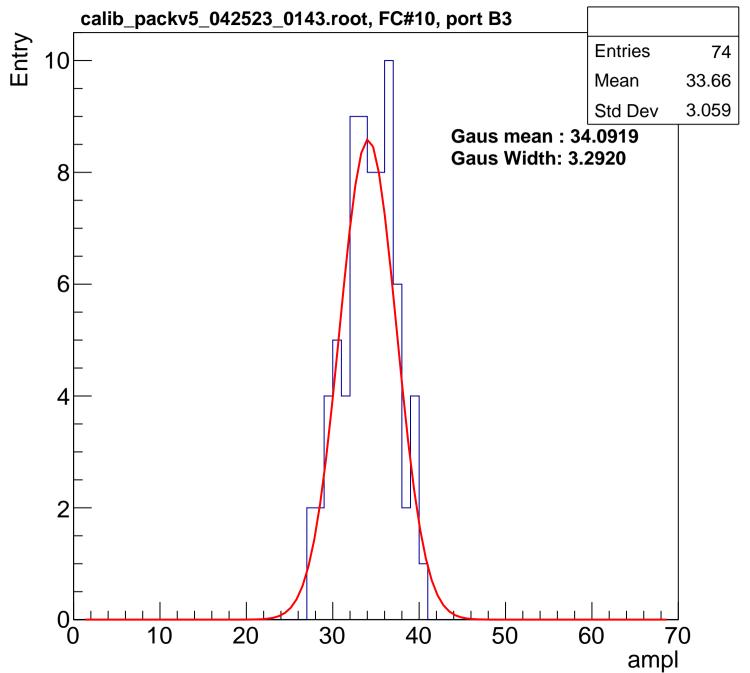
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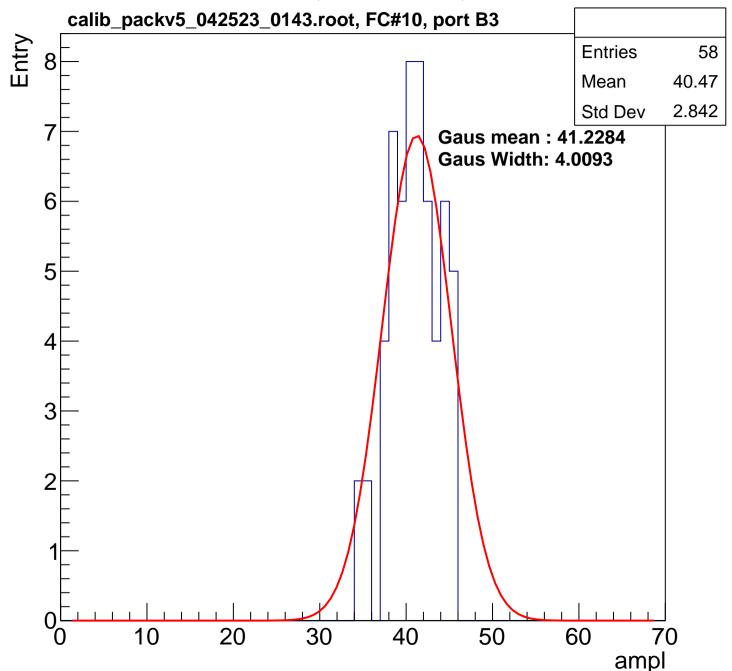
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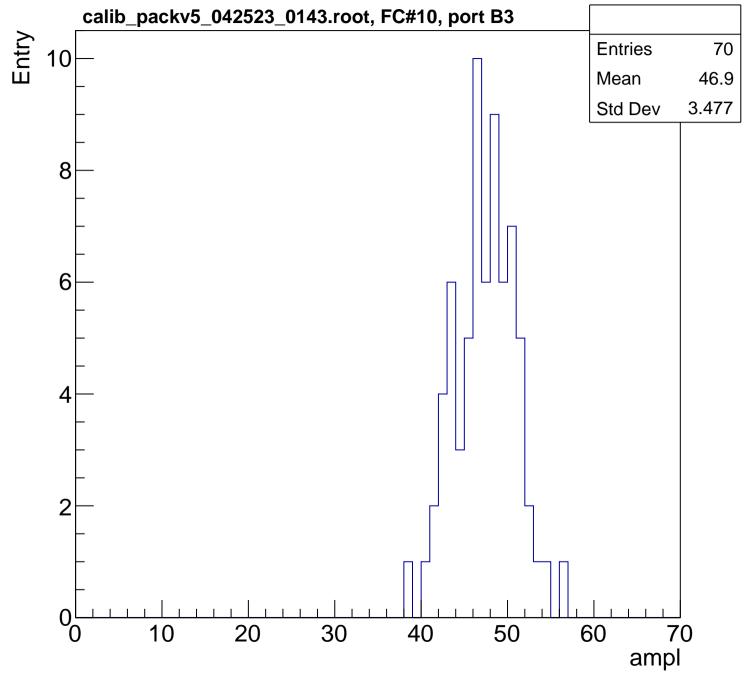
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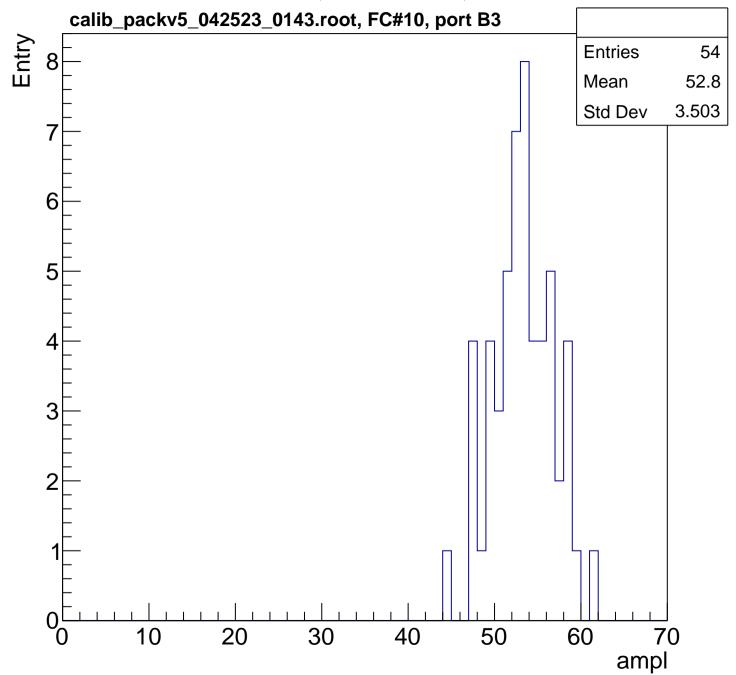
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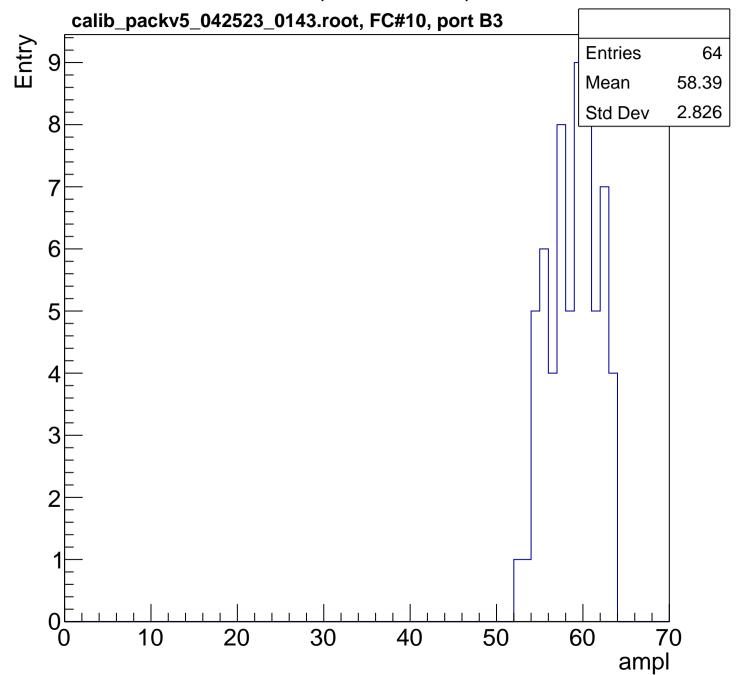


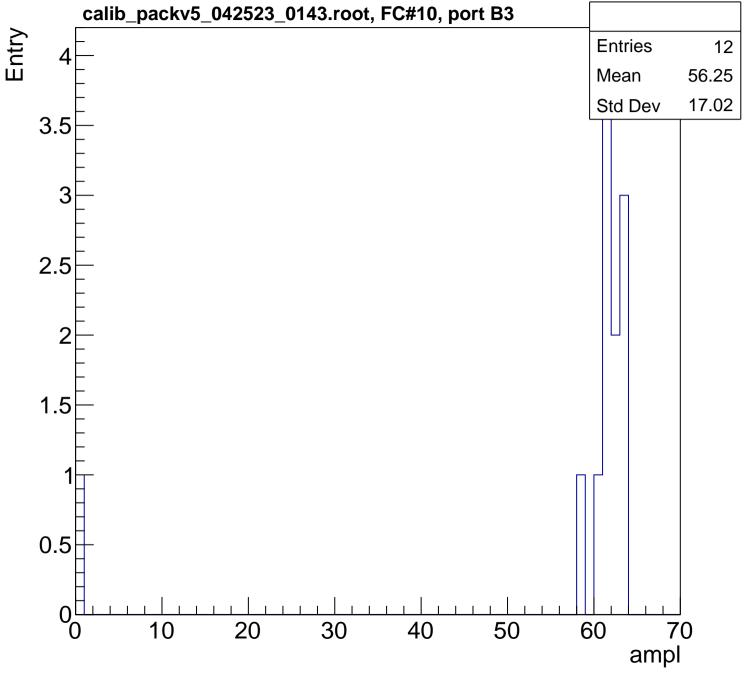


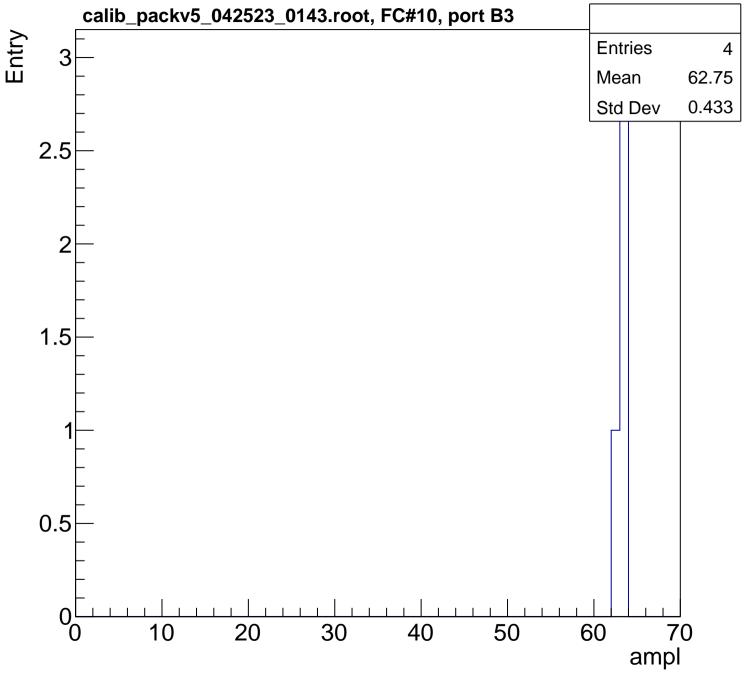


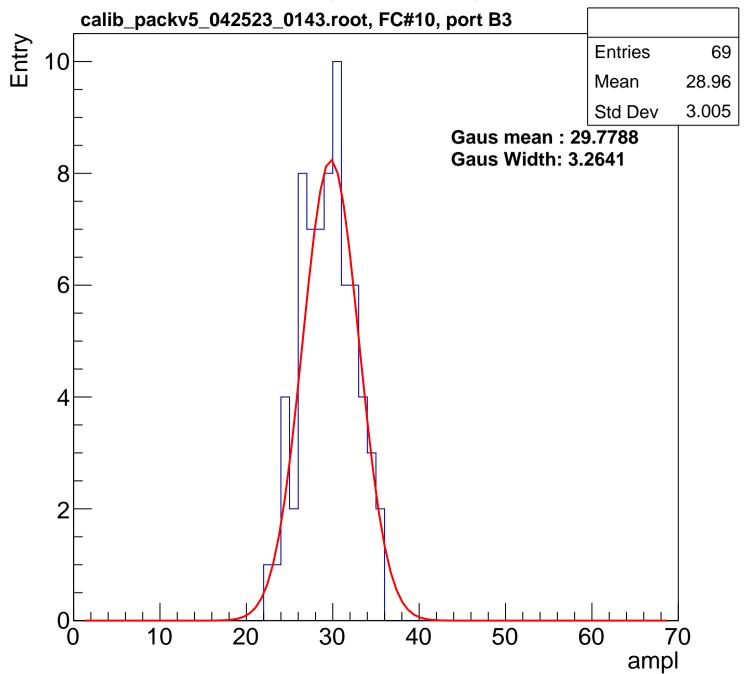


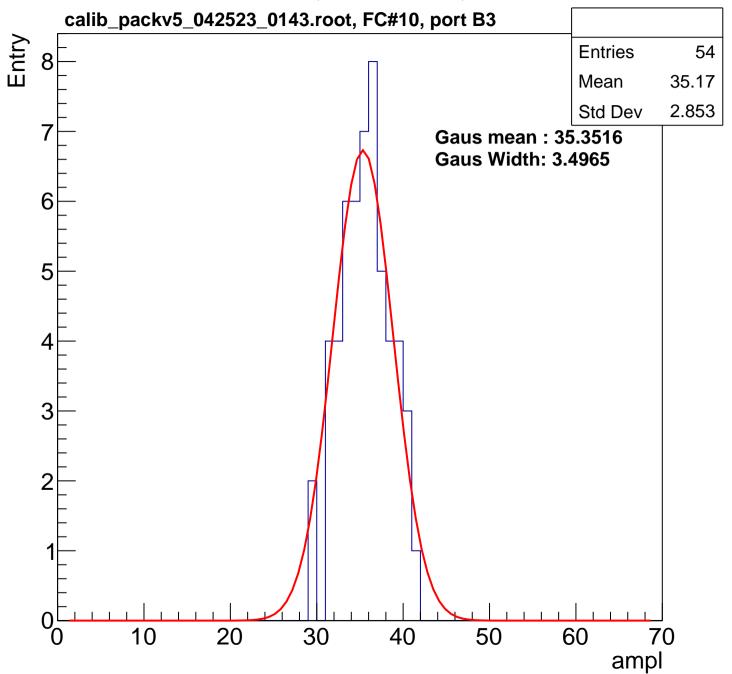


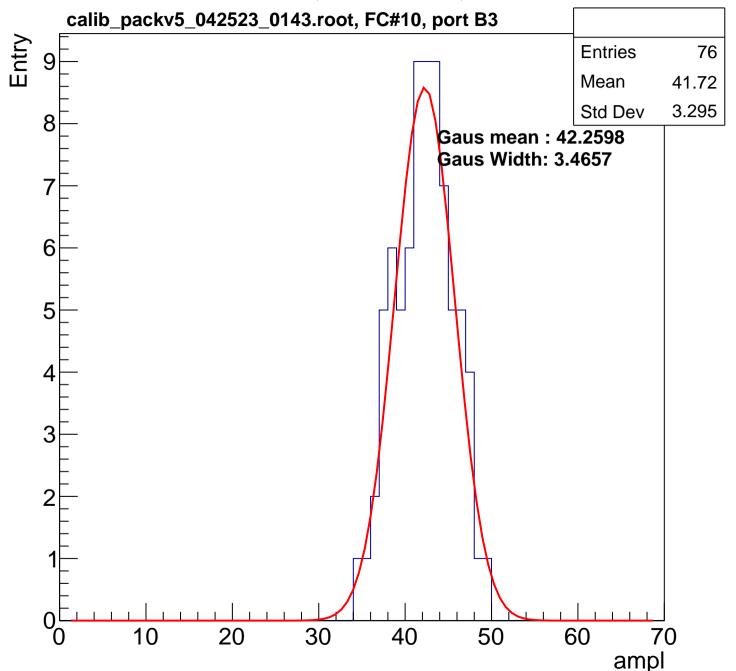


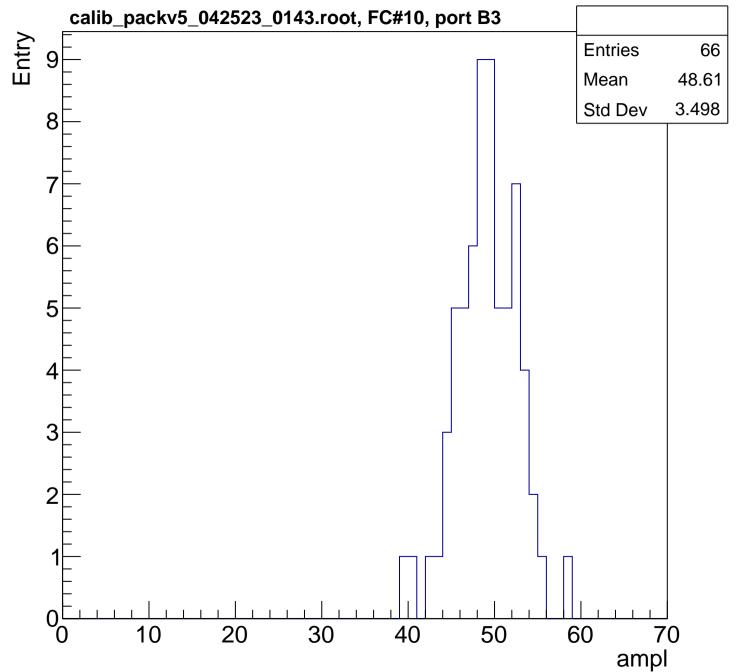


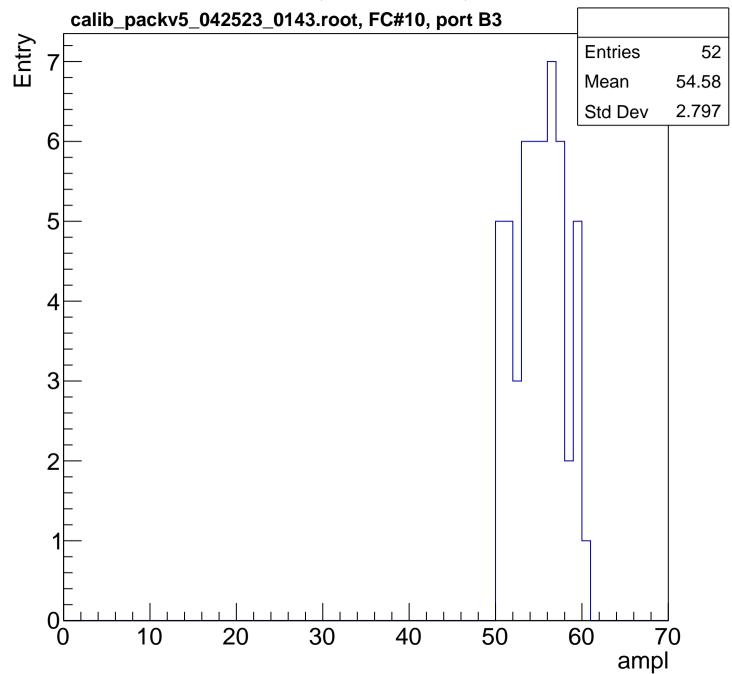


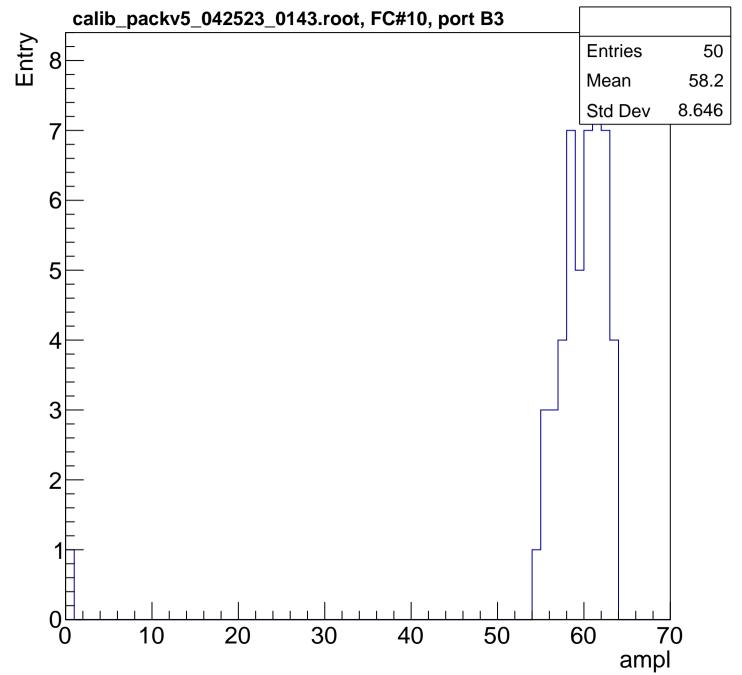


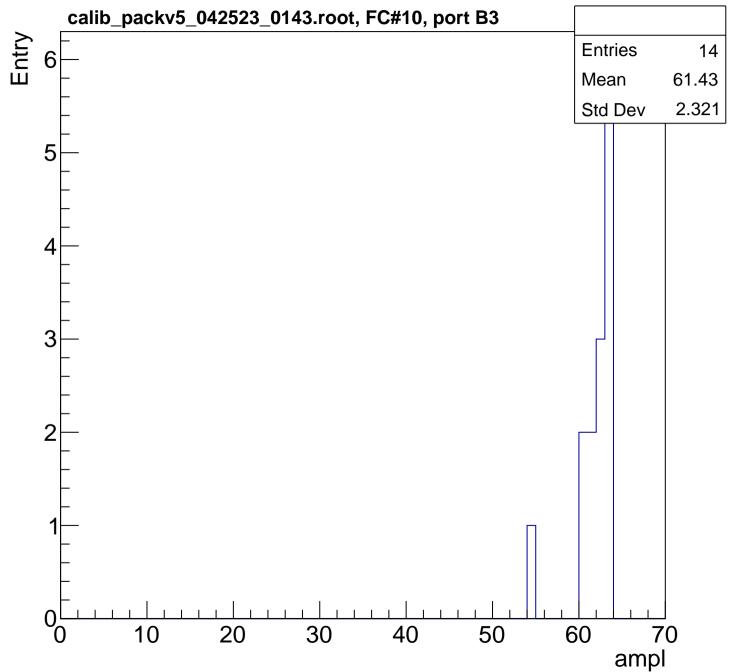


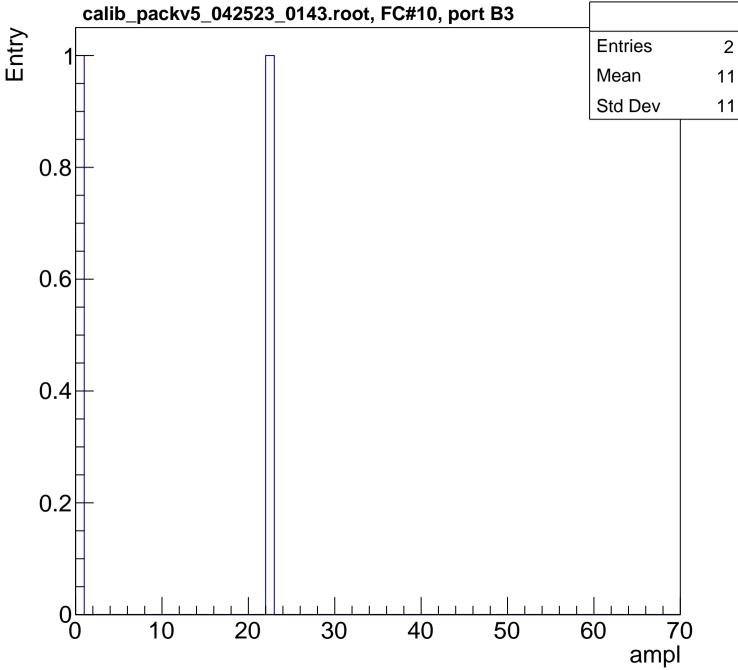


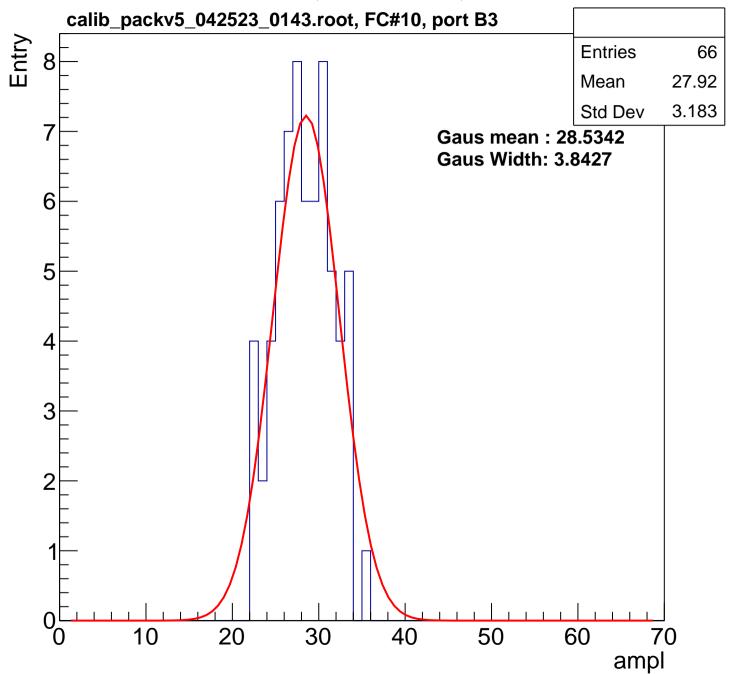


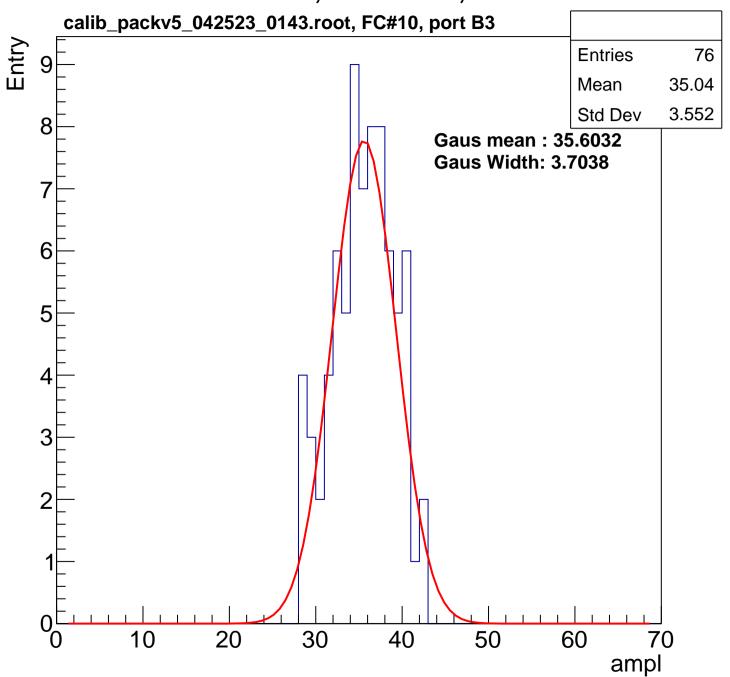


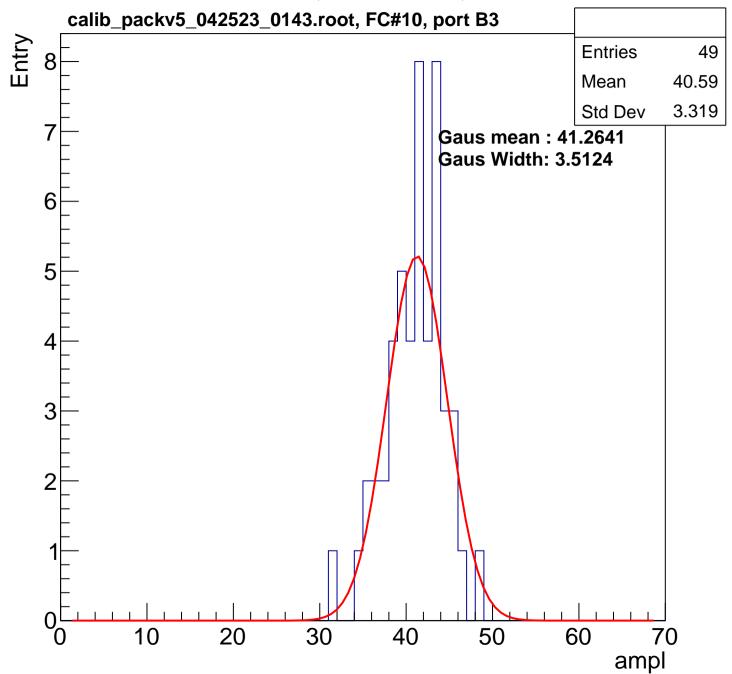


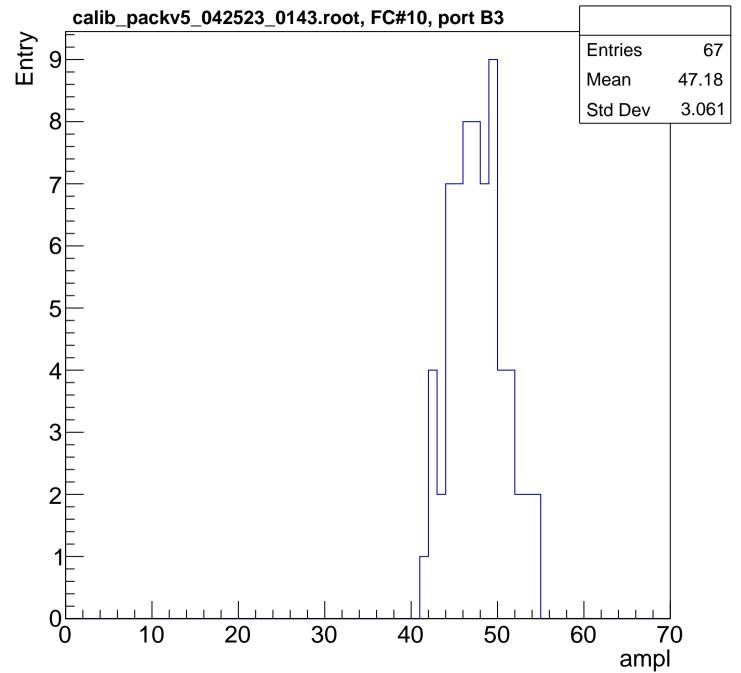


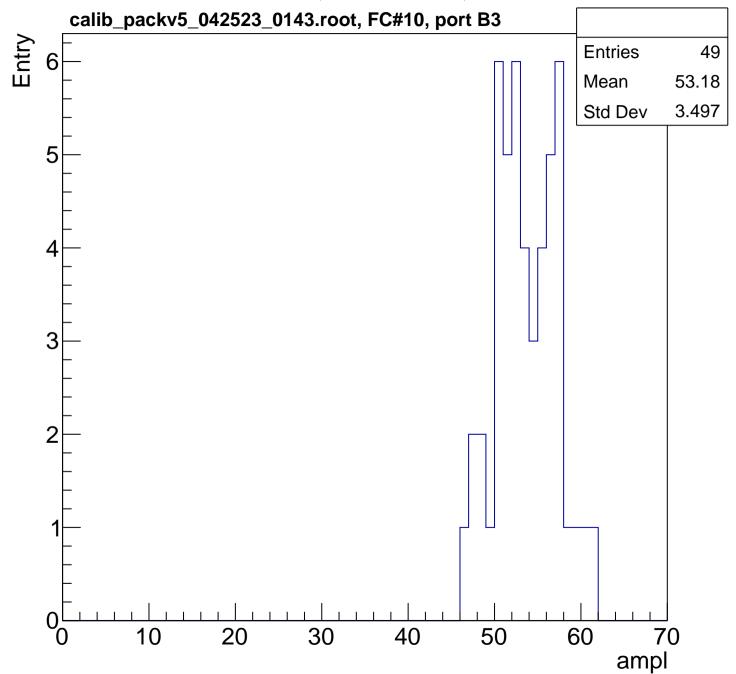


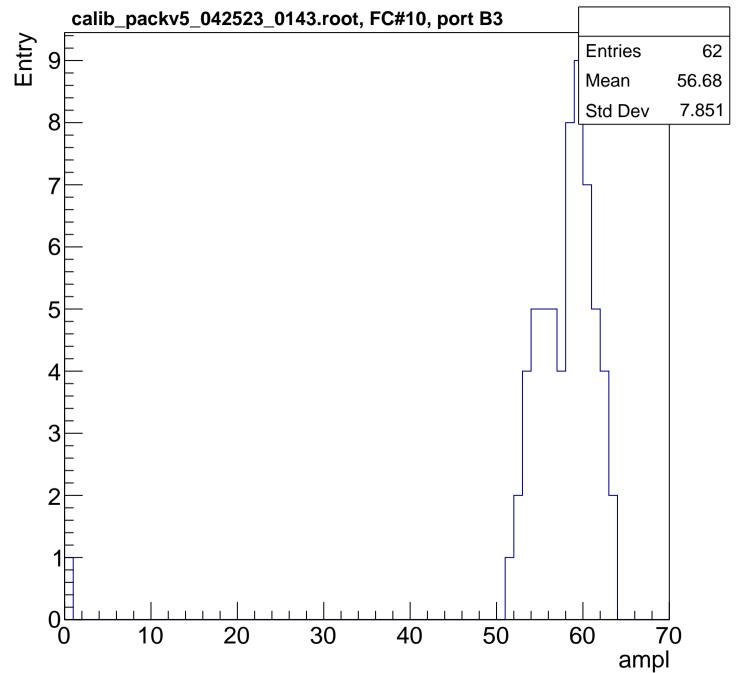


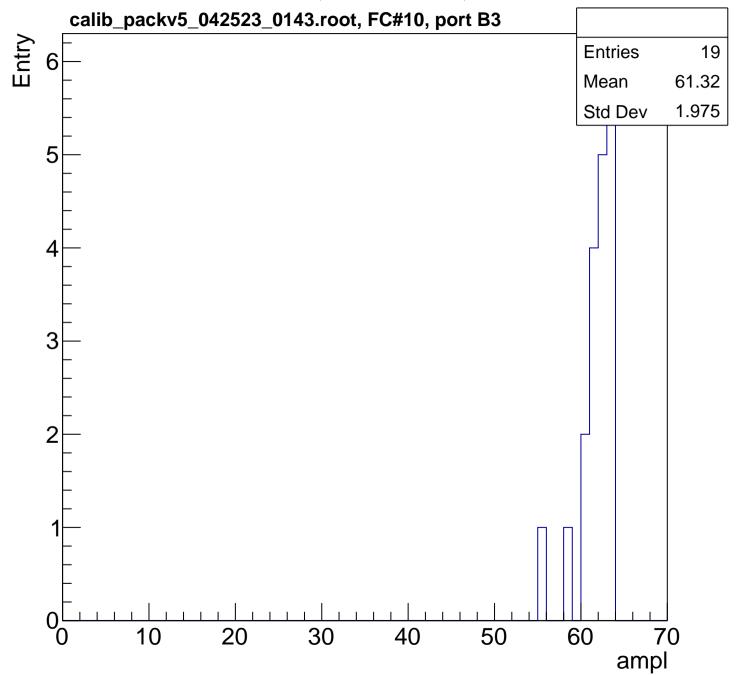


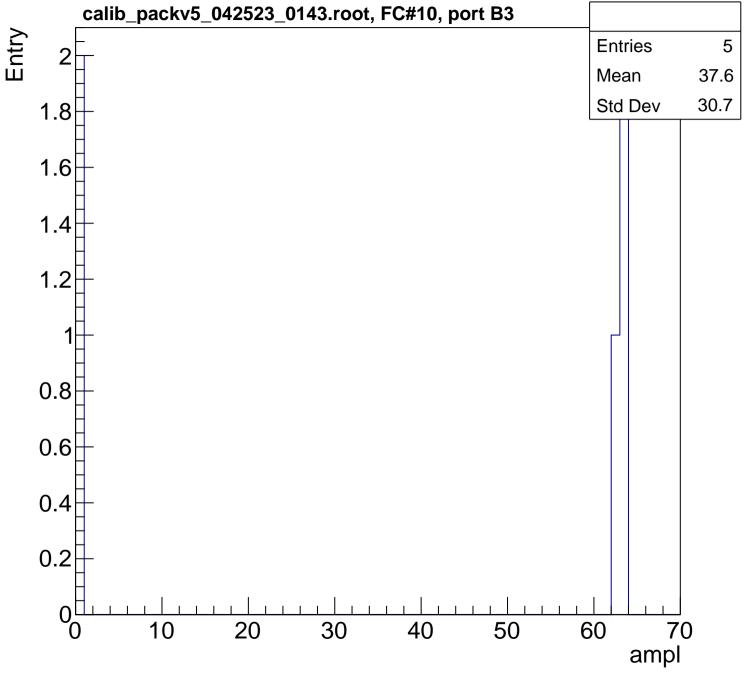


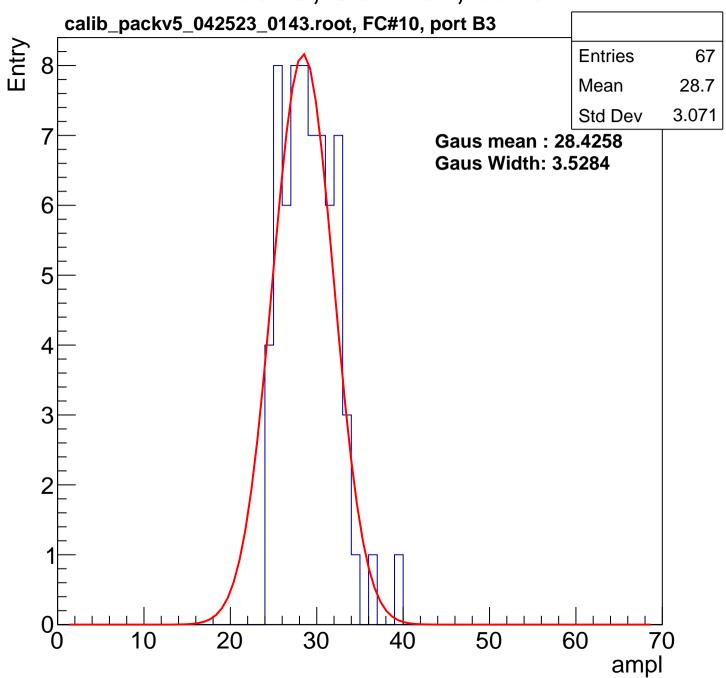


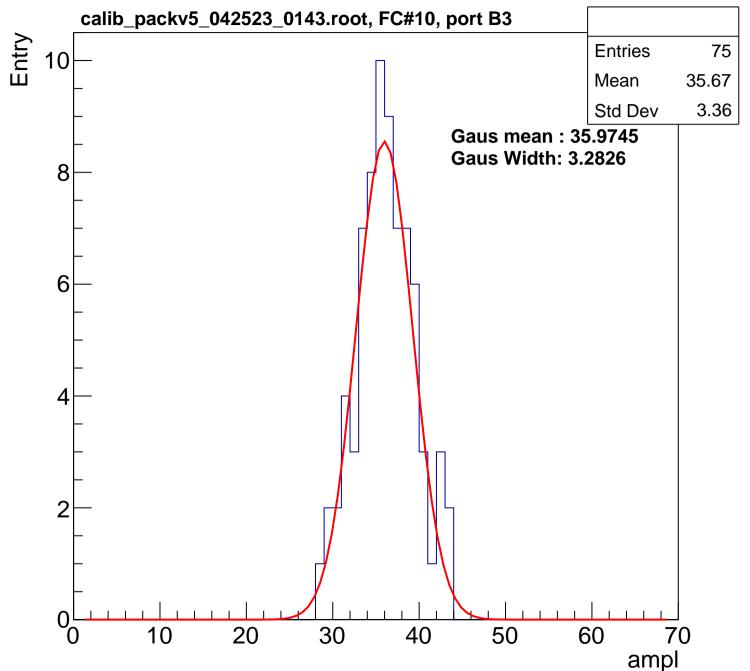


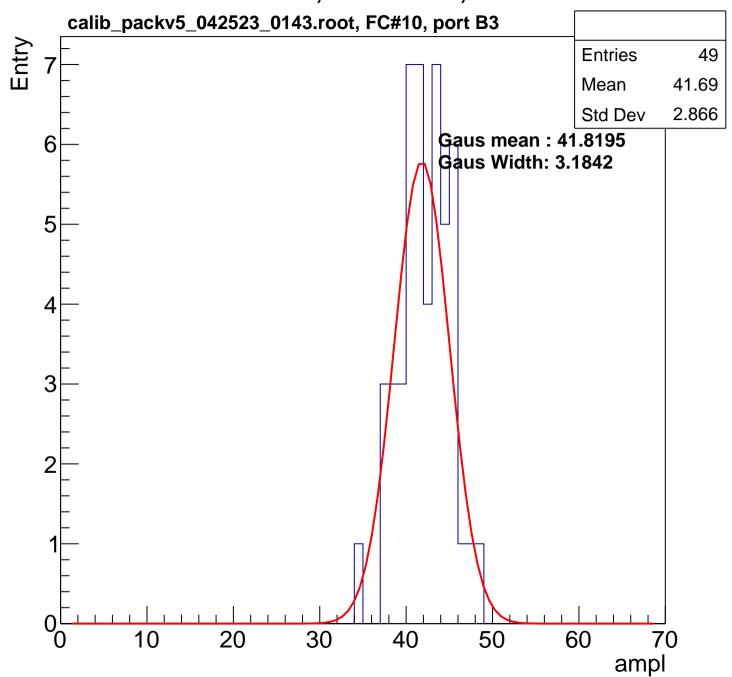


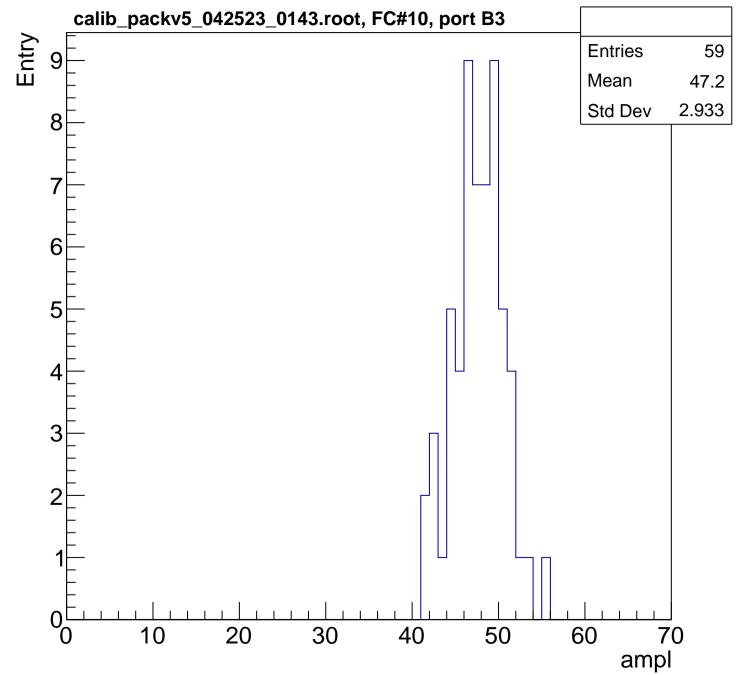


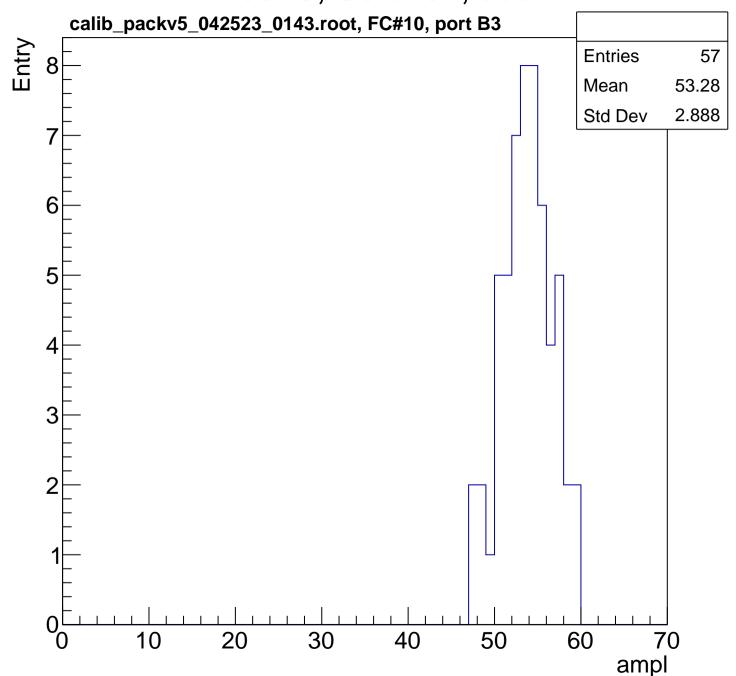


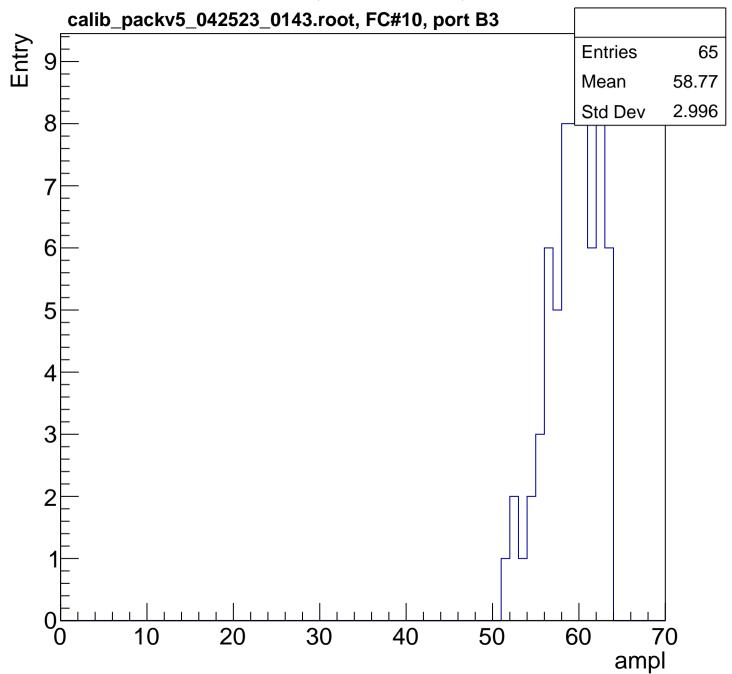


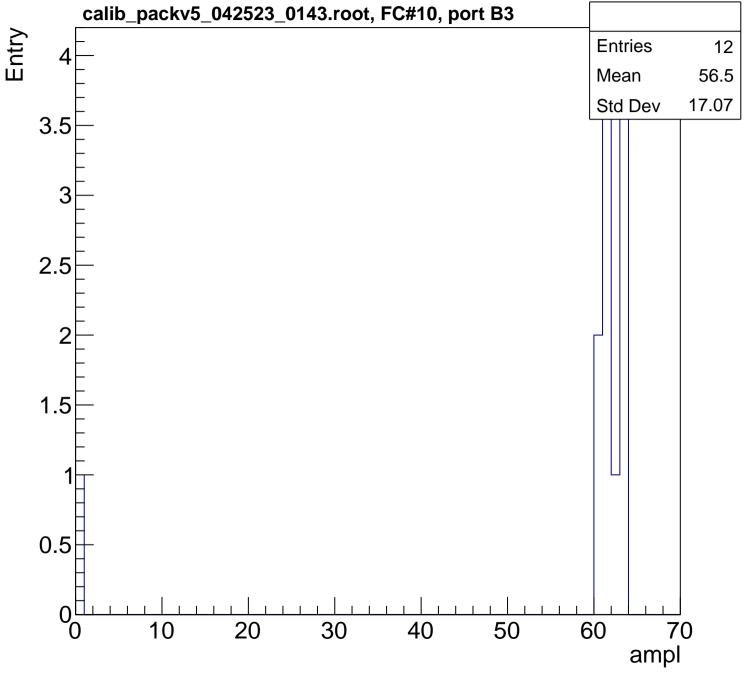




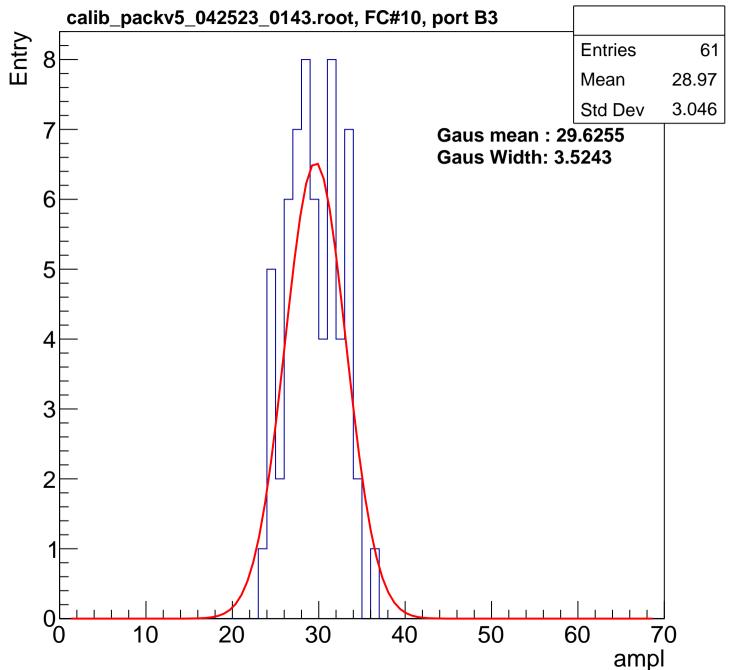


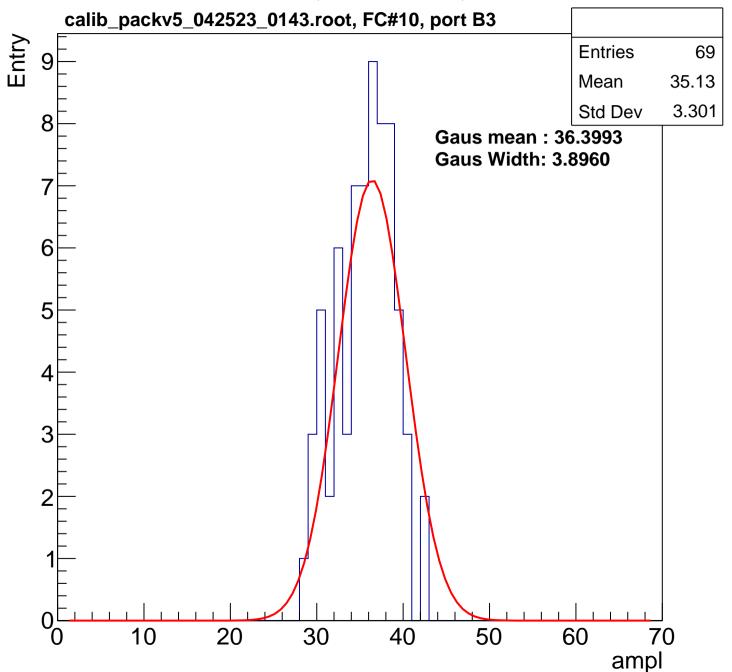


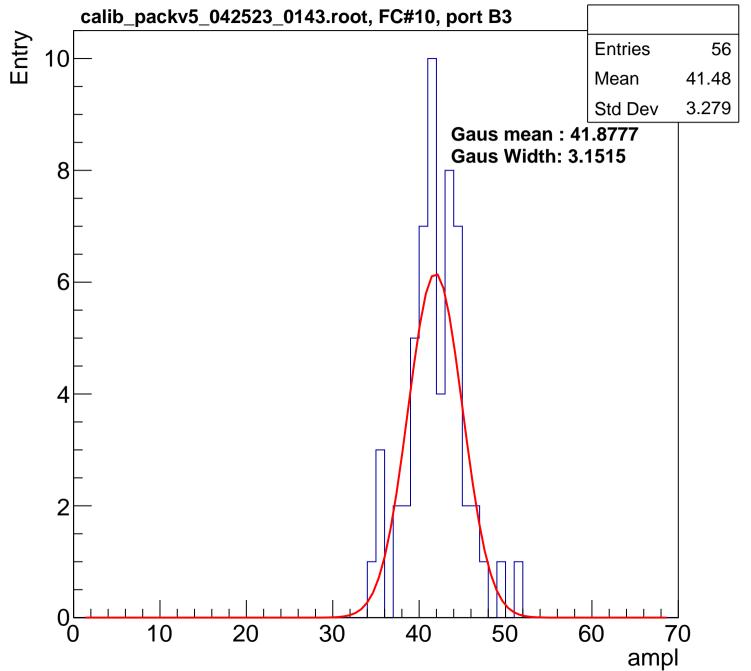


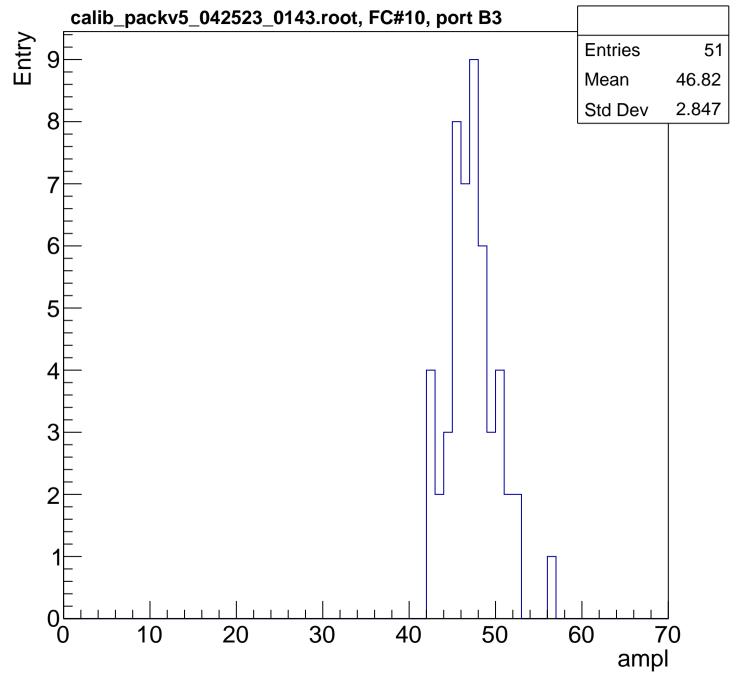


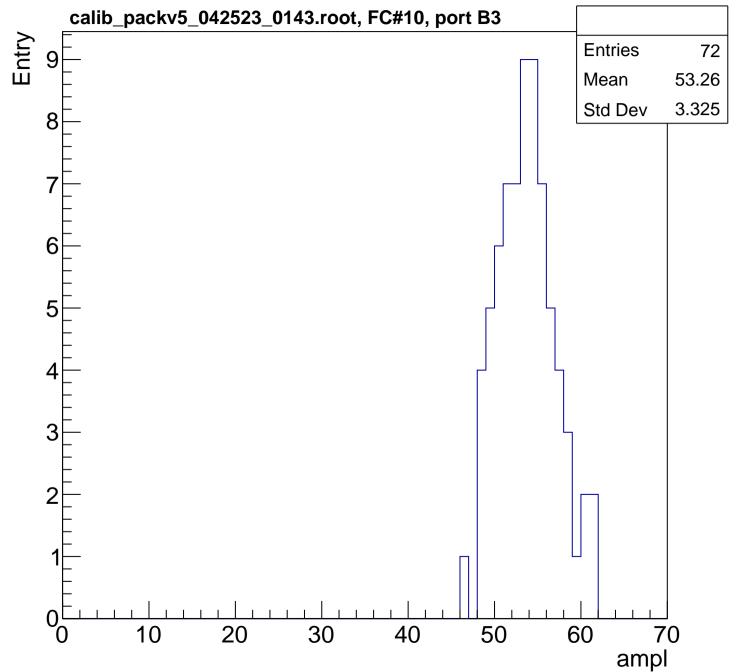
B1L002S, U9-ch57, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

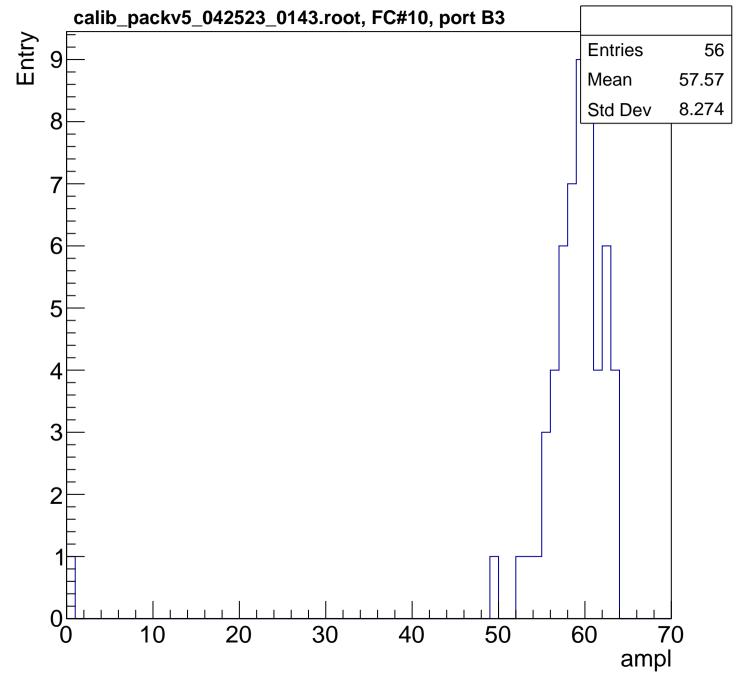


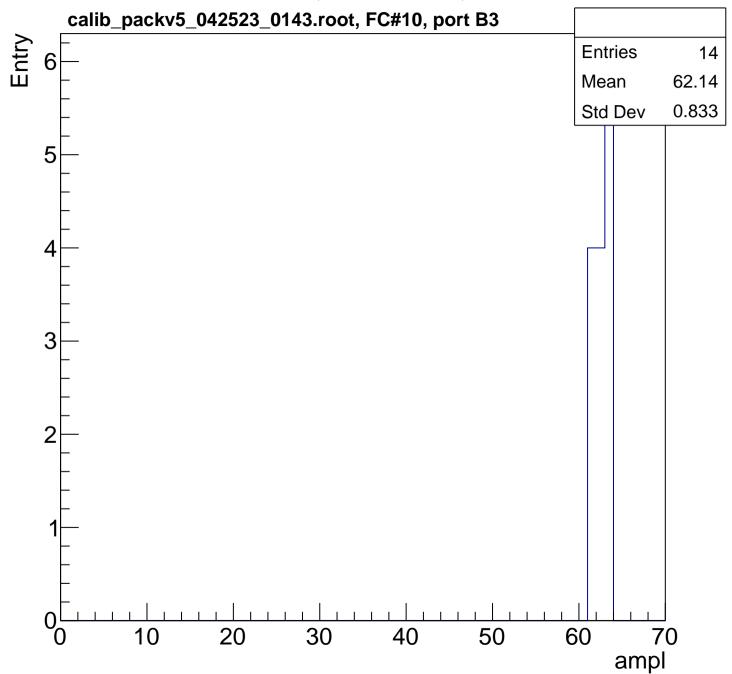




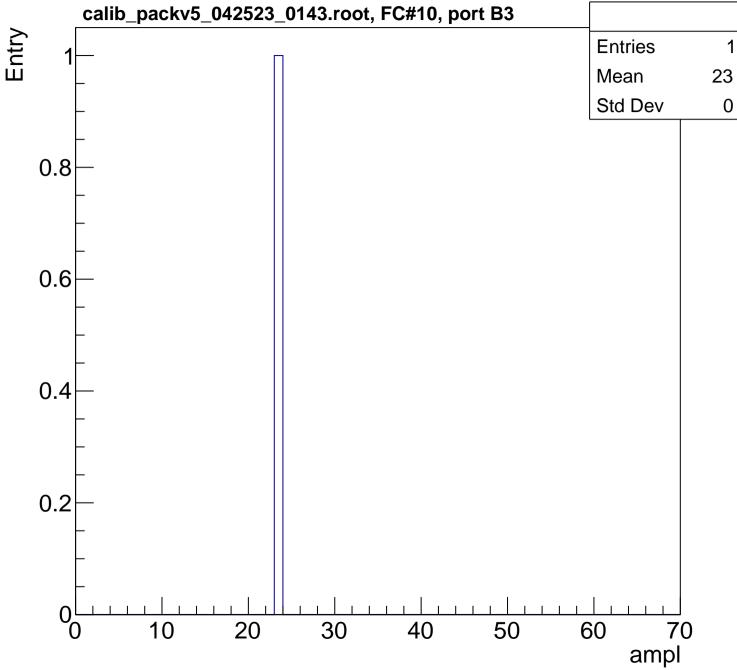


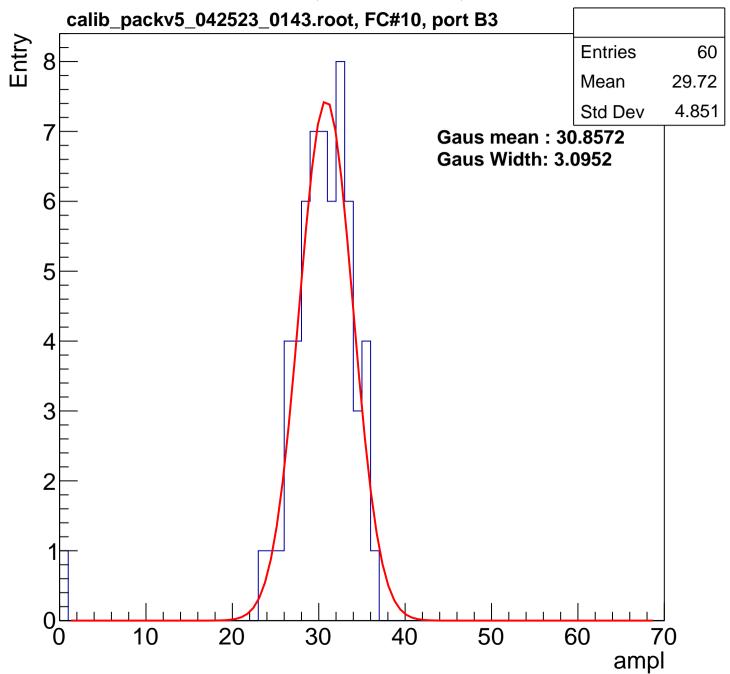


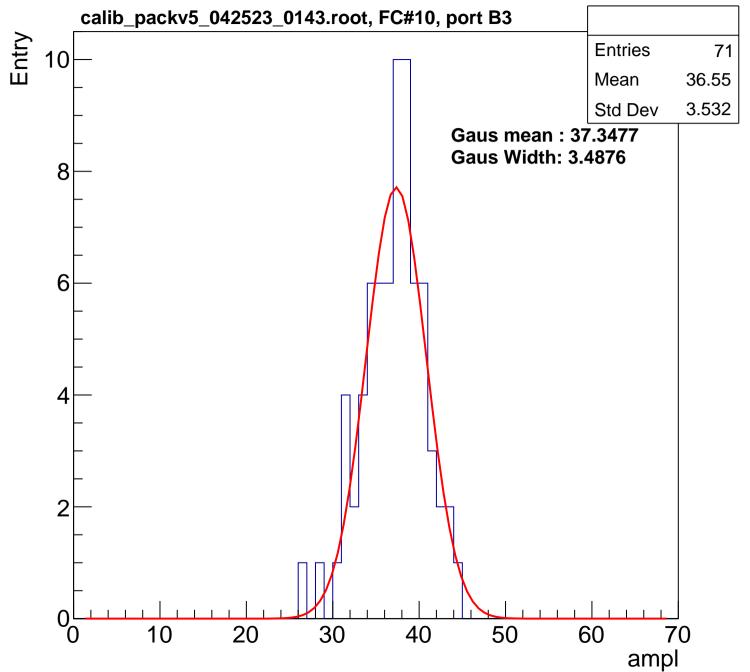


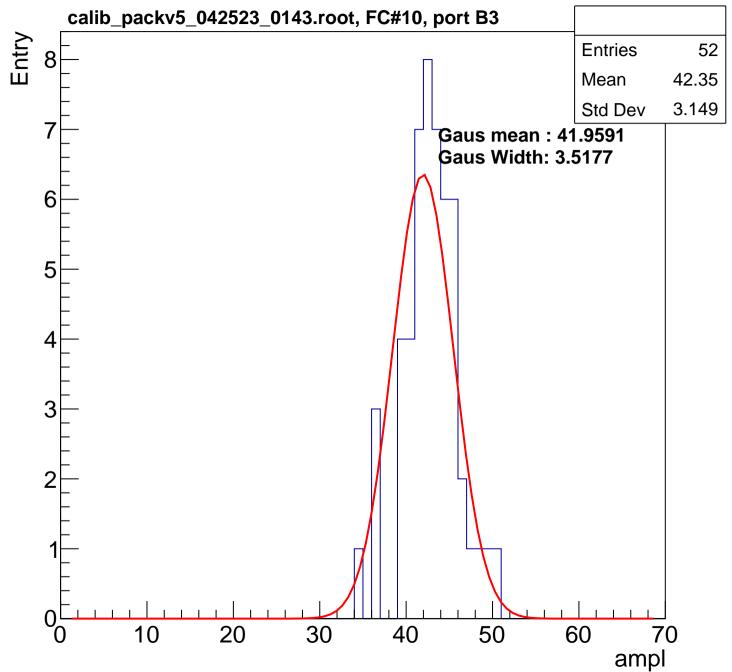


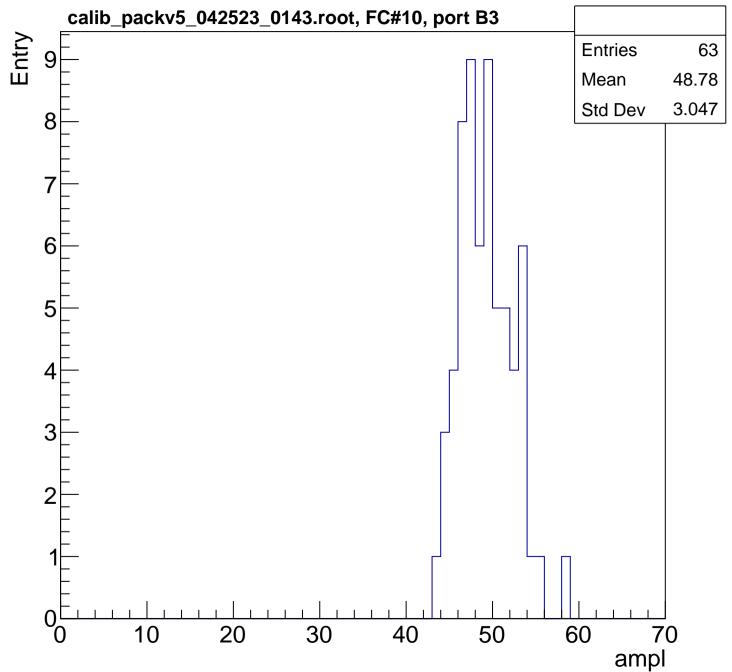
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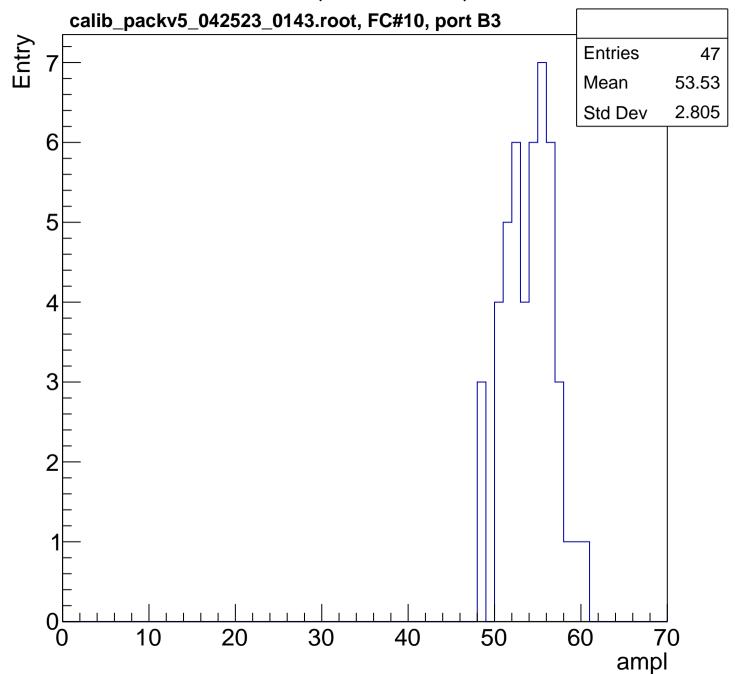


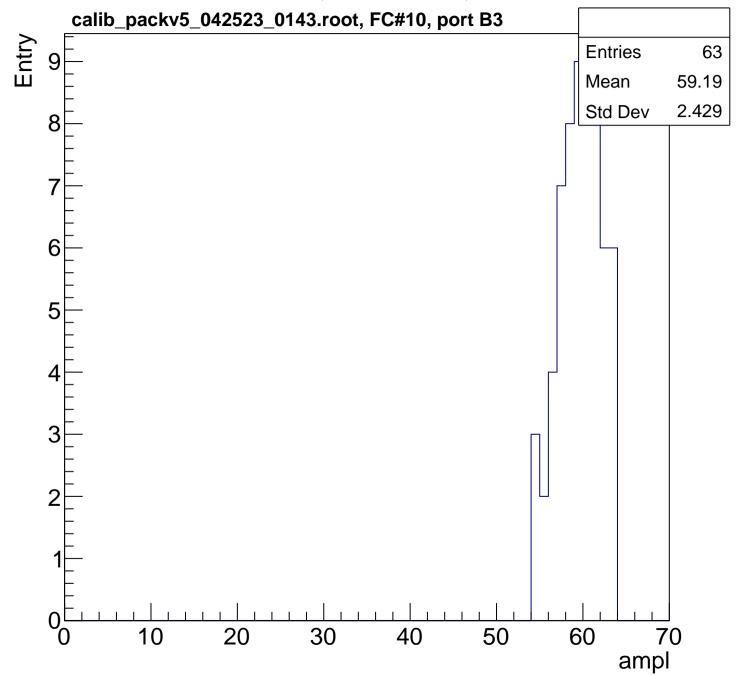


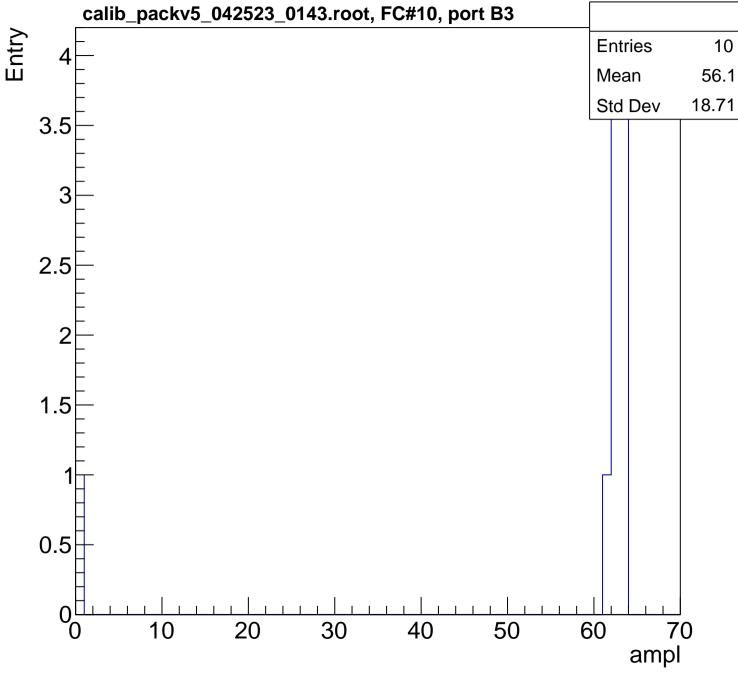




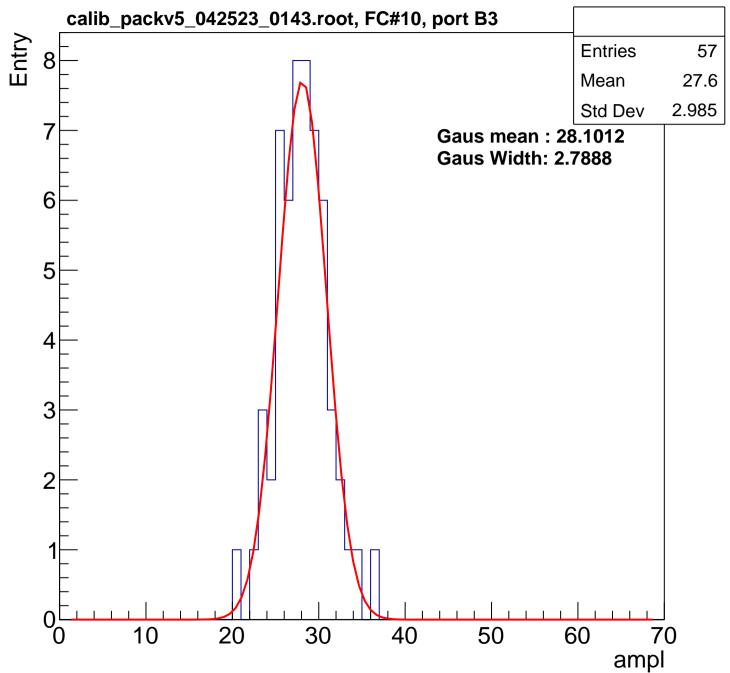


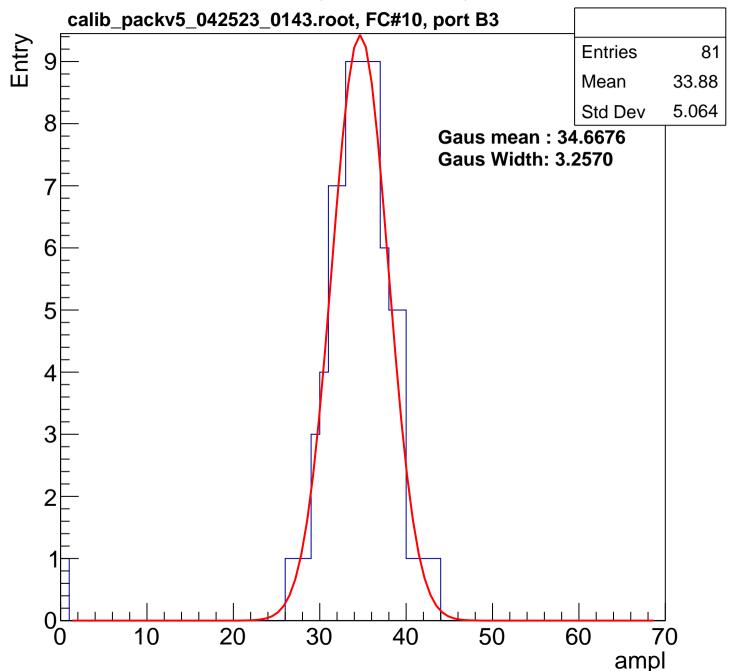


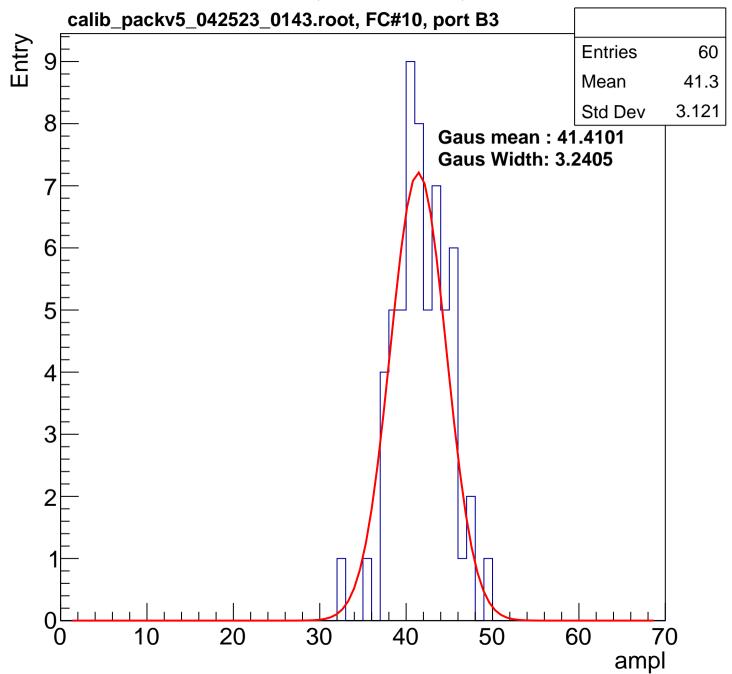


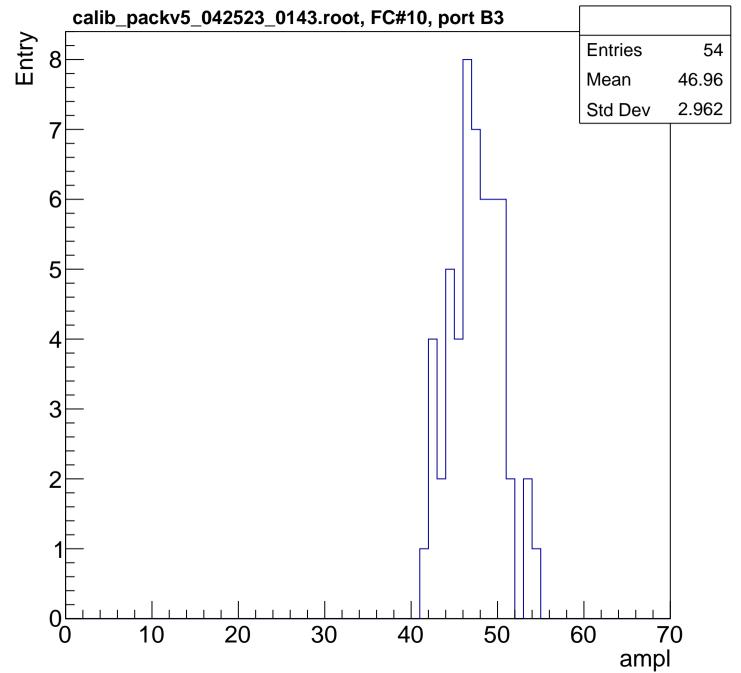


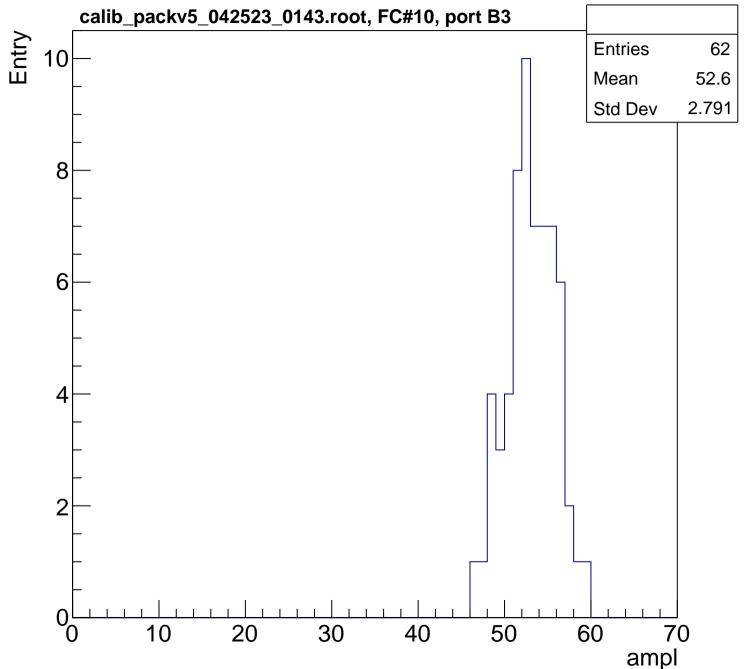


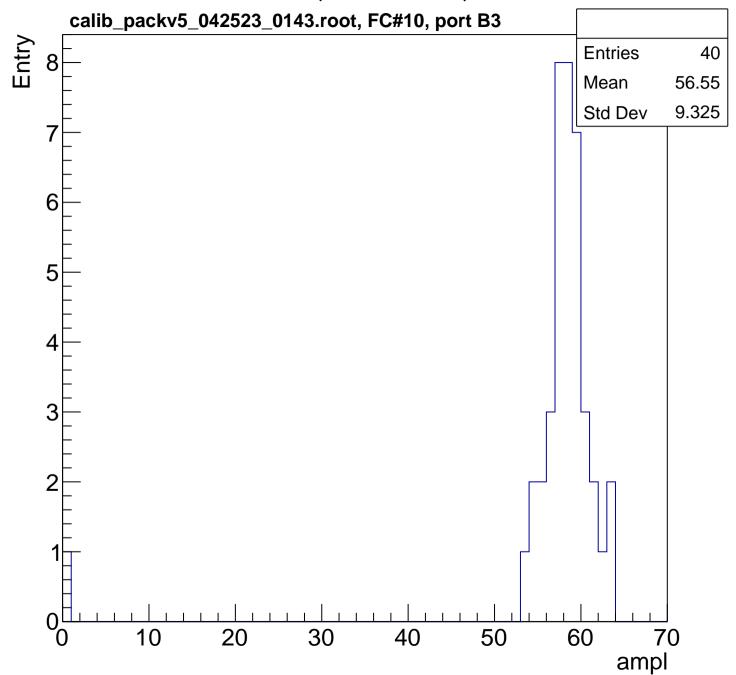


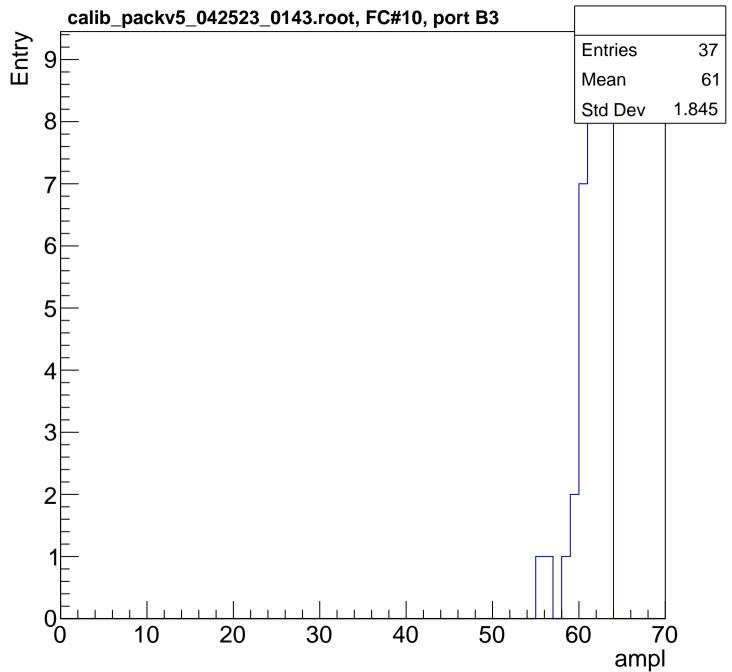












B1L002S, U9-ch60, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2

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40

50

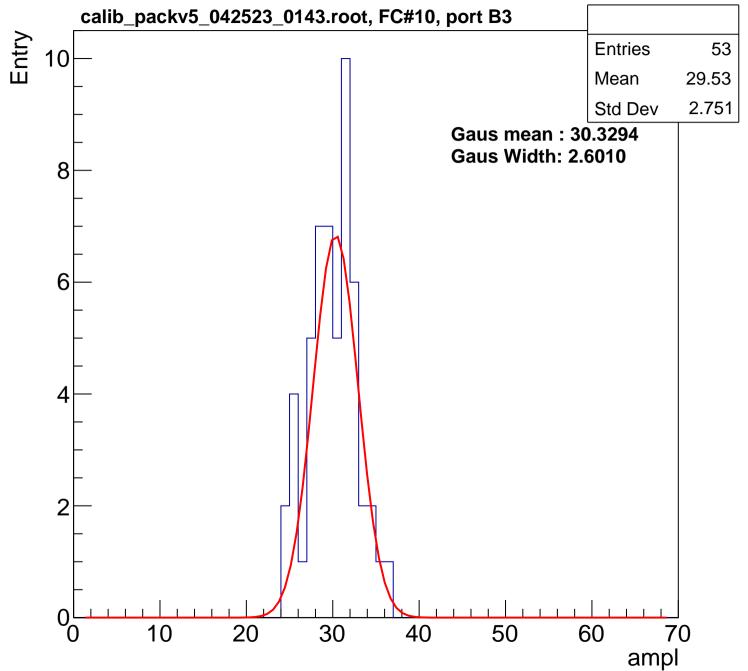
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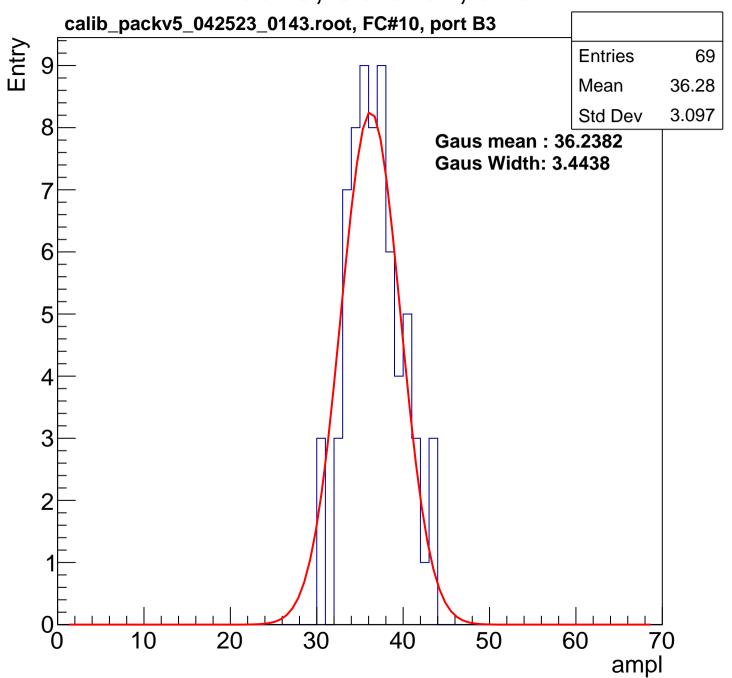
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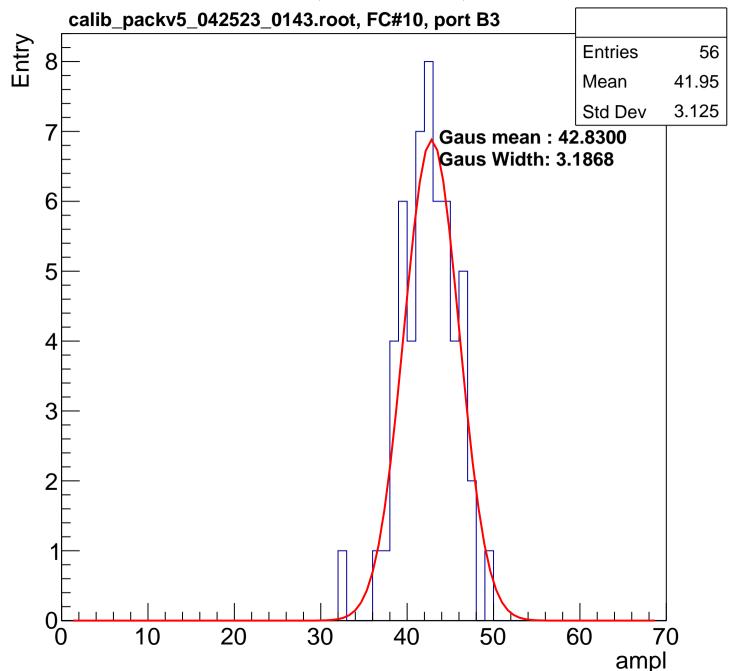
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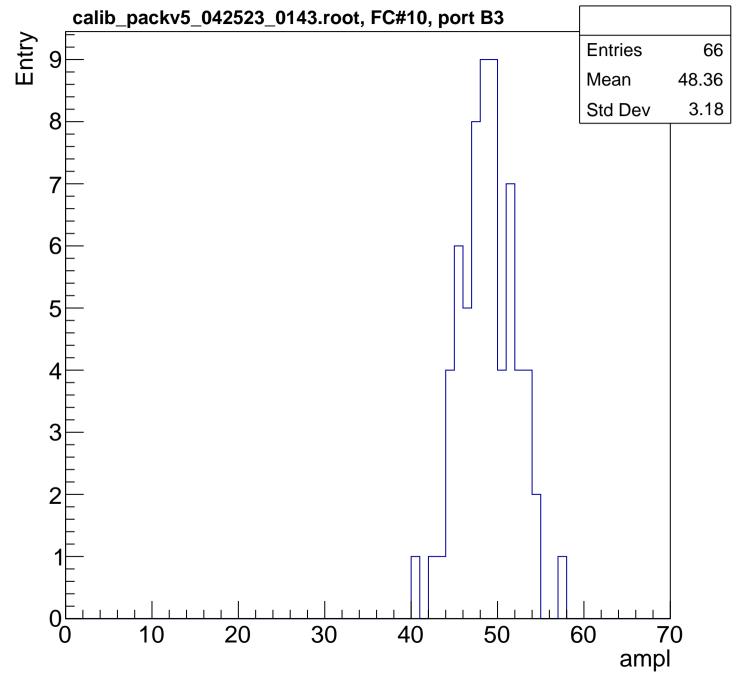
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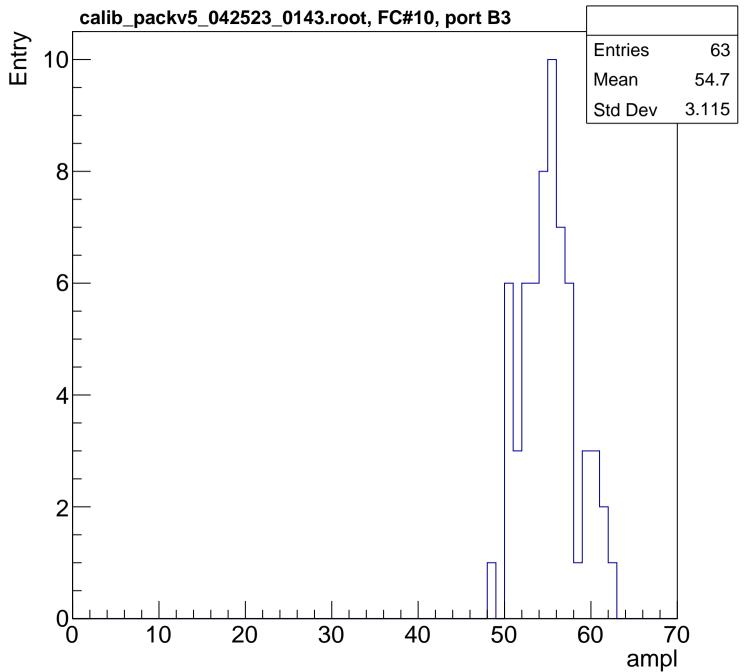
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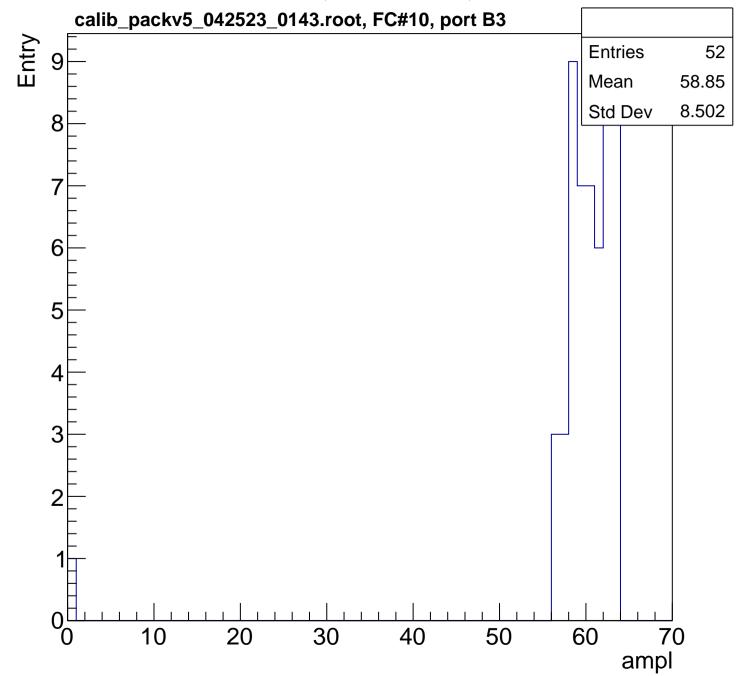


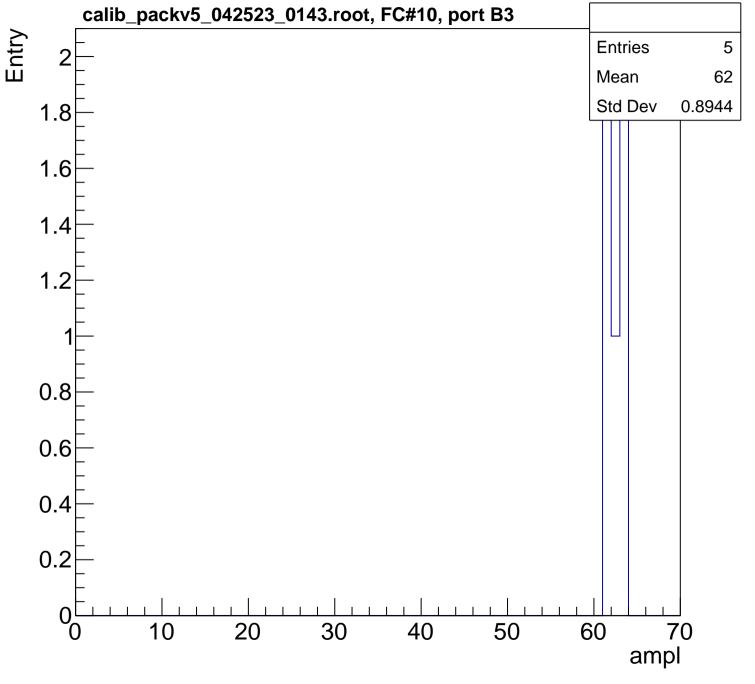


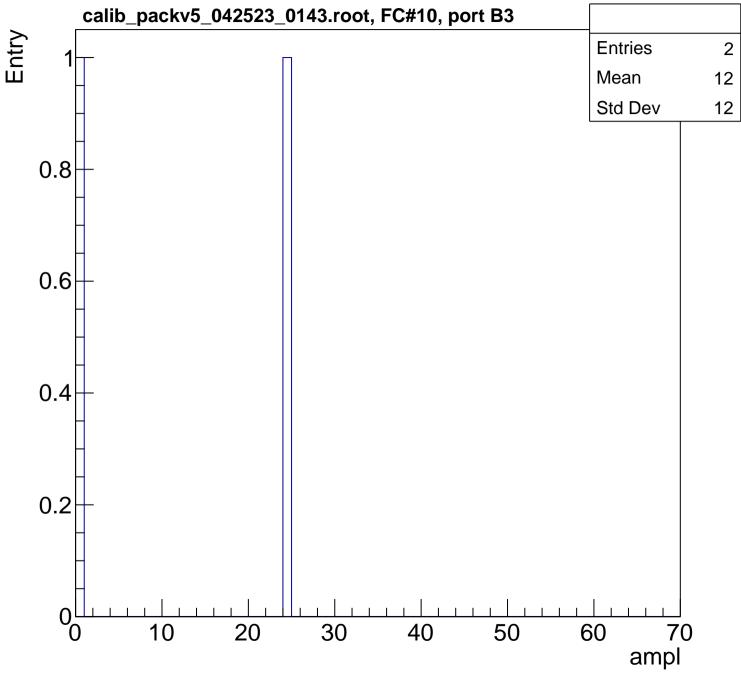


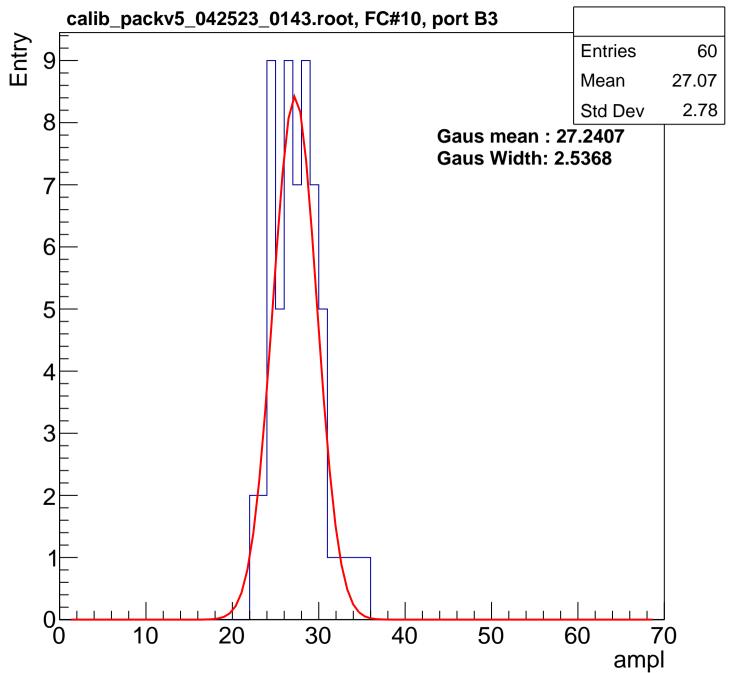


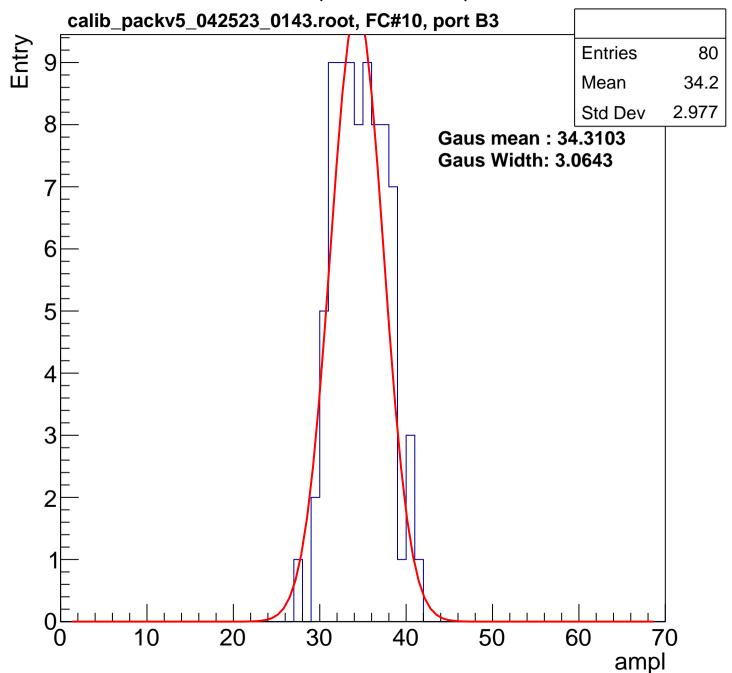


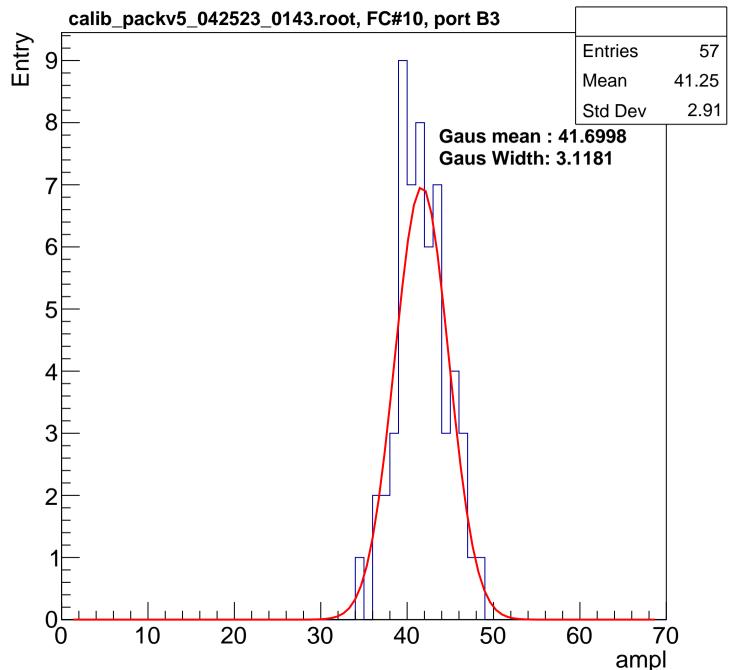


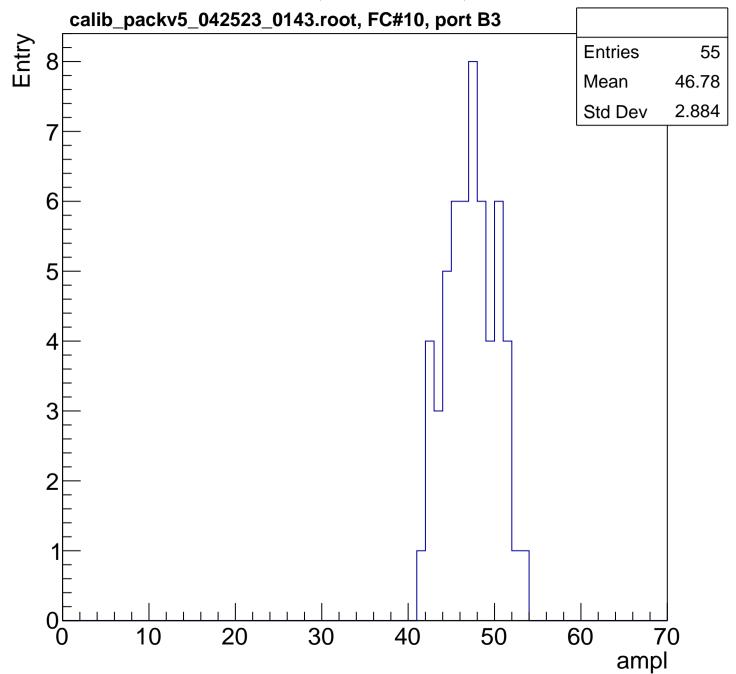


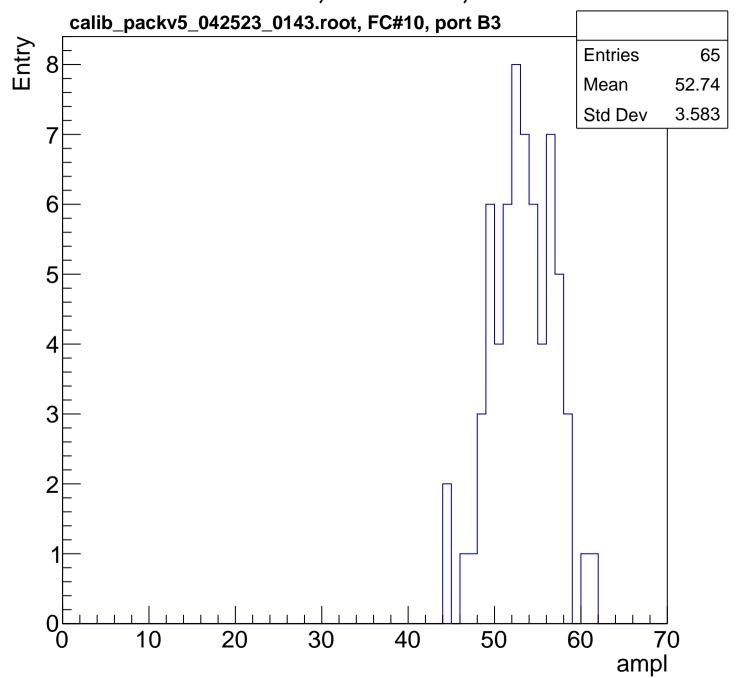


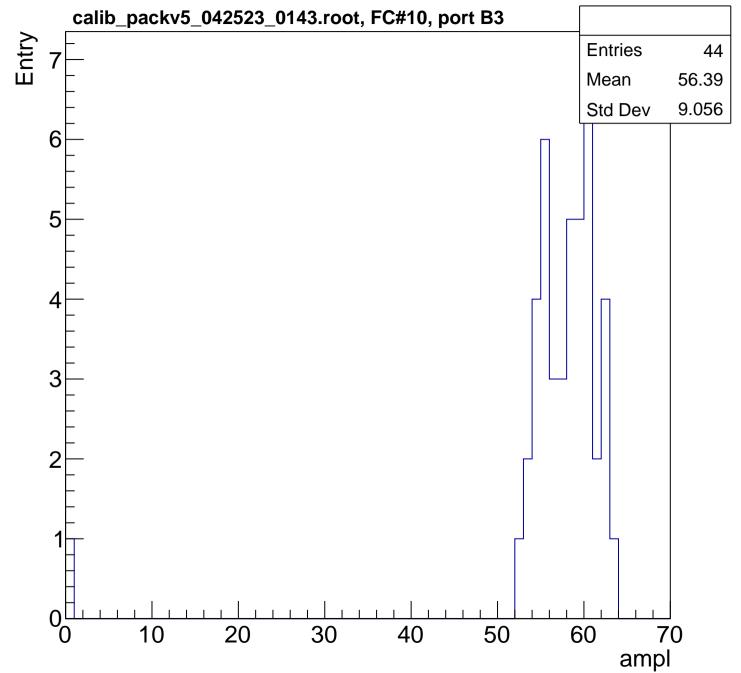


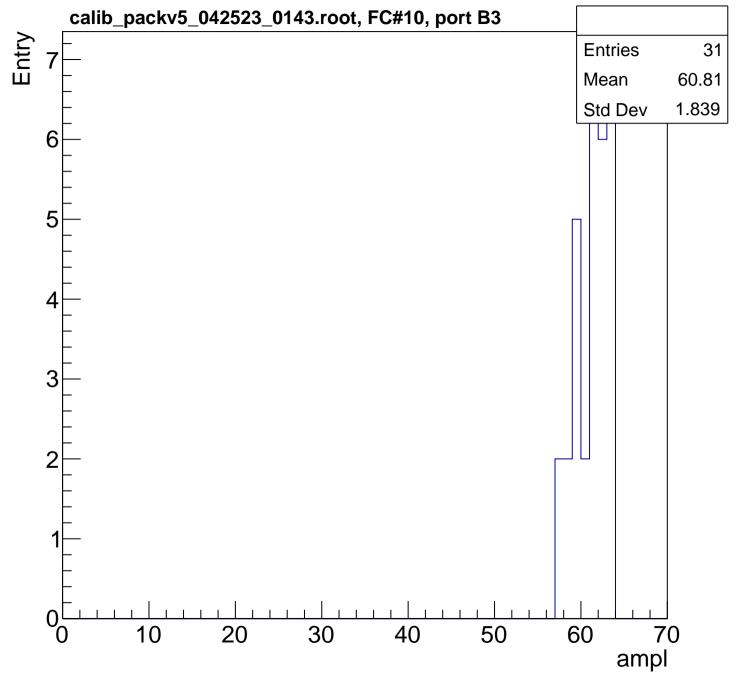


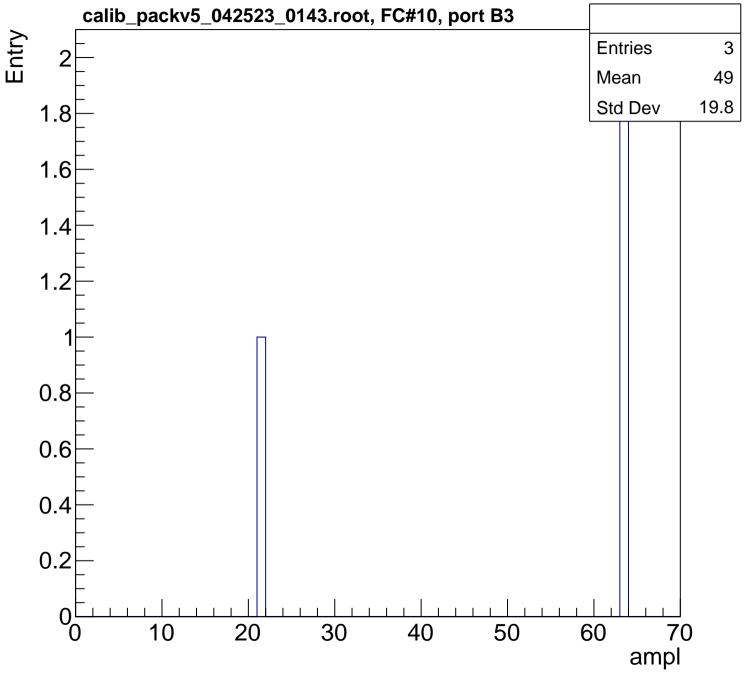


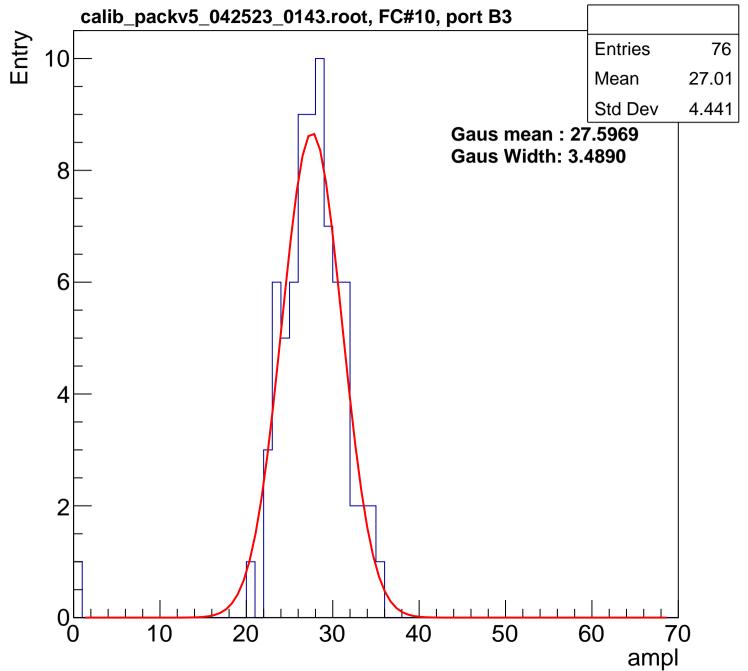


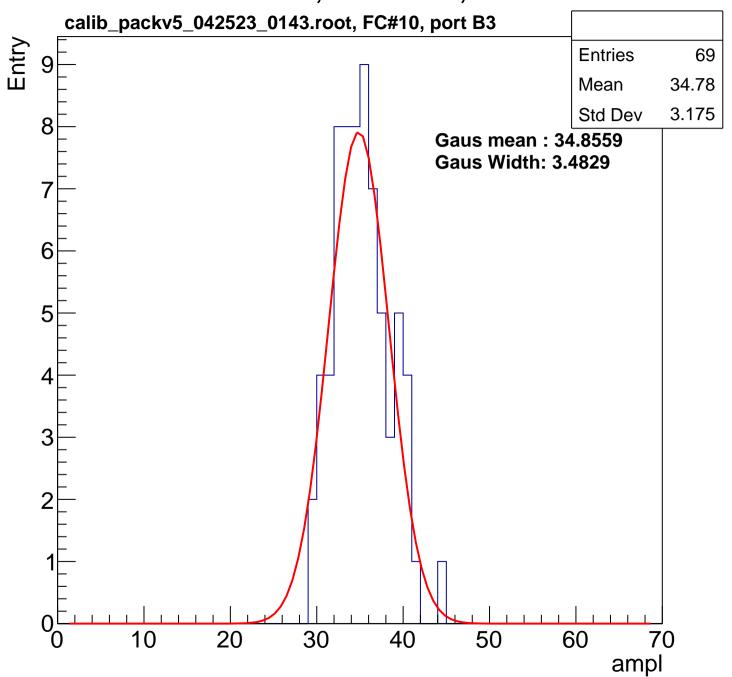


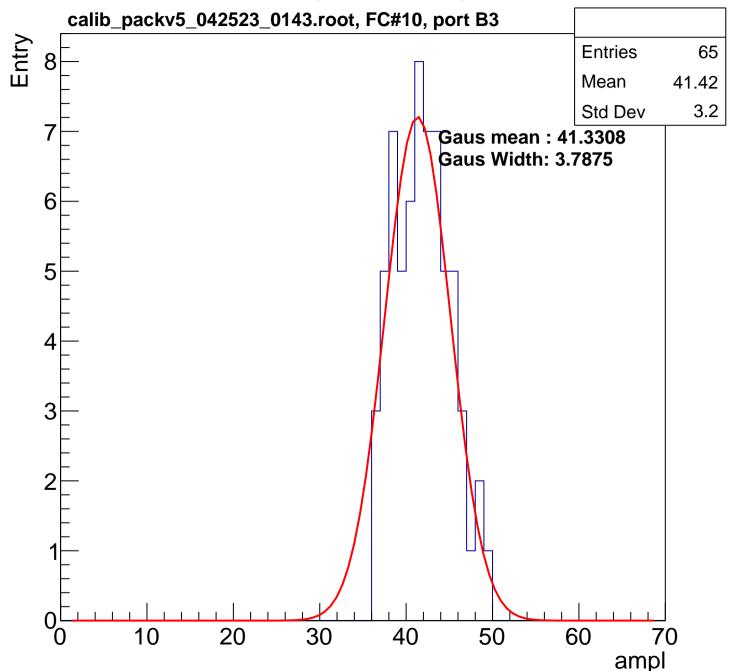


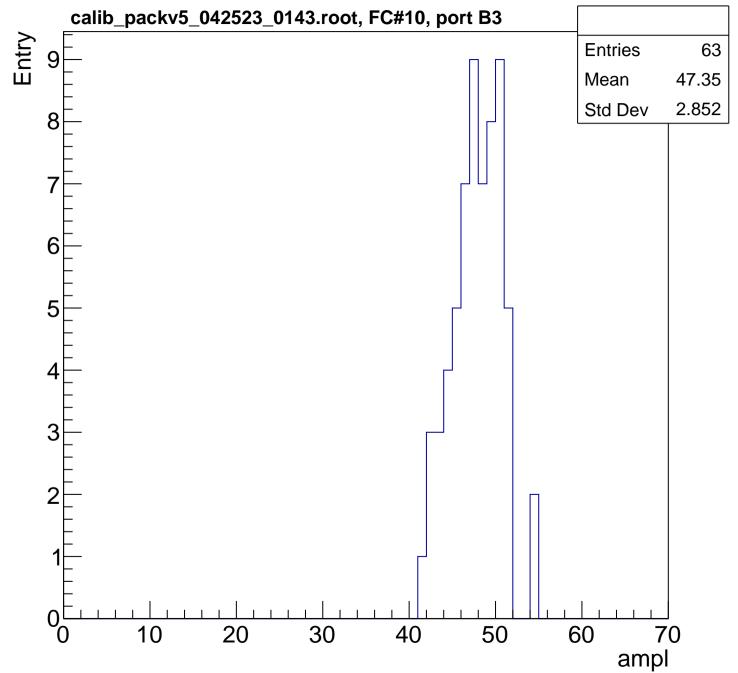


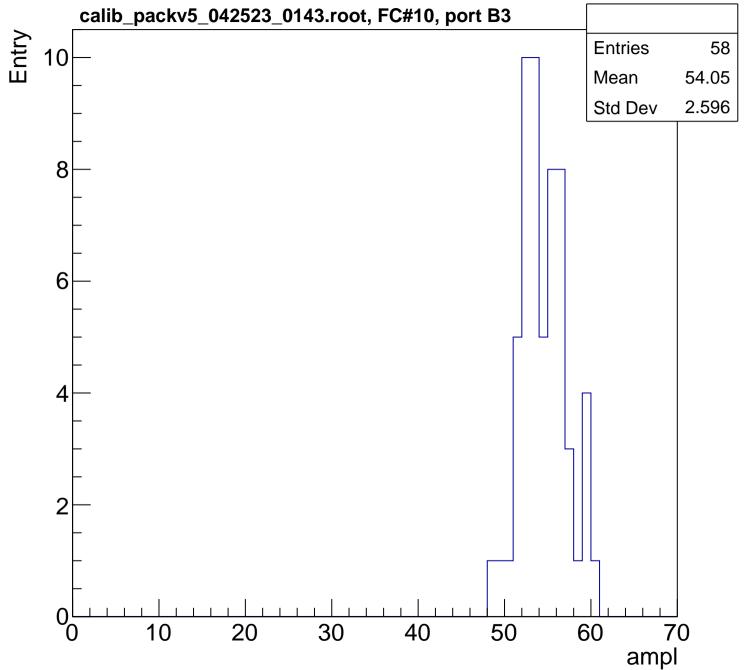


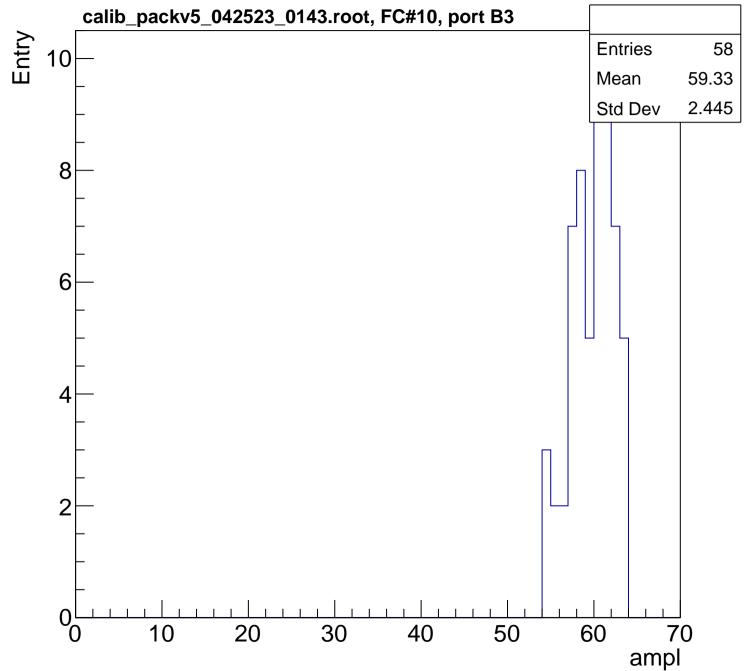


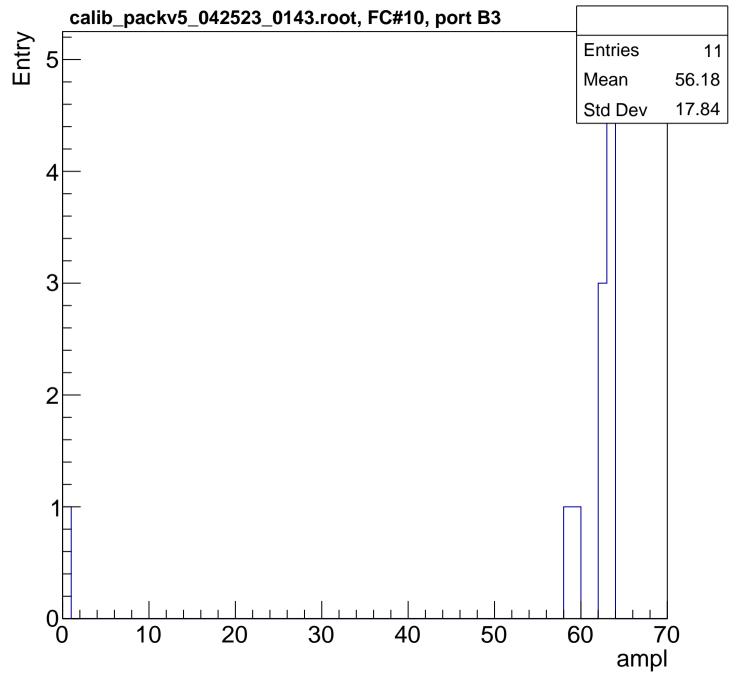




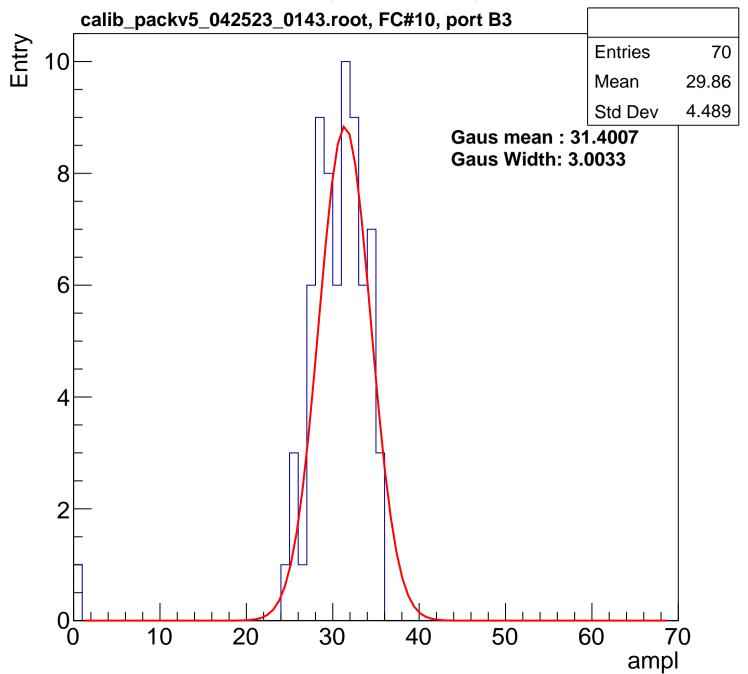


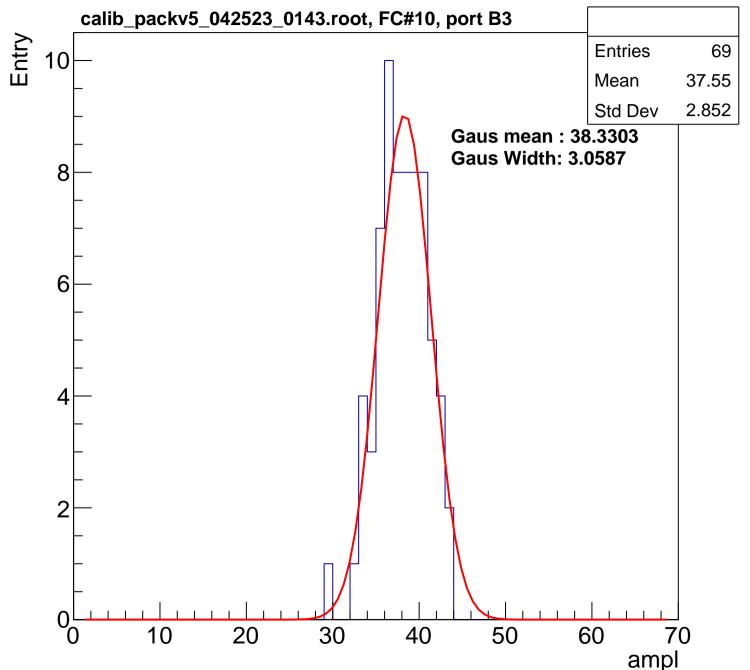


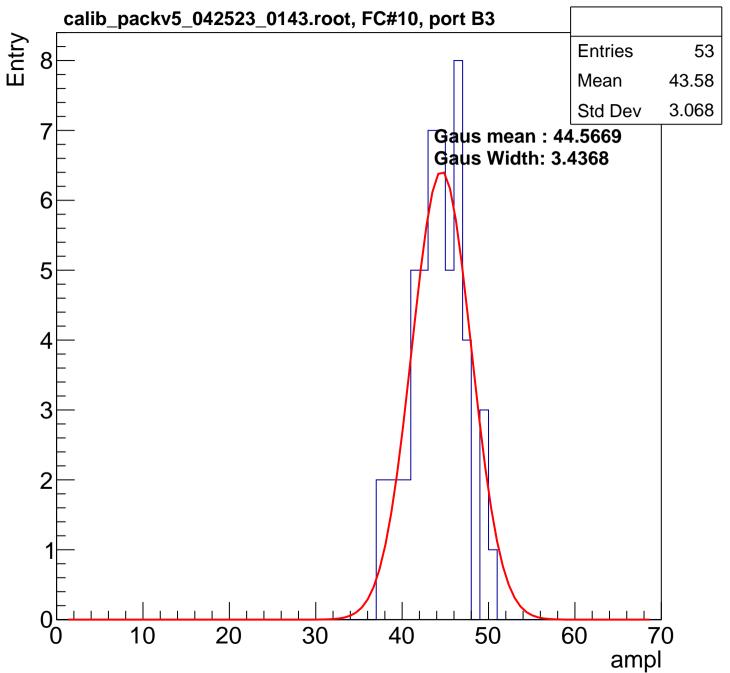


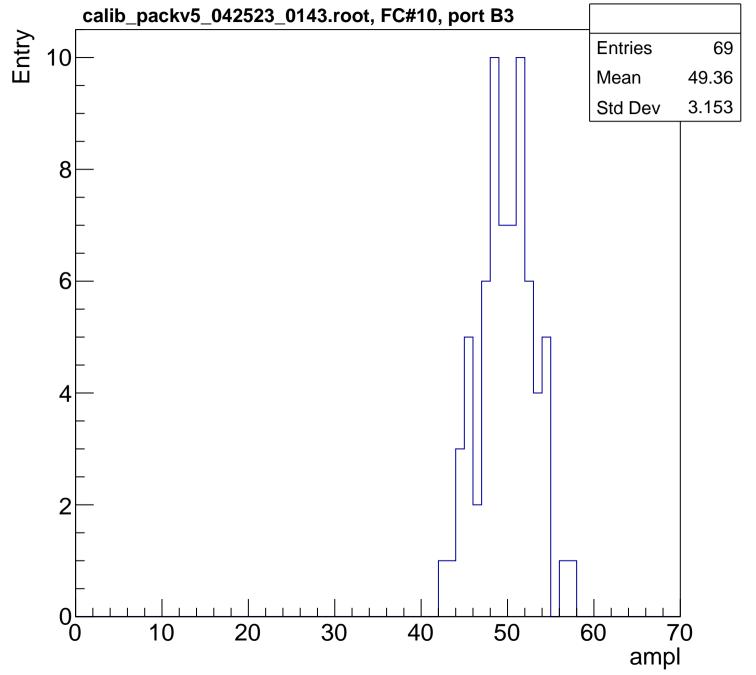


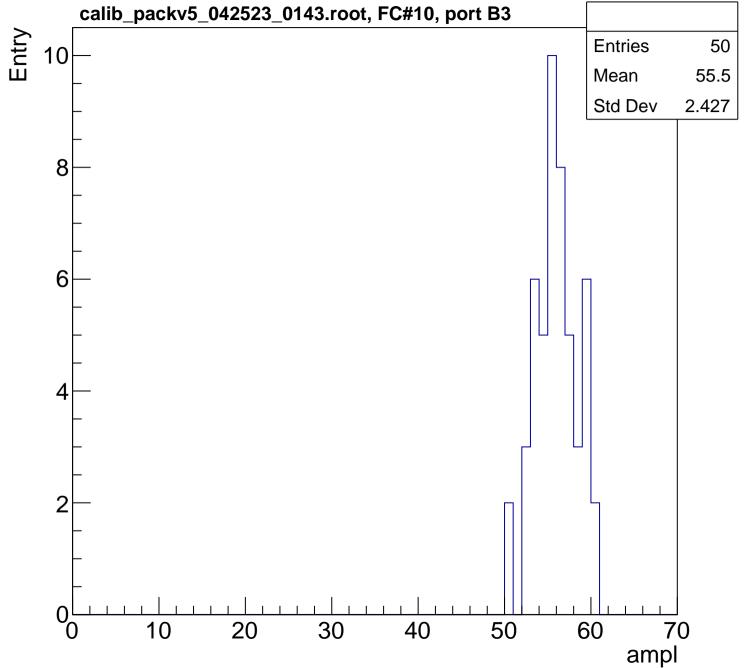


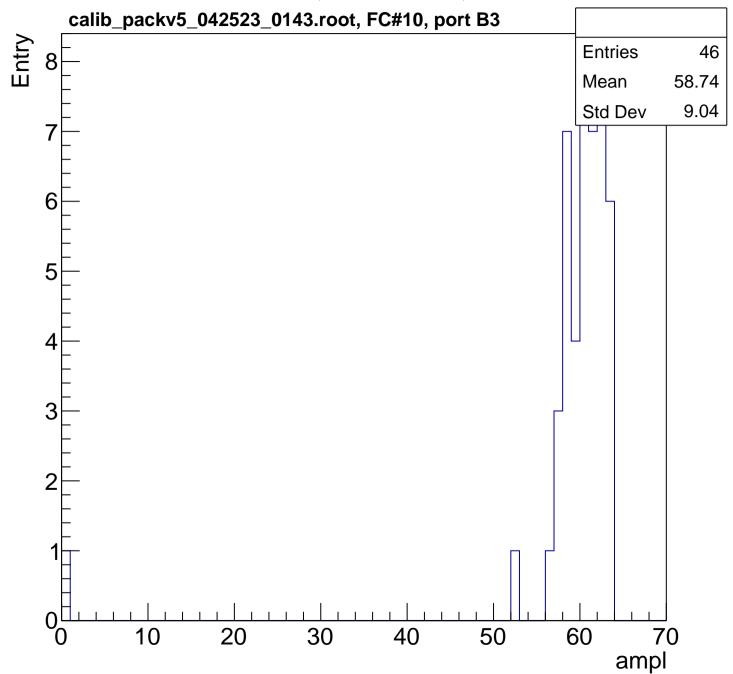


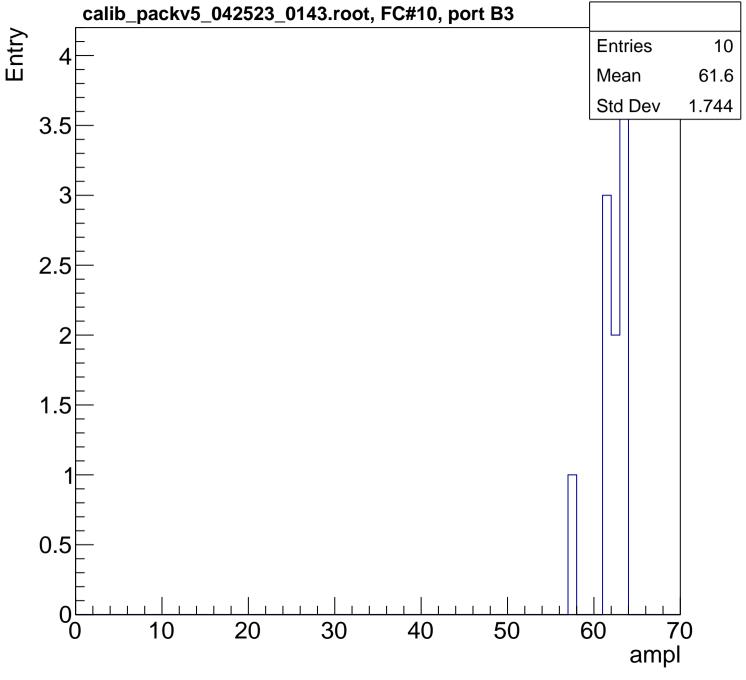


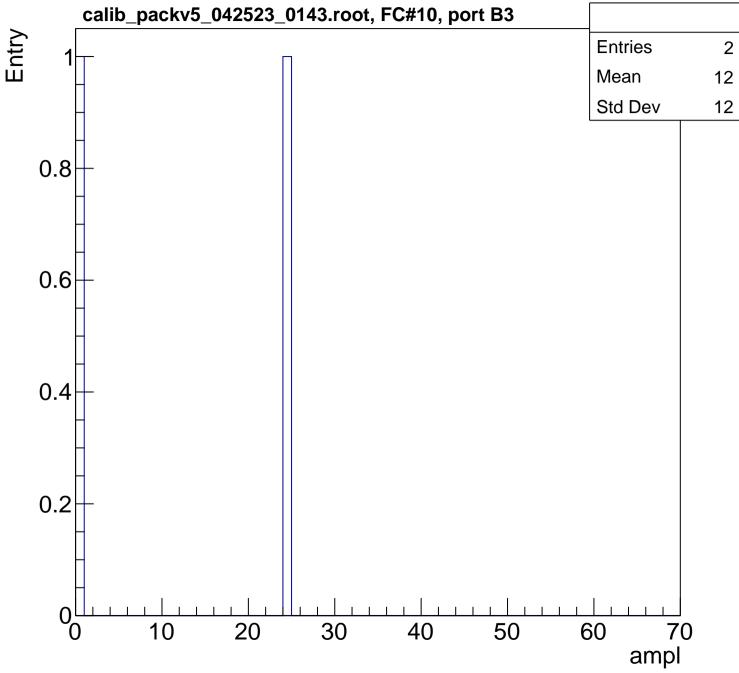


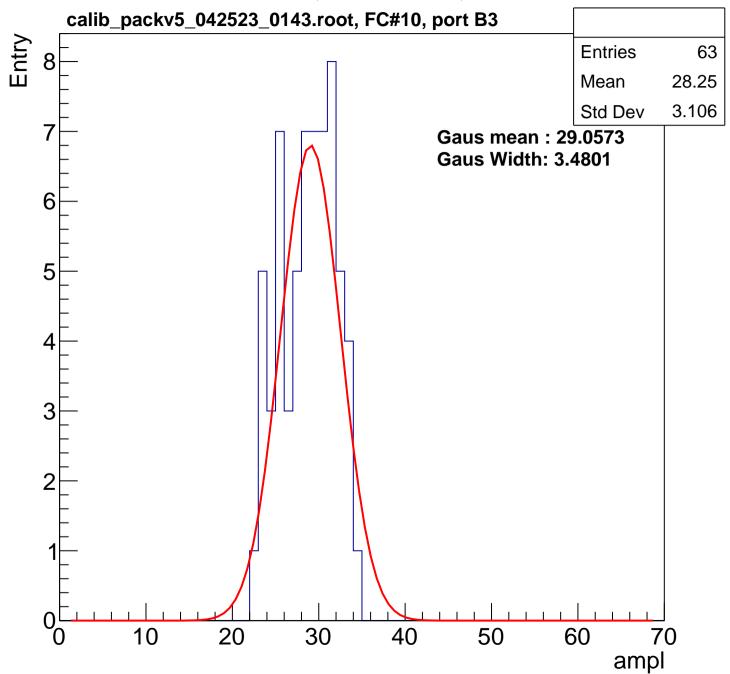


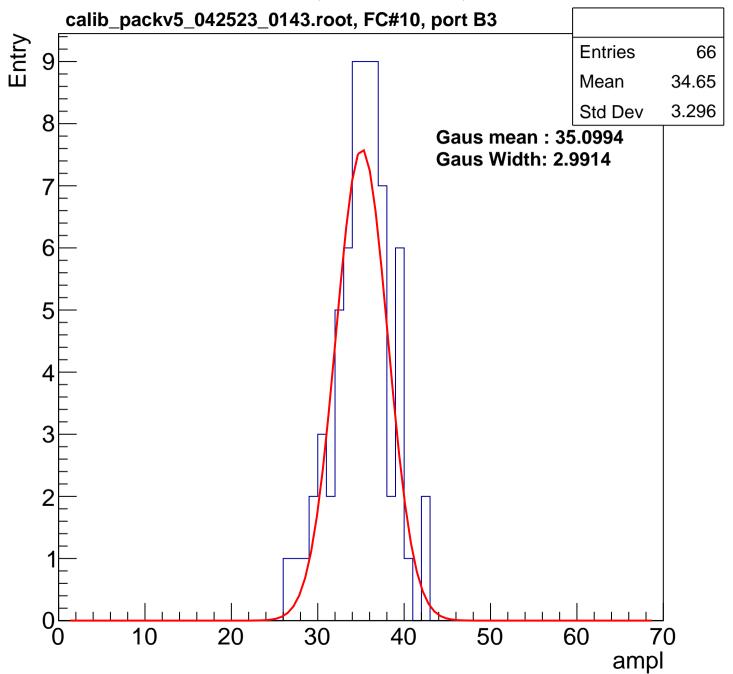


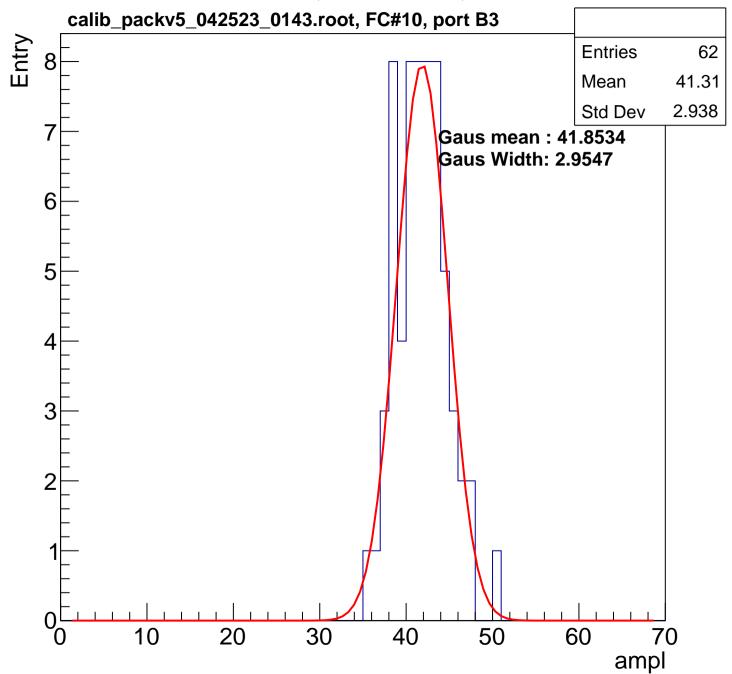


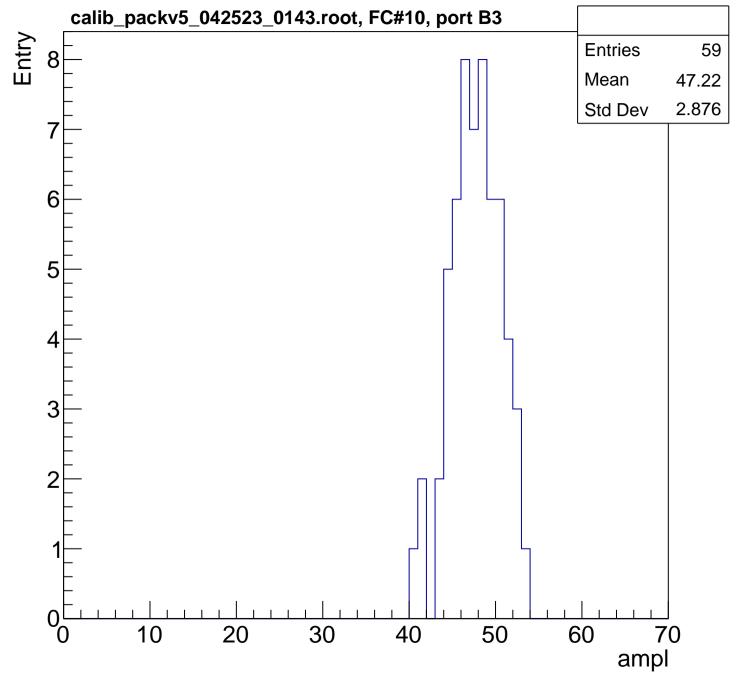


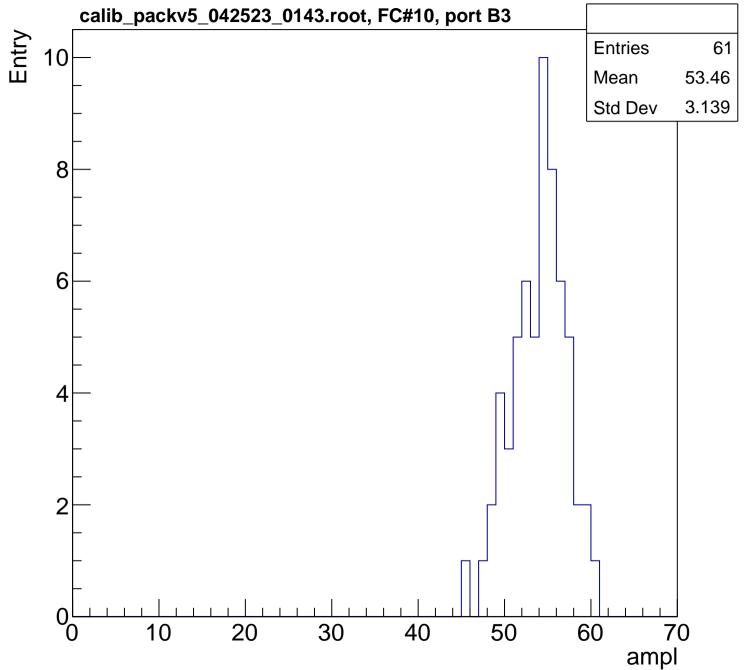


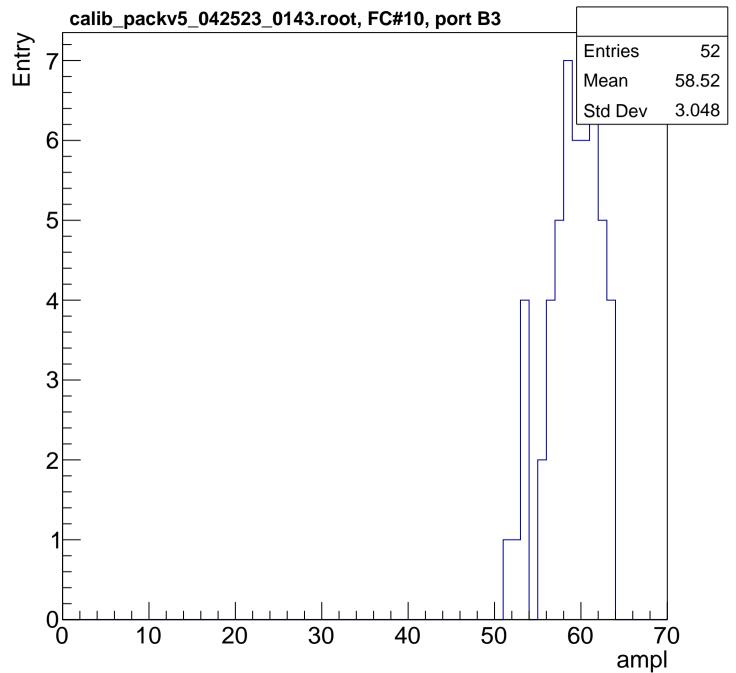


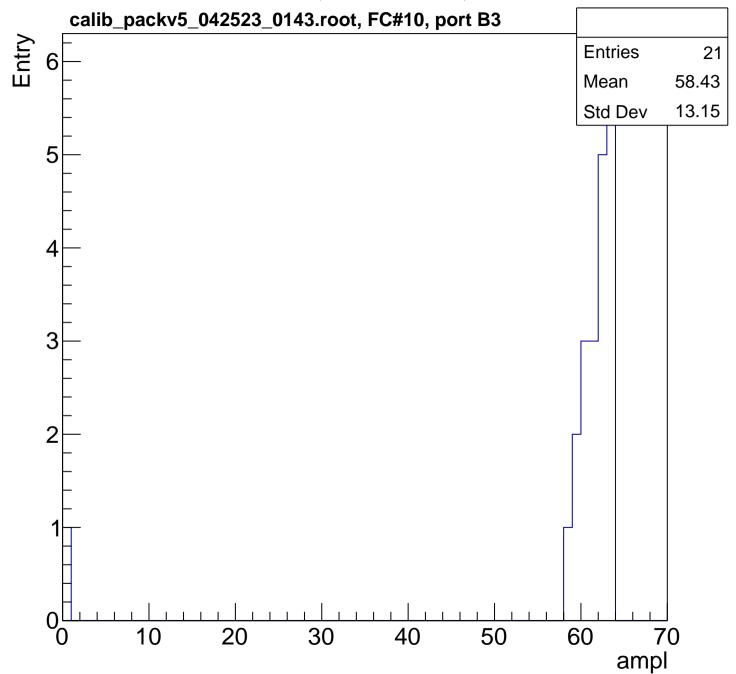




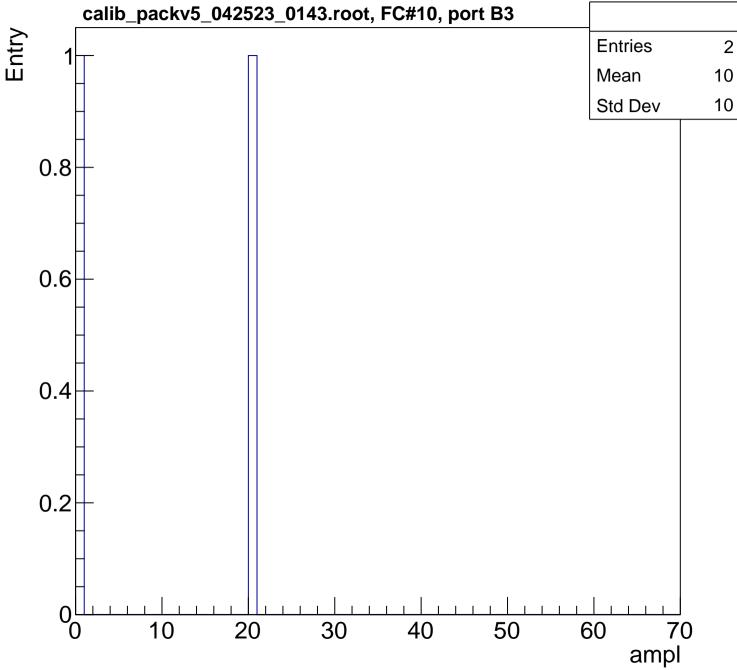


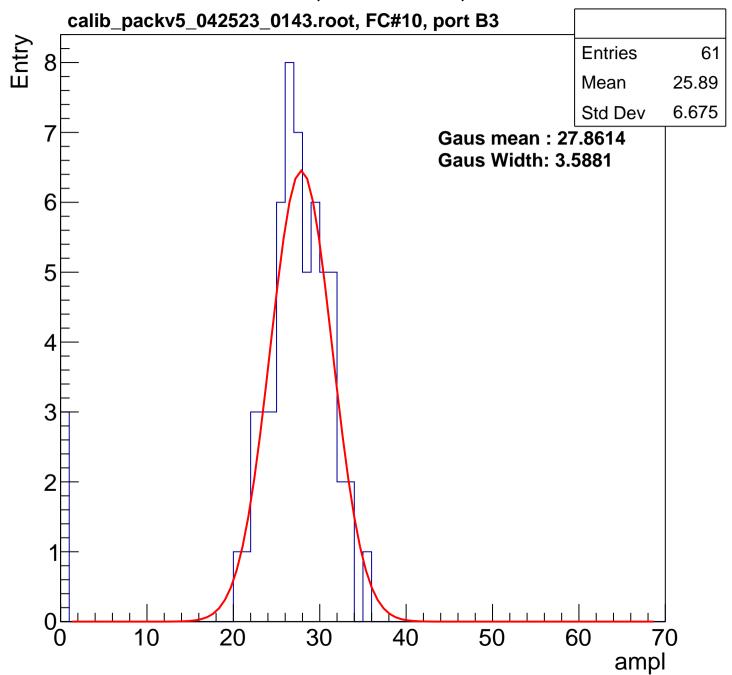


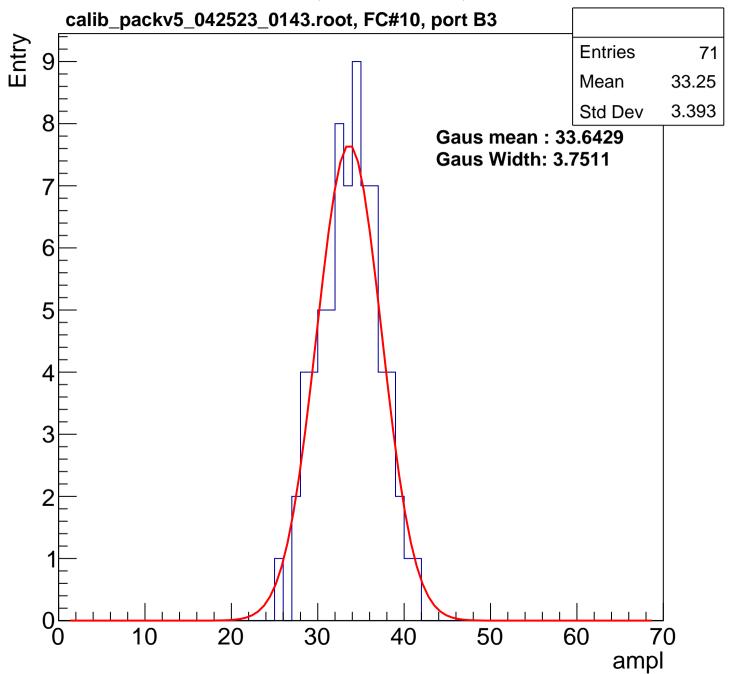


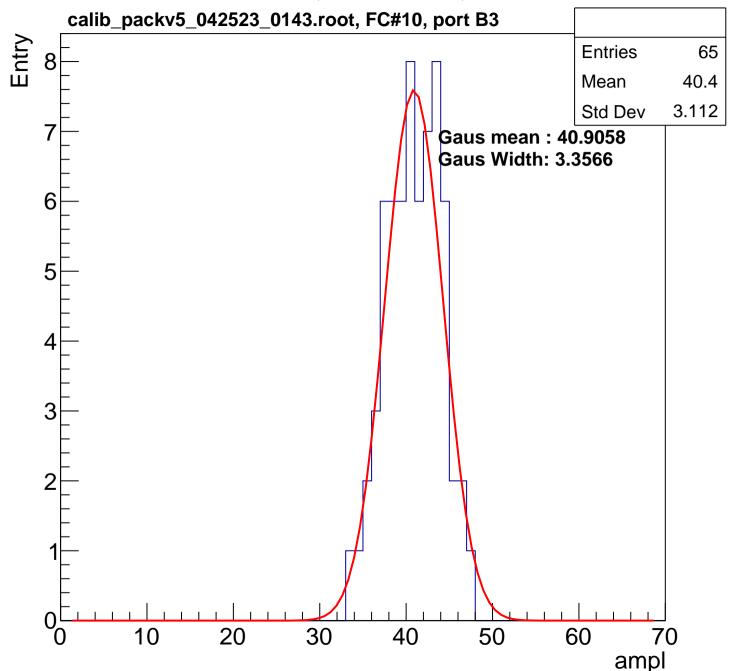


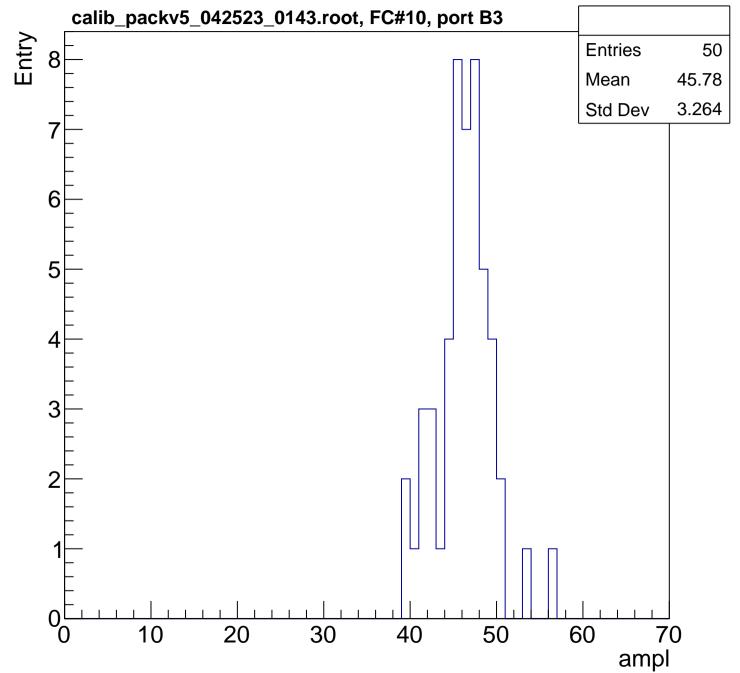
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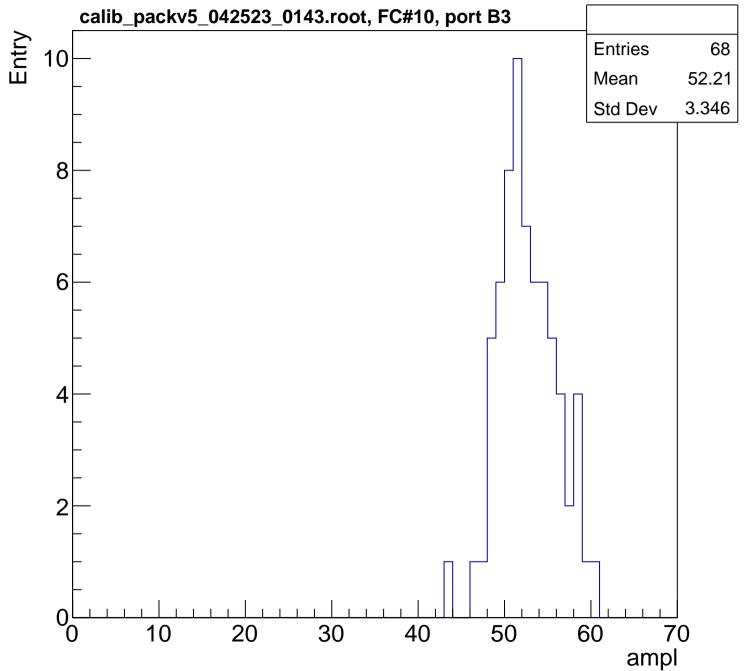


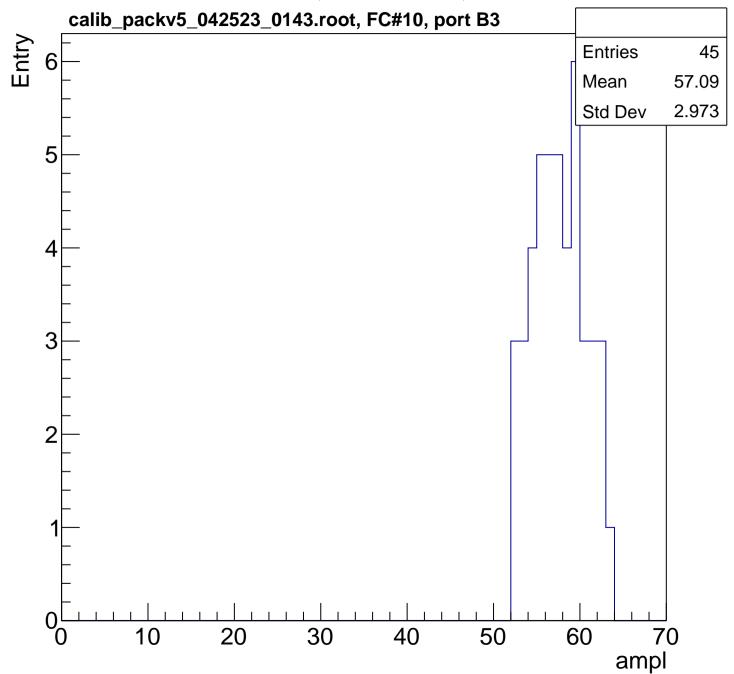


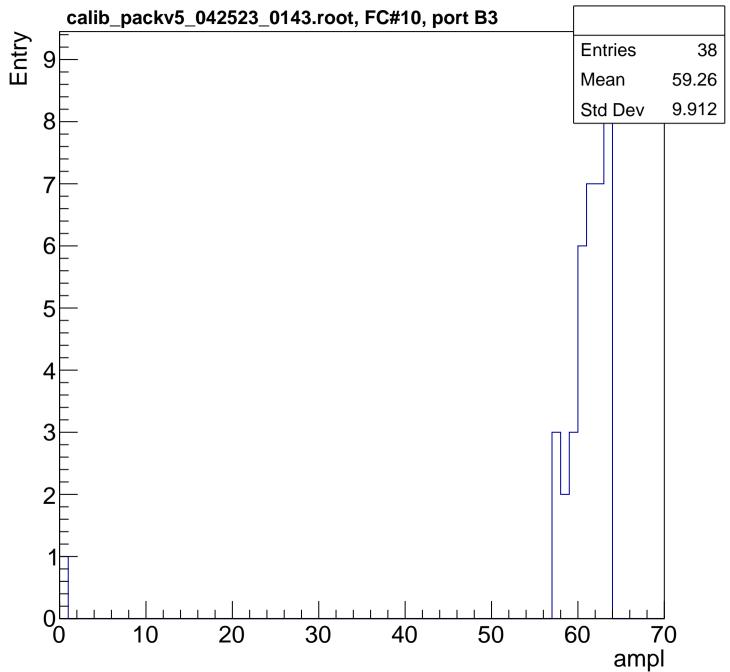




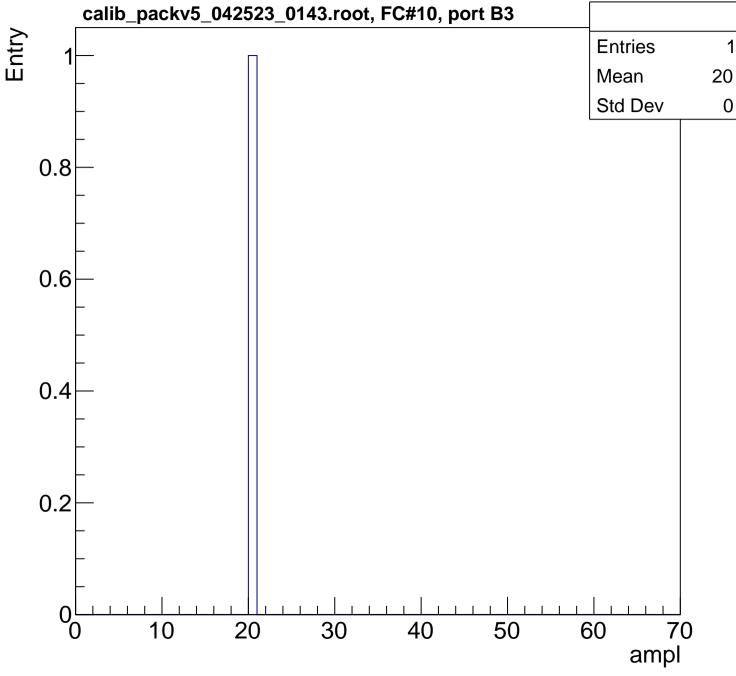


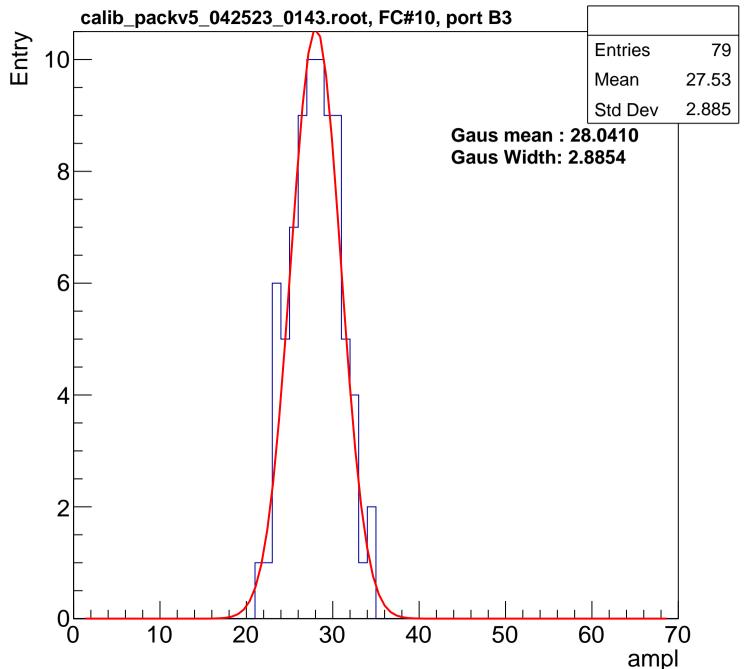


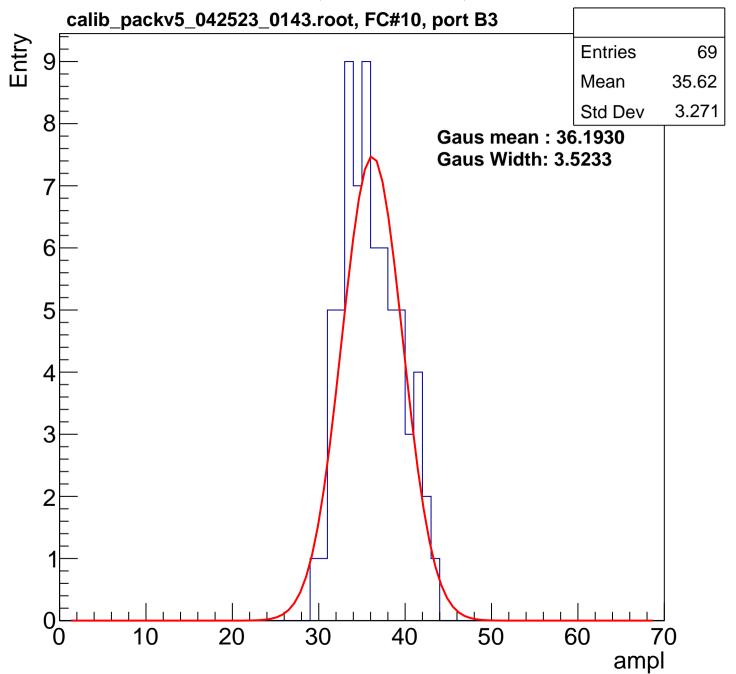


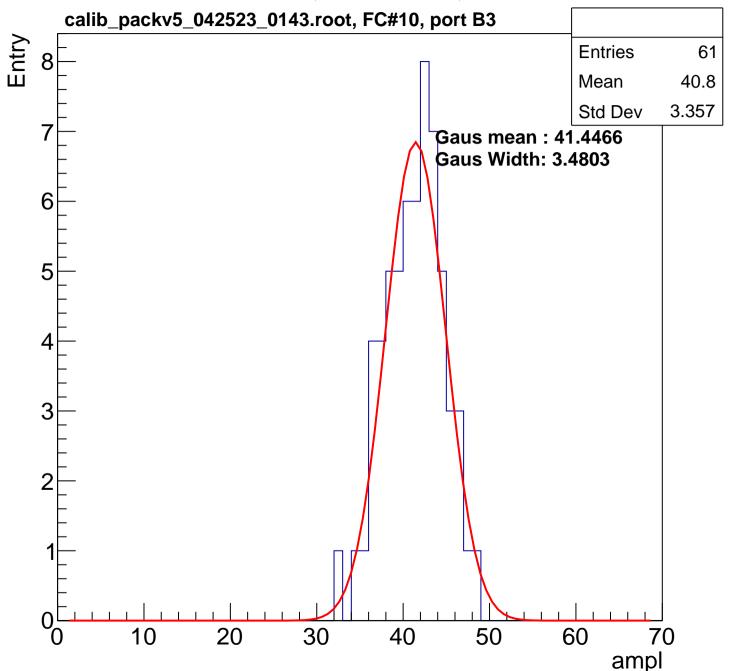


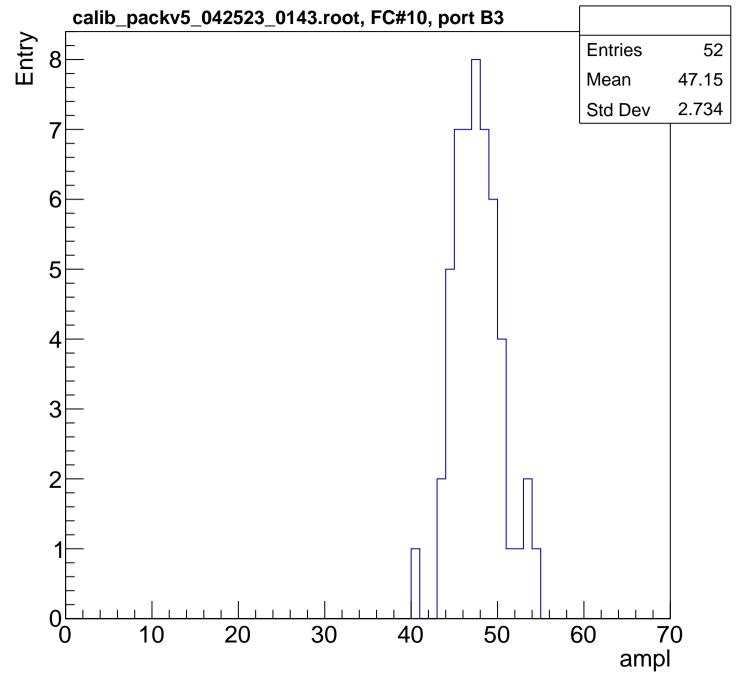
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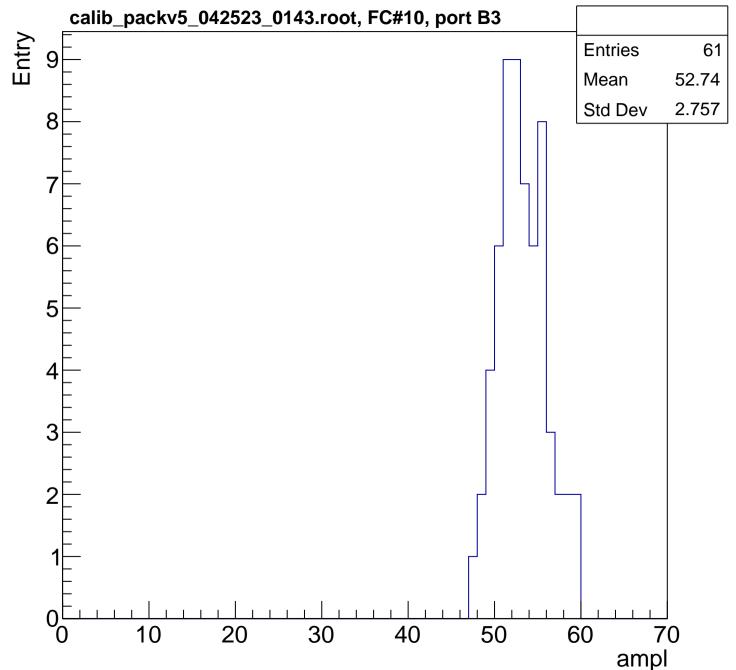


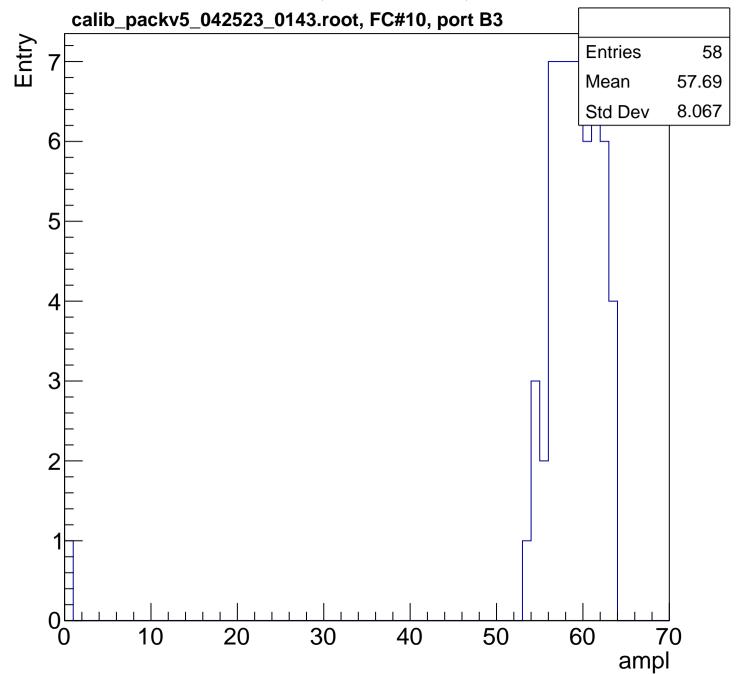


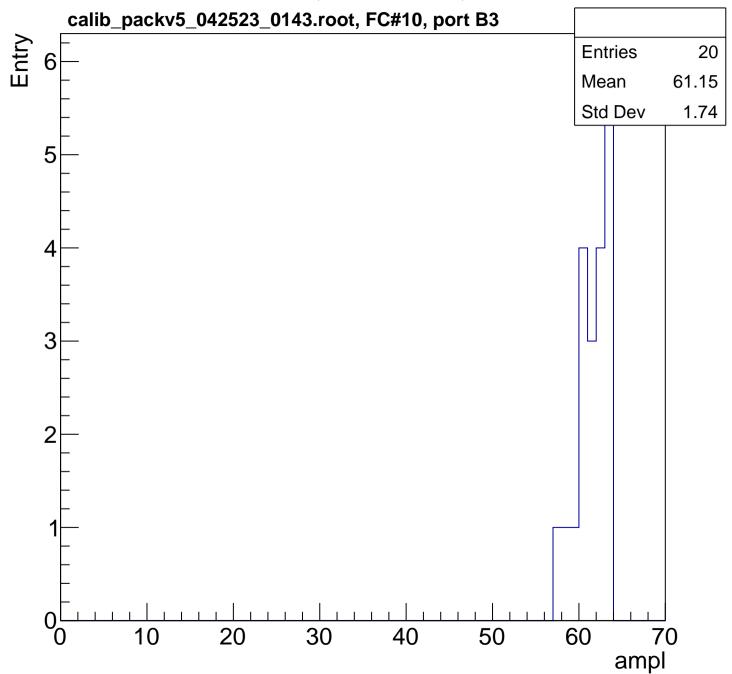




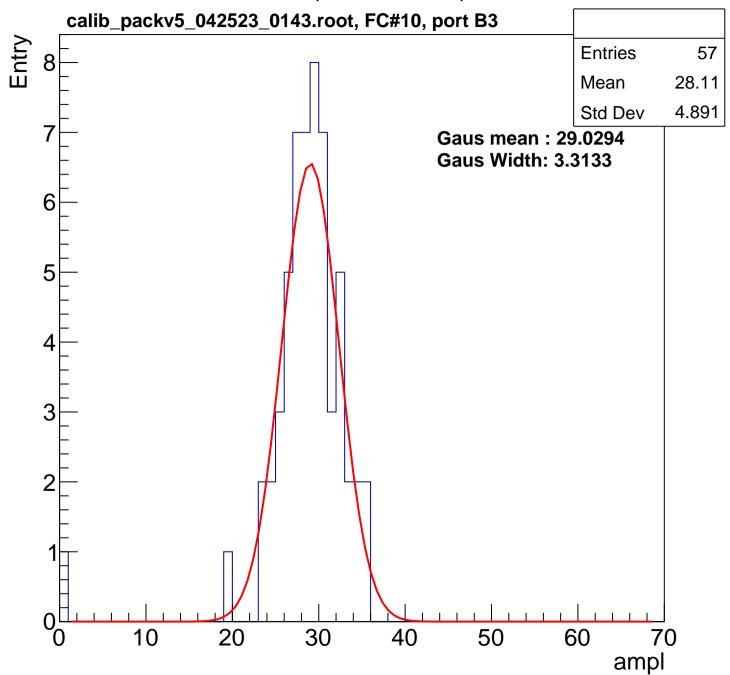


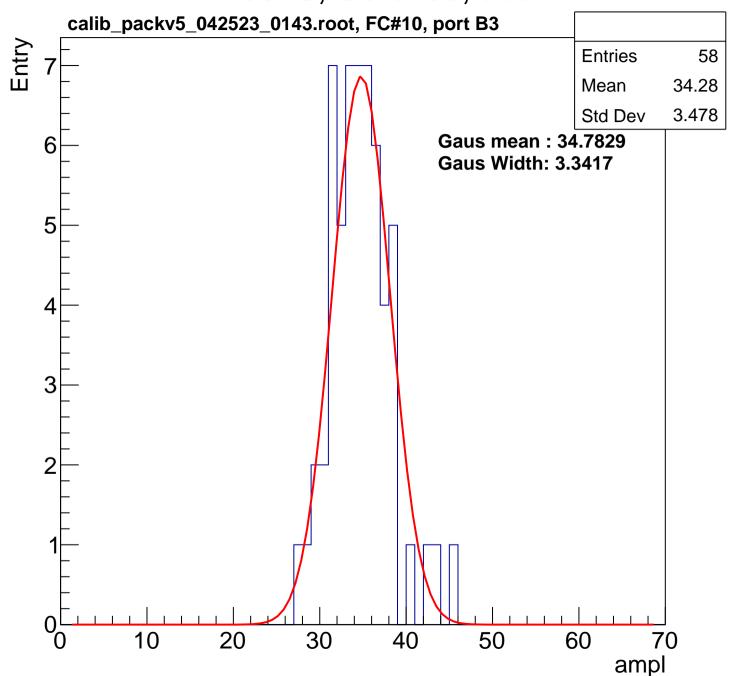


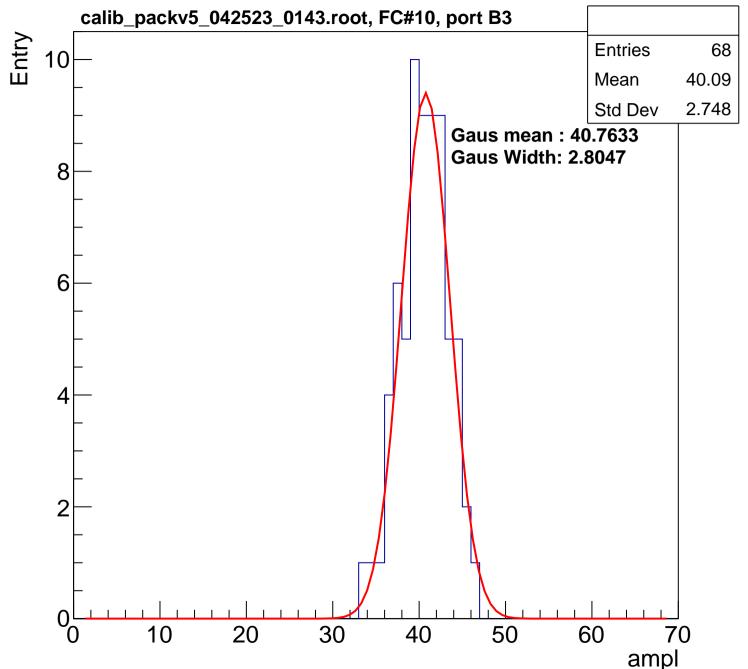


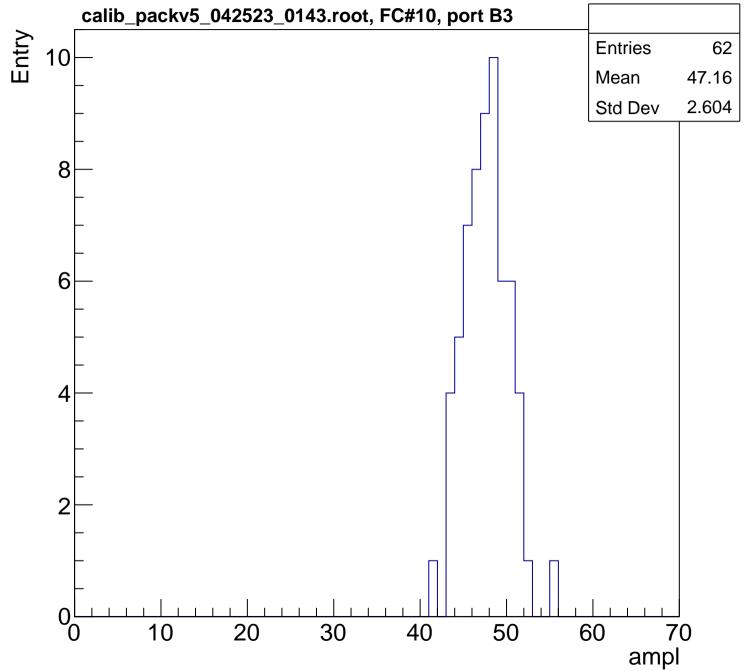


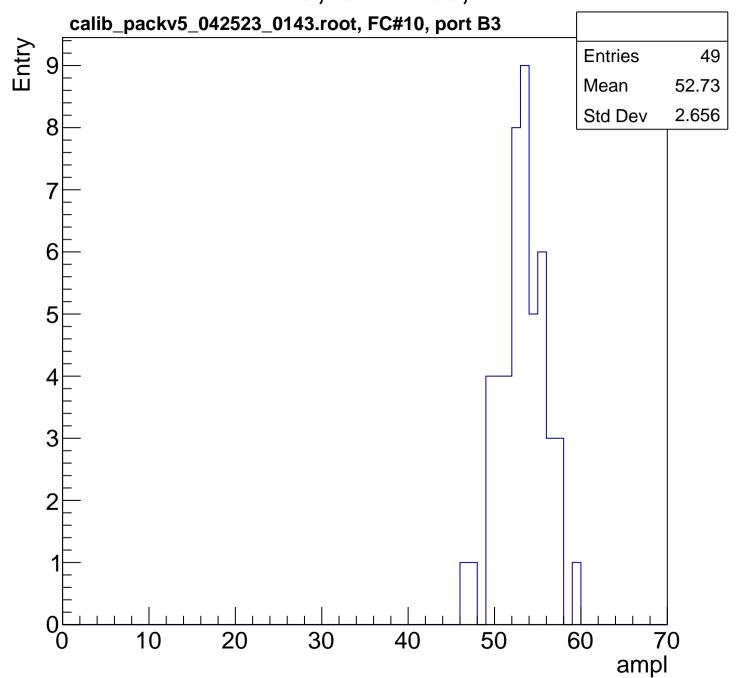
B1L002S, U9-ch67, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

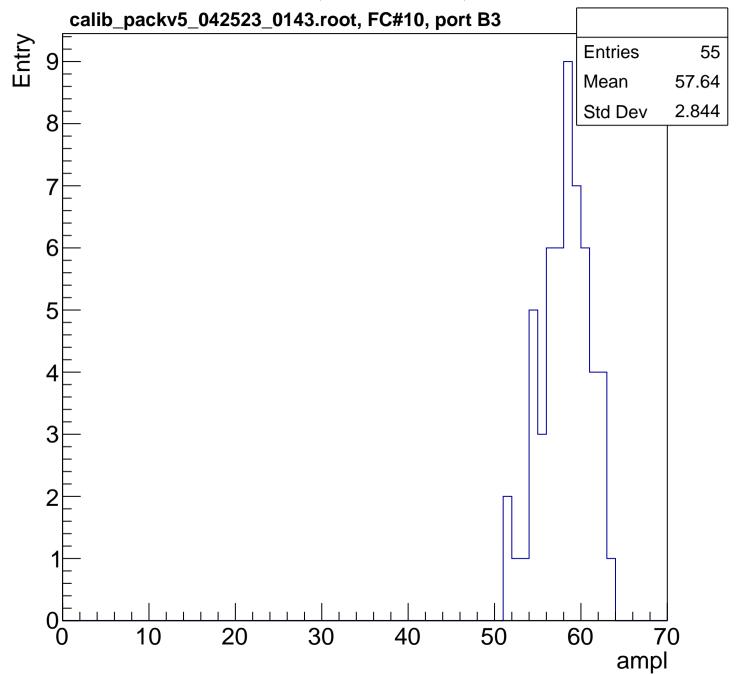


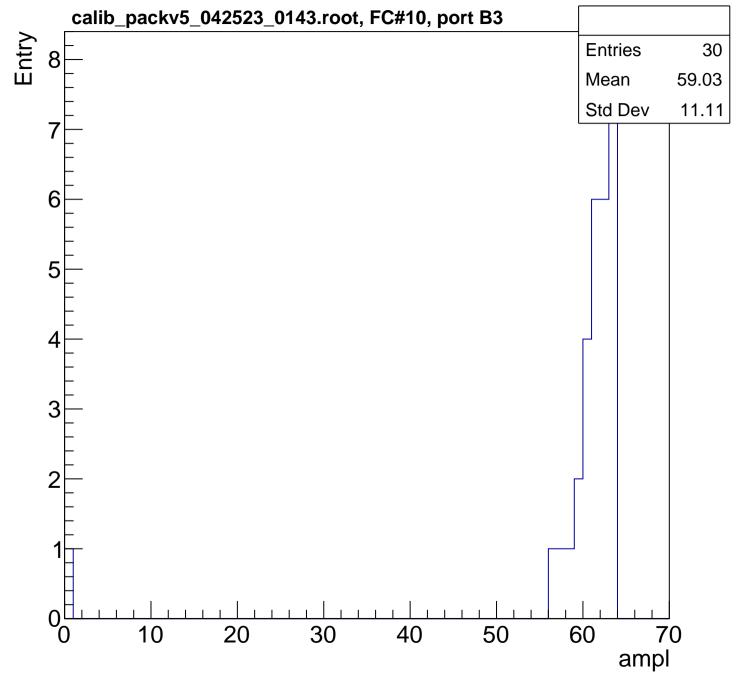




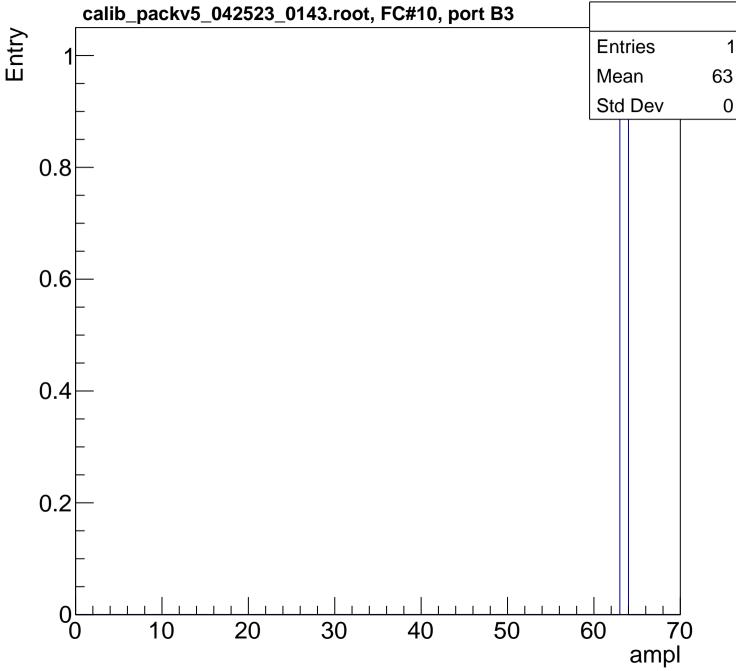


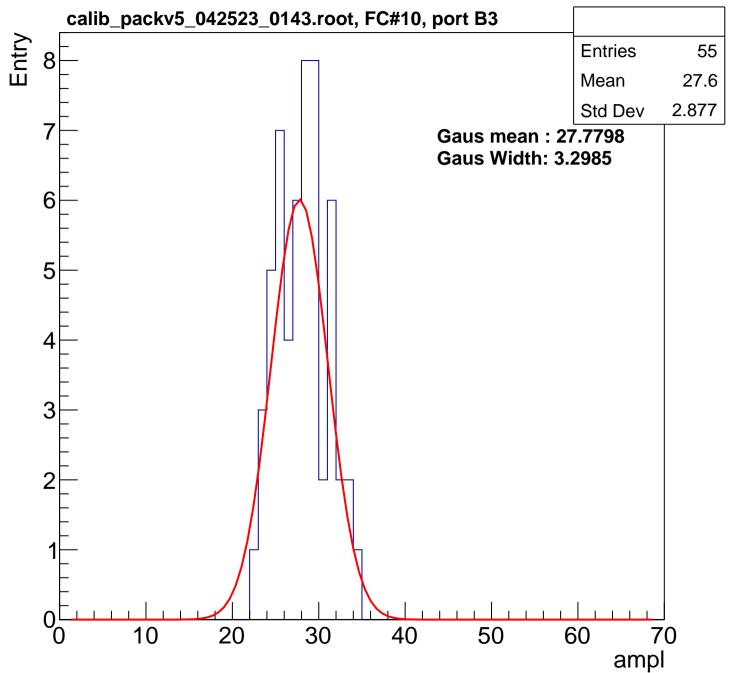


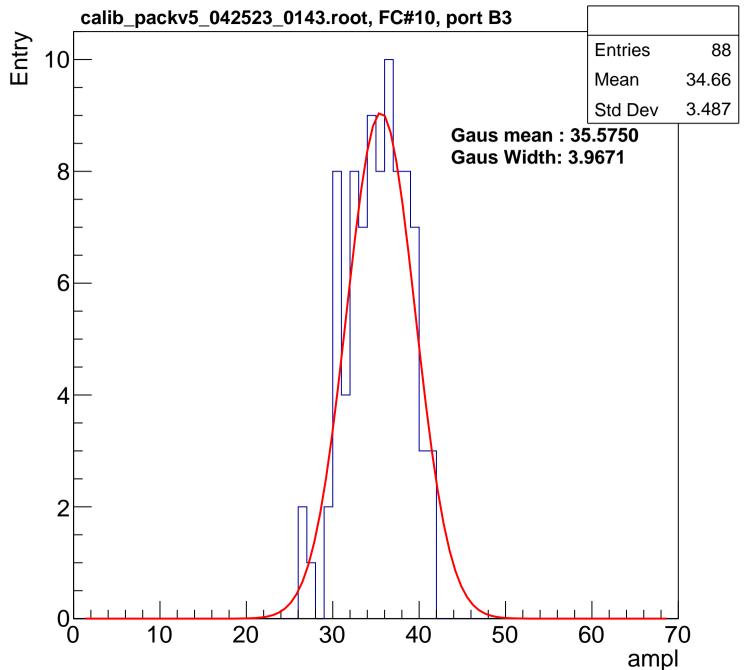


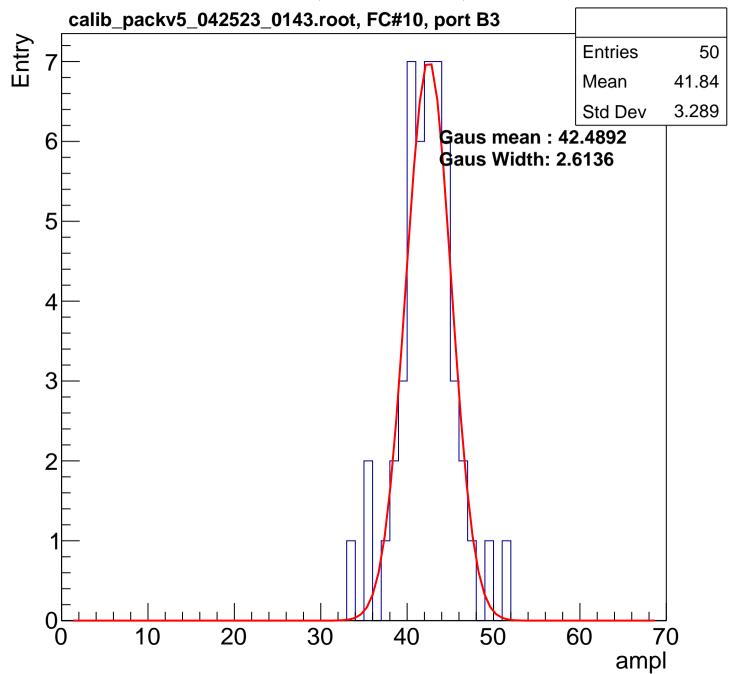


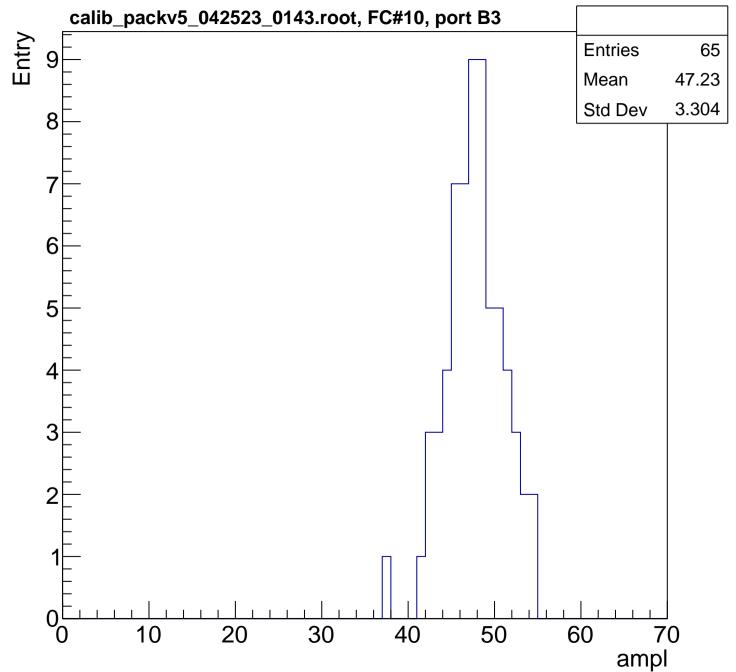
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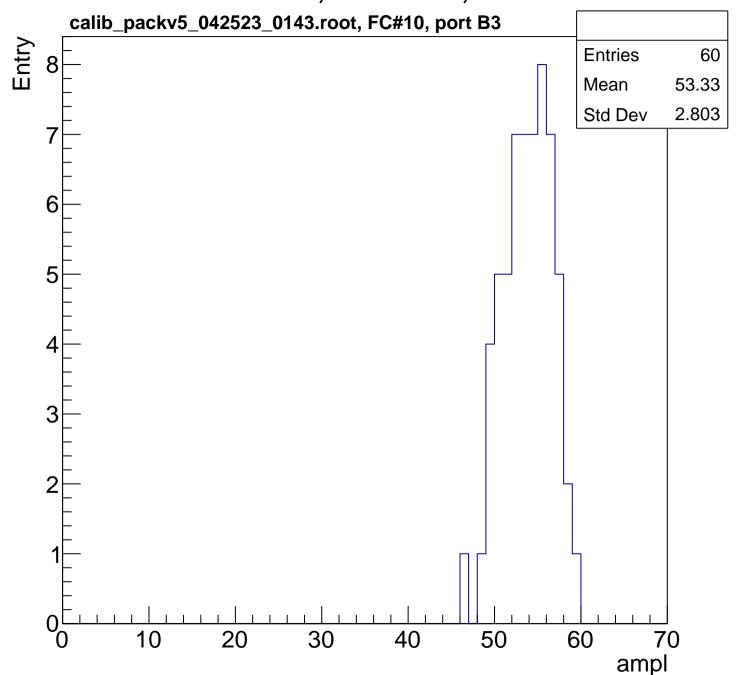


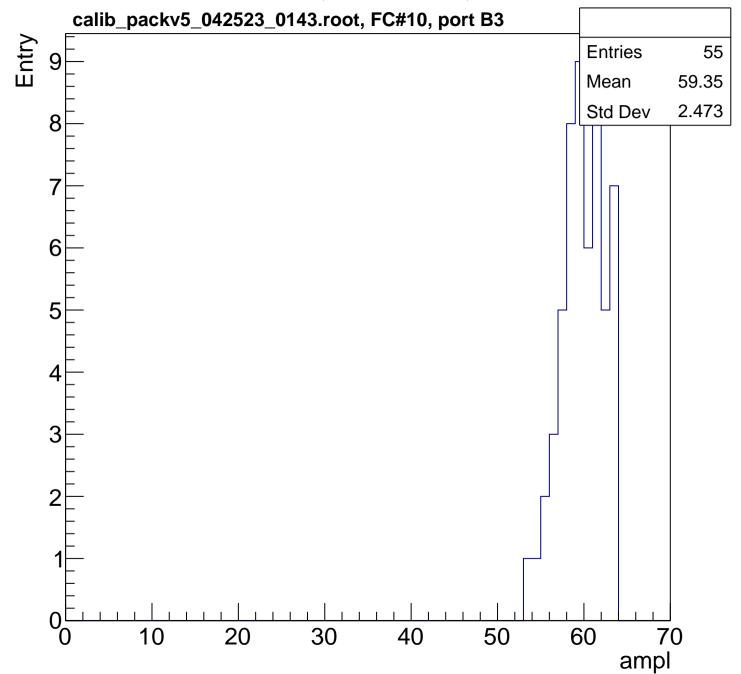


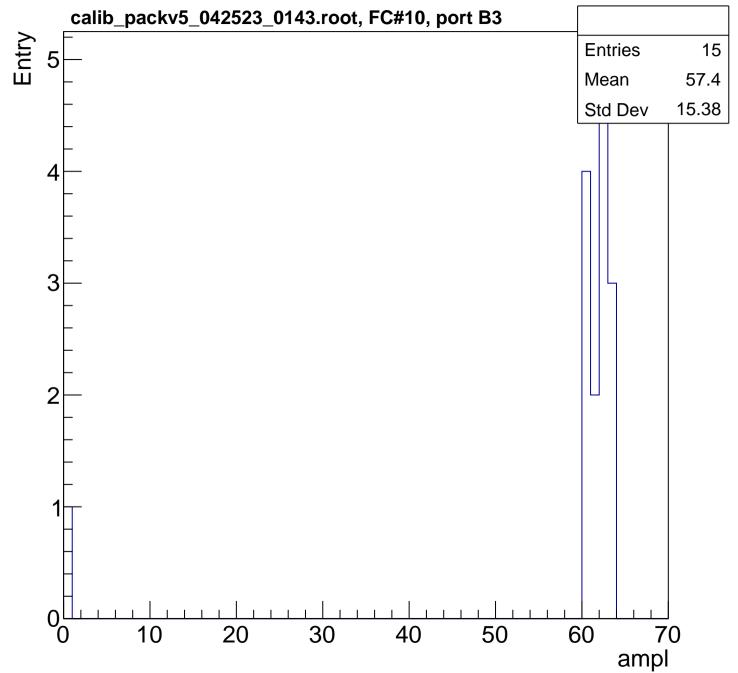


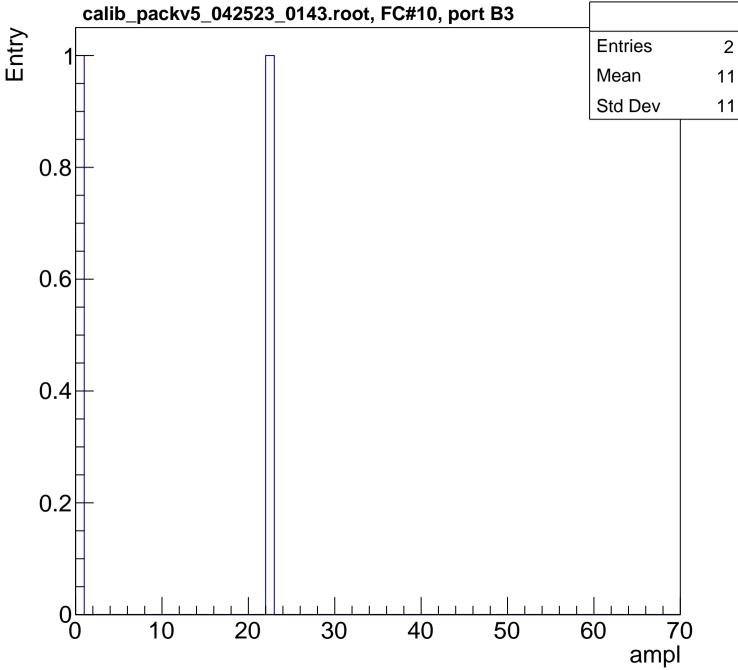


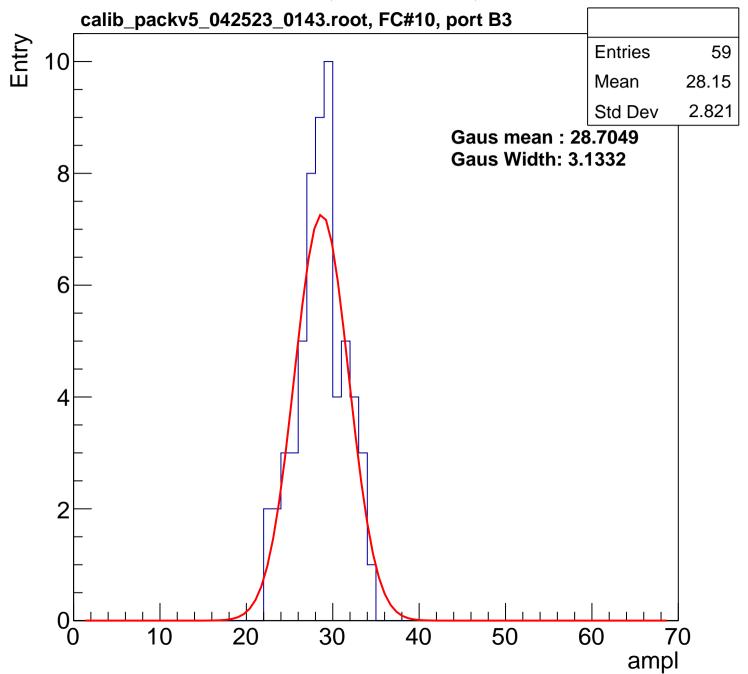


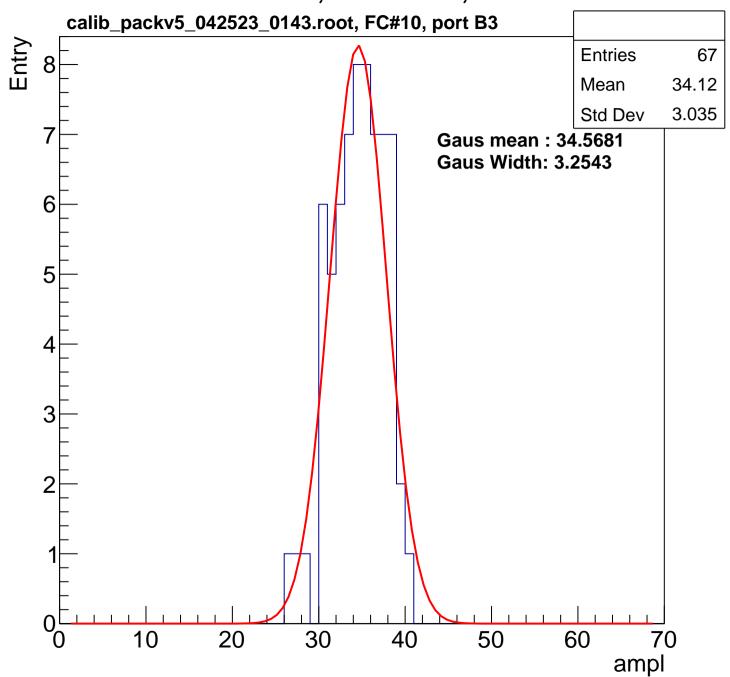


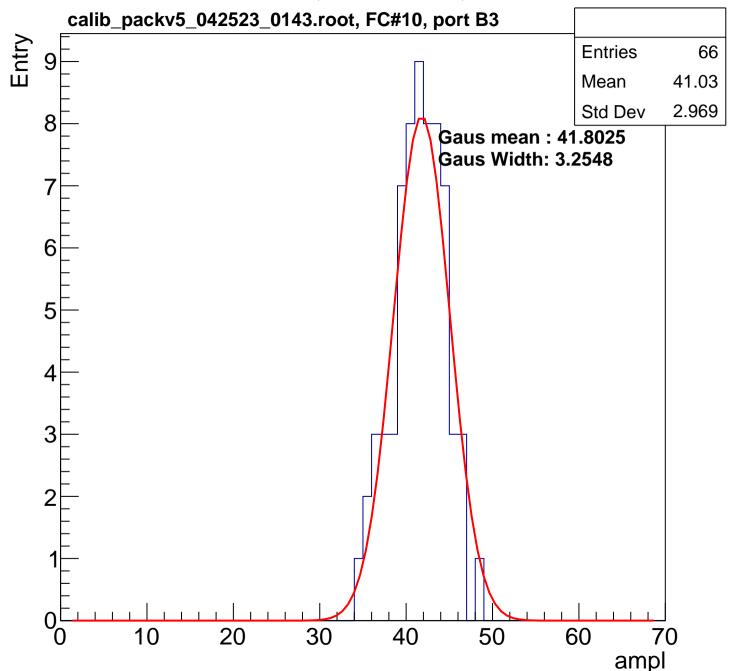


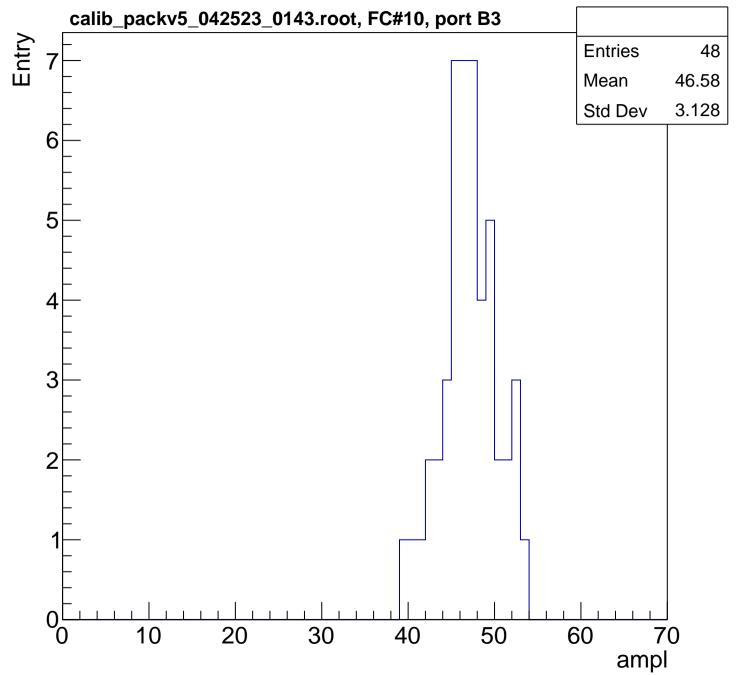


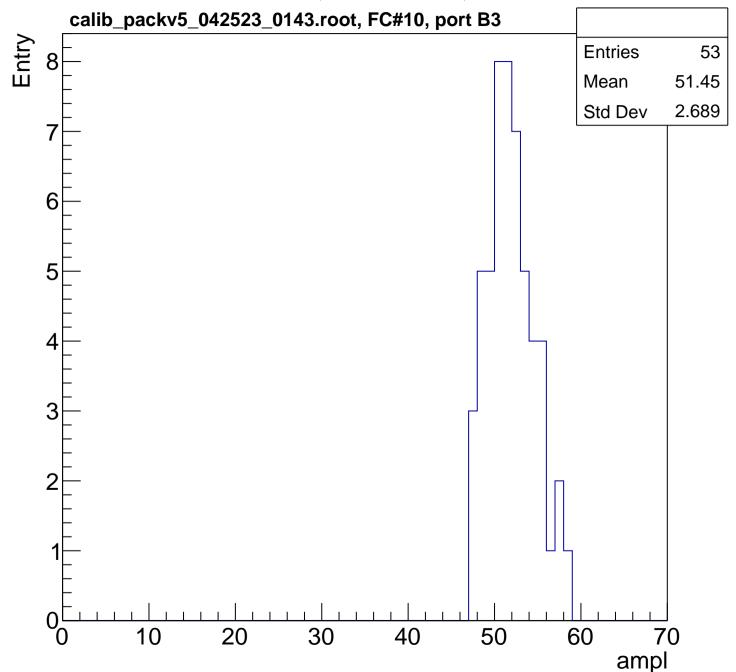


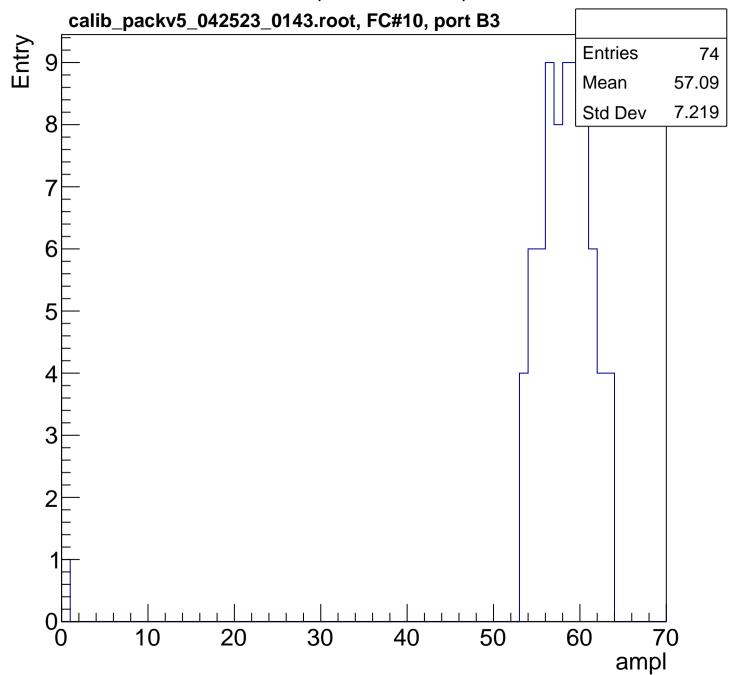


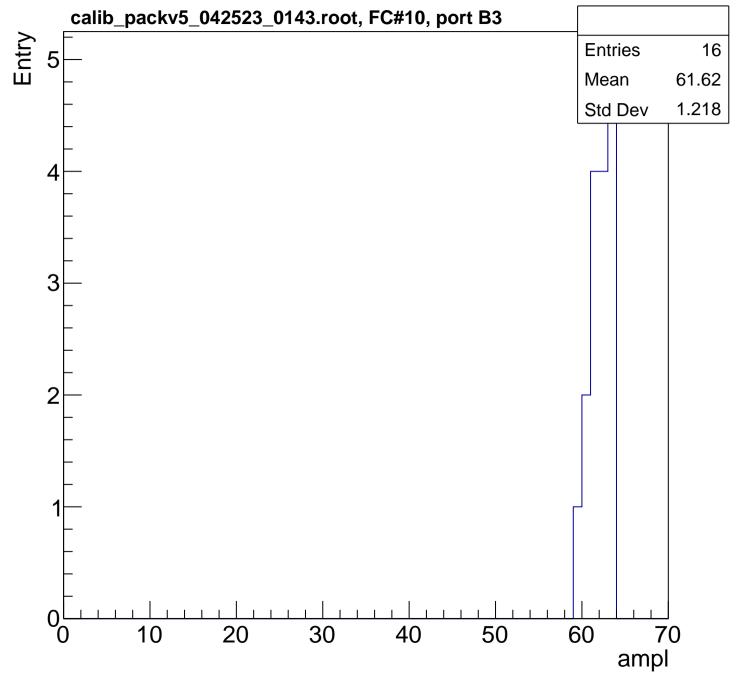


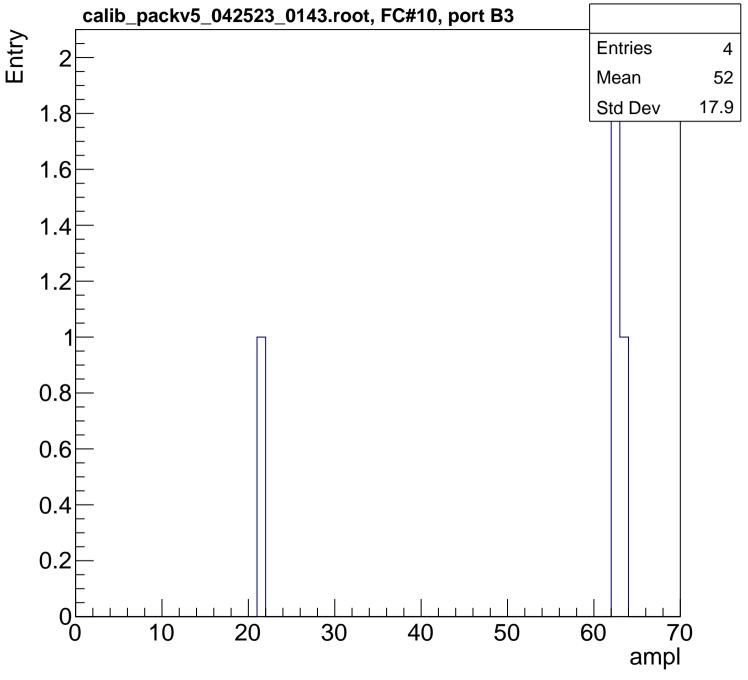


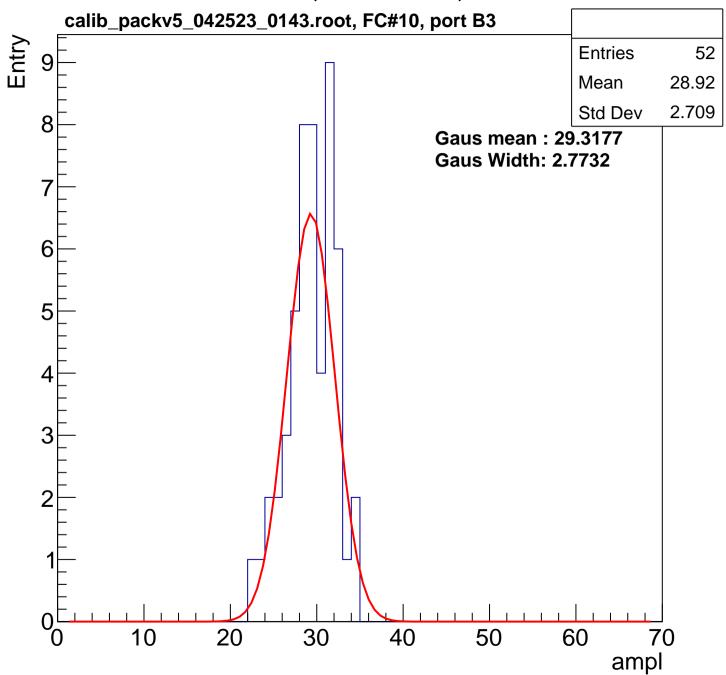


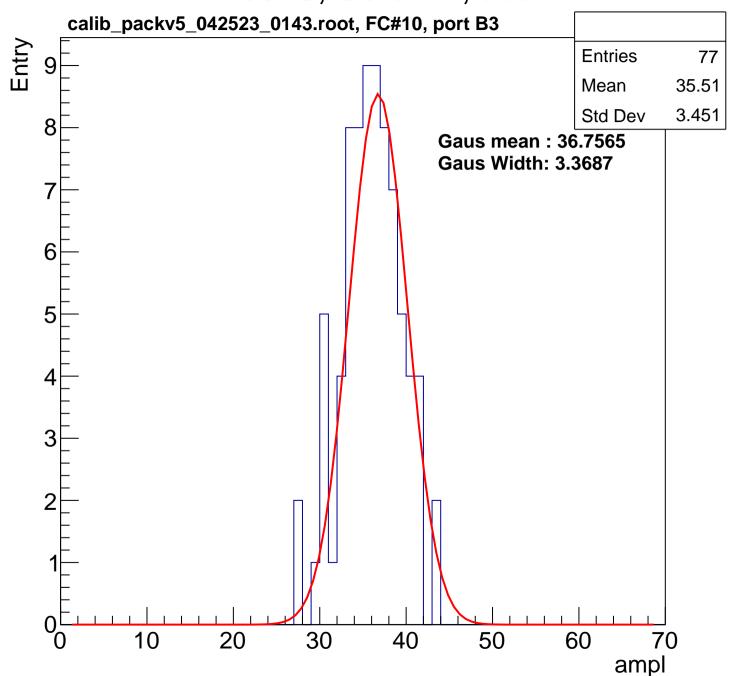


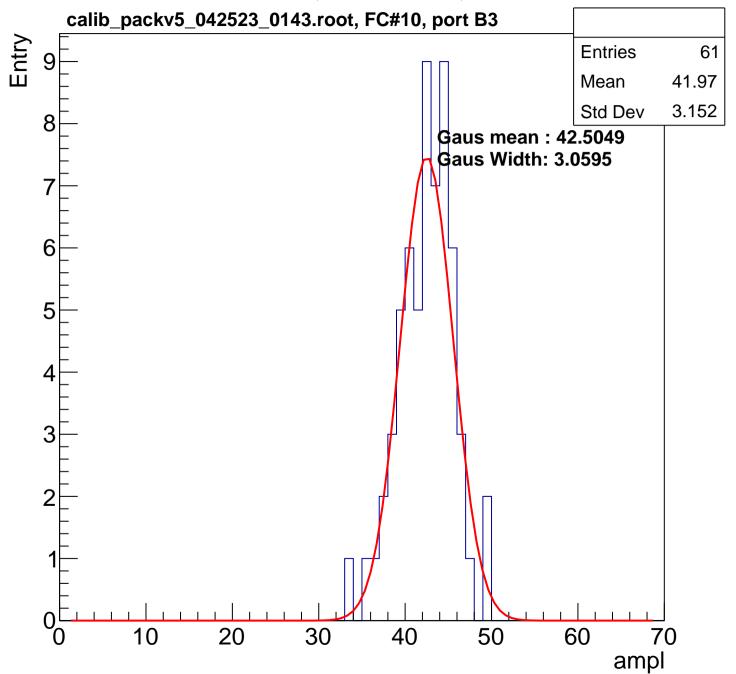


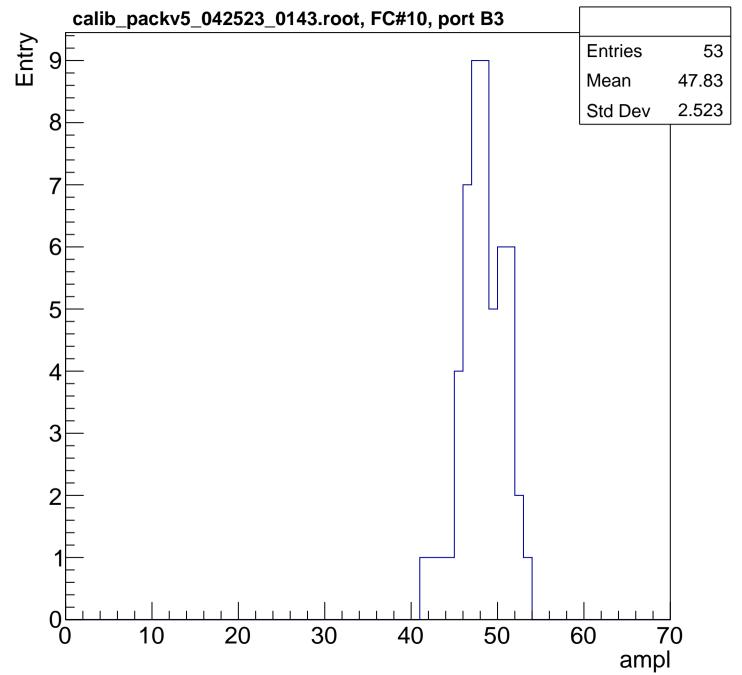


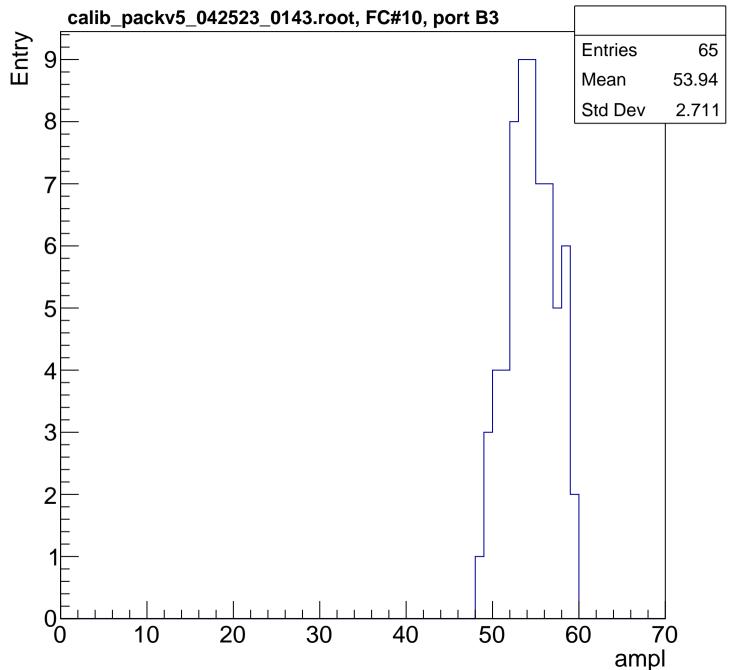


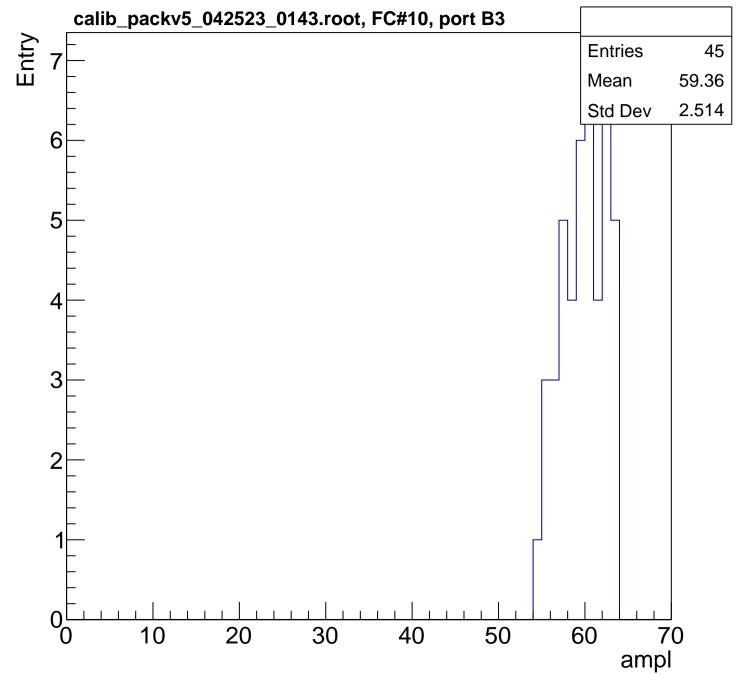


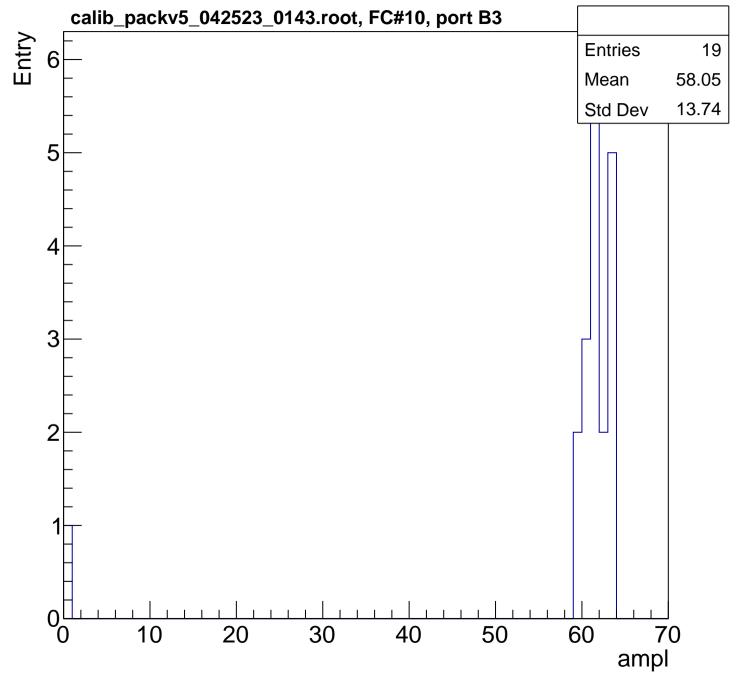


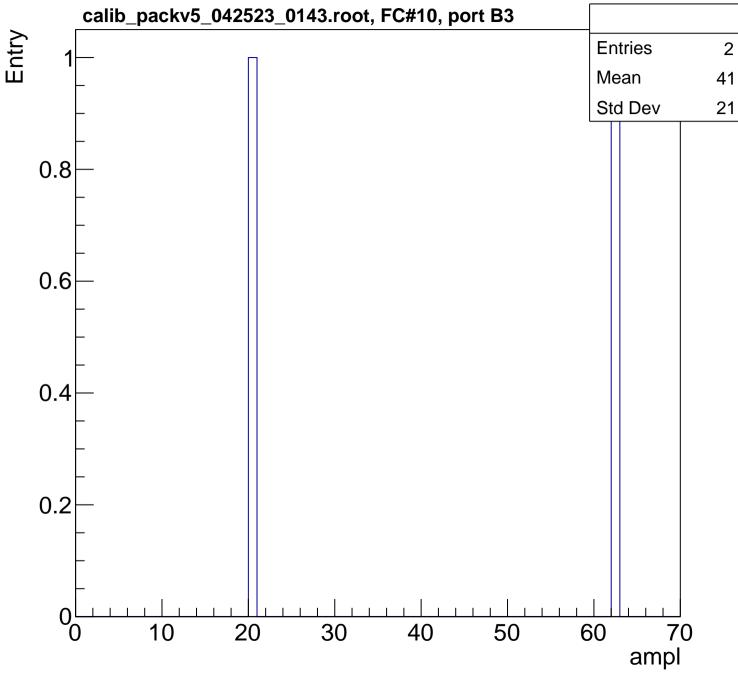


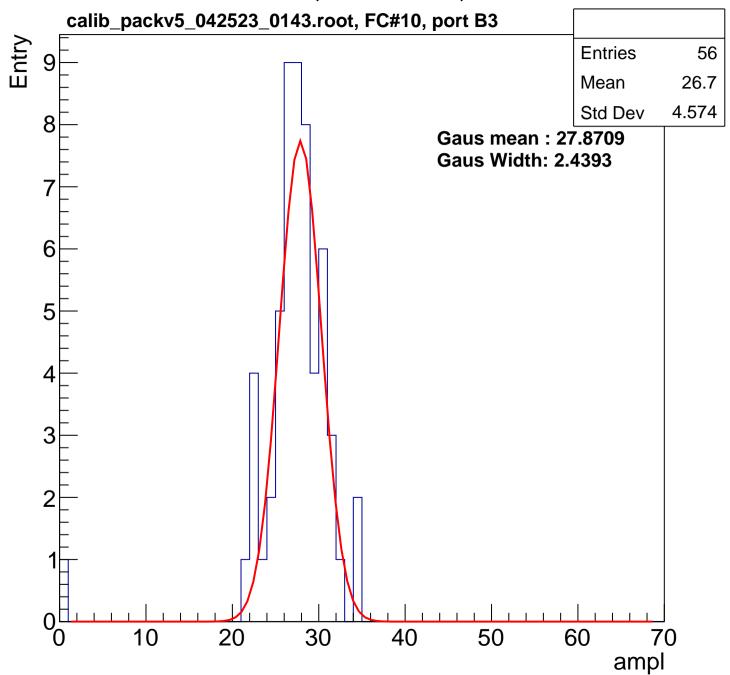


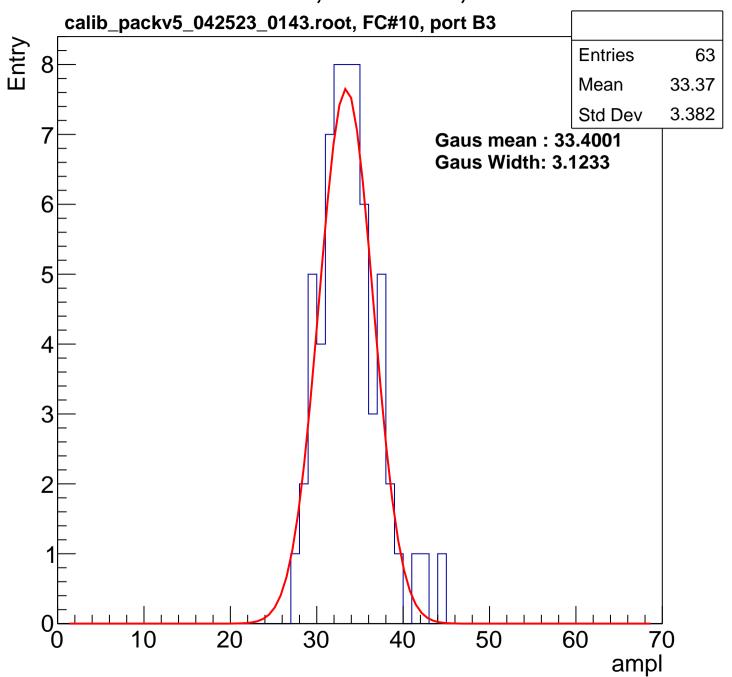


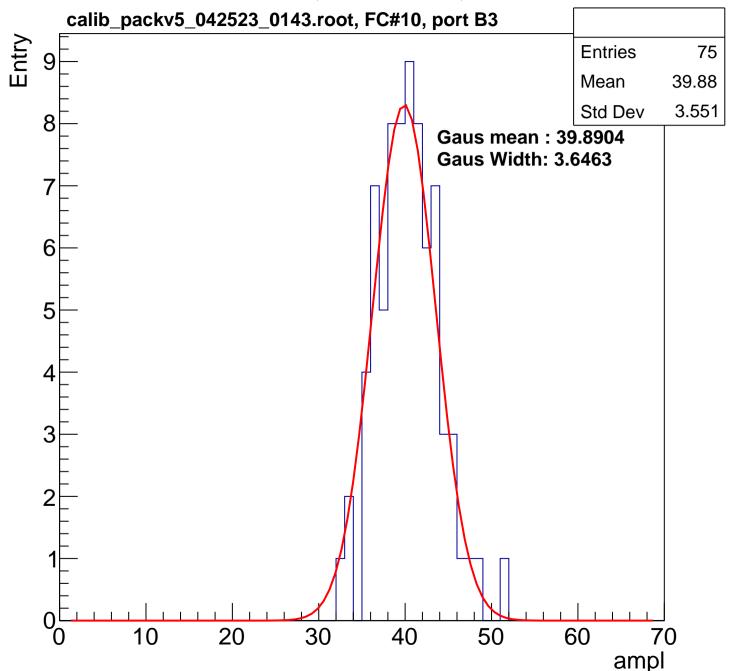


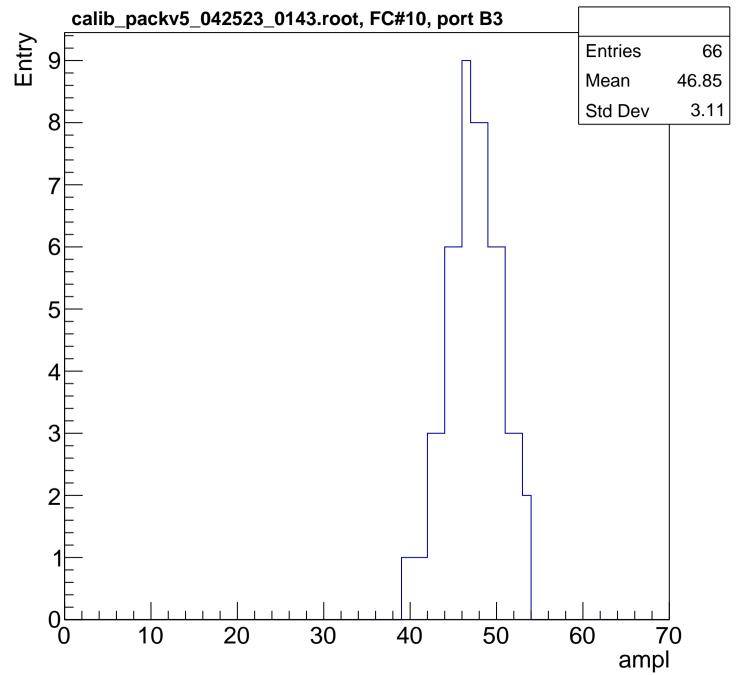


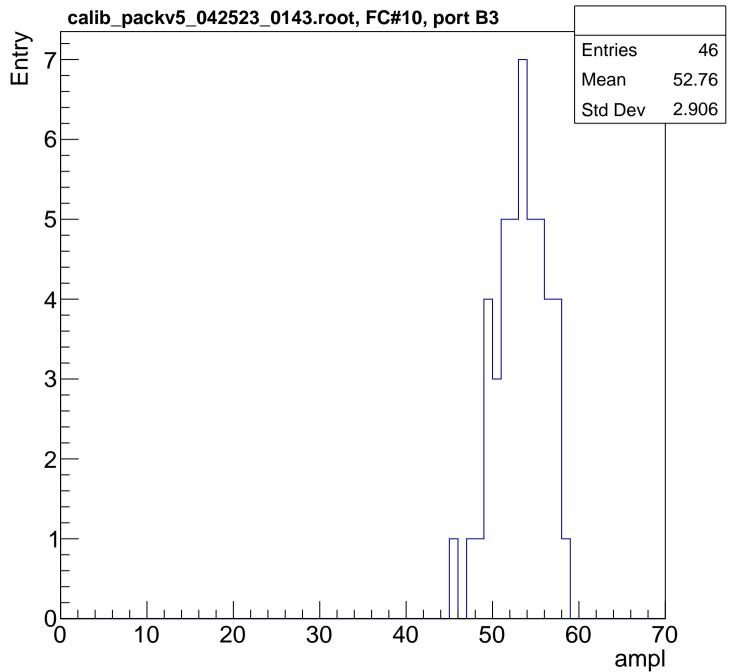


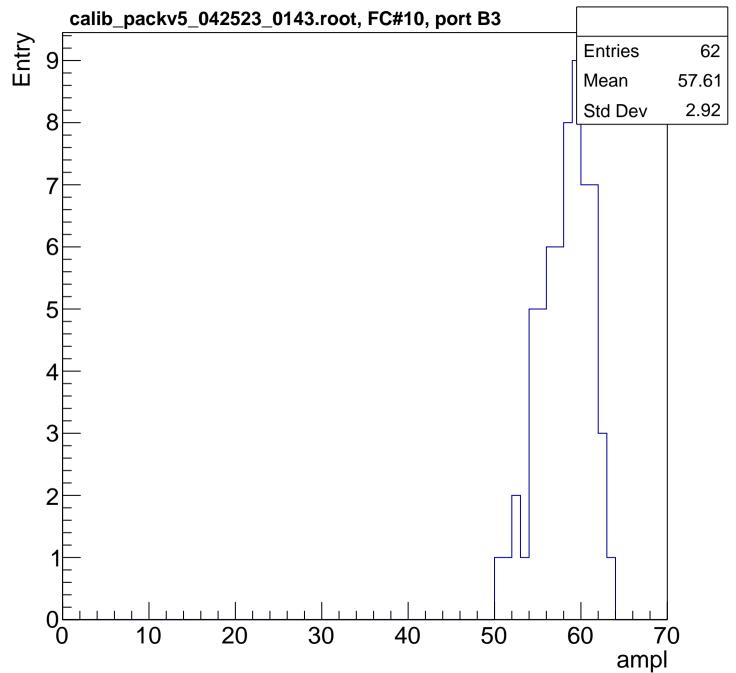


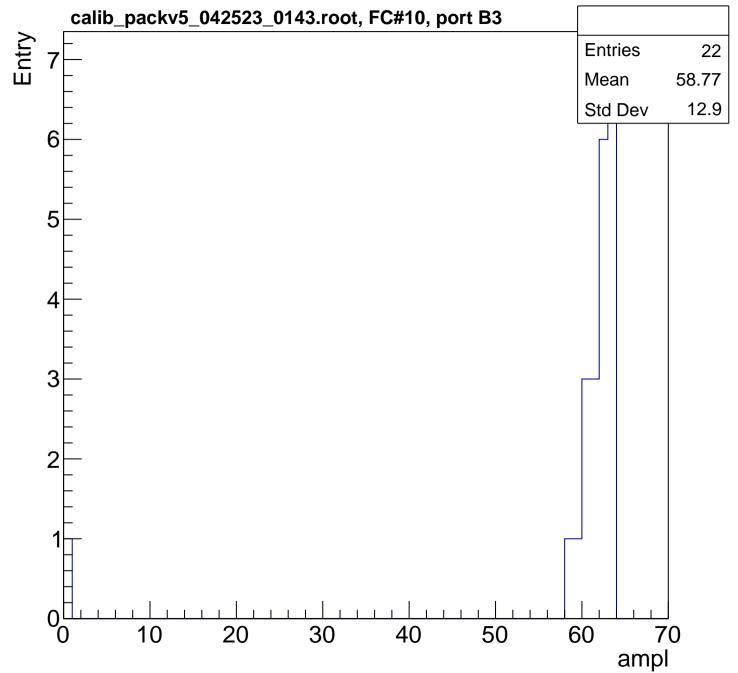


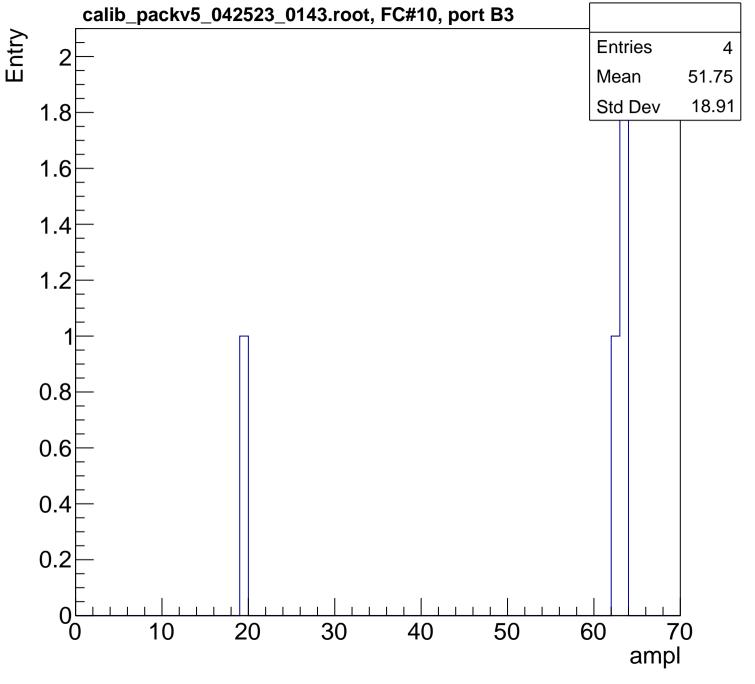


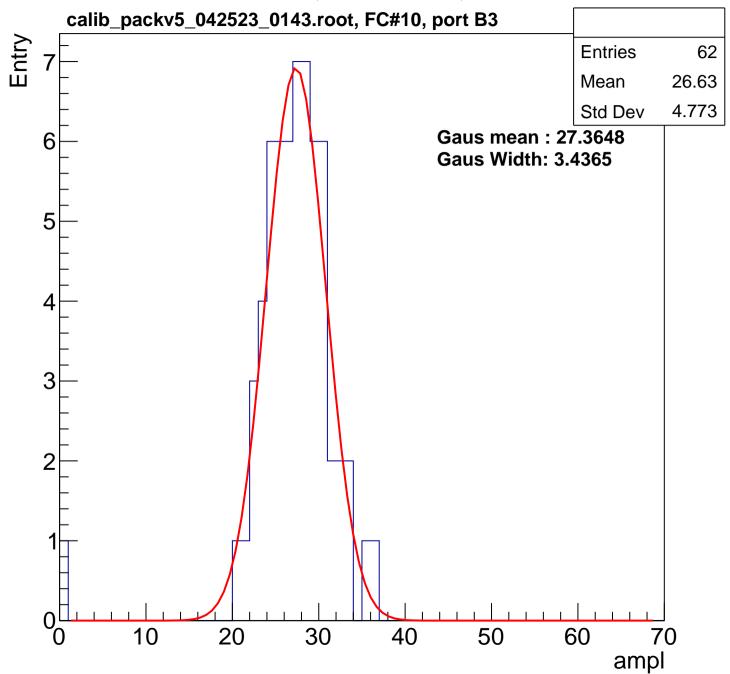


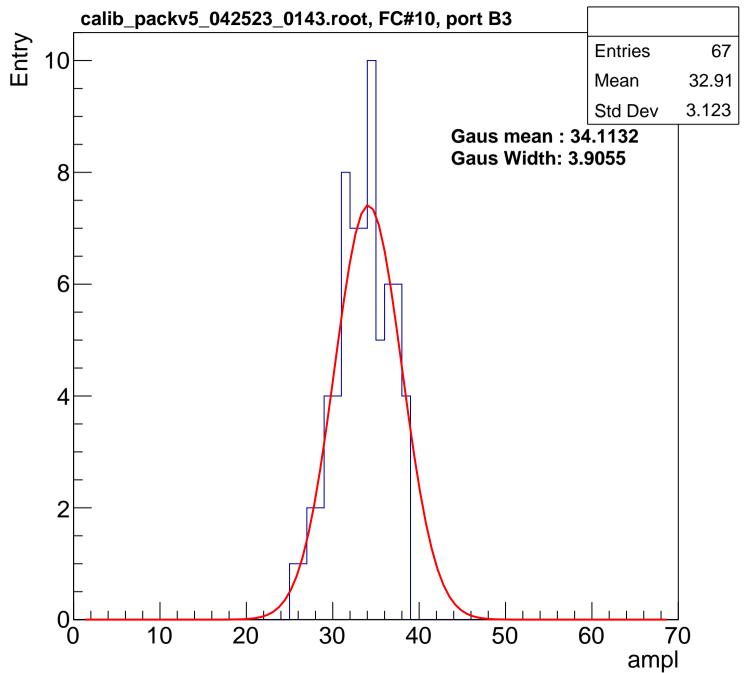


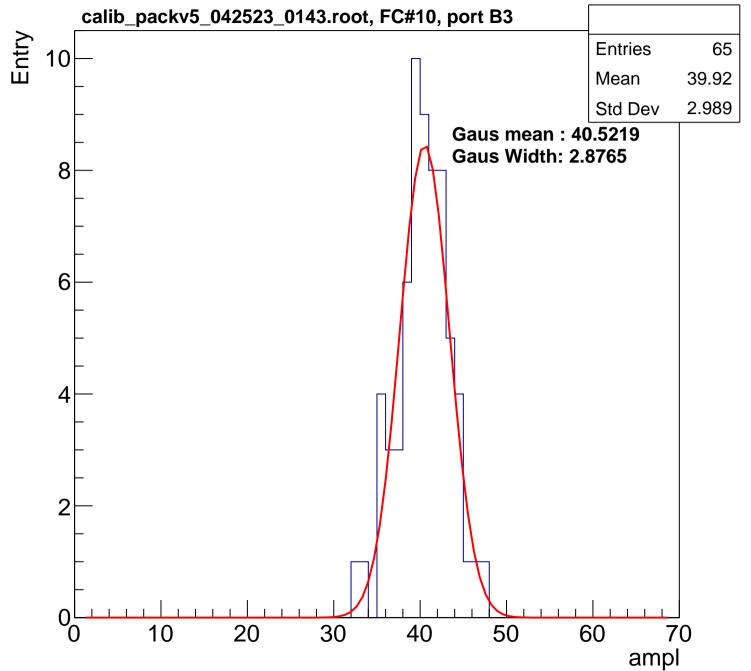


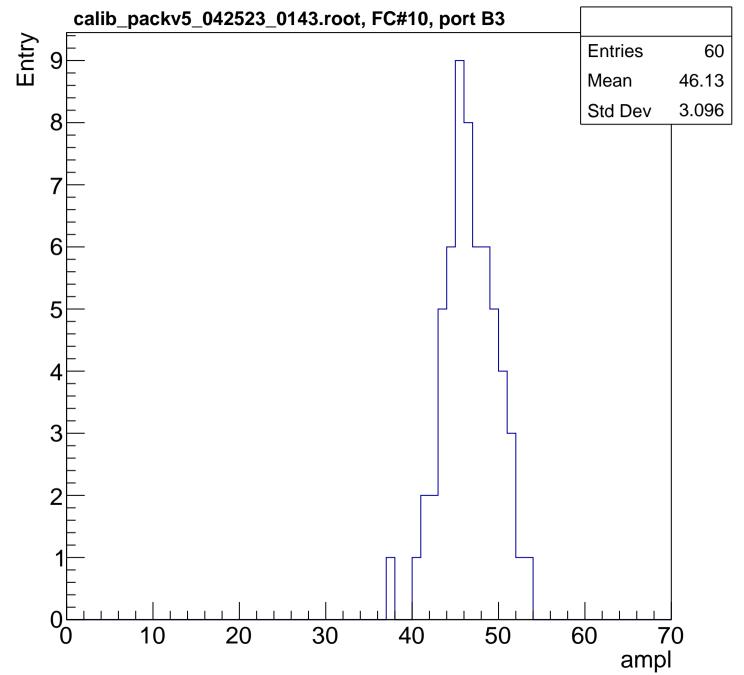


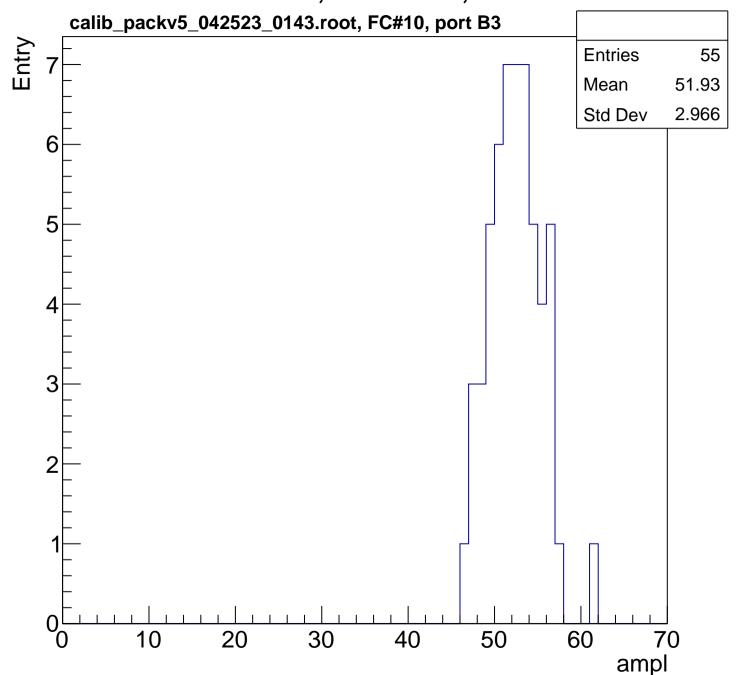


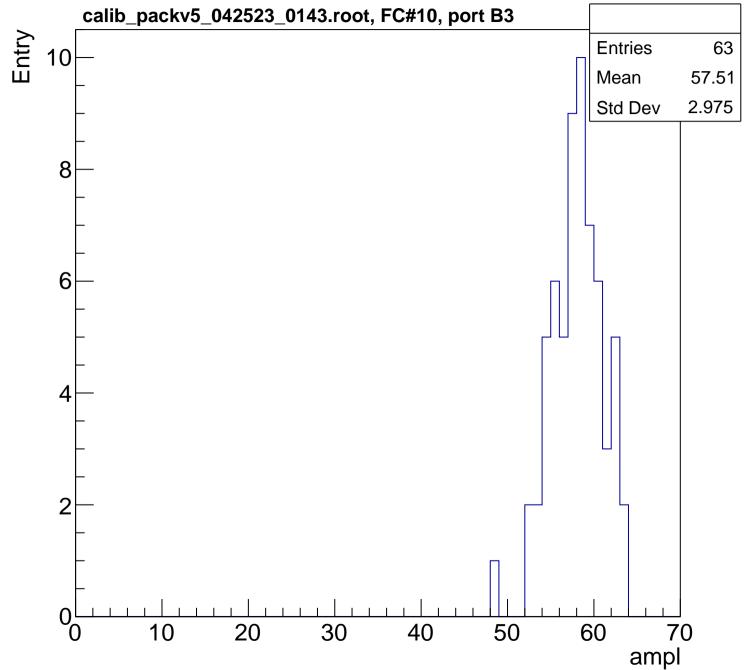


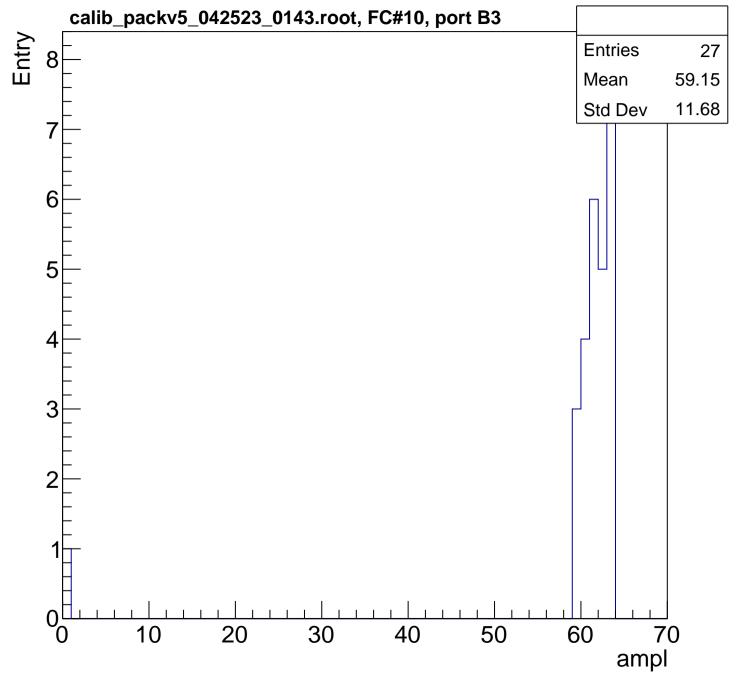




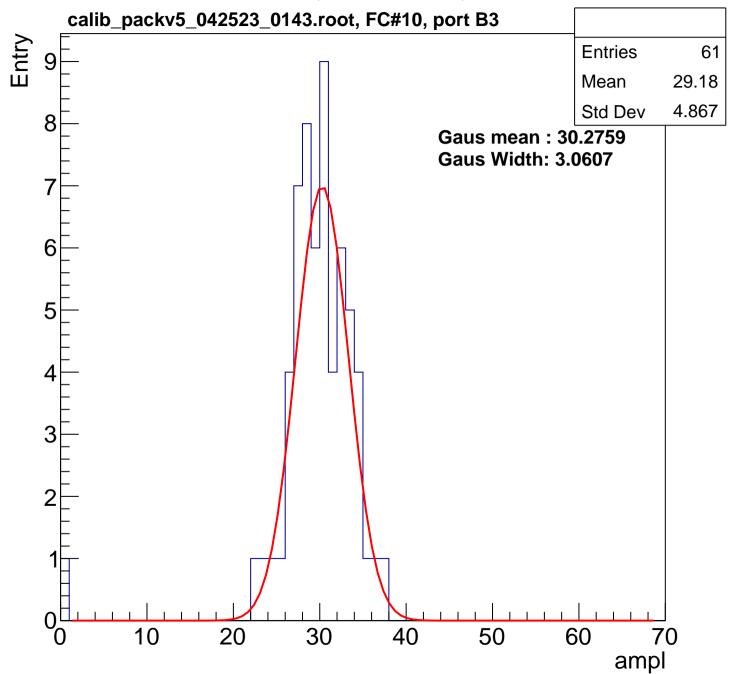


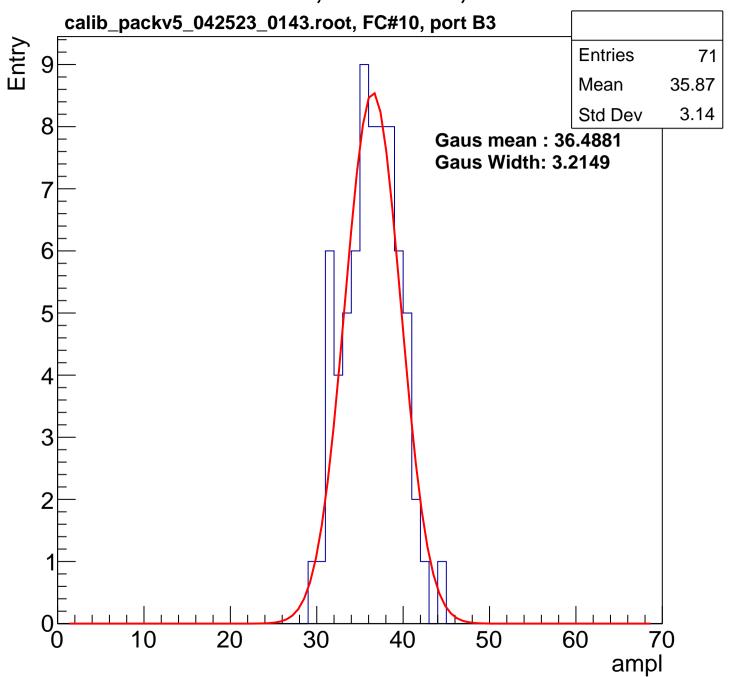


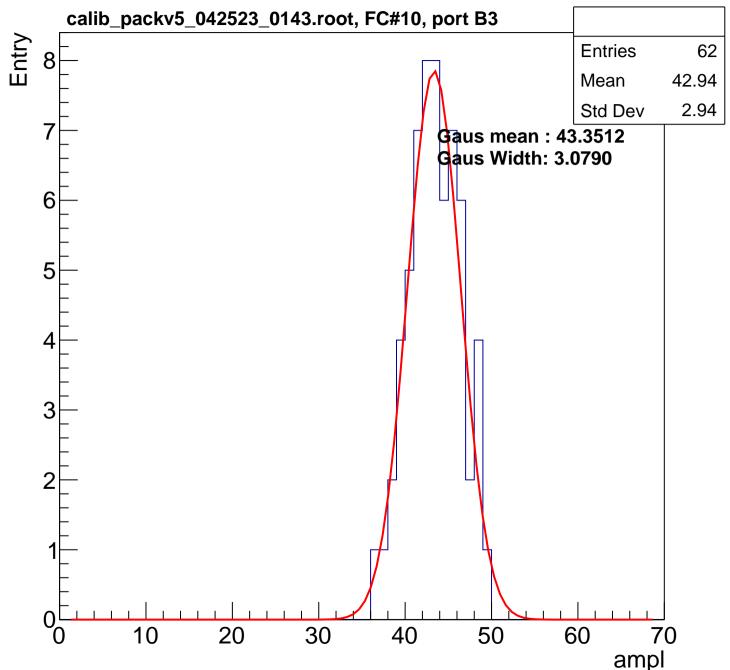


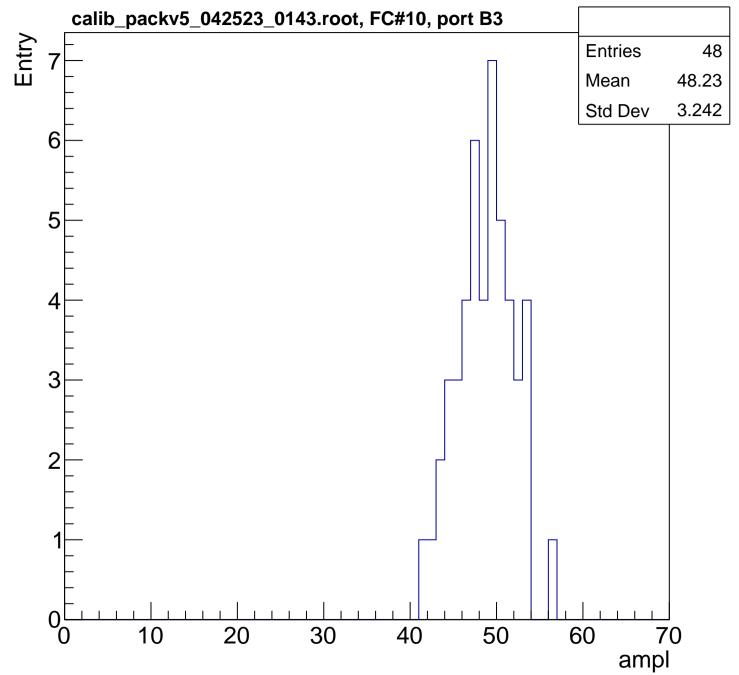


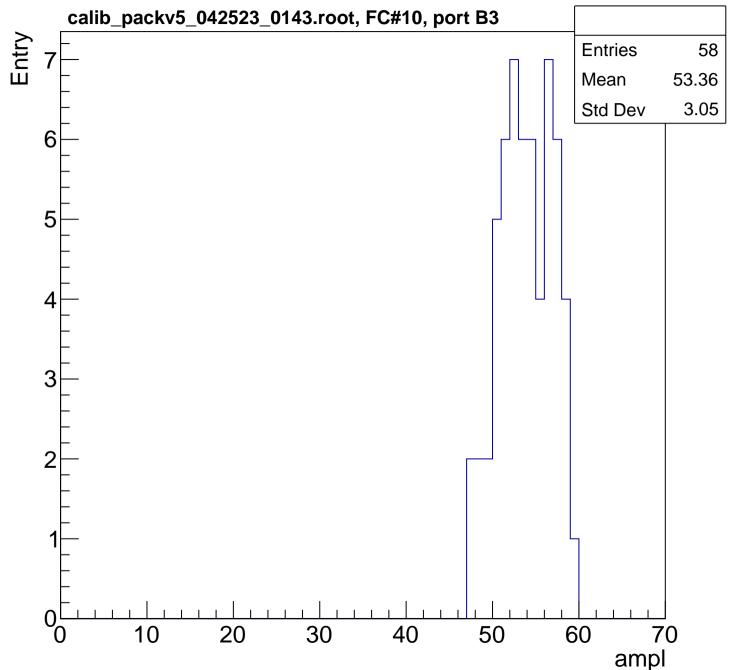
B1L002S, U9-ch73, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

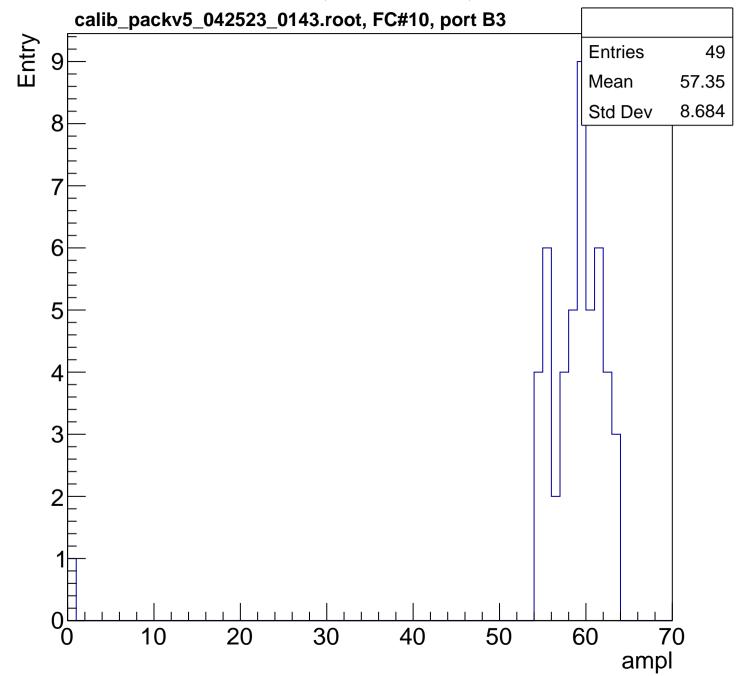


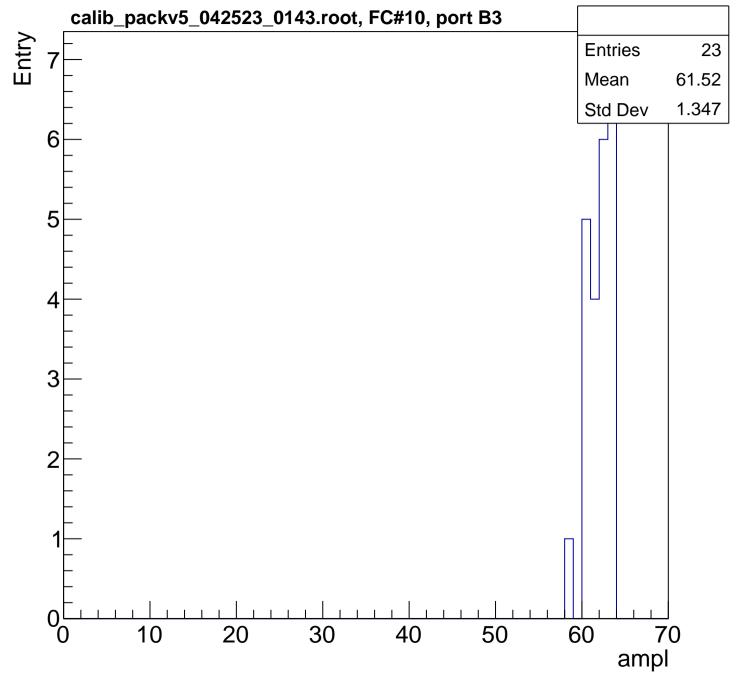




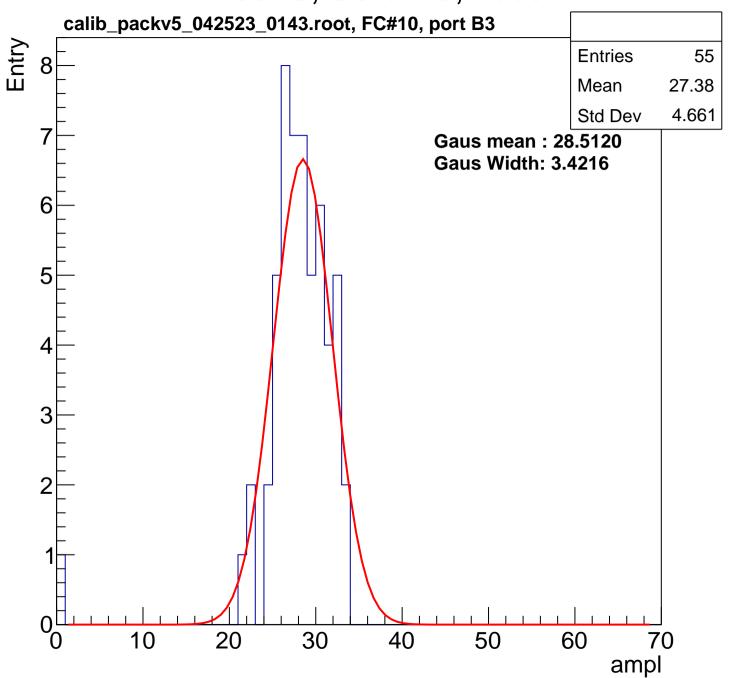


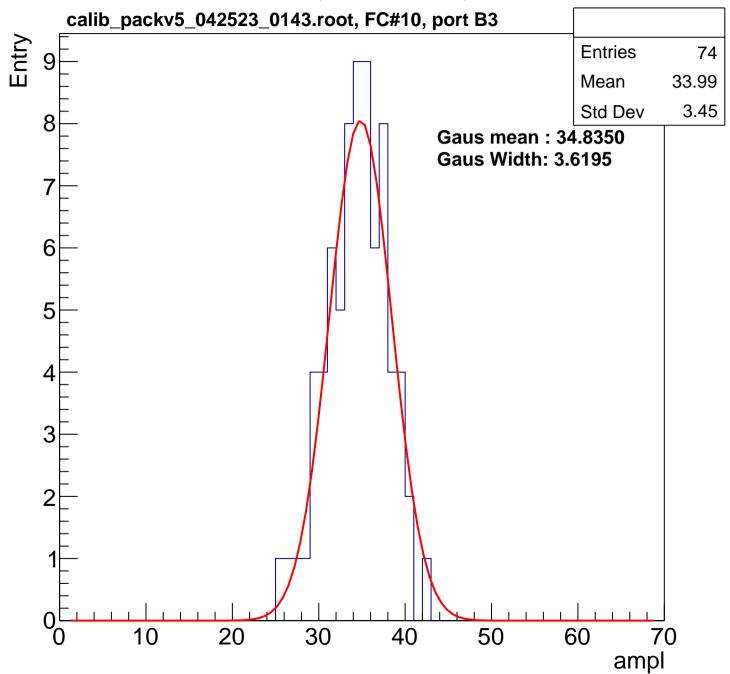


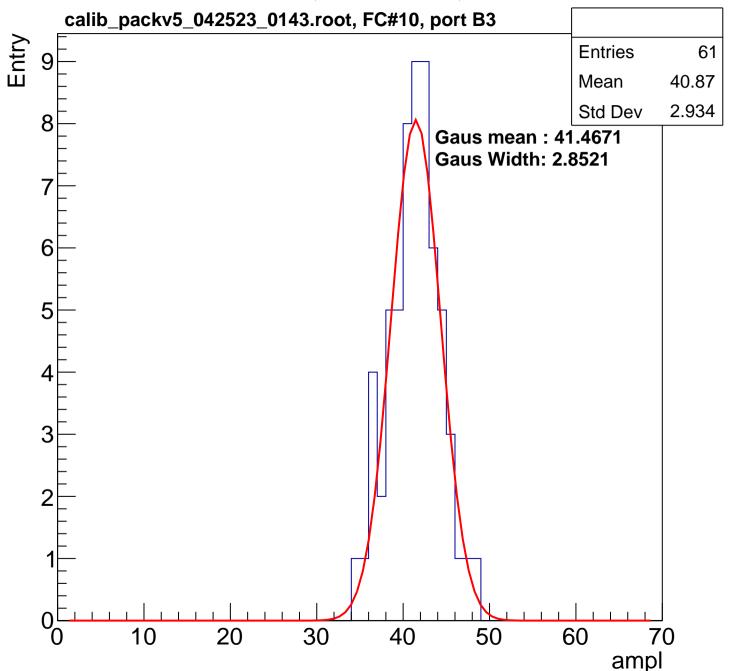


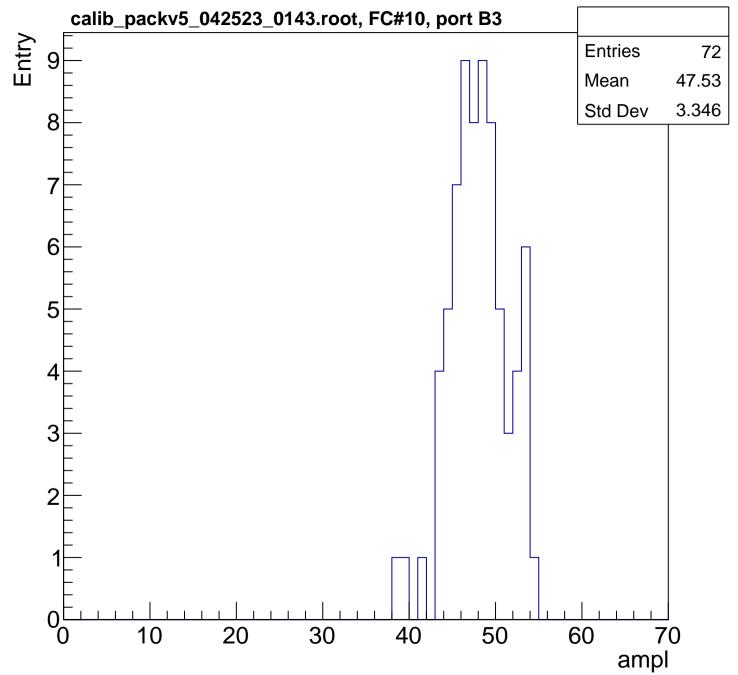


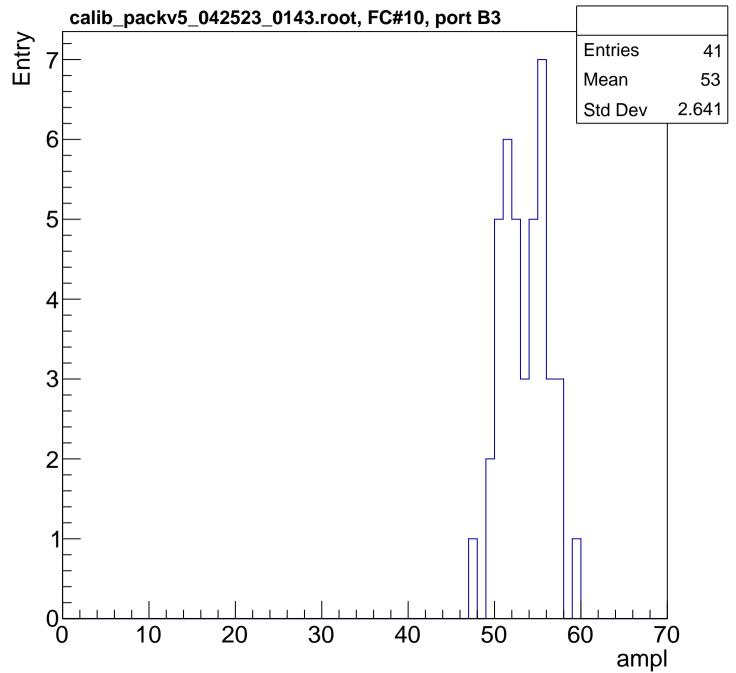


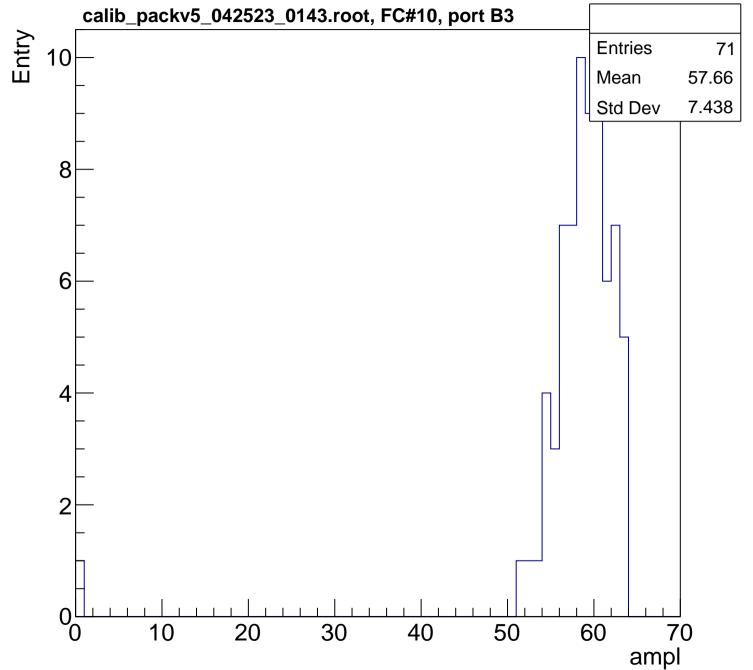


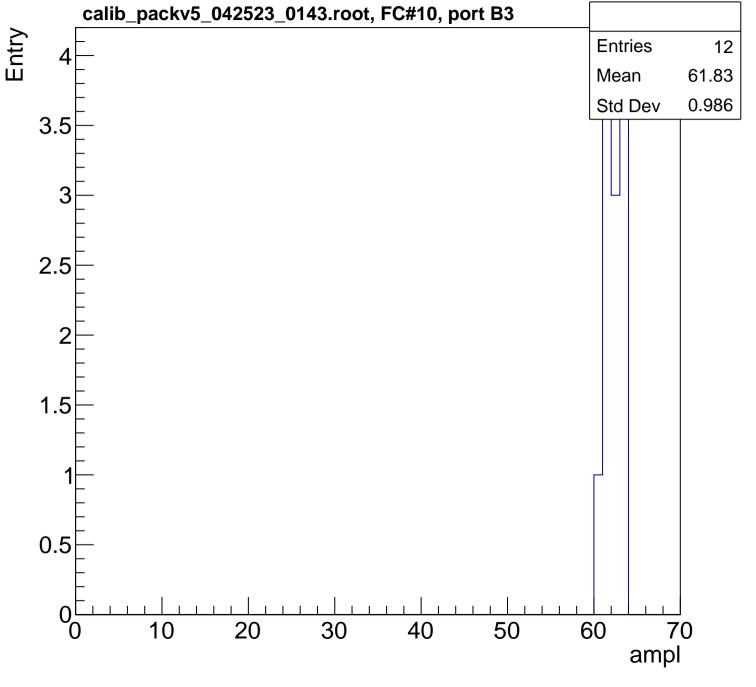




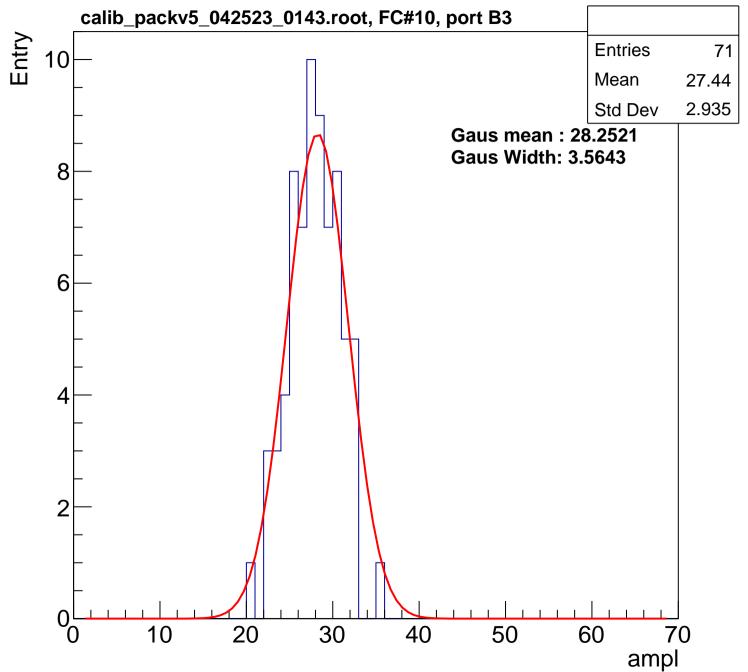


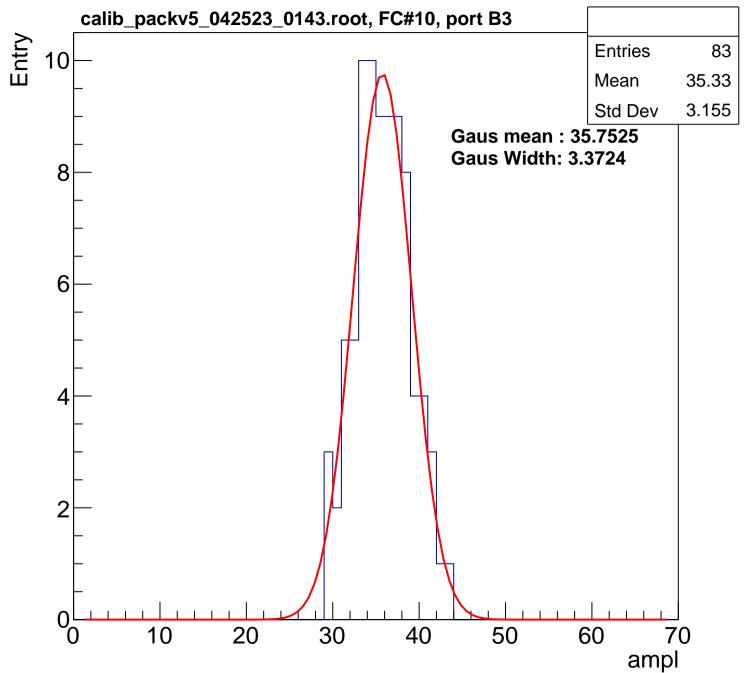


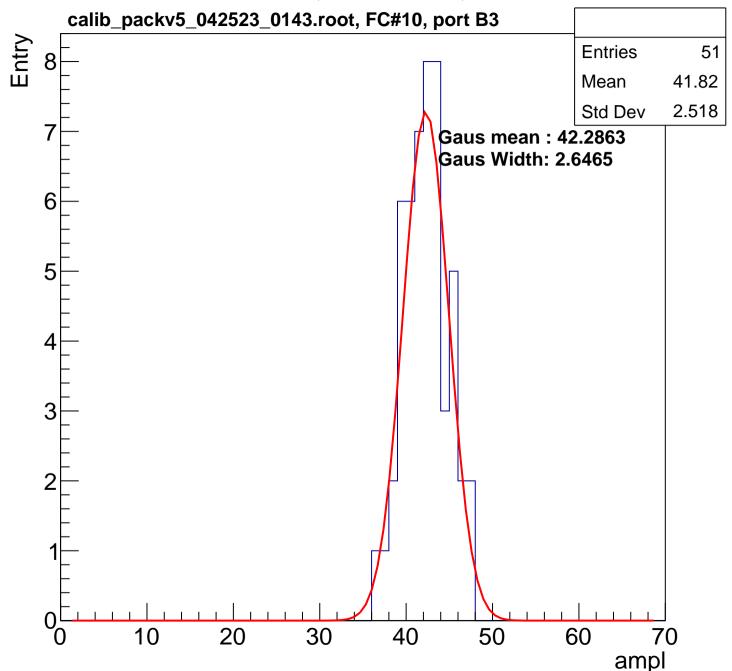


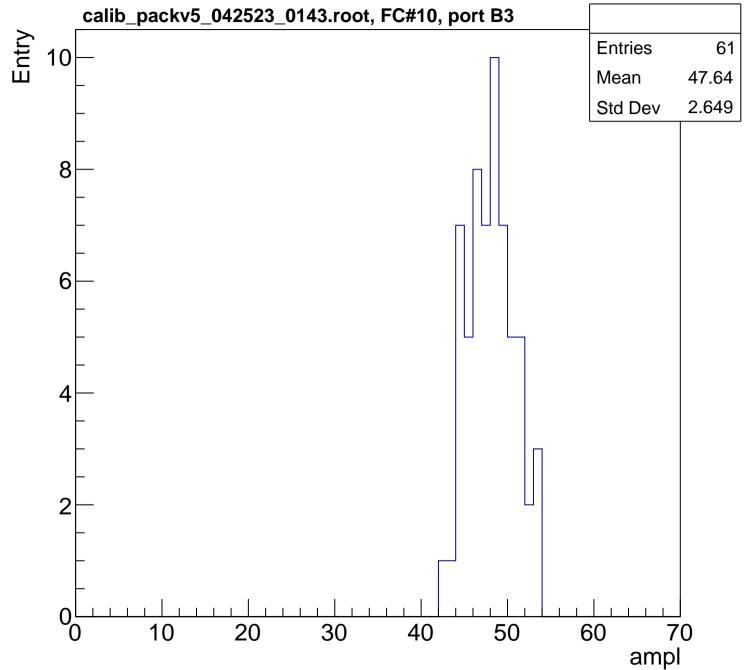


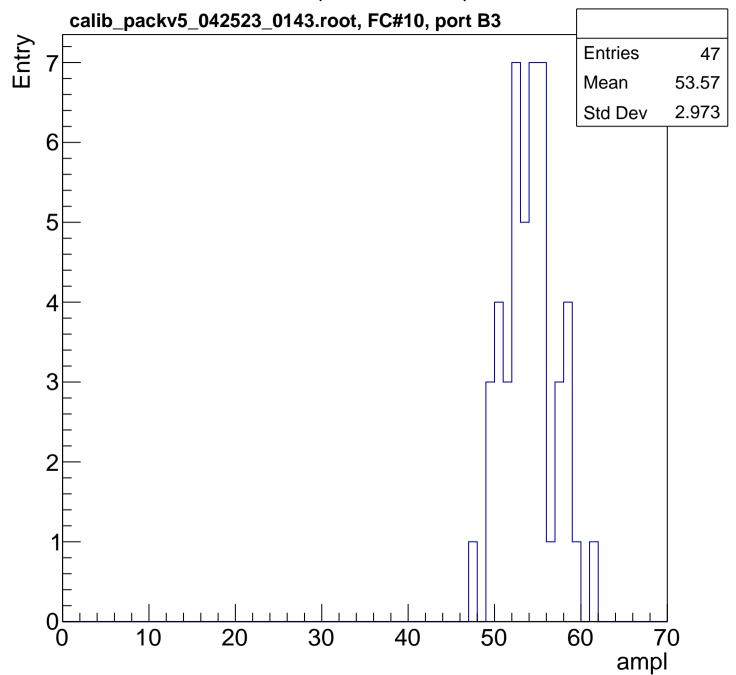


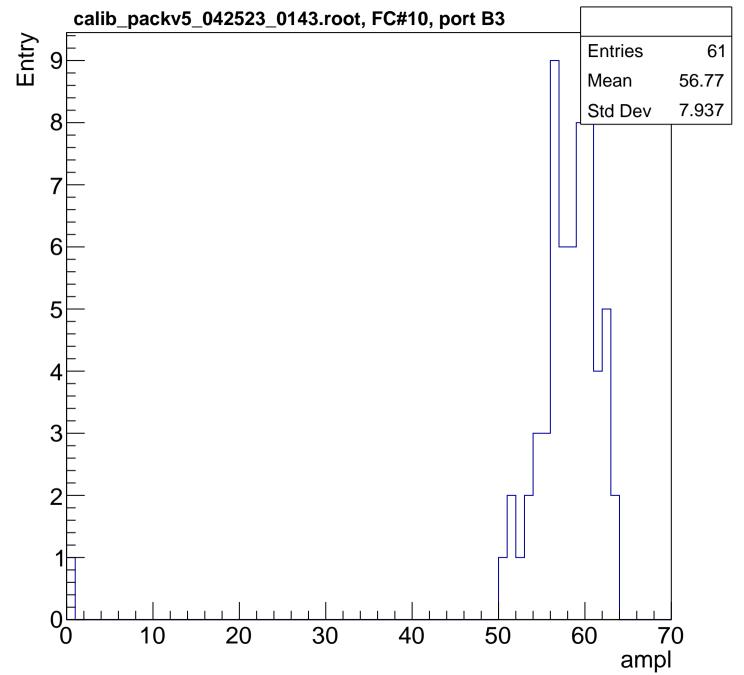


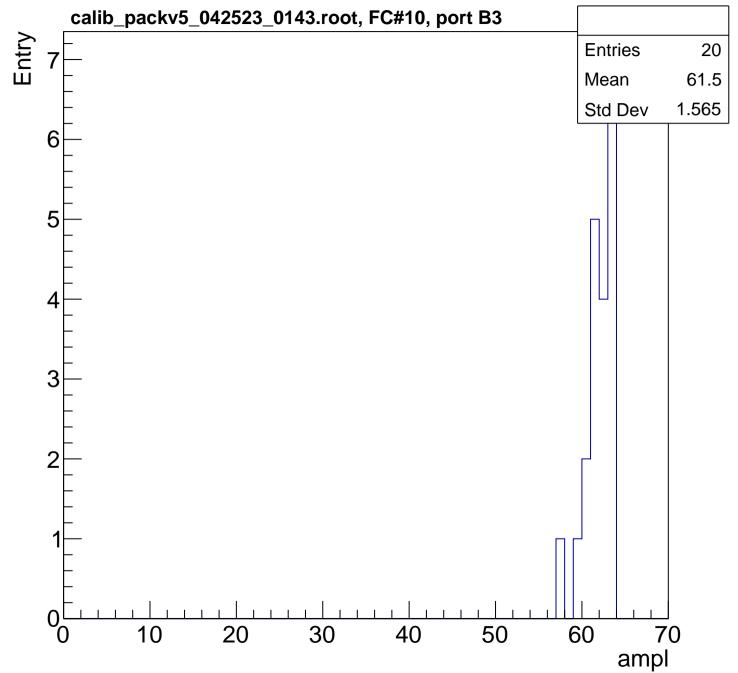


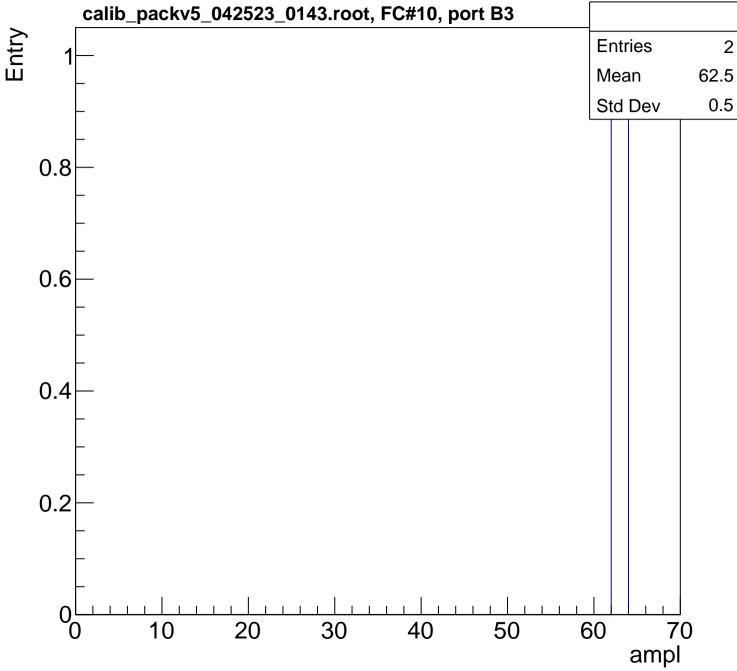


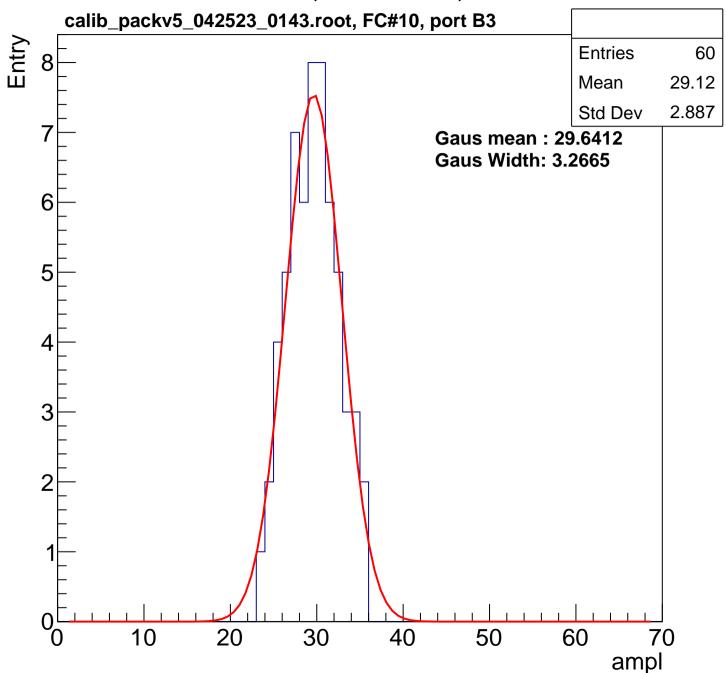


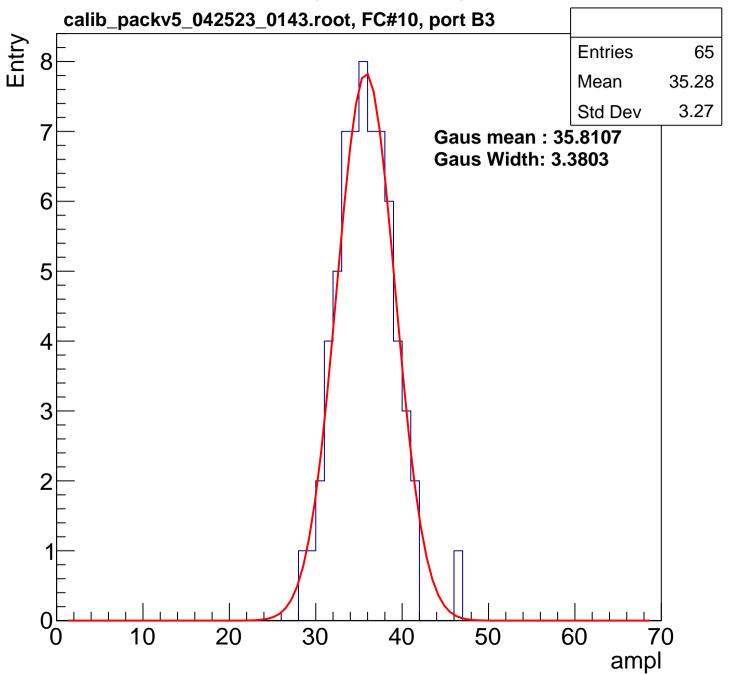


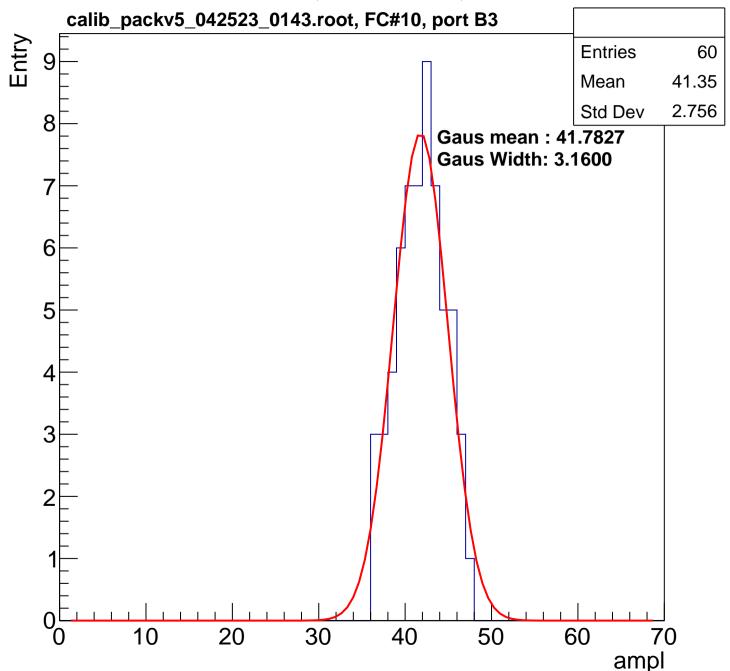


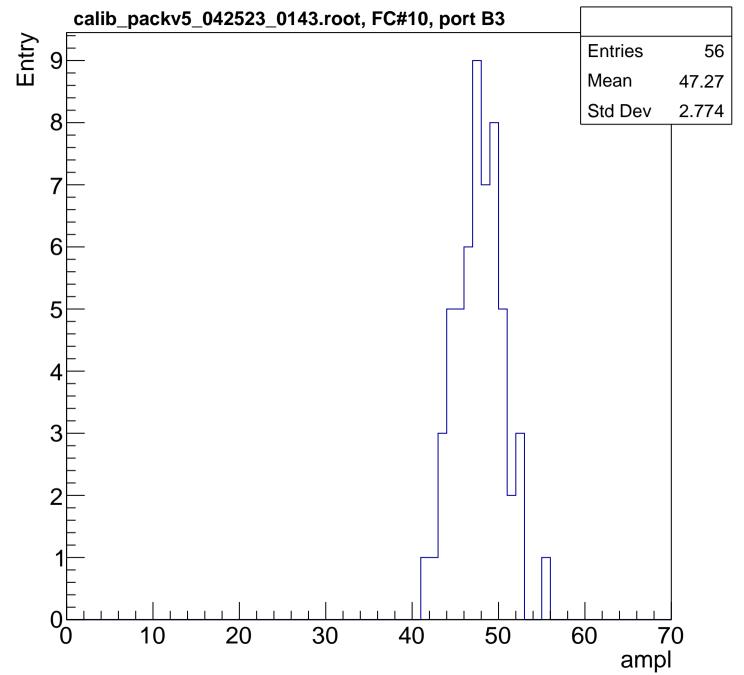


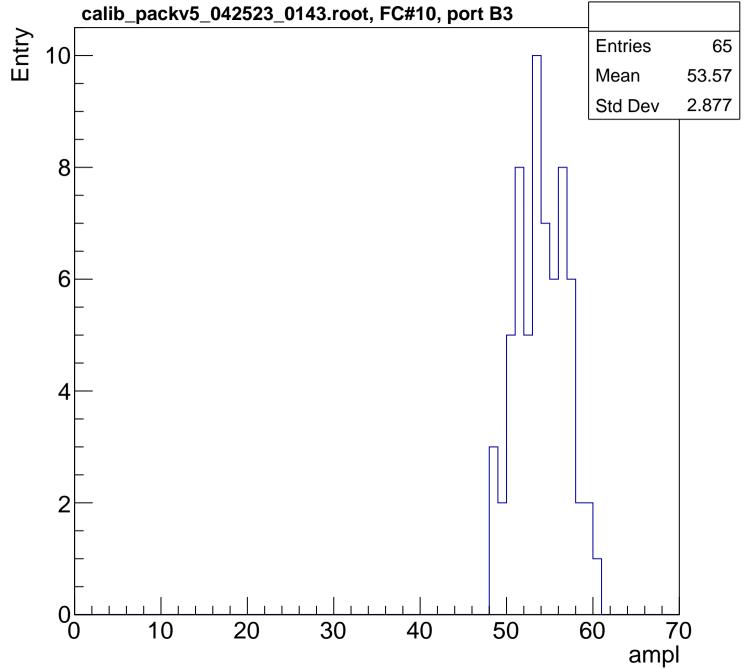


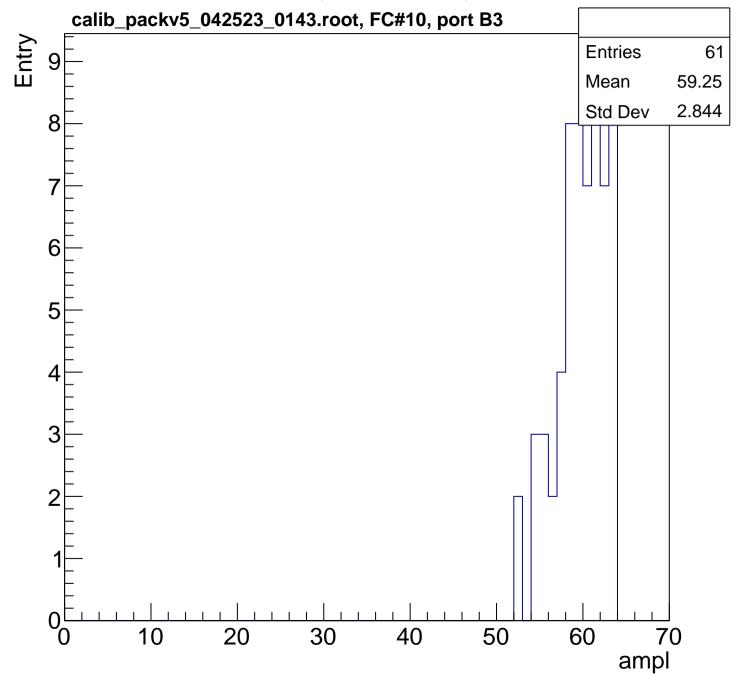


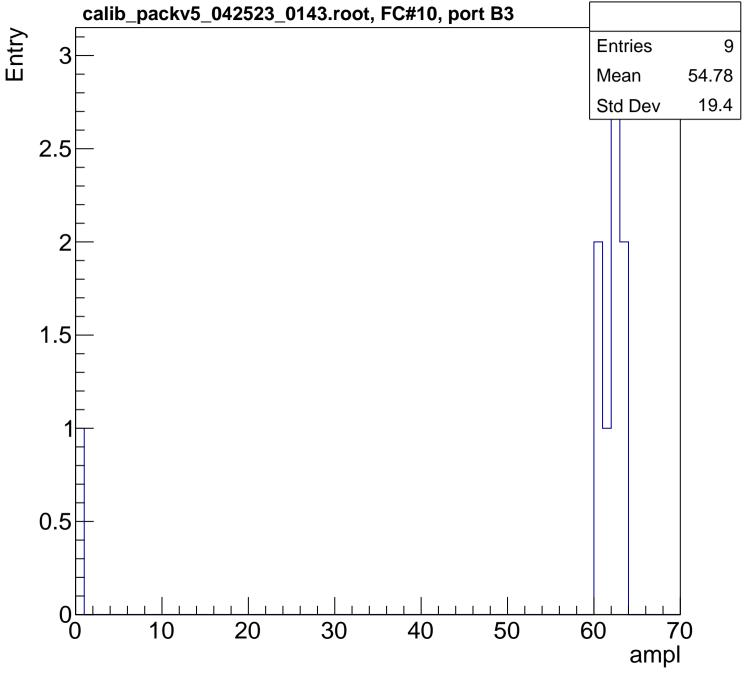




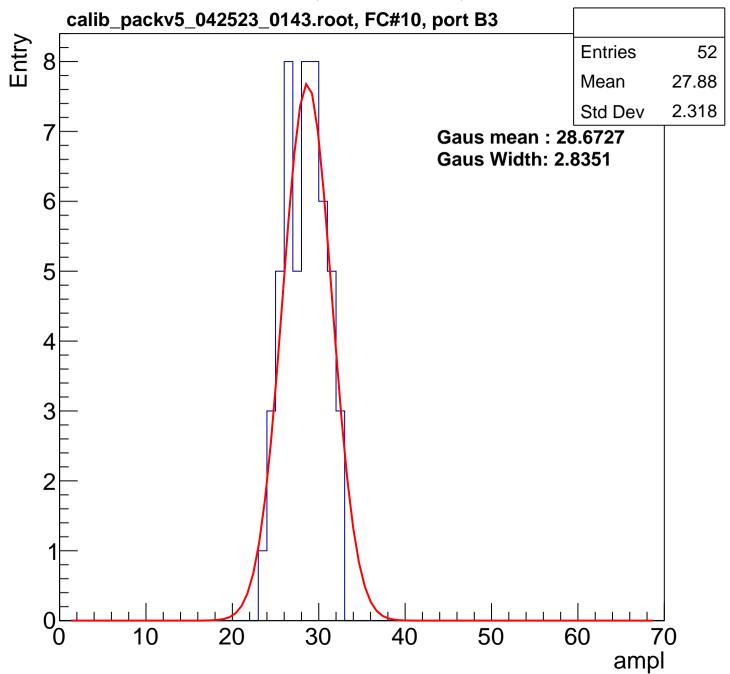


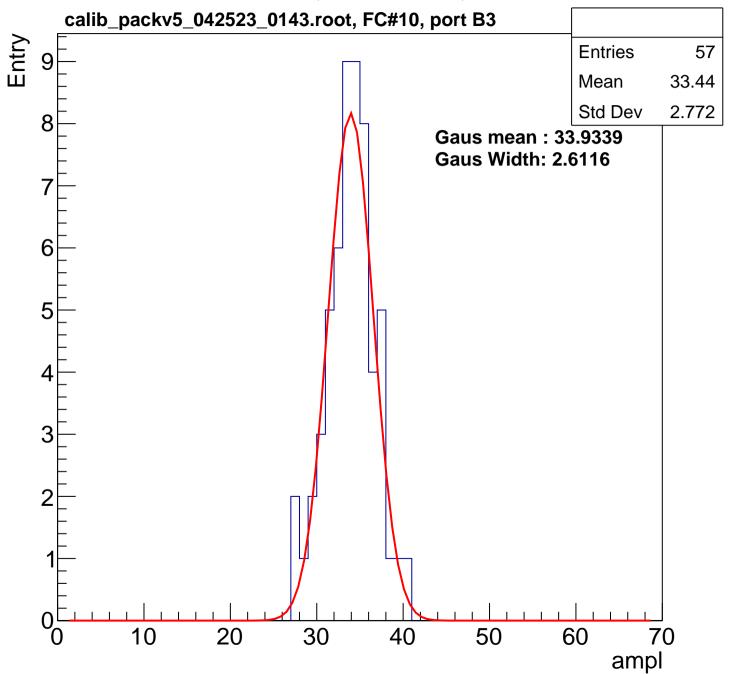


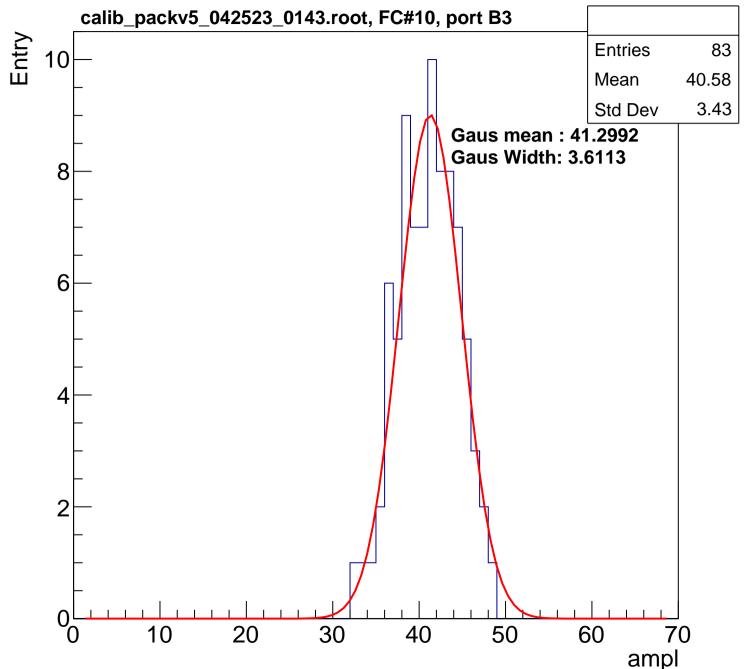


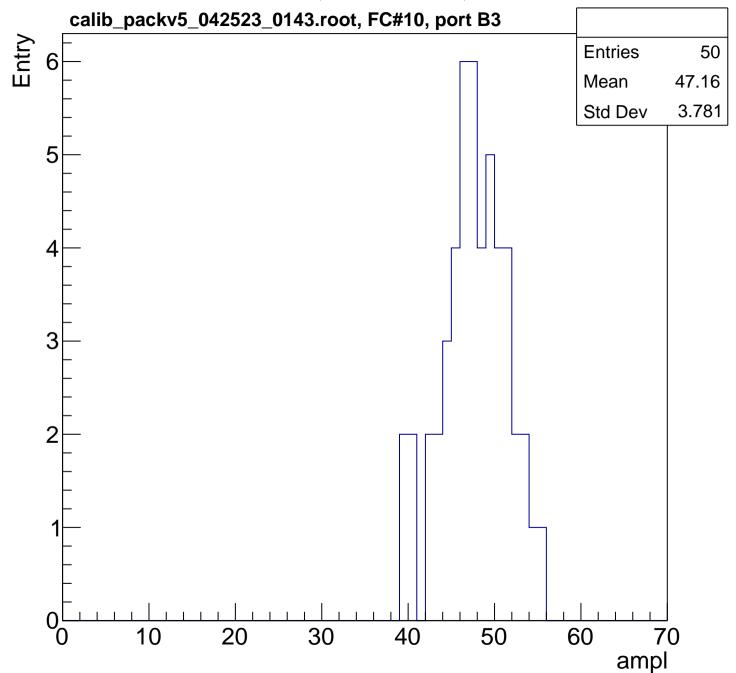


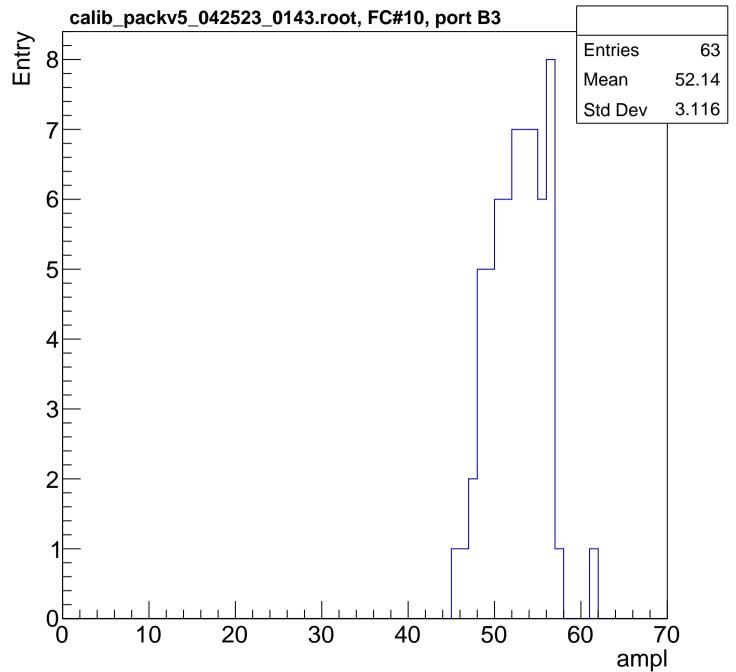


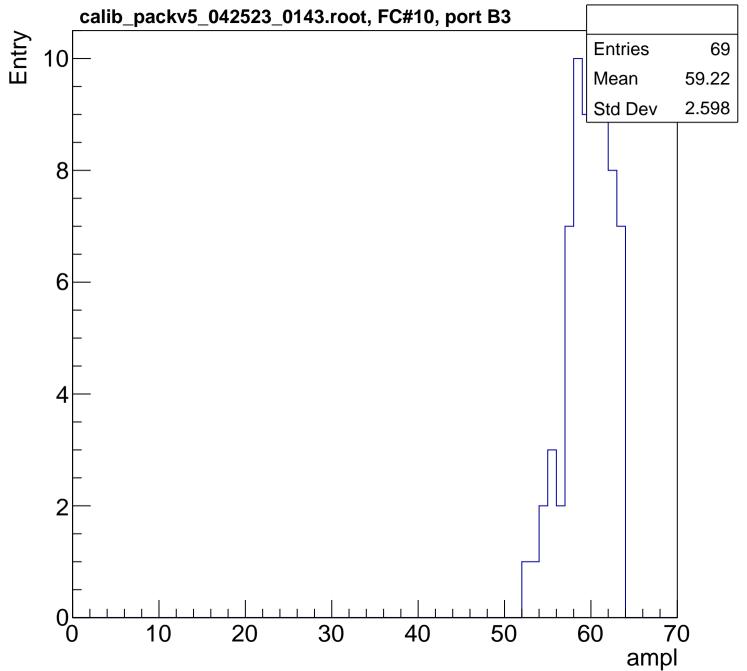


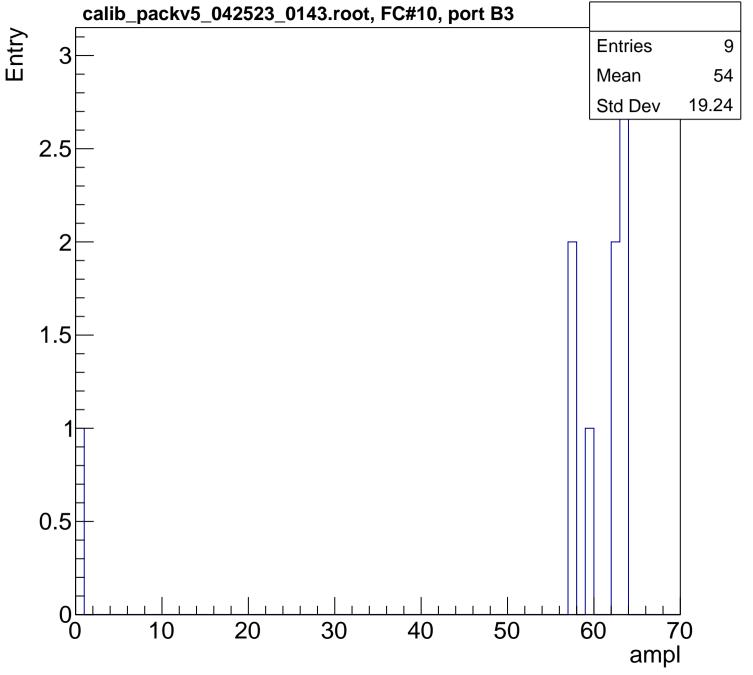




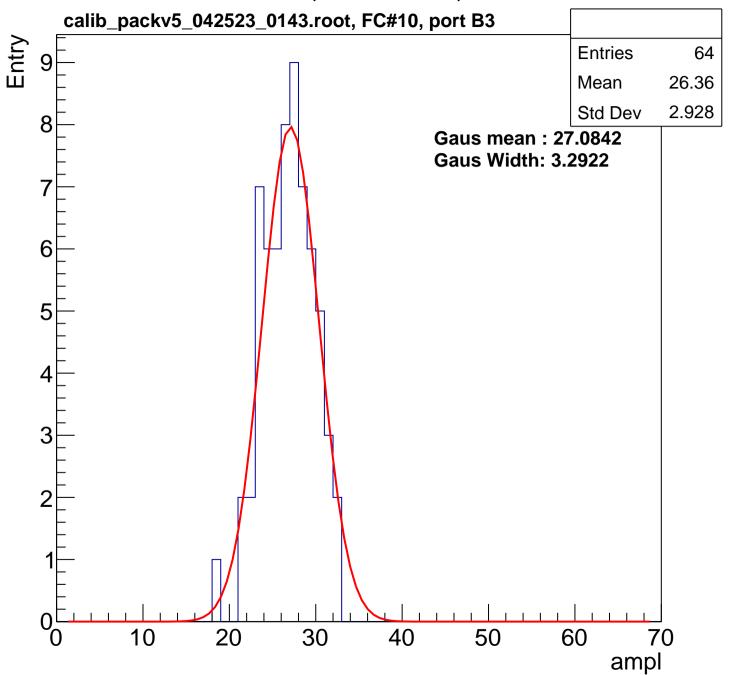


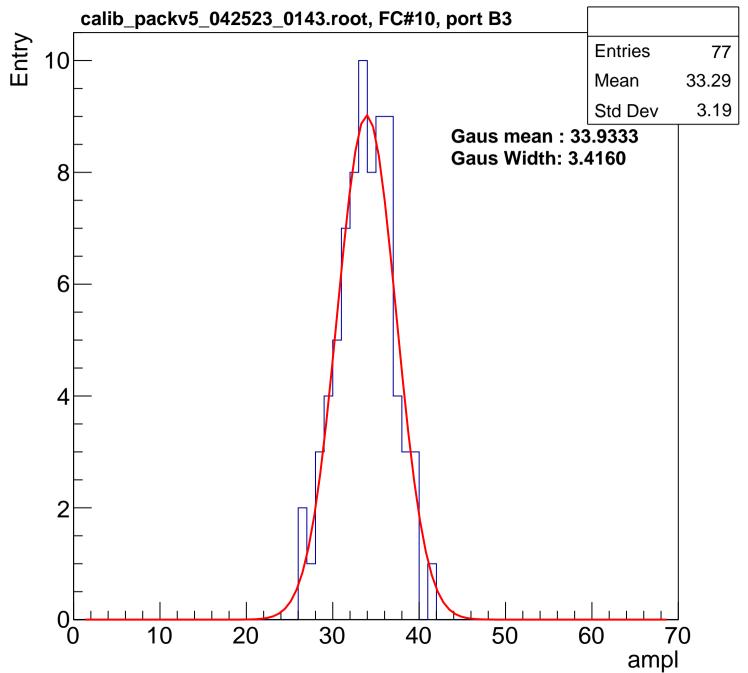


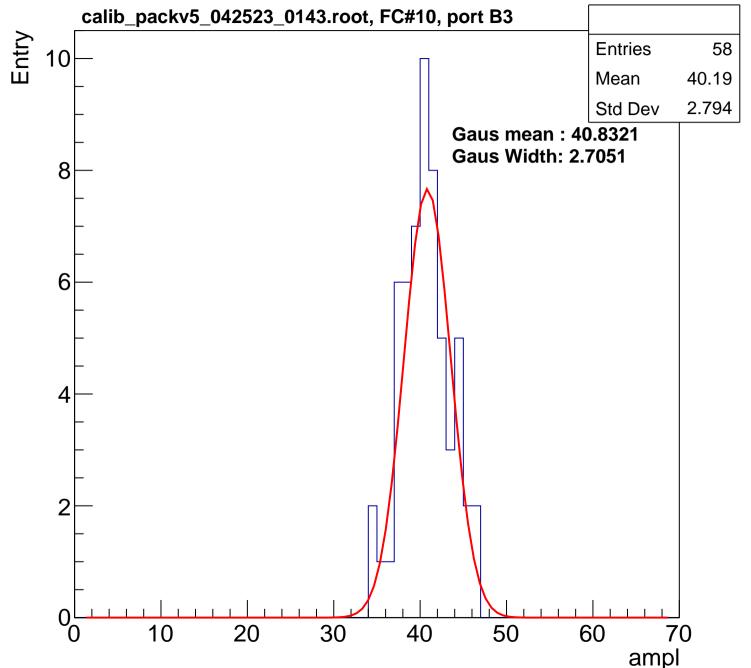


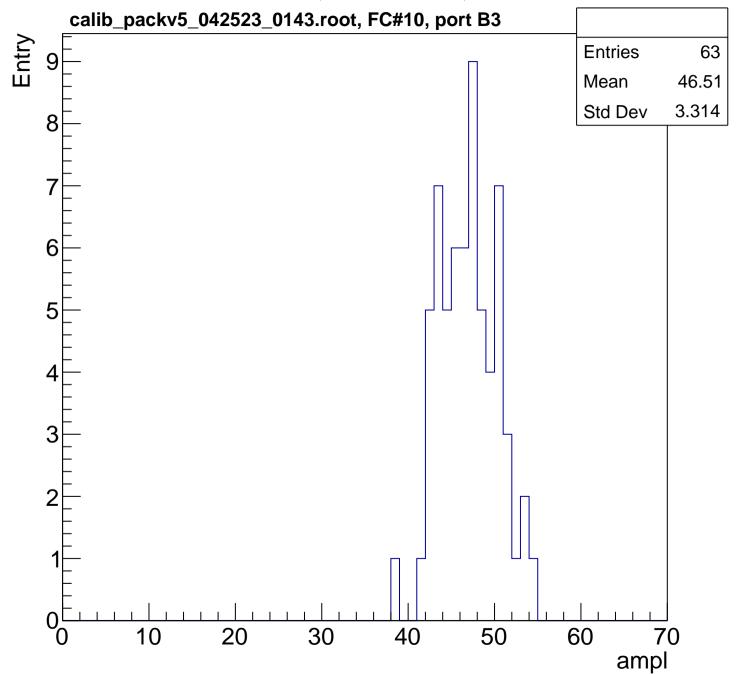


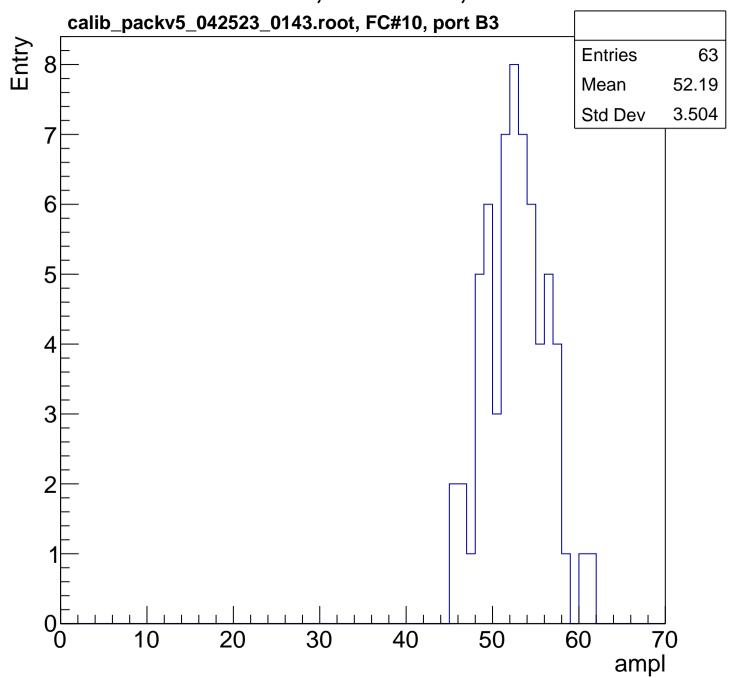
B1L002S, U9-ch78, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

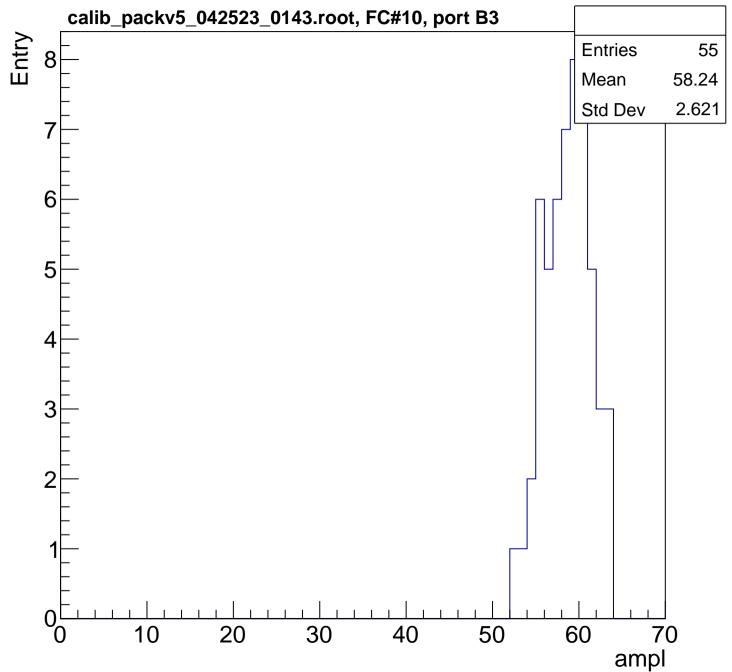


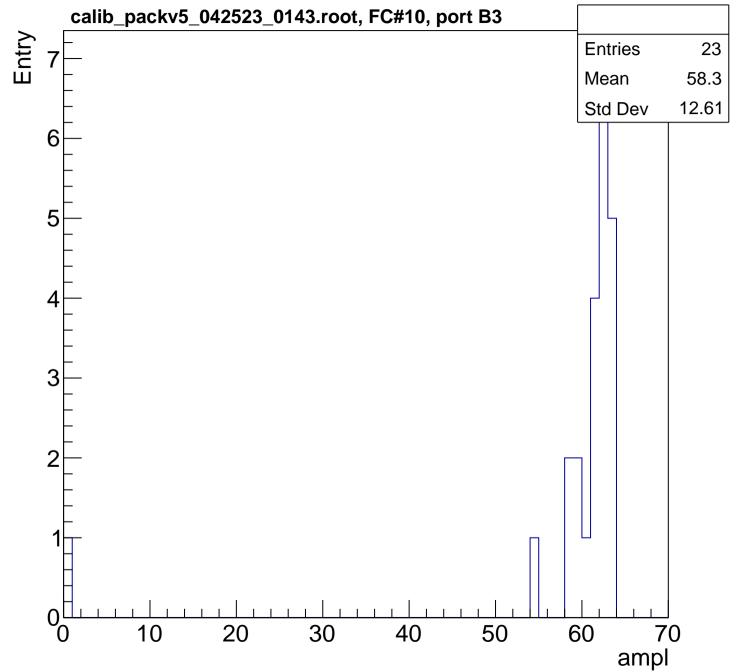


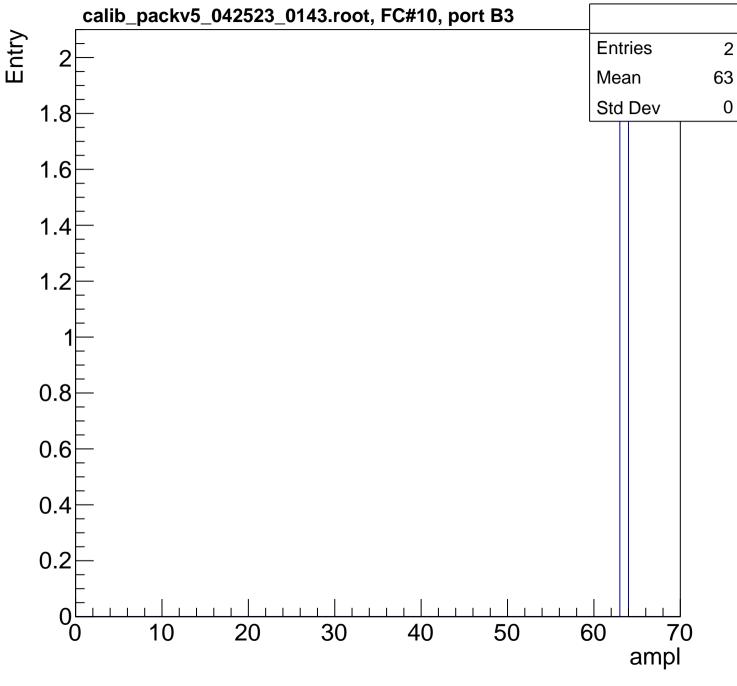


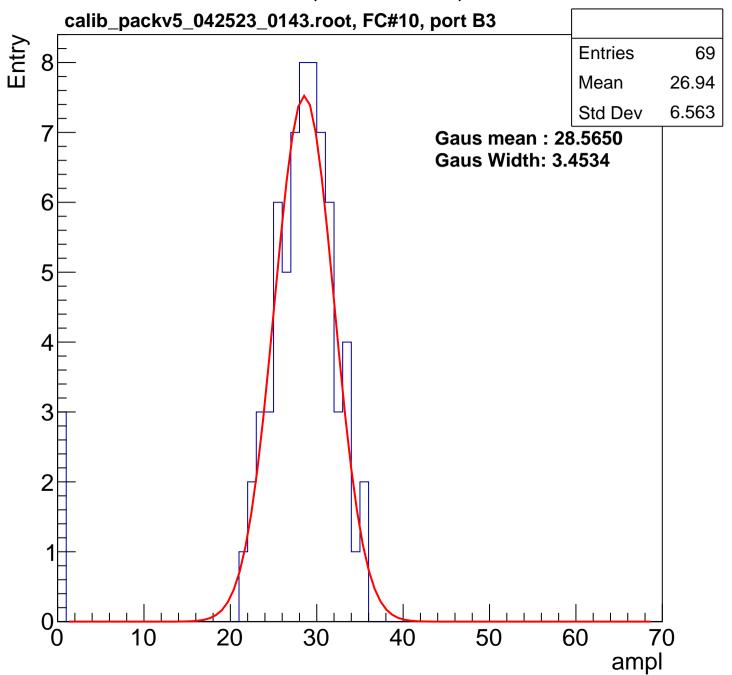


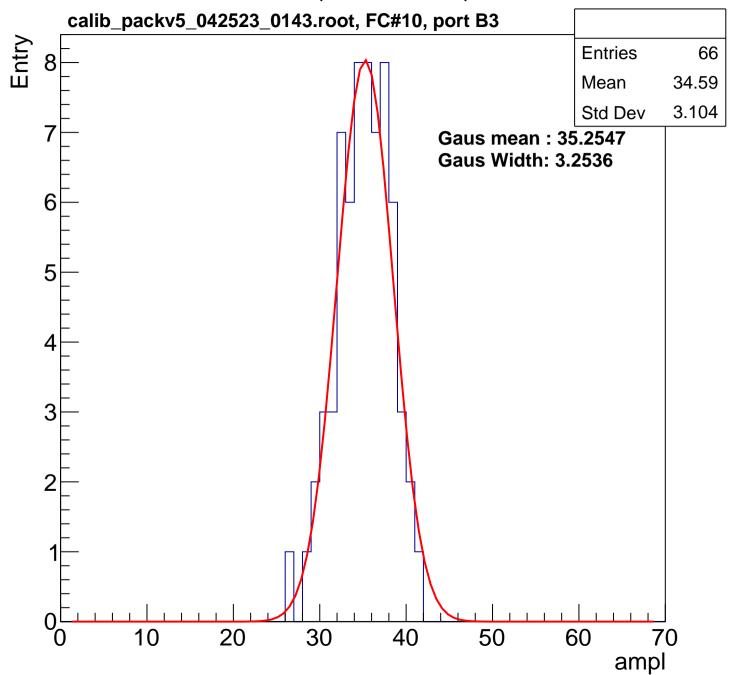


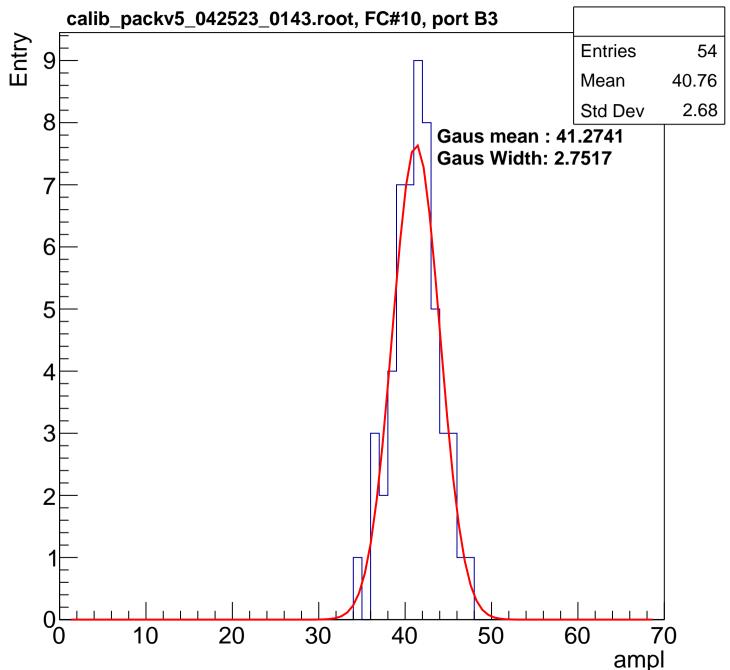


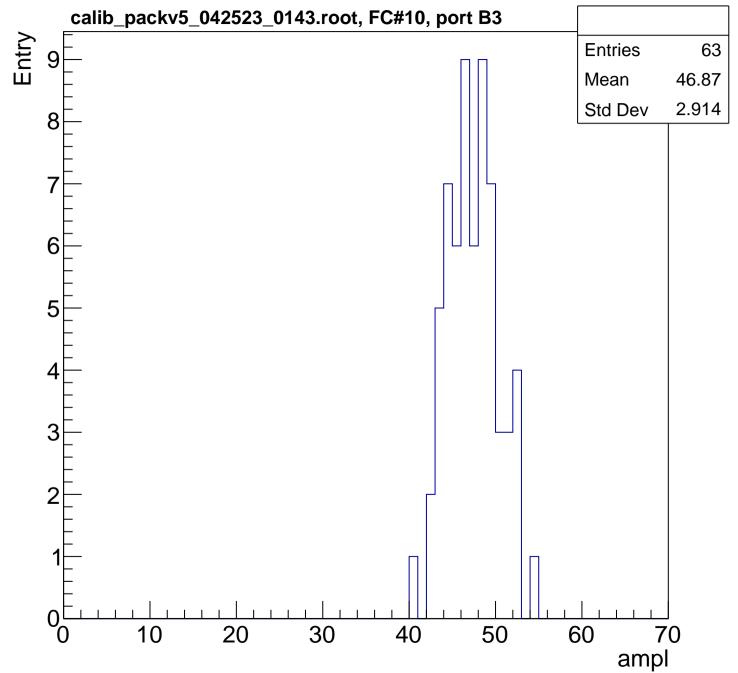


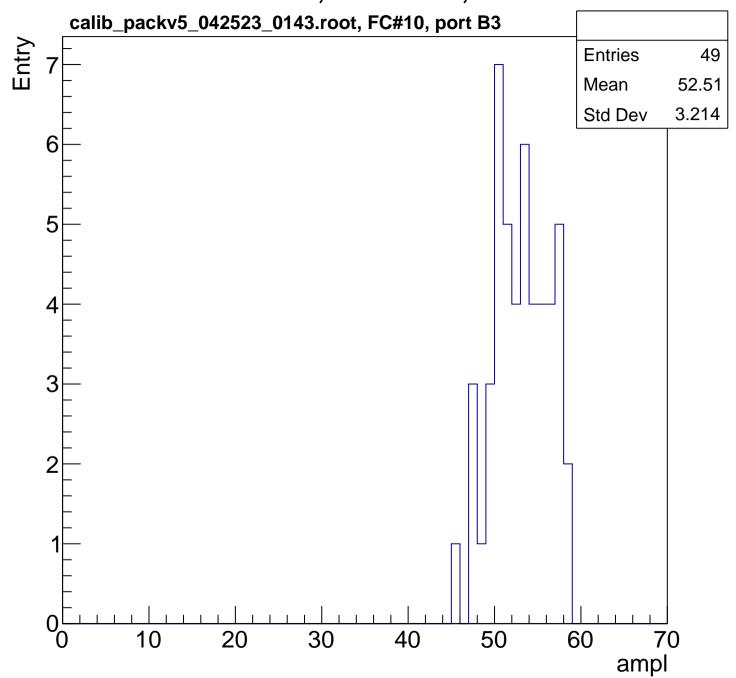


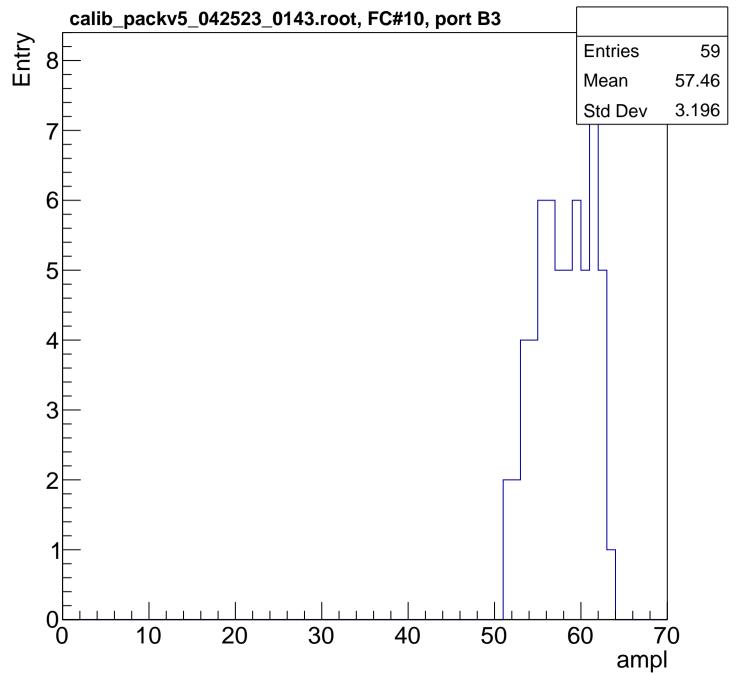


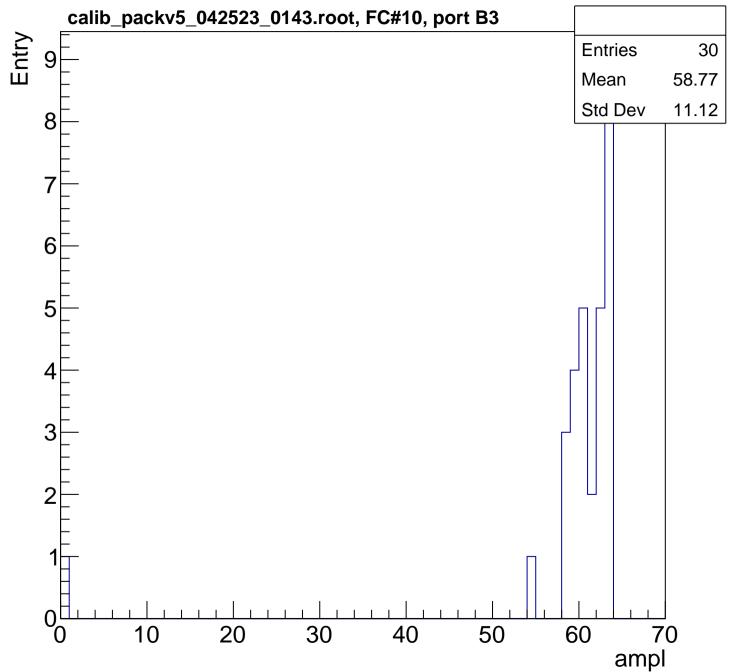


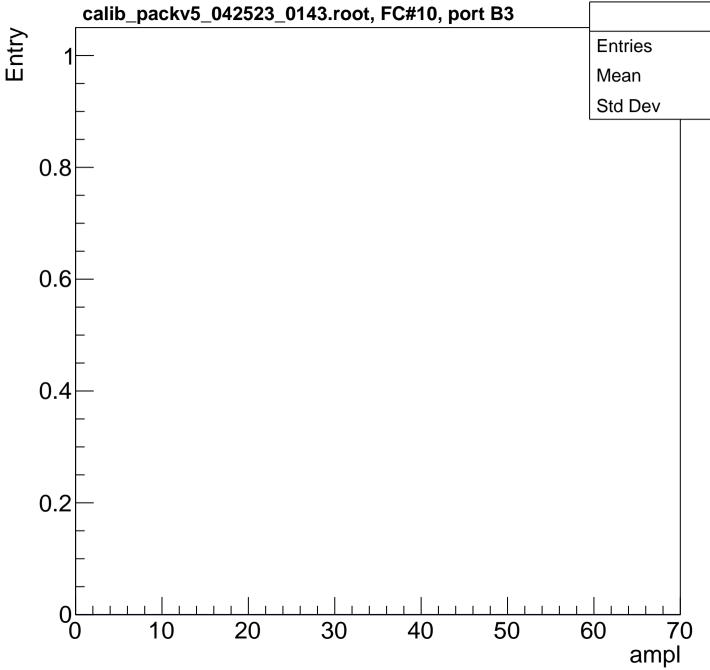


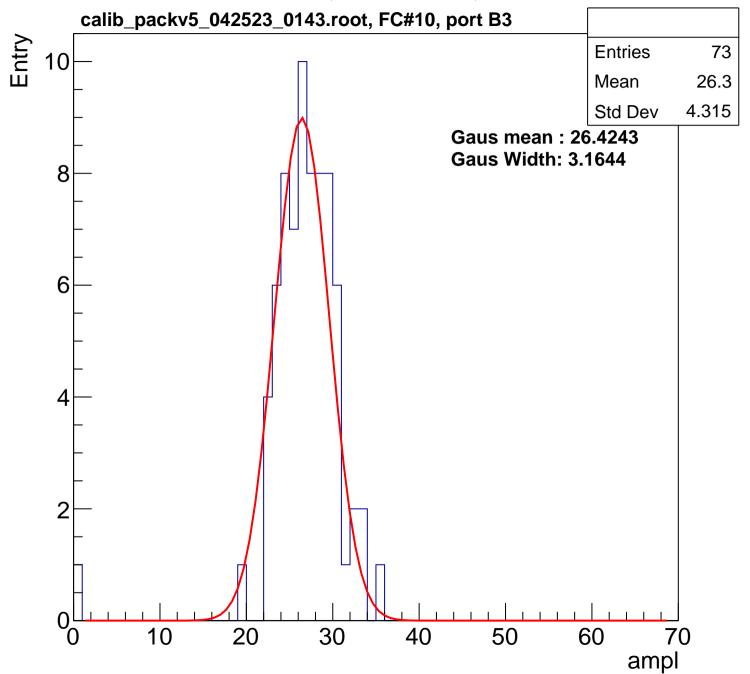


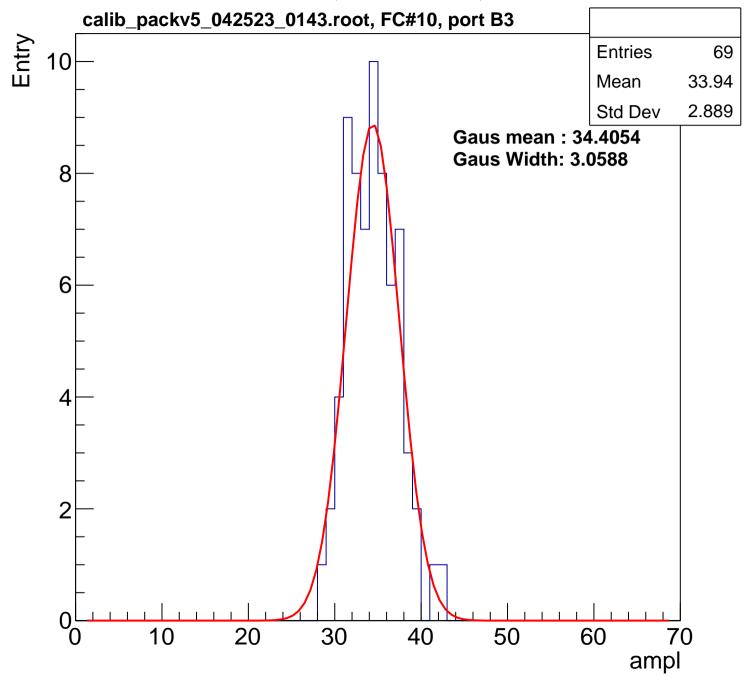


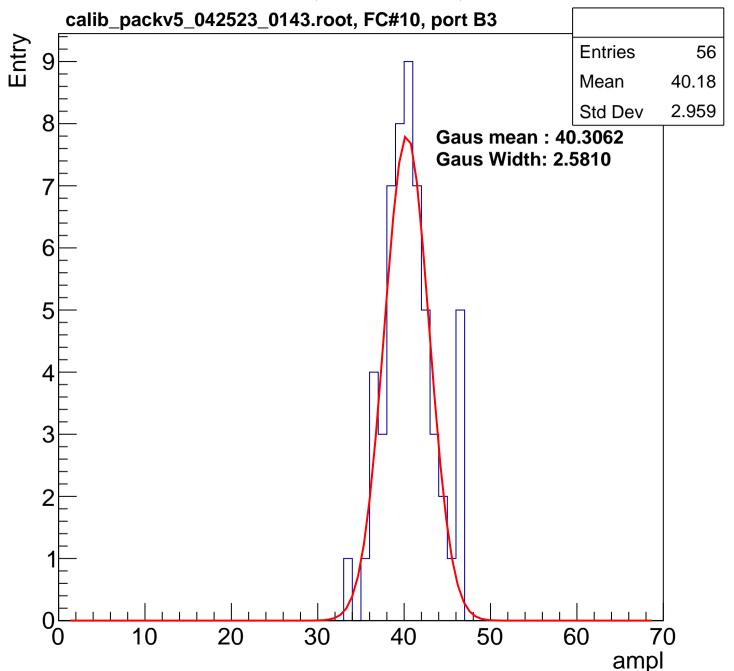


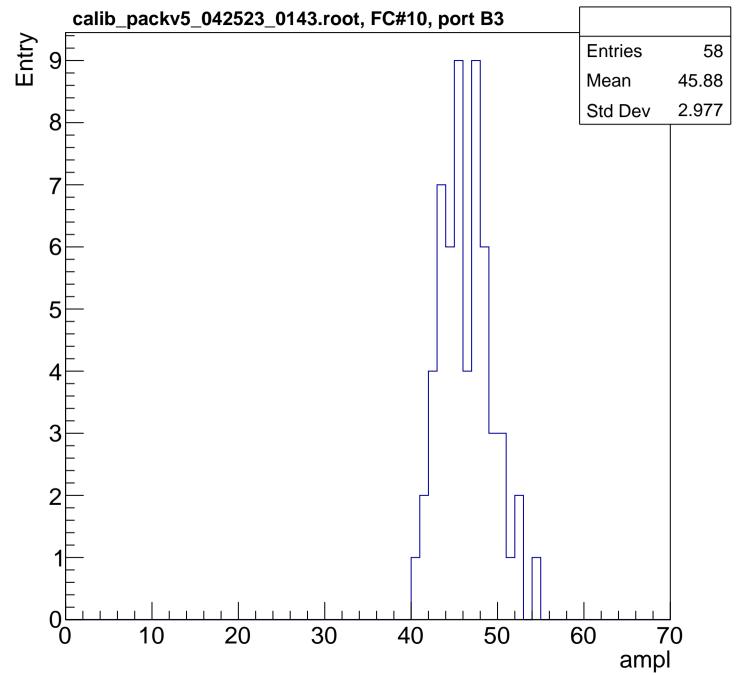


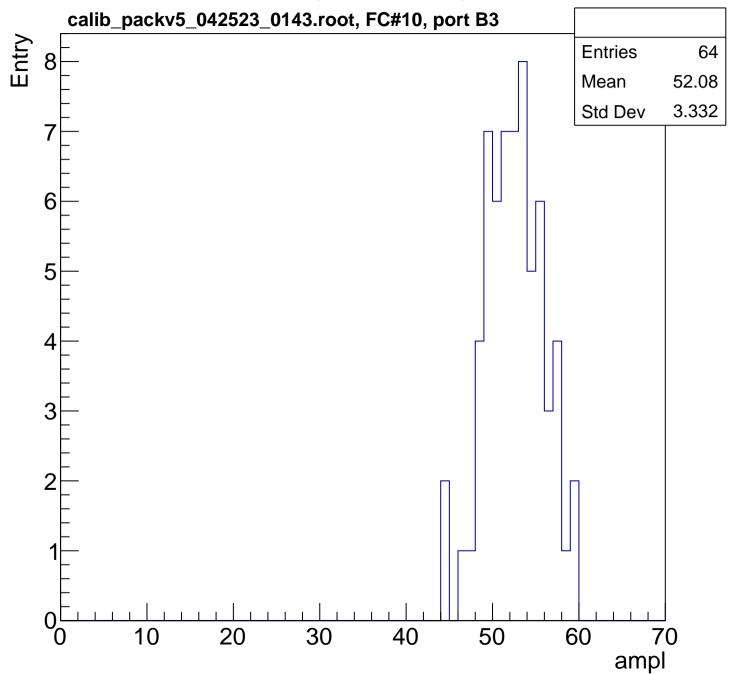


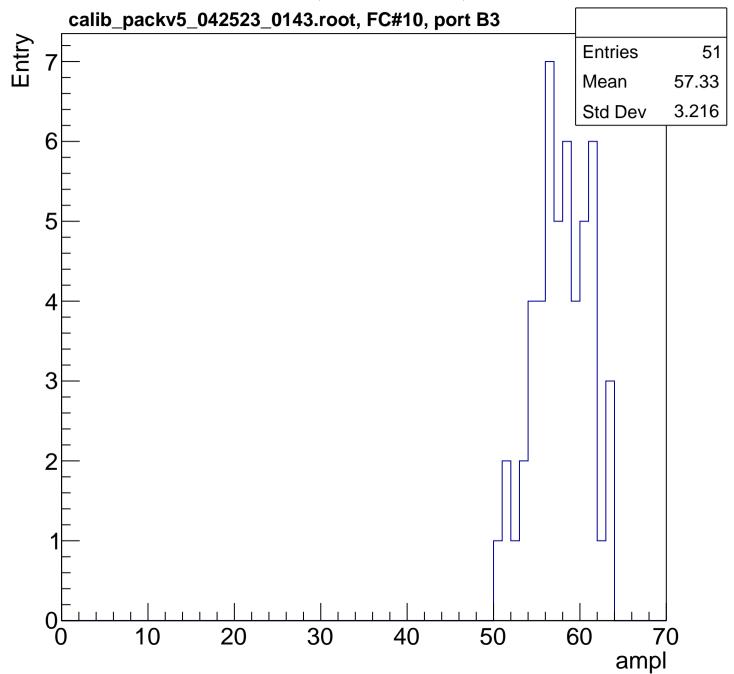


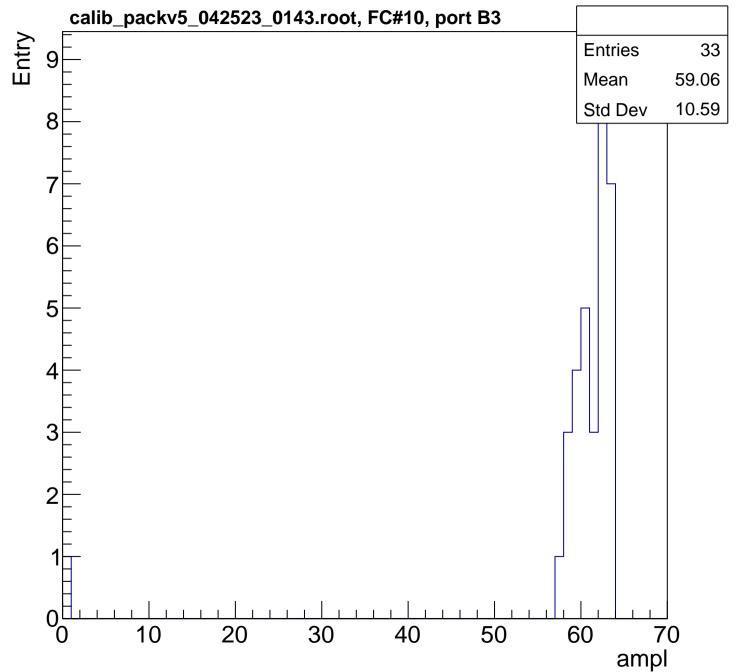


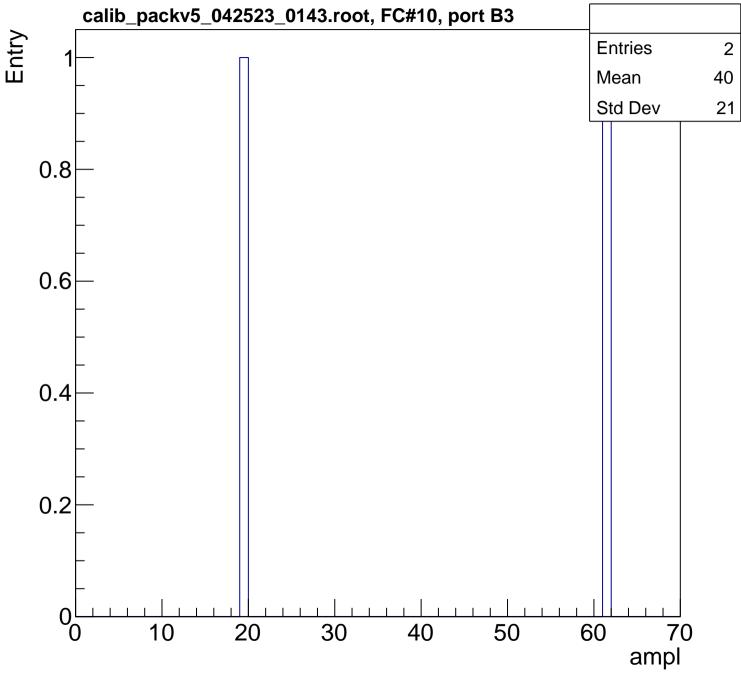


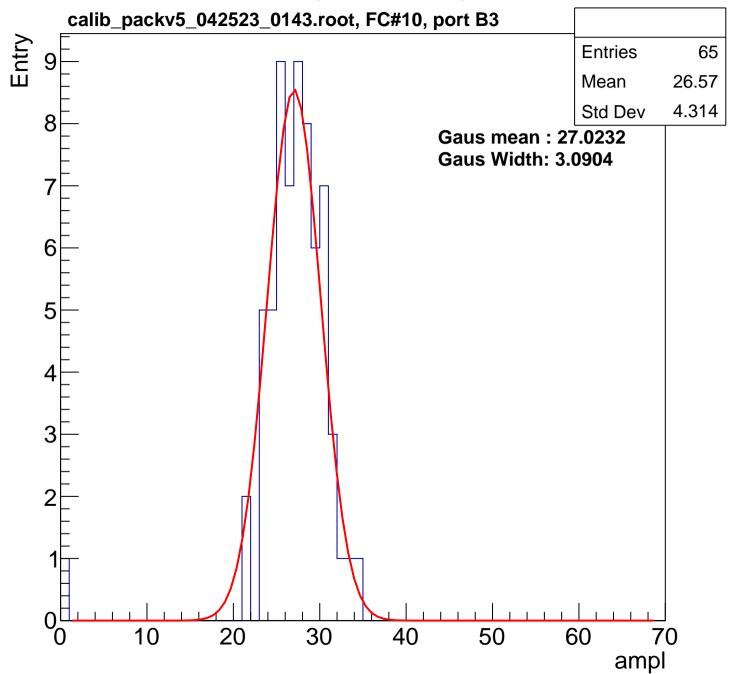


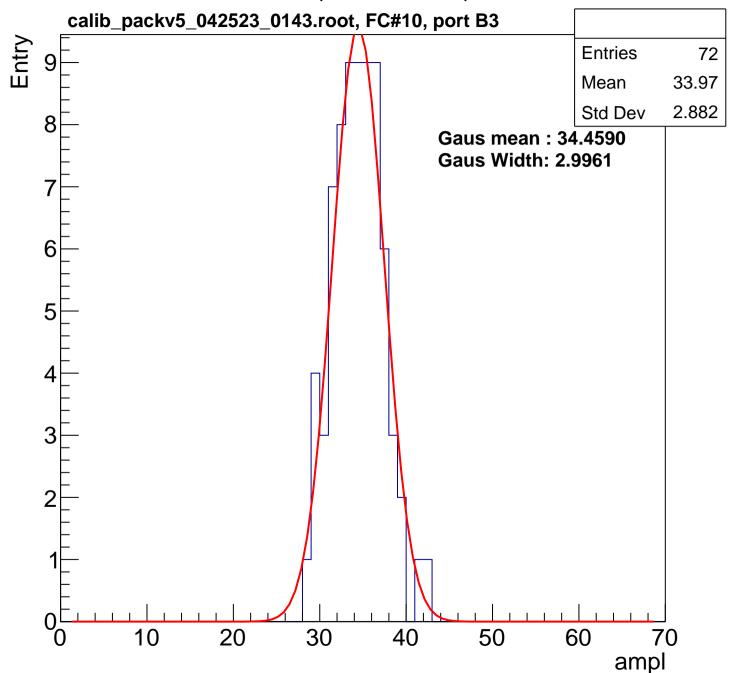


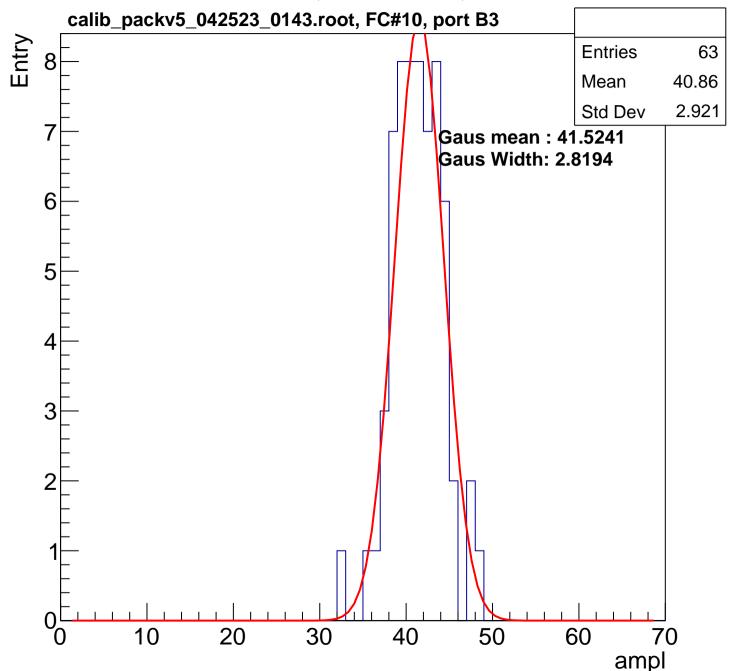


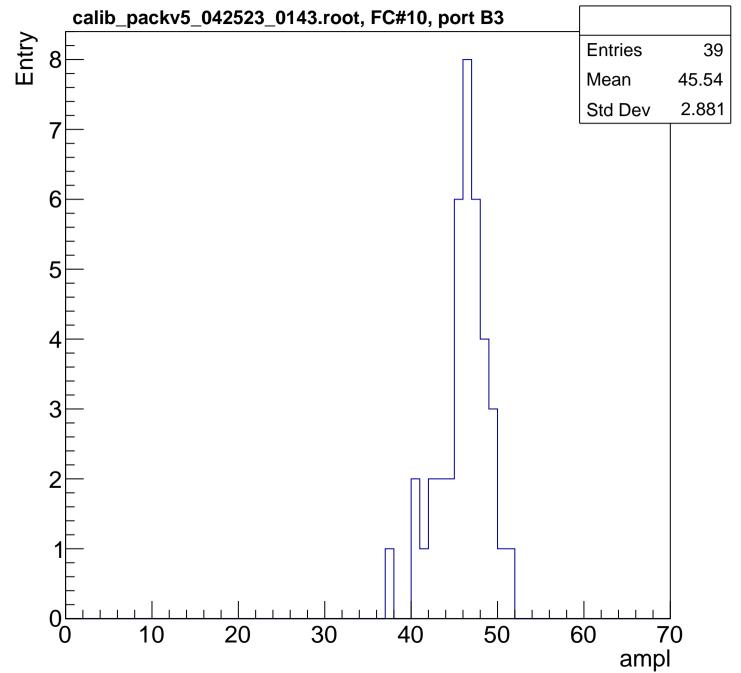


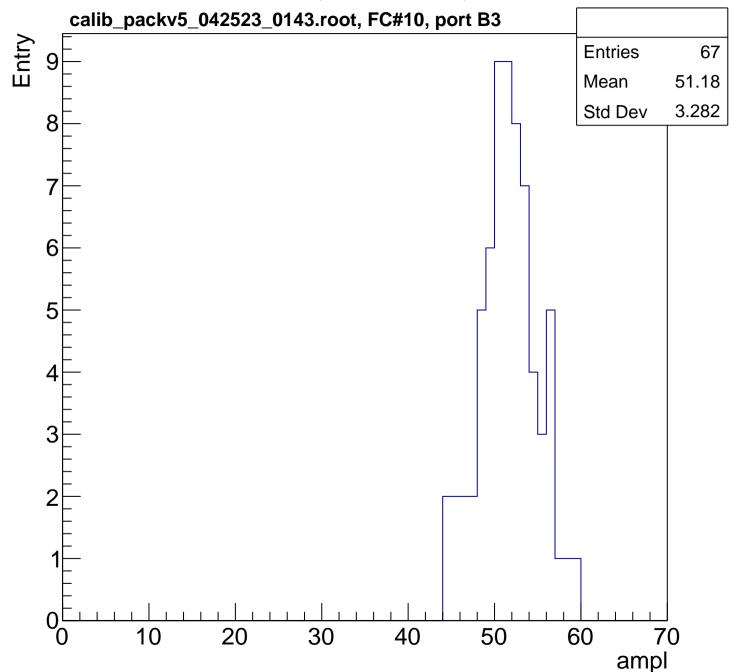


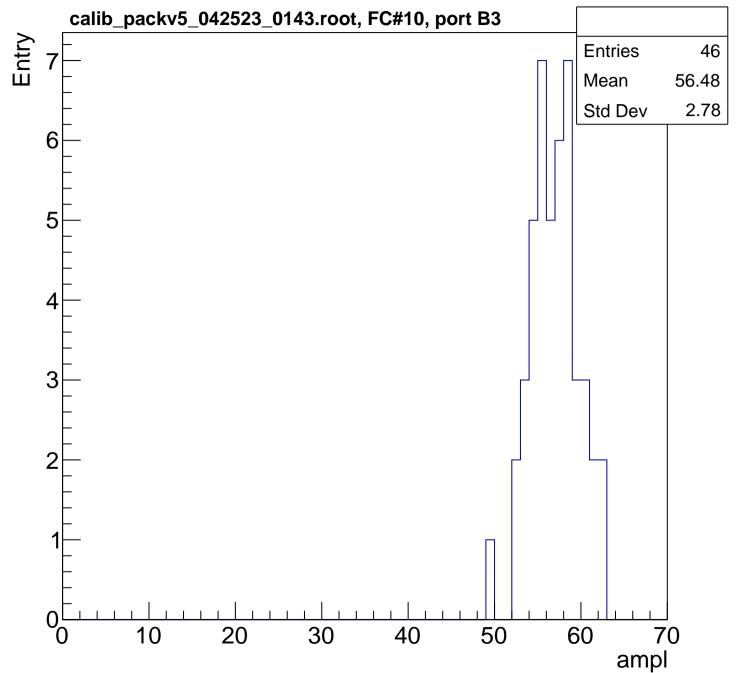


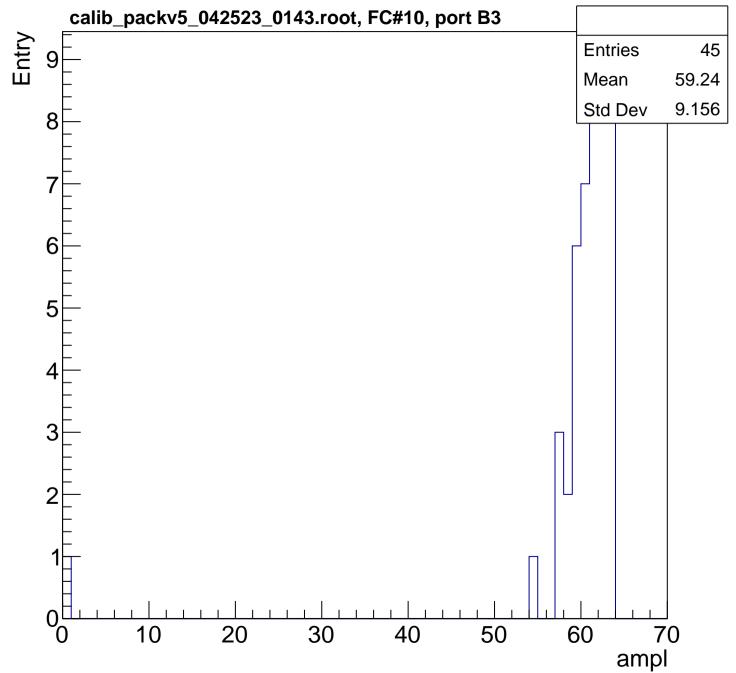




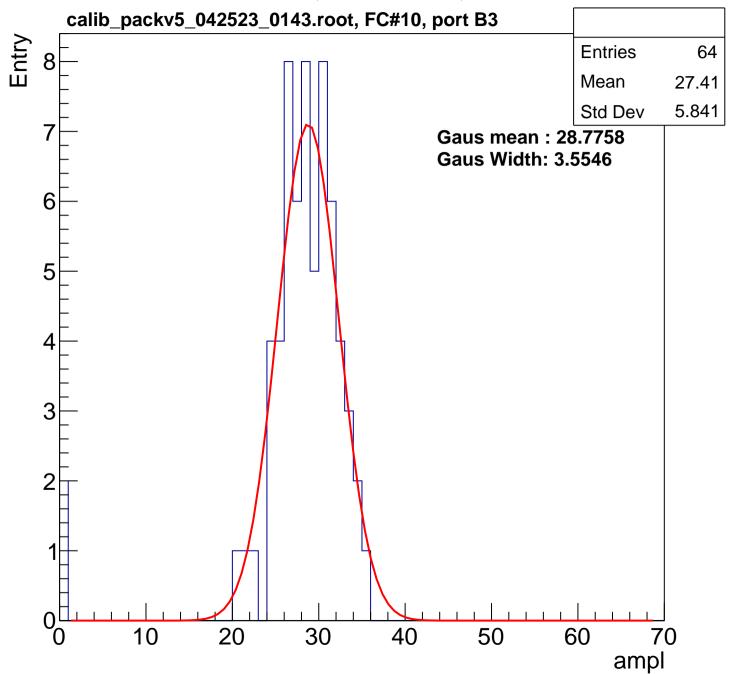


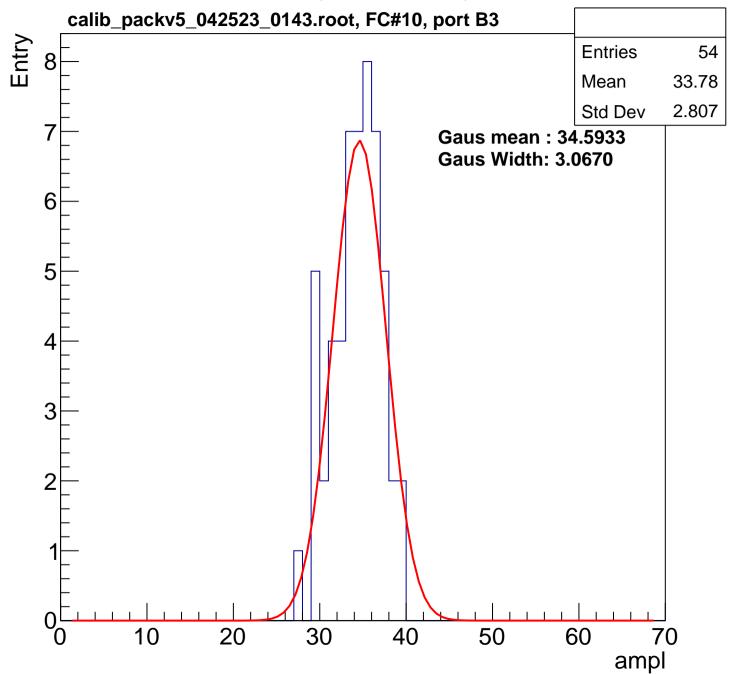


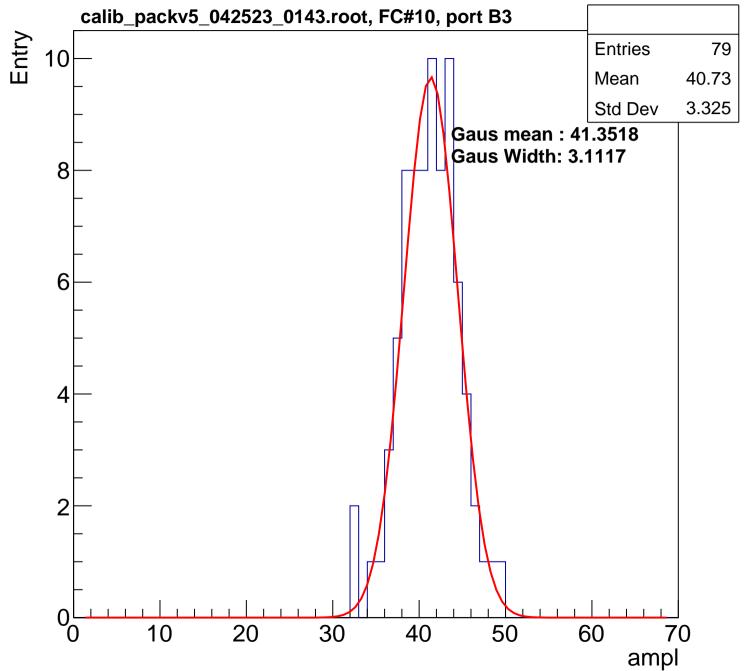


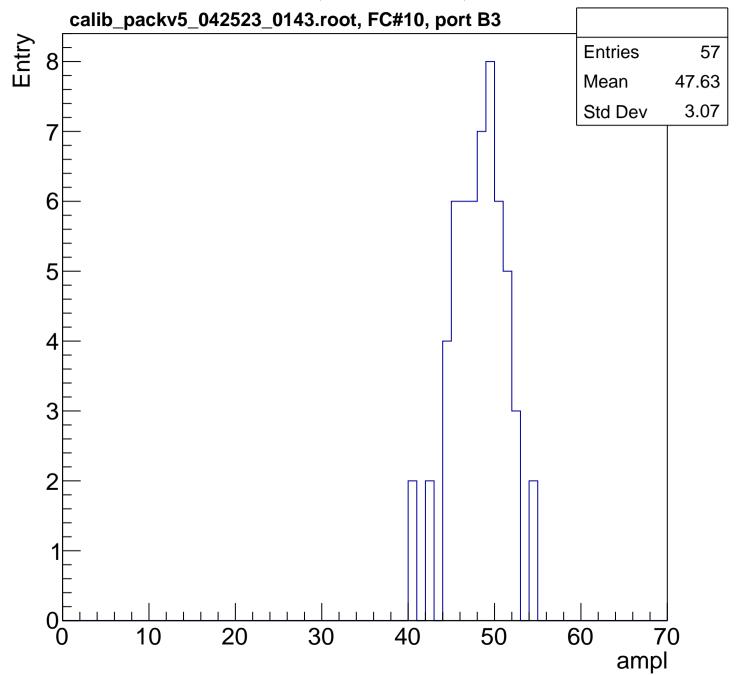


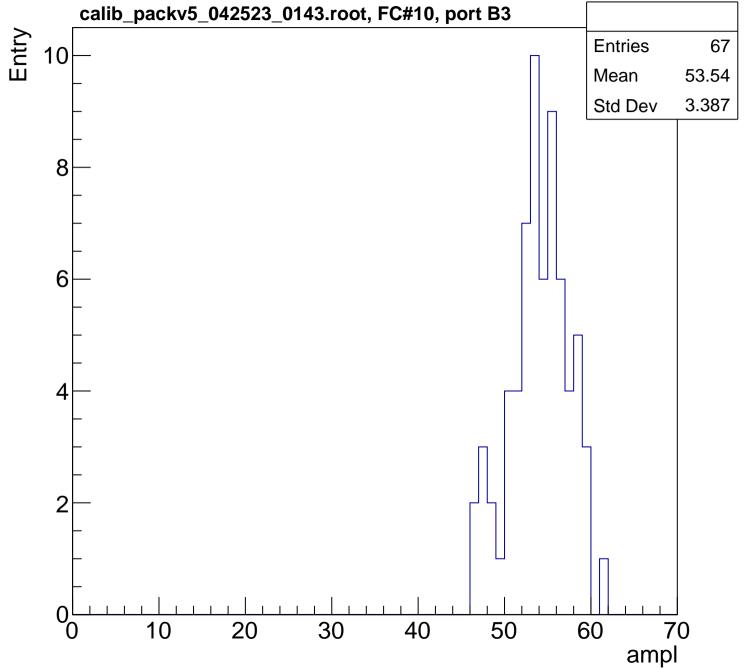


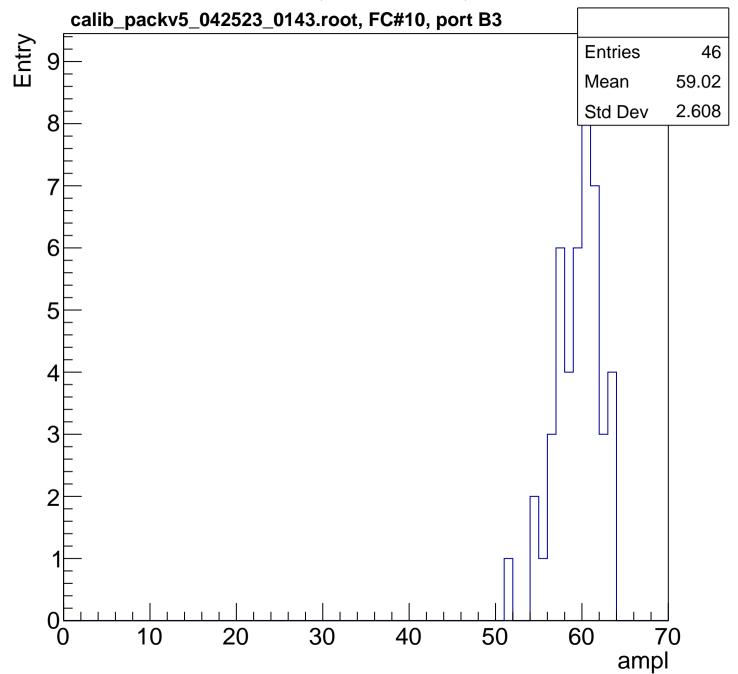


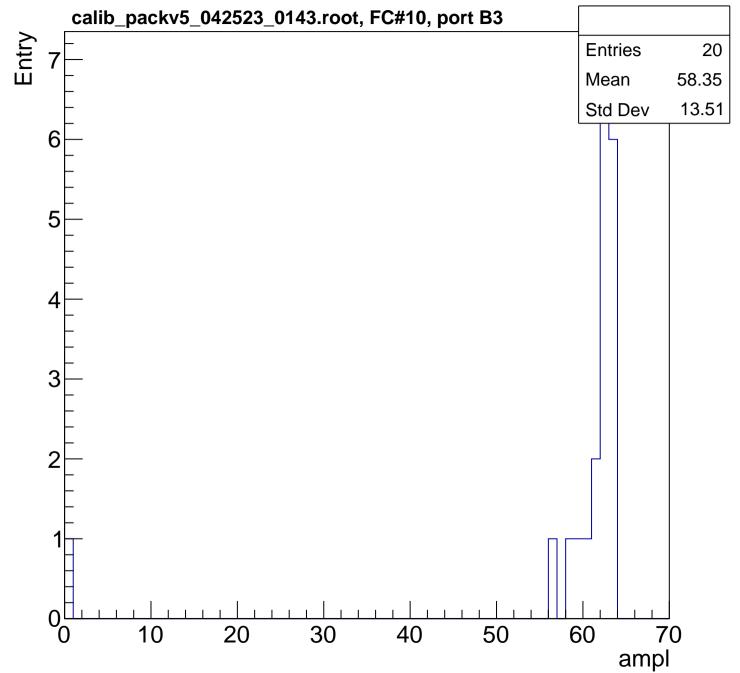


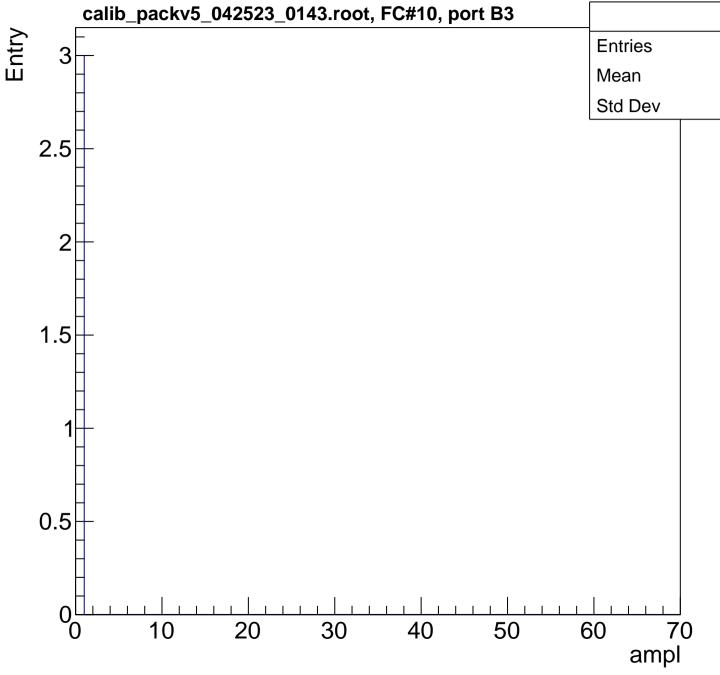


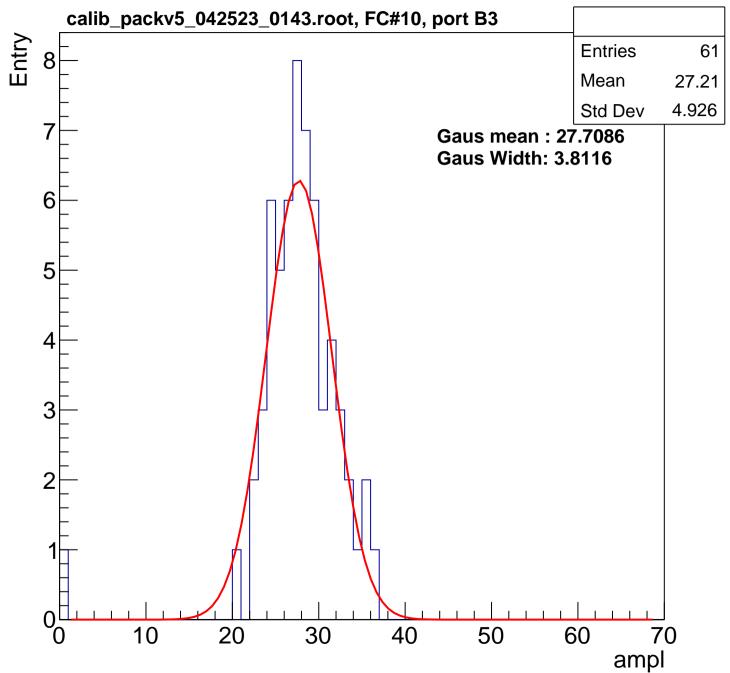


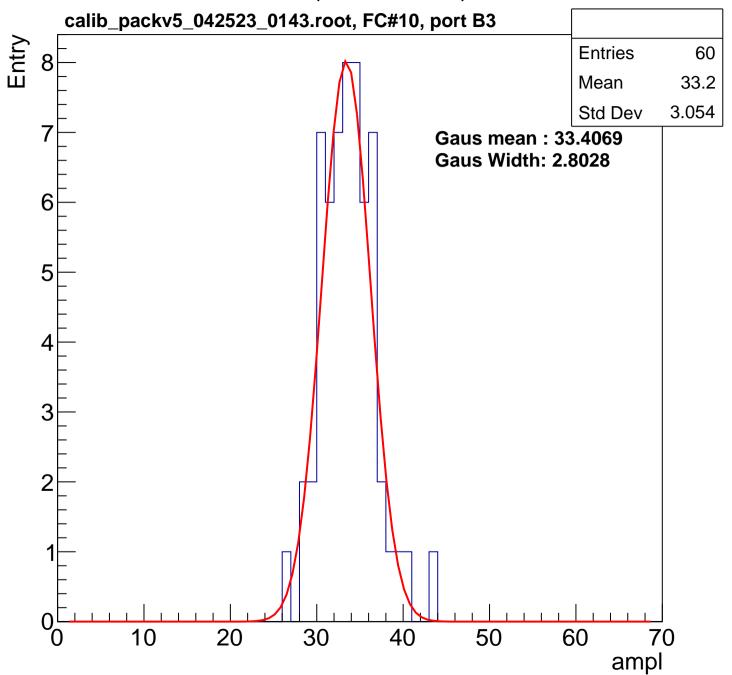


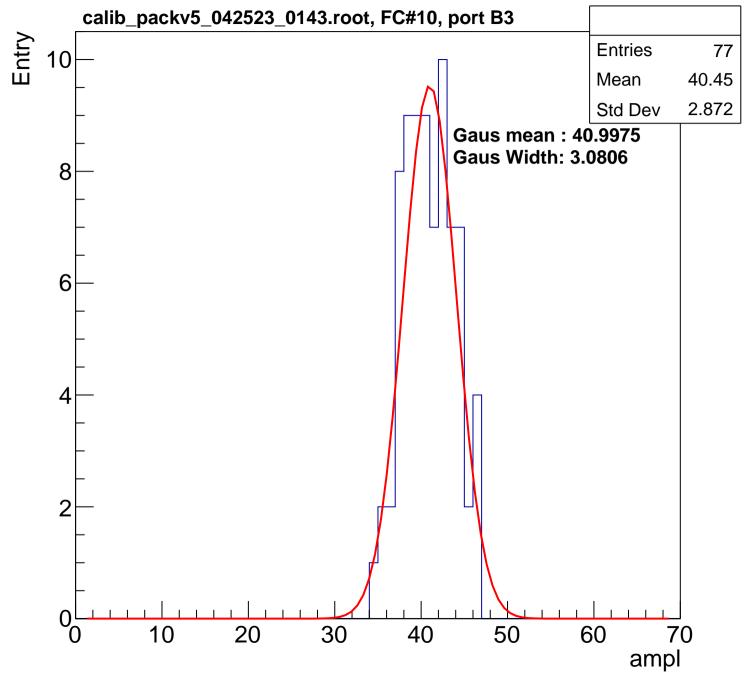


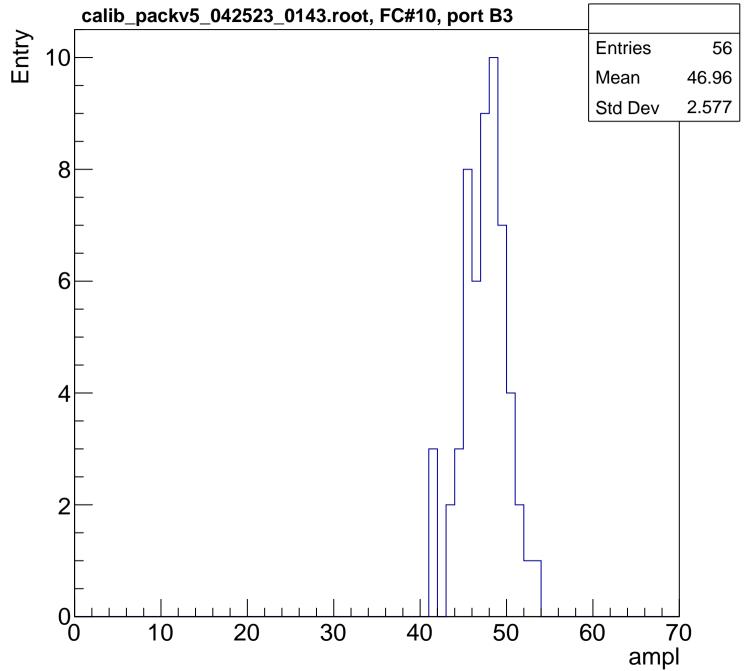


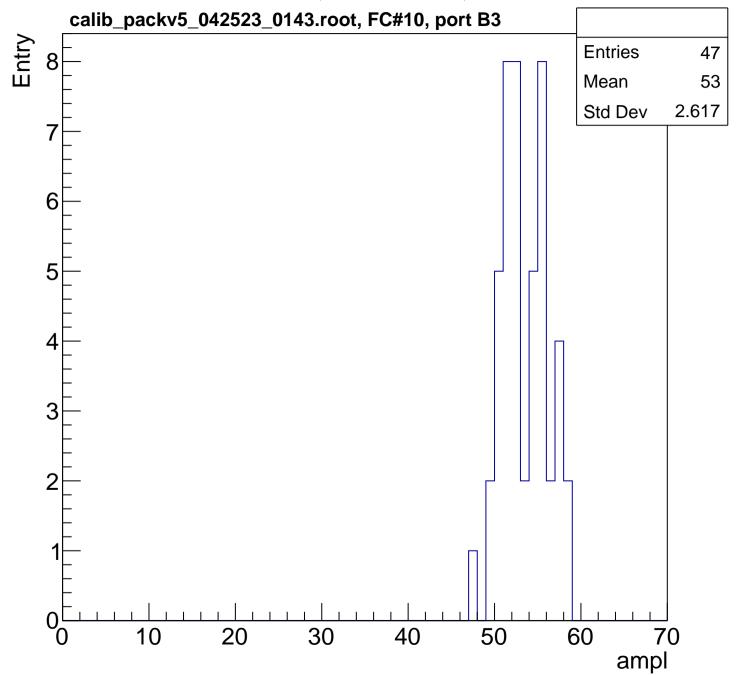


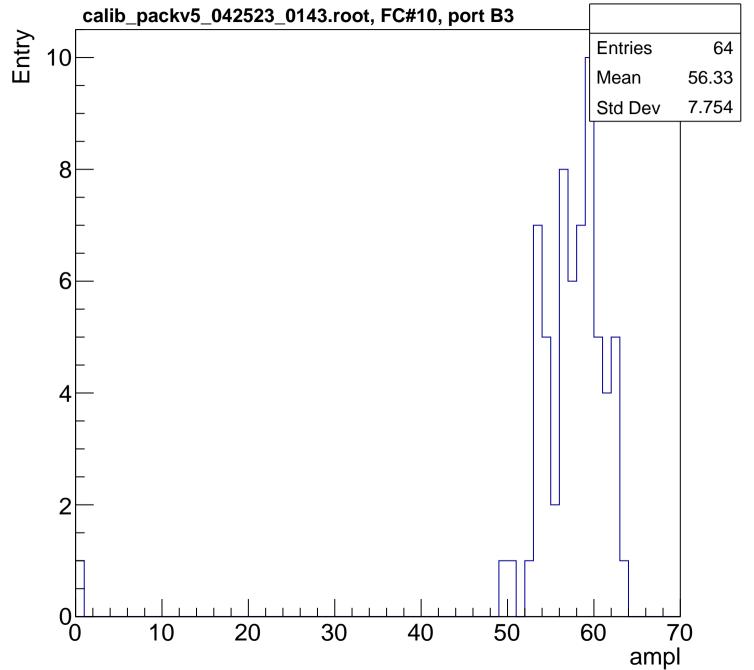


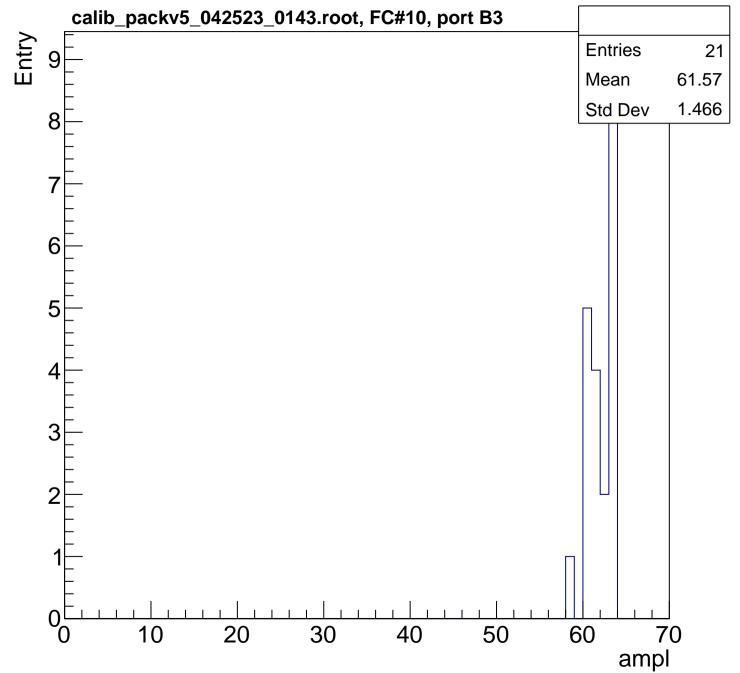


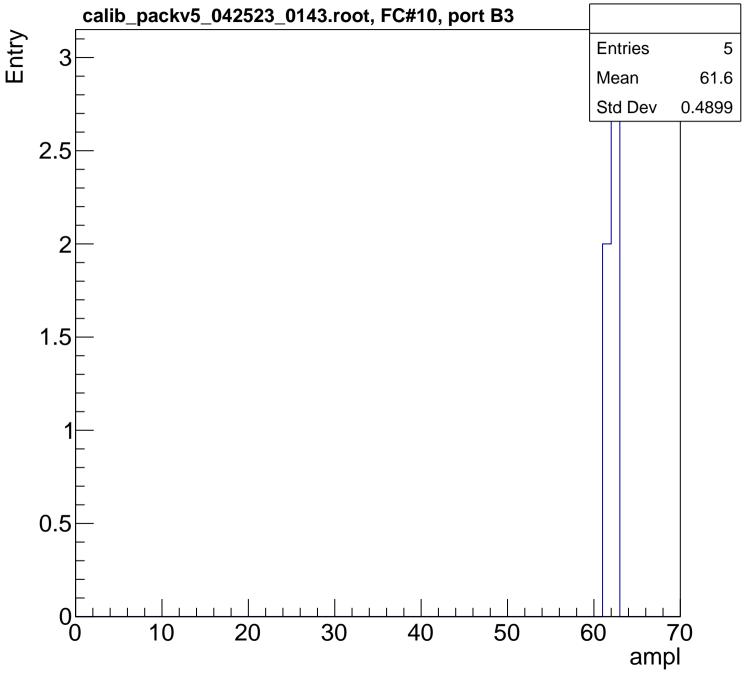


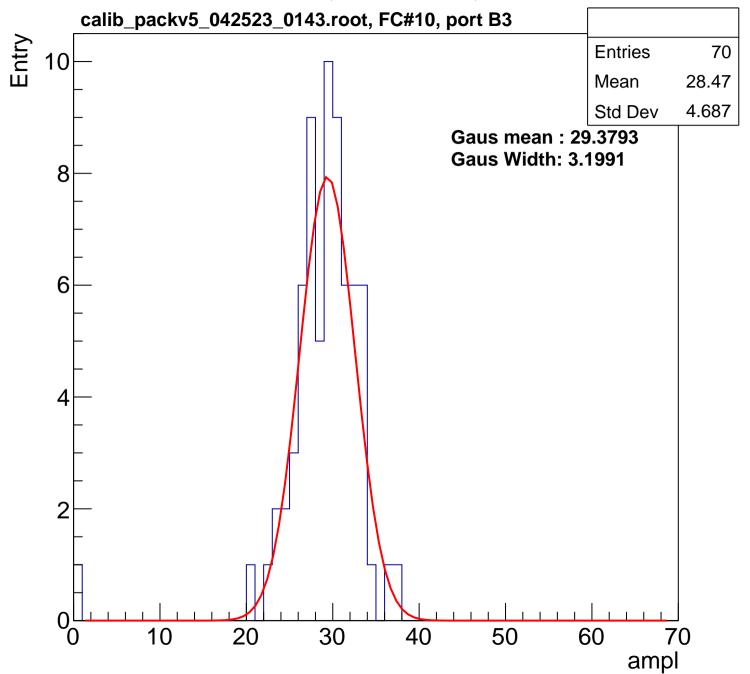


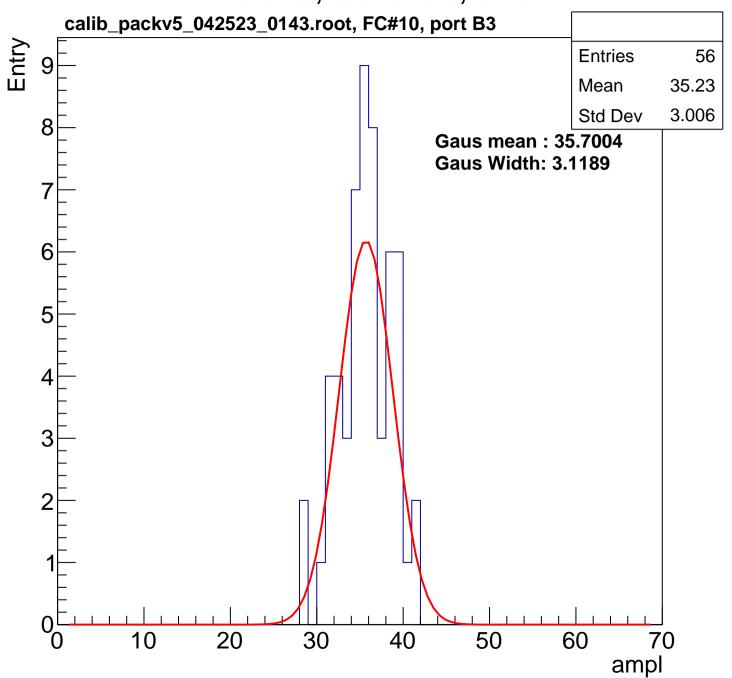


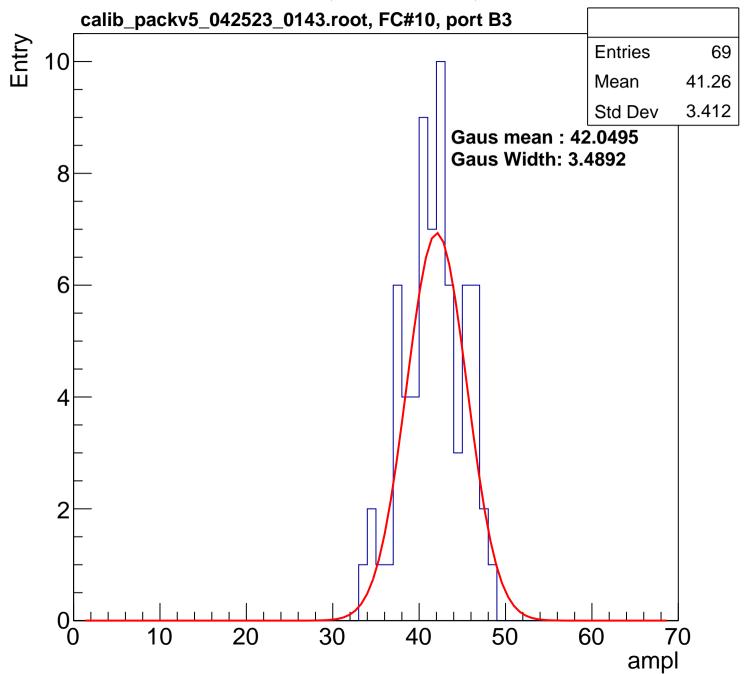


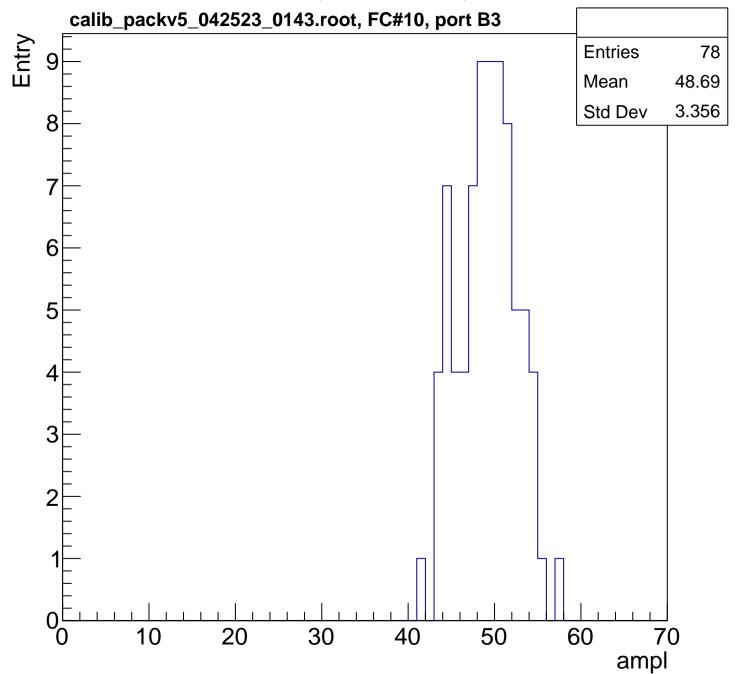


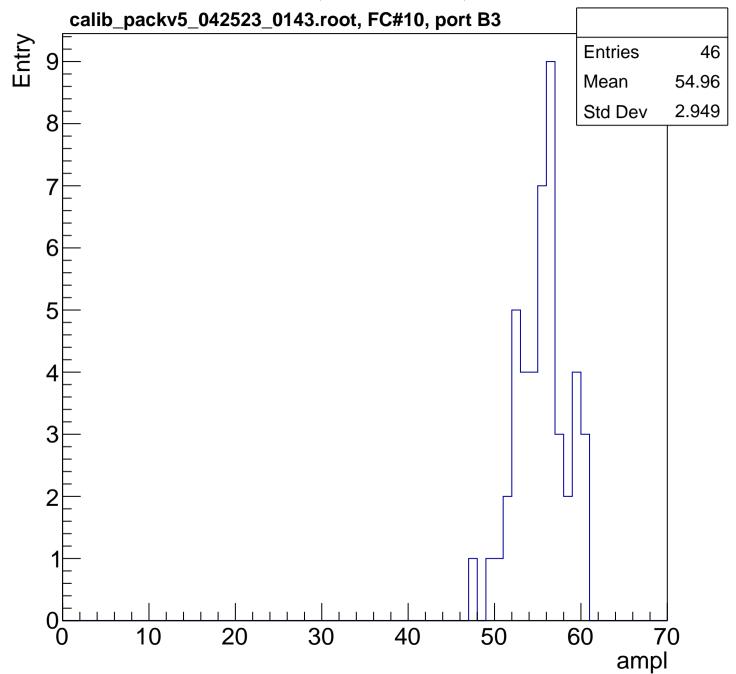


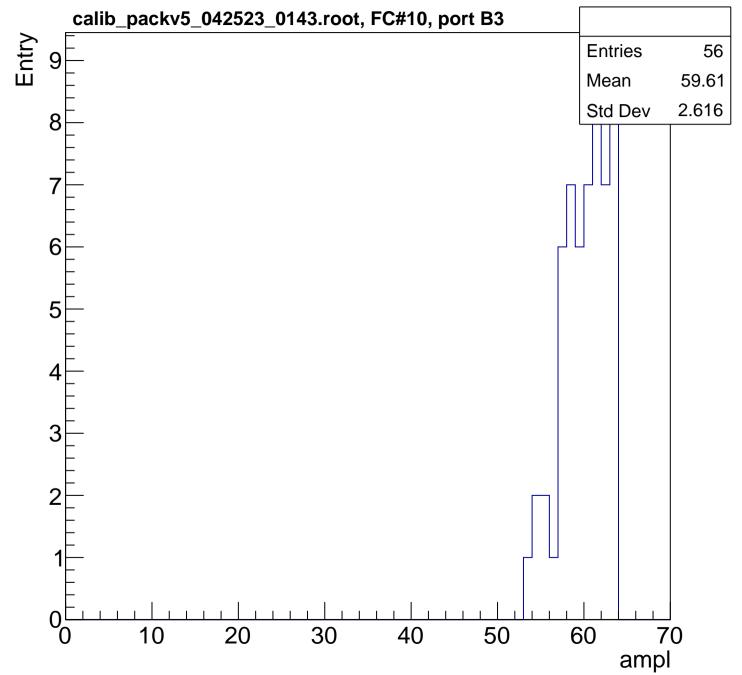


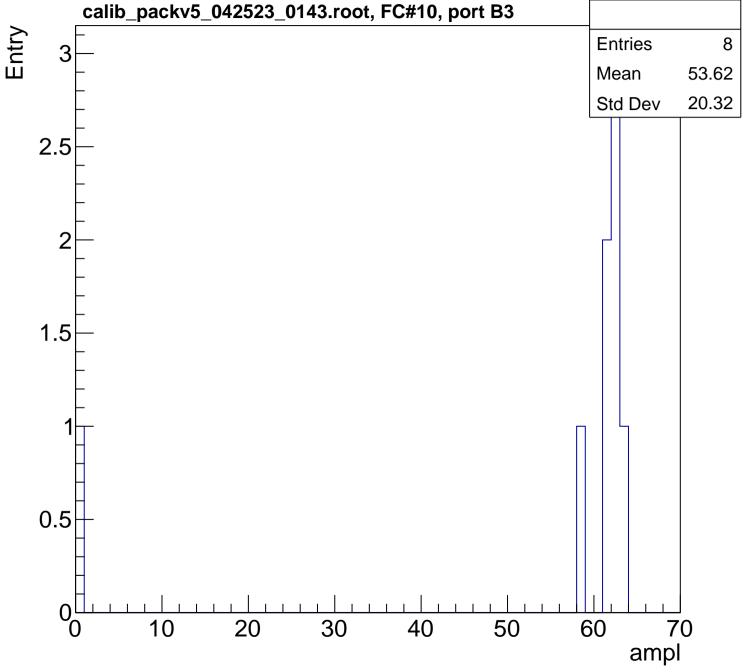




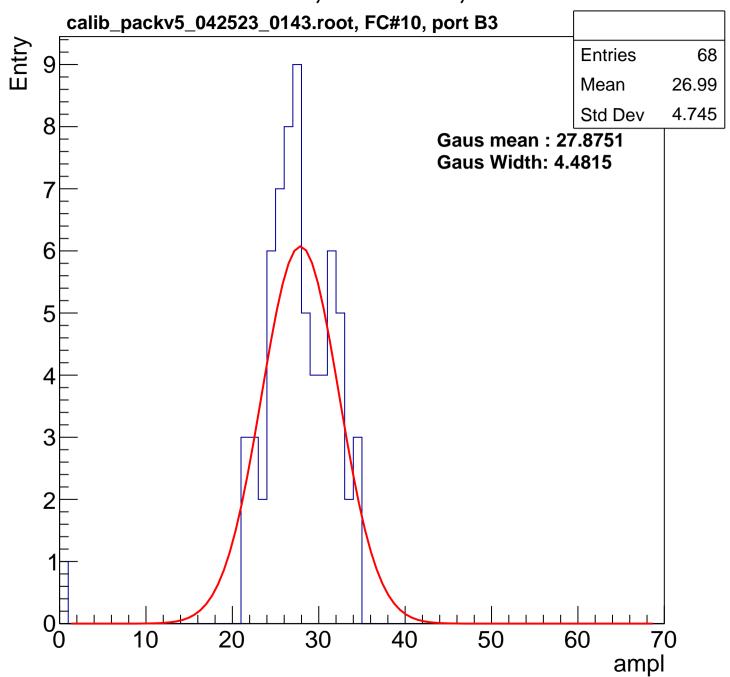


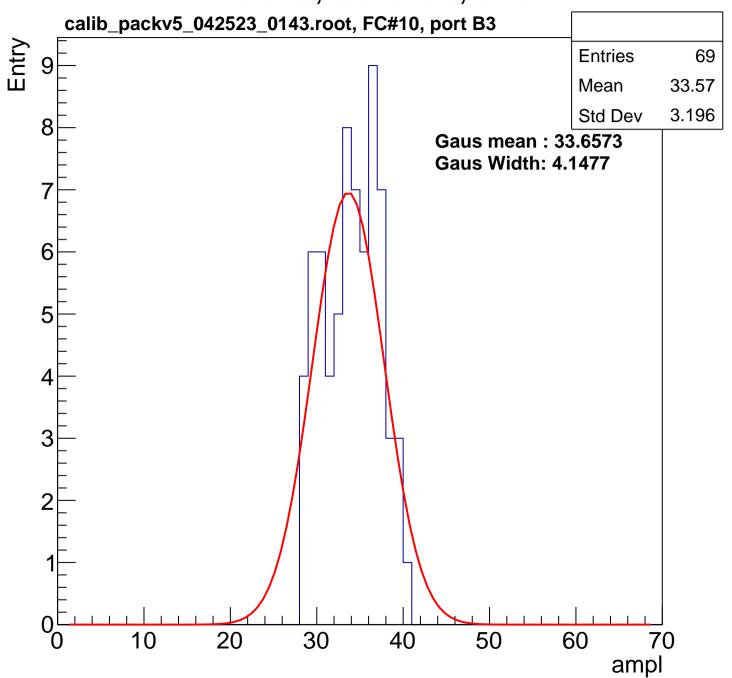


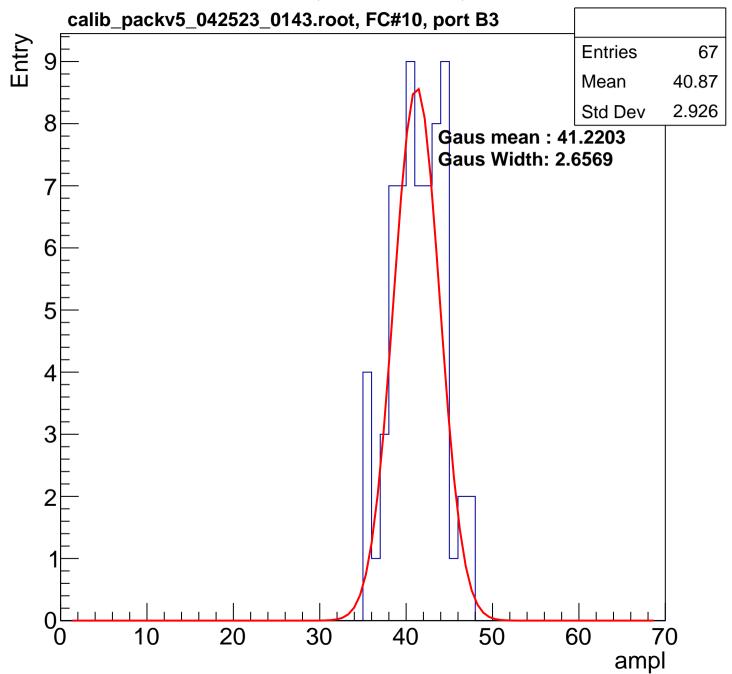


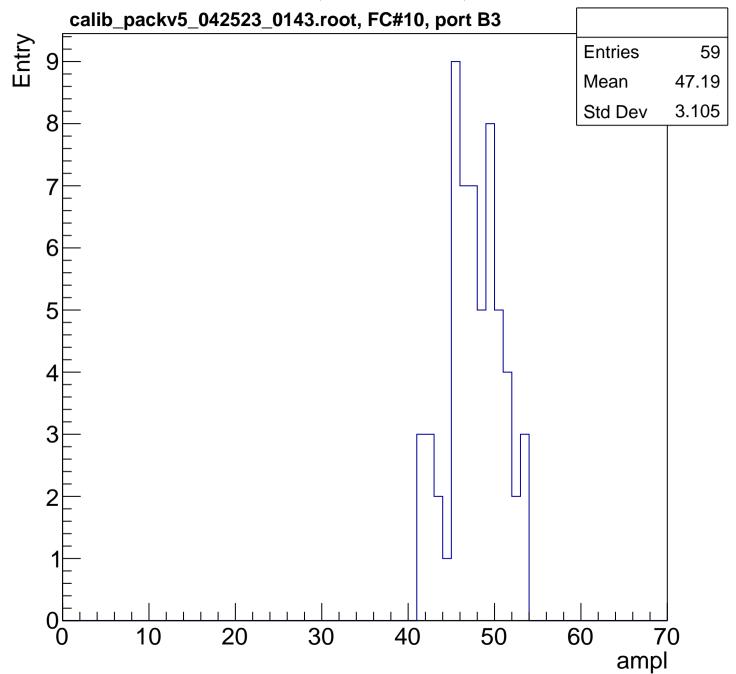


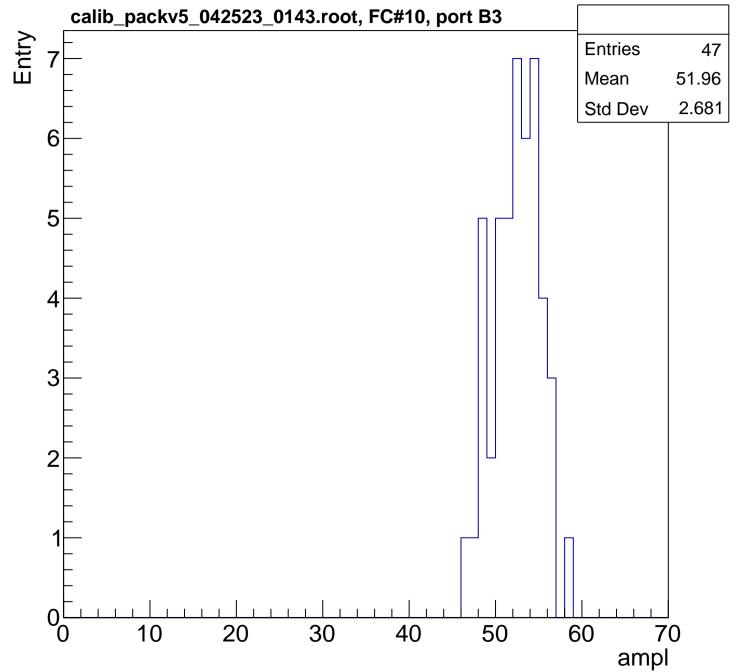
B1L002S, U9-ch85, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

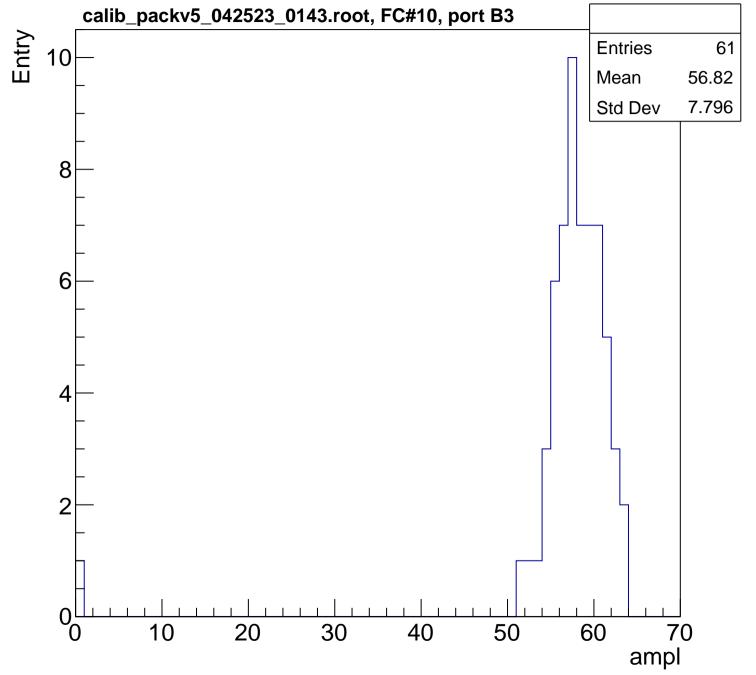


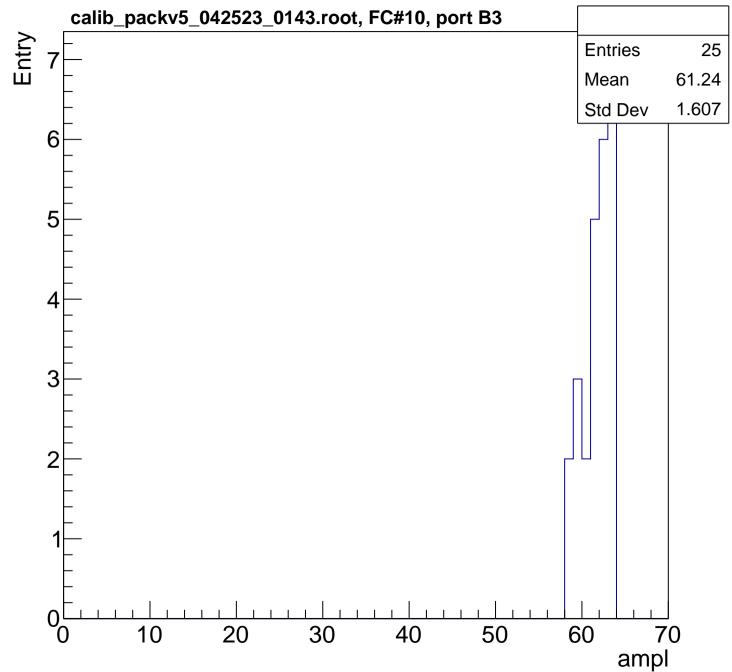


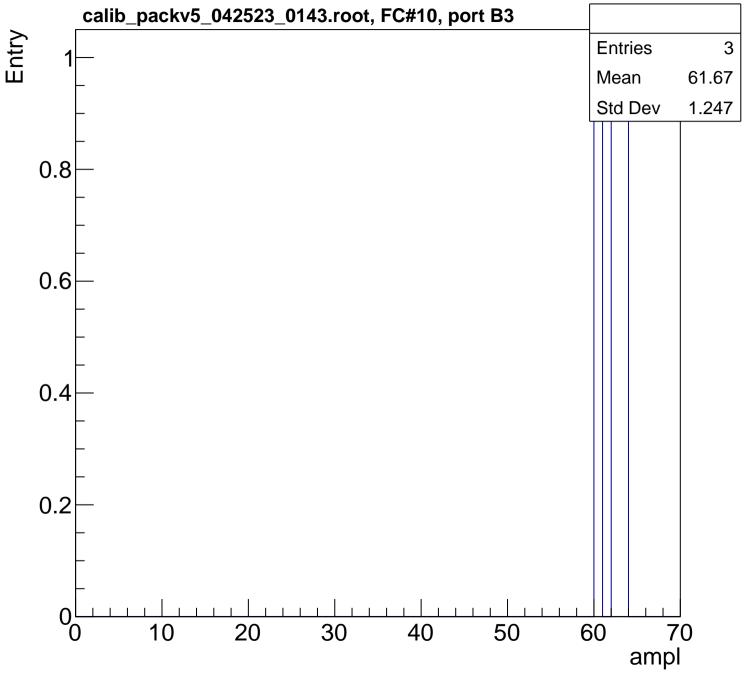


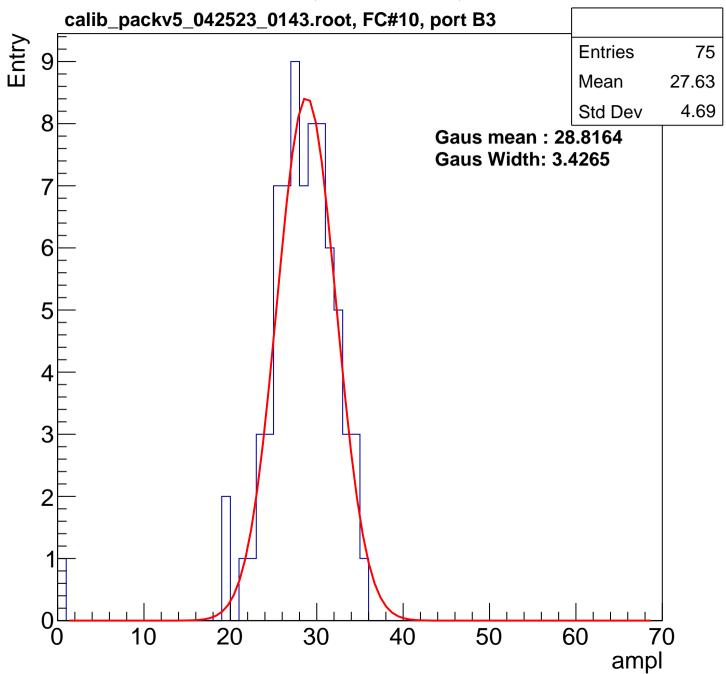


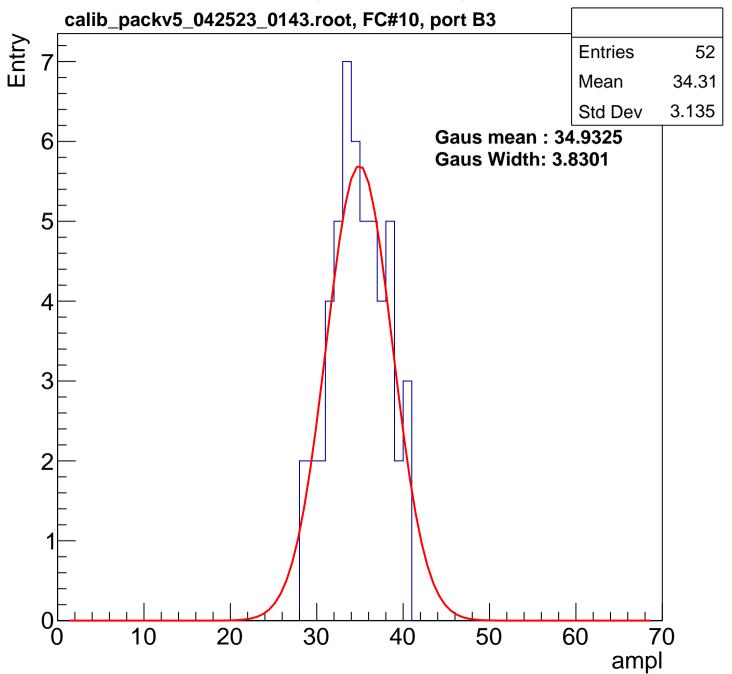


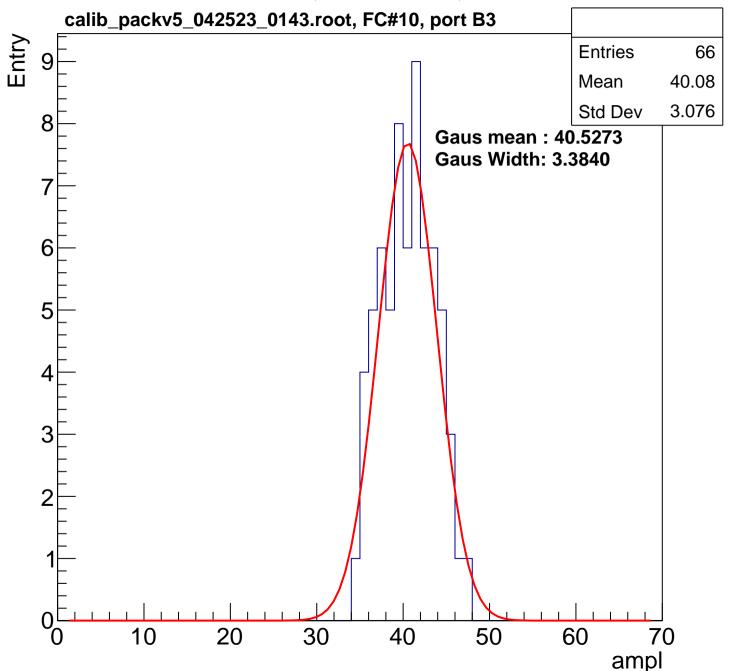


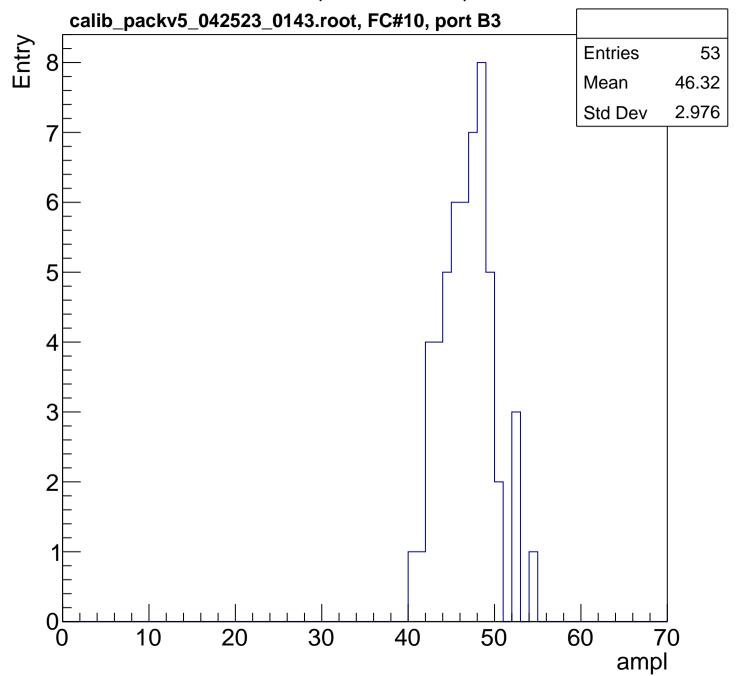


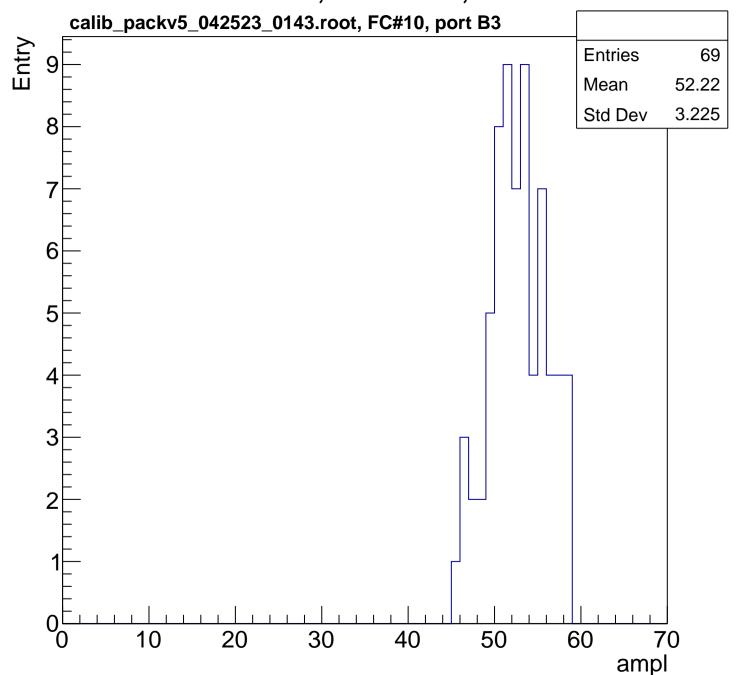


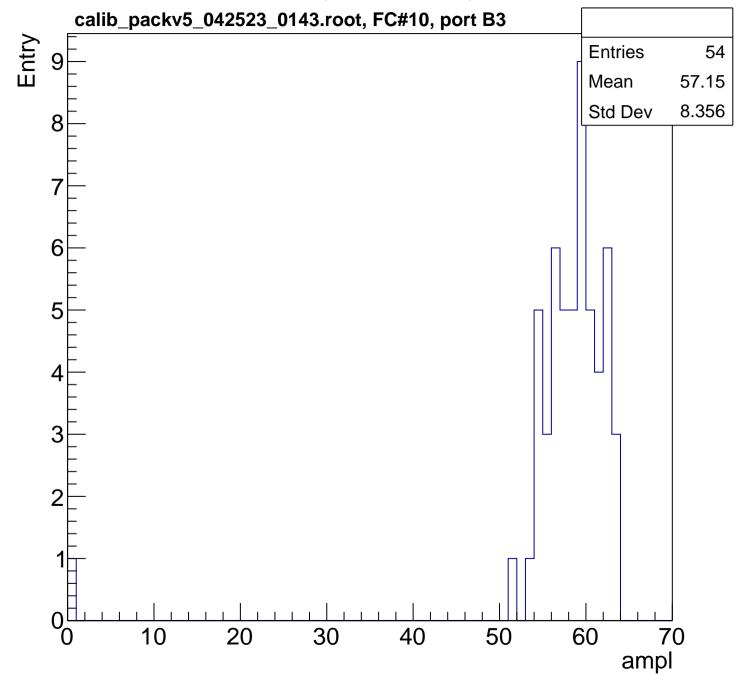


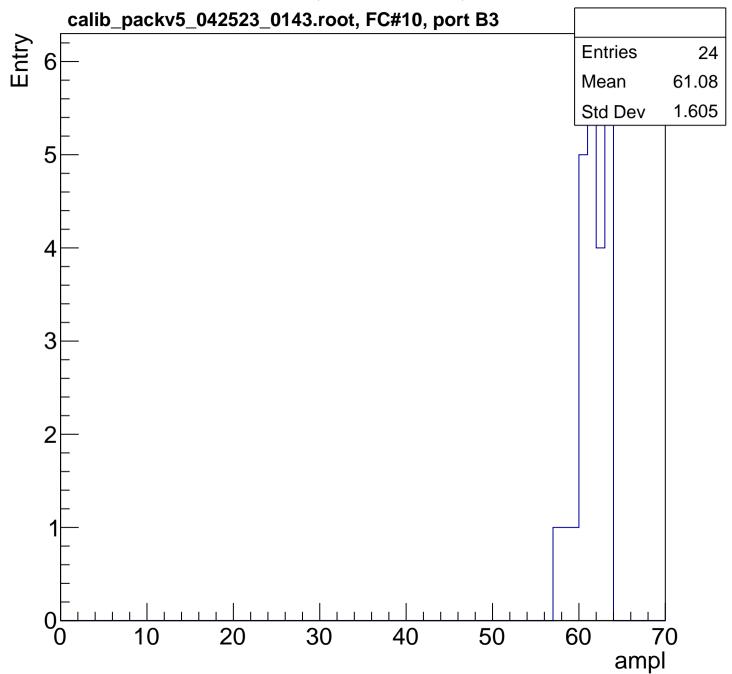


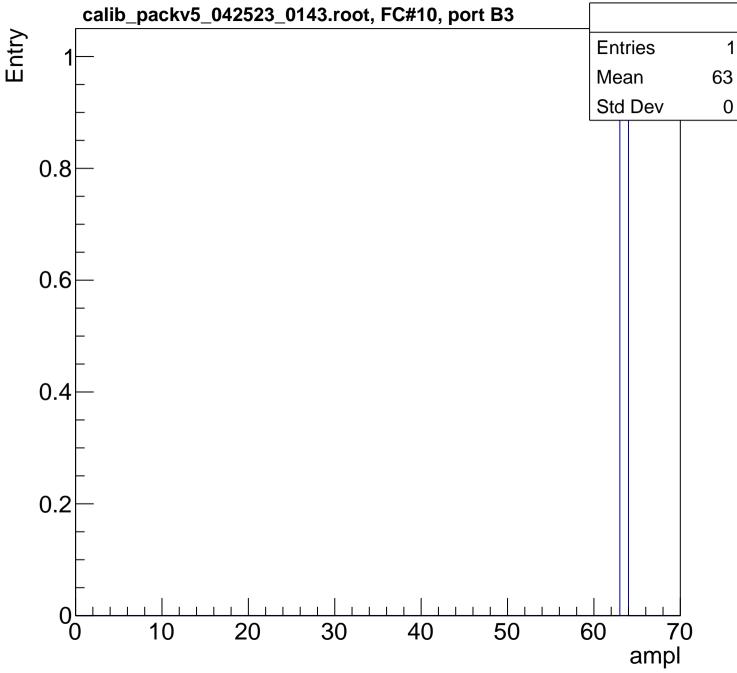


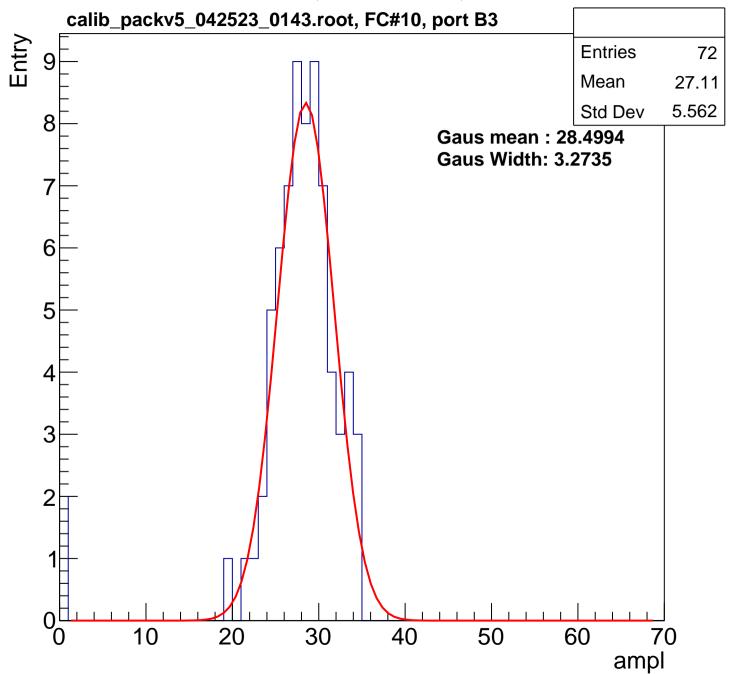


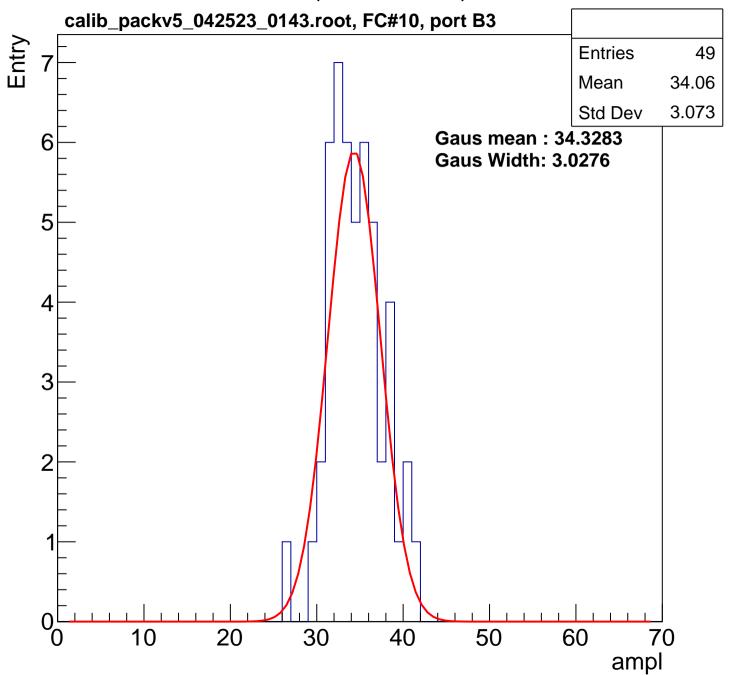


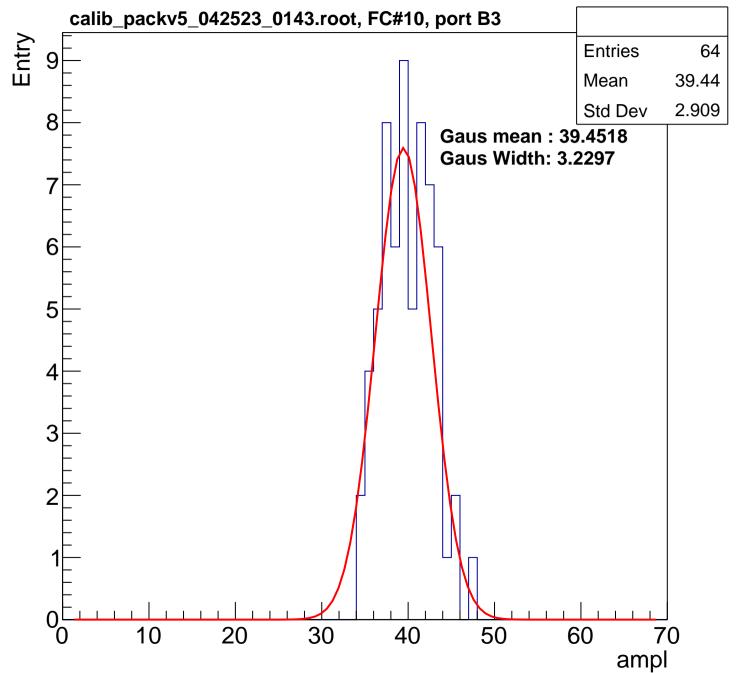


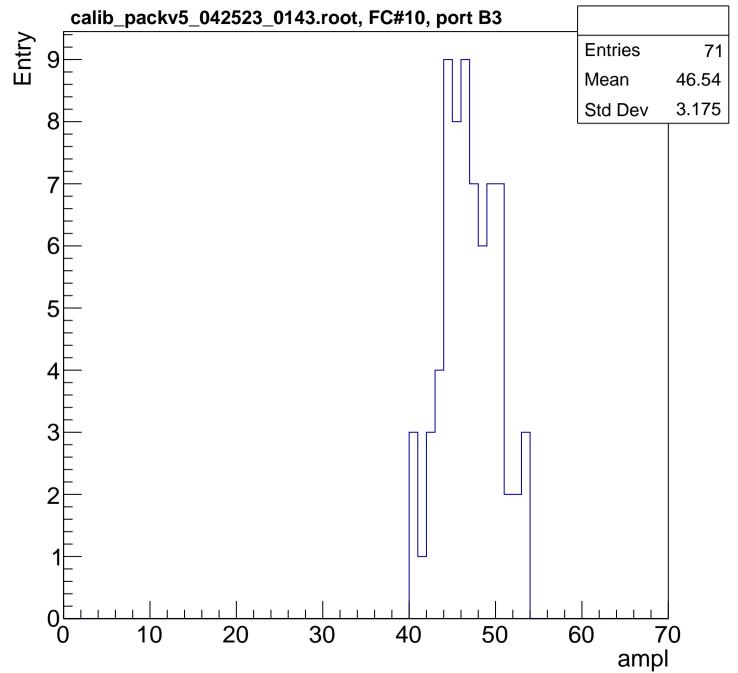


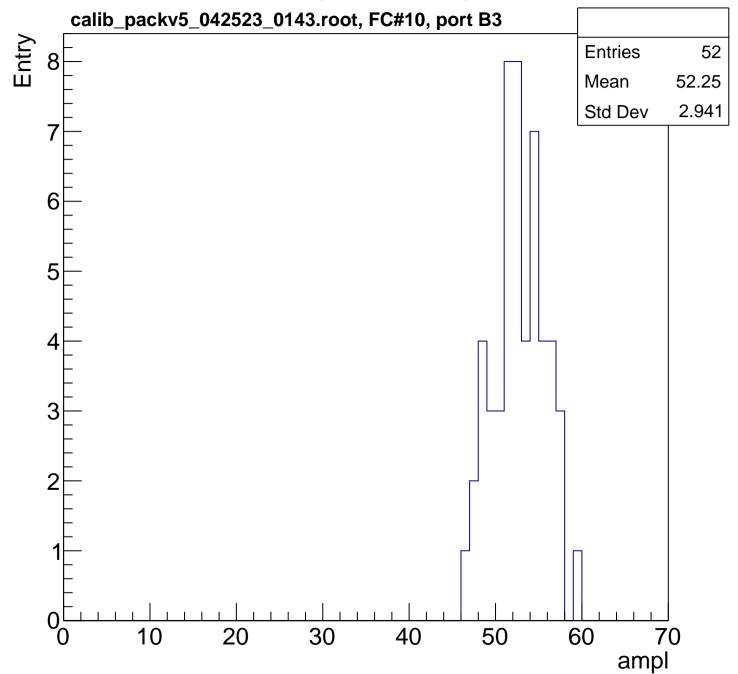


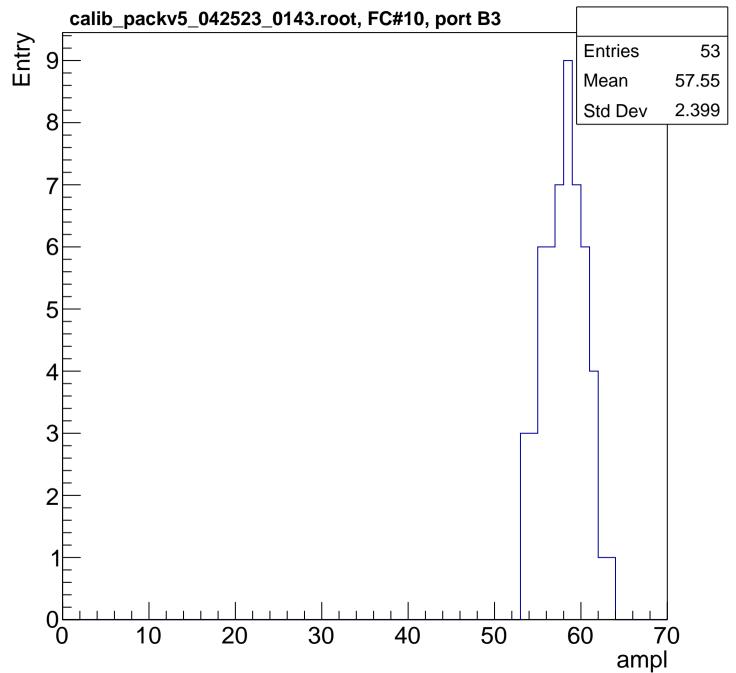


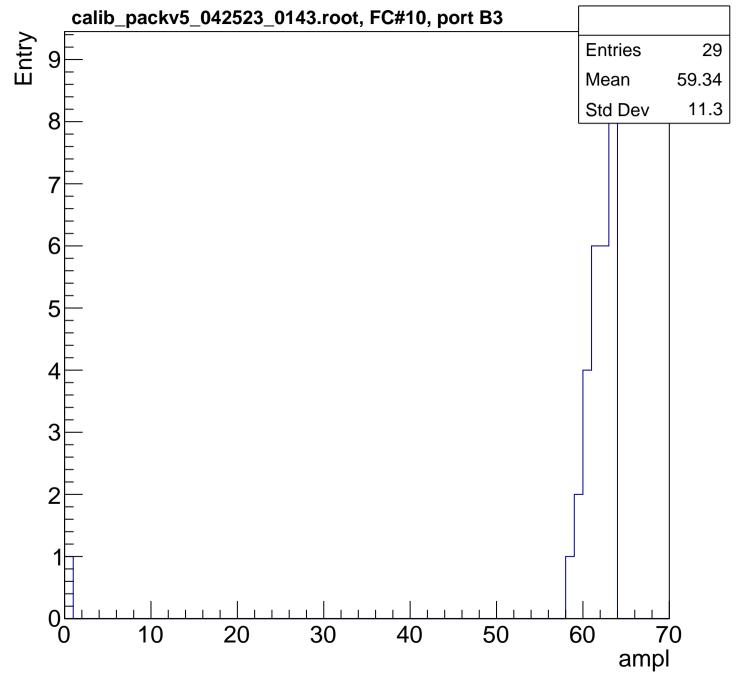


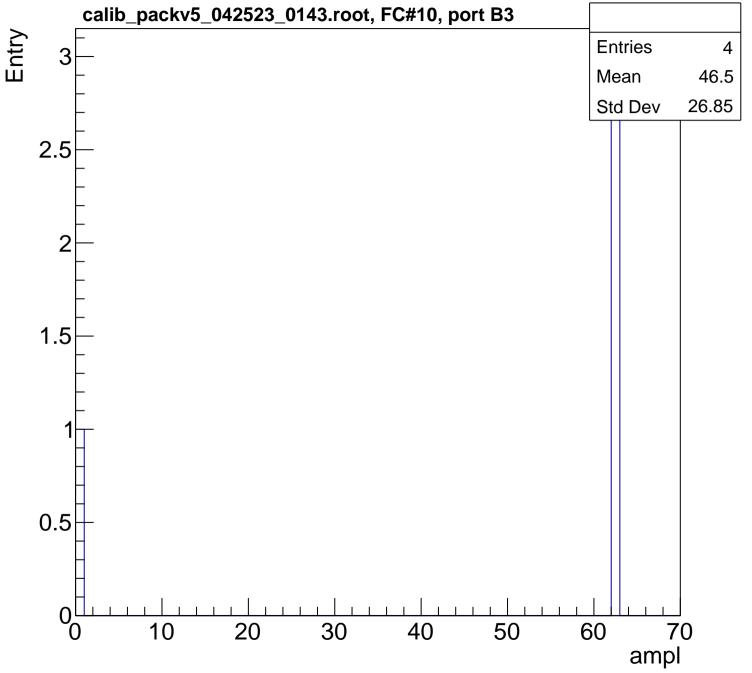


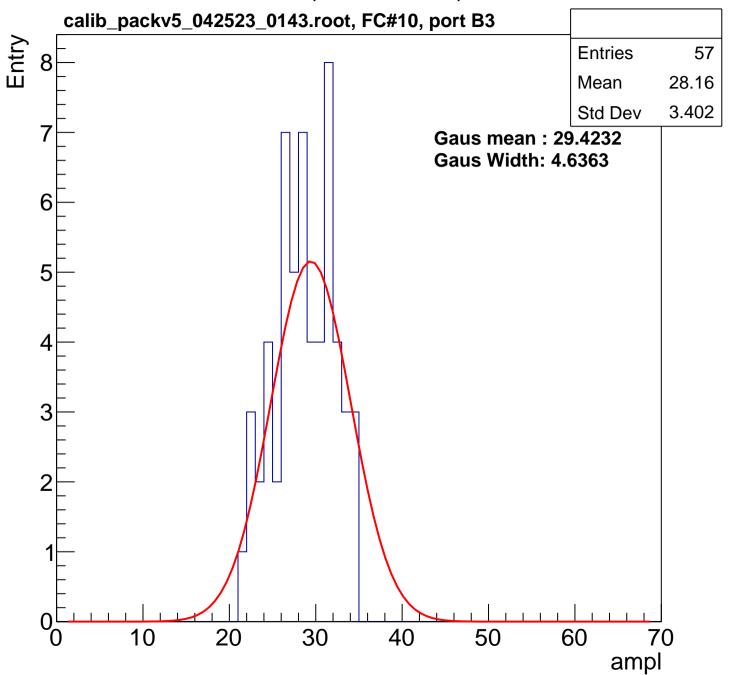


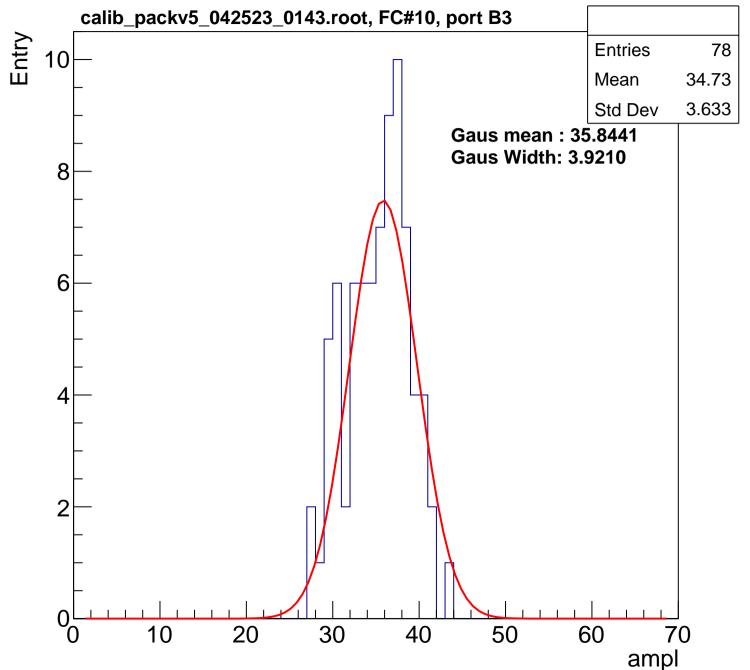


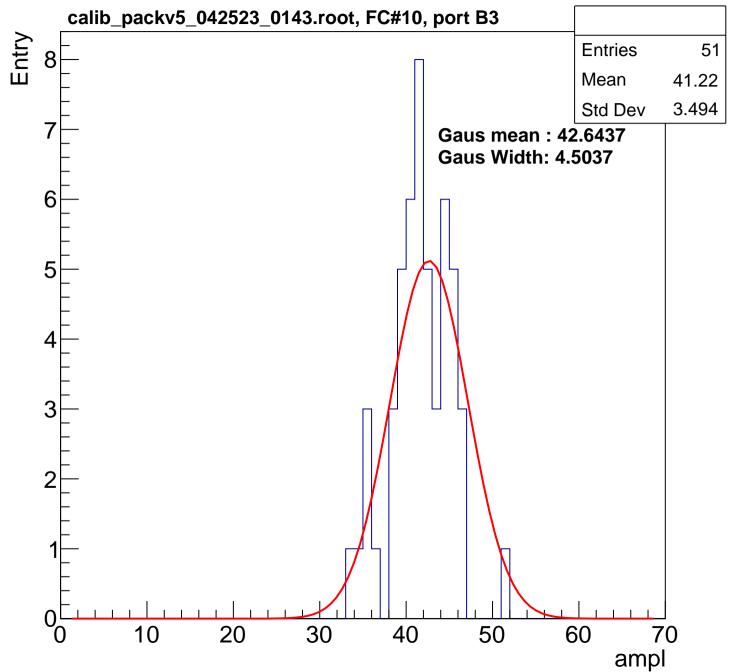


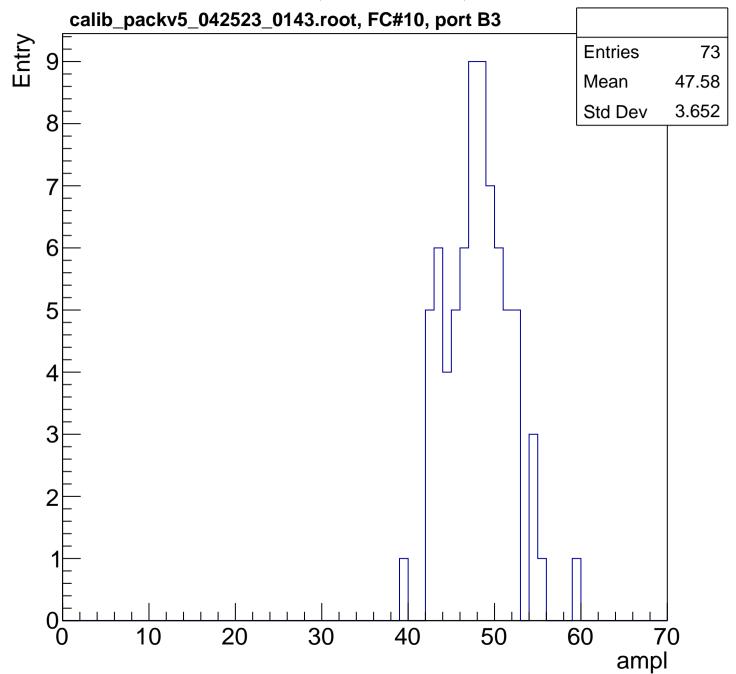


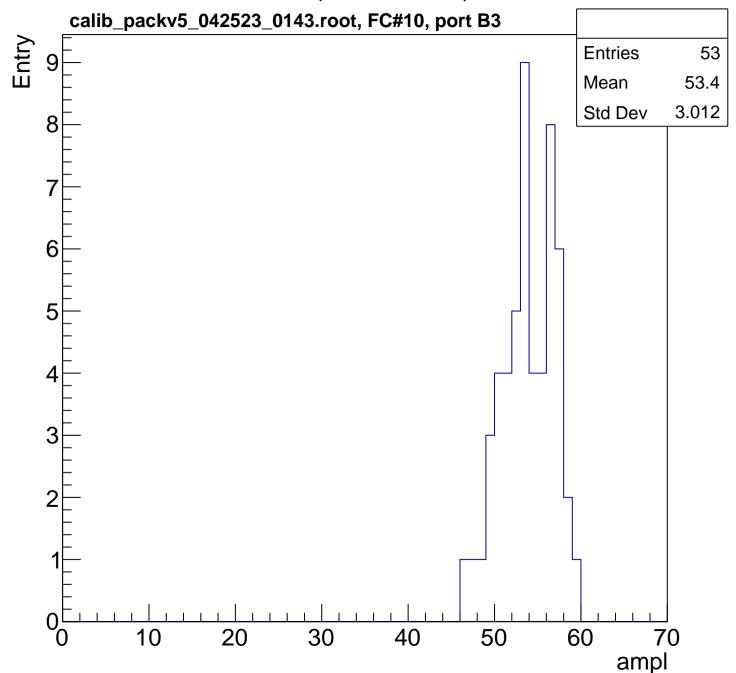


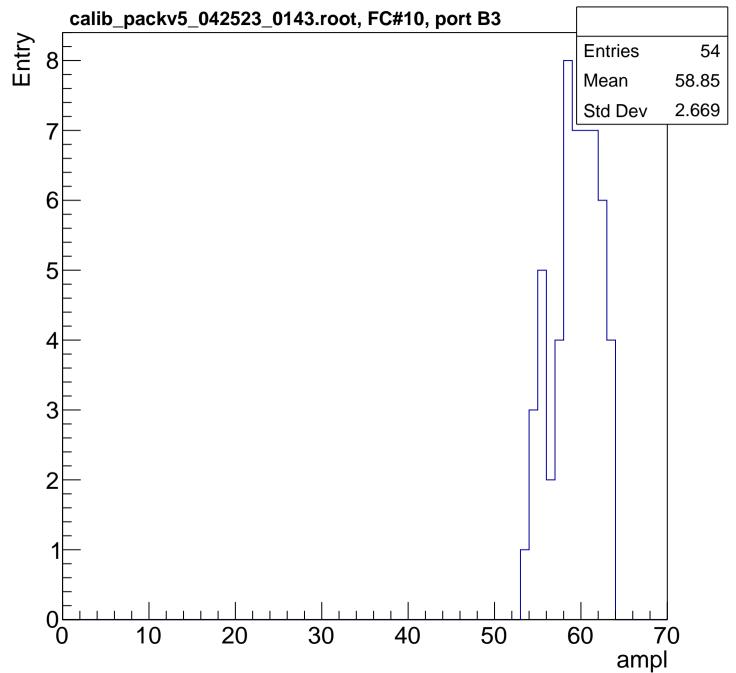


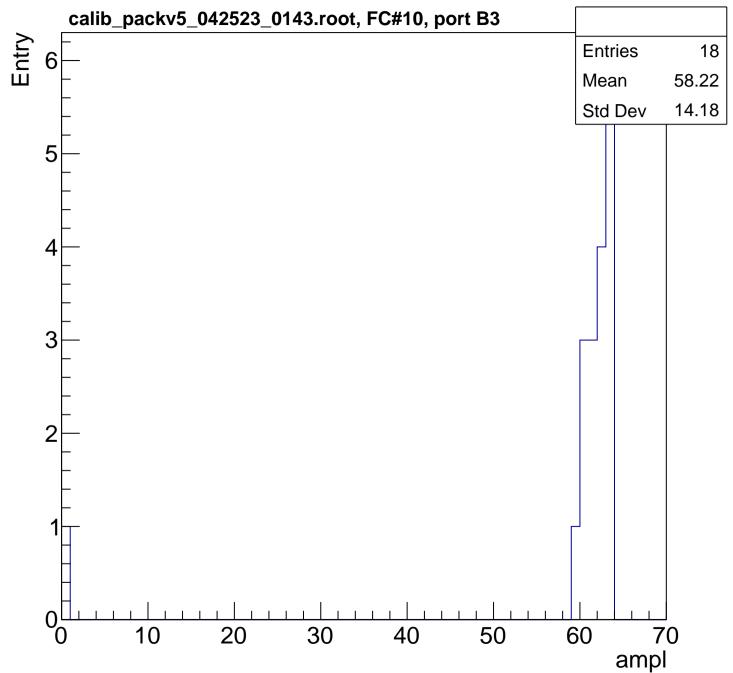






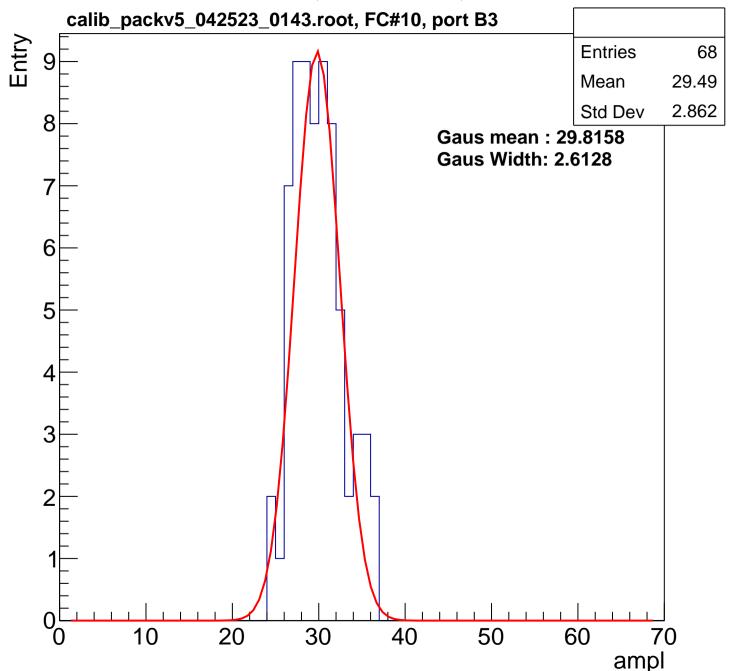


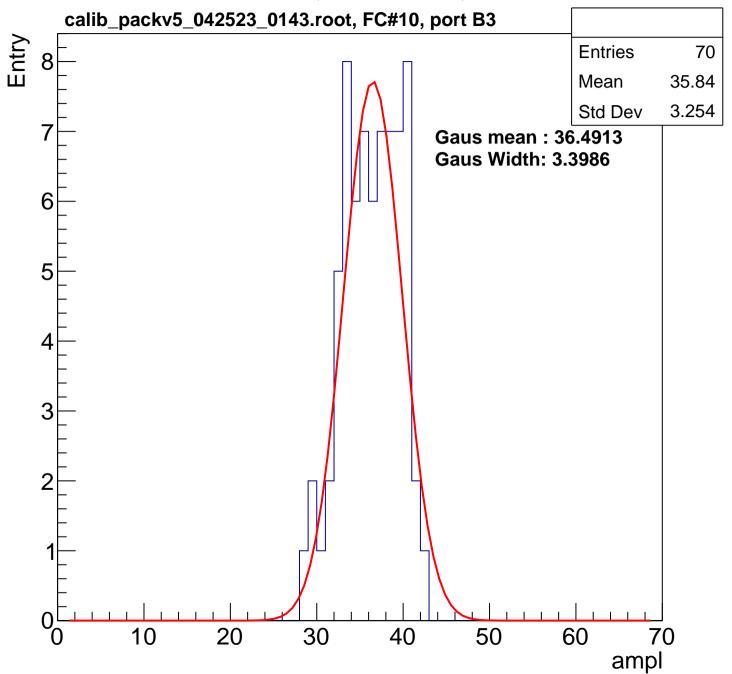


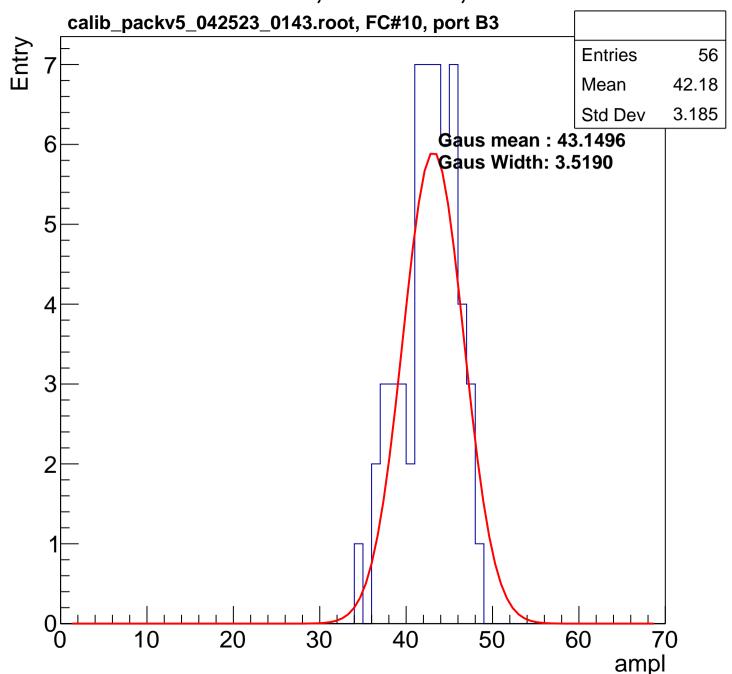


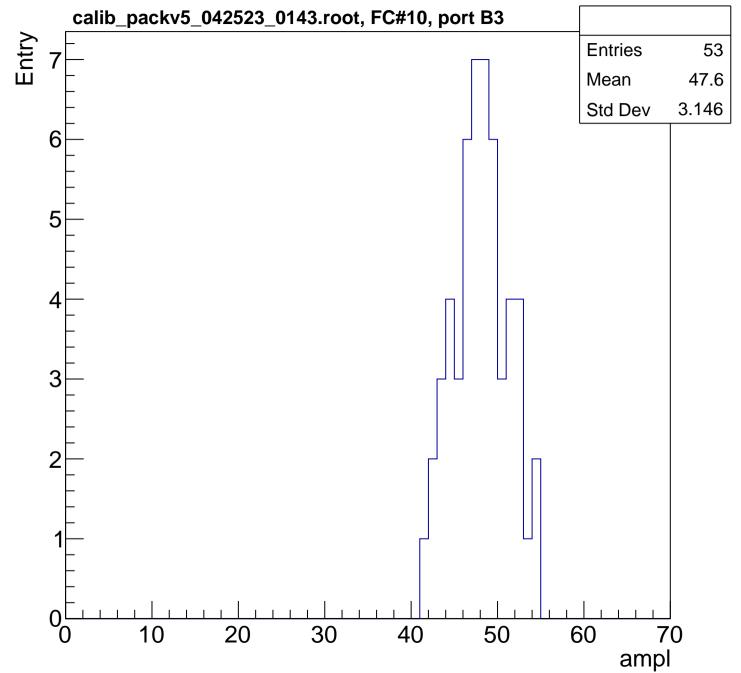
B1L002S, U9-ch89, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70

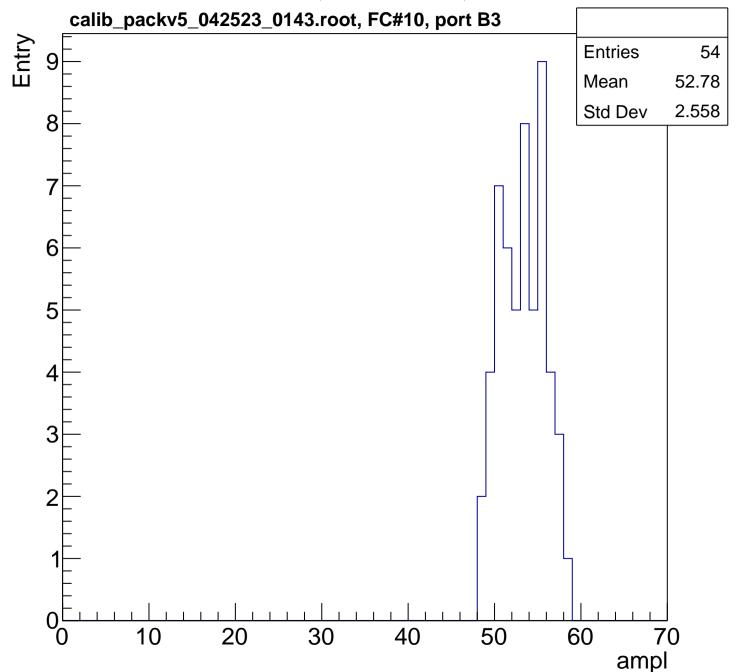
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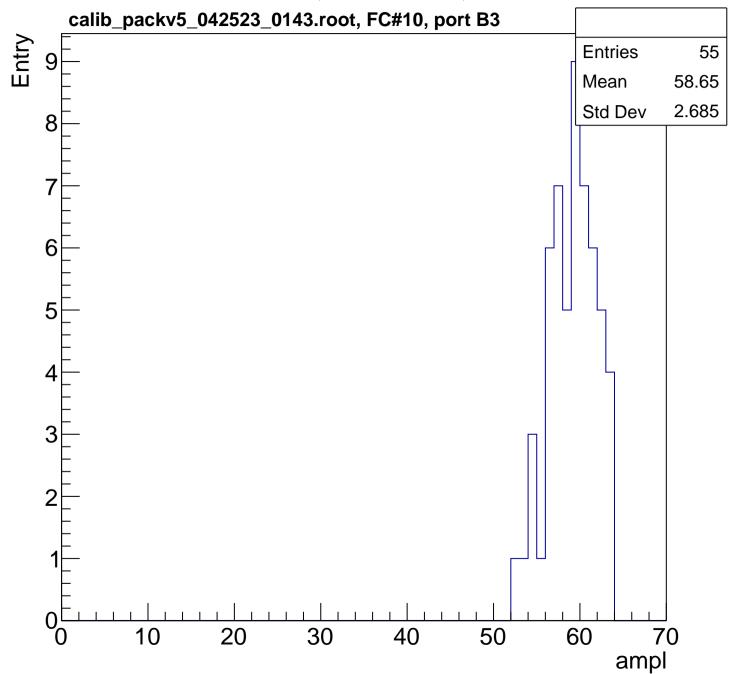


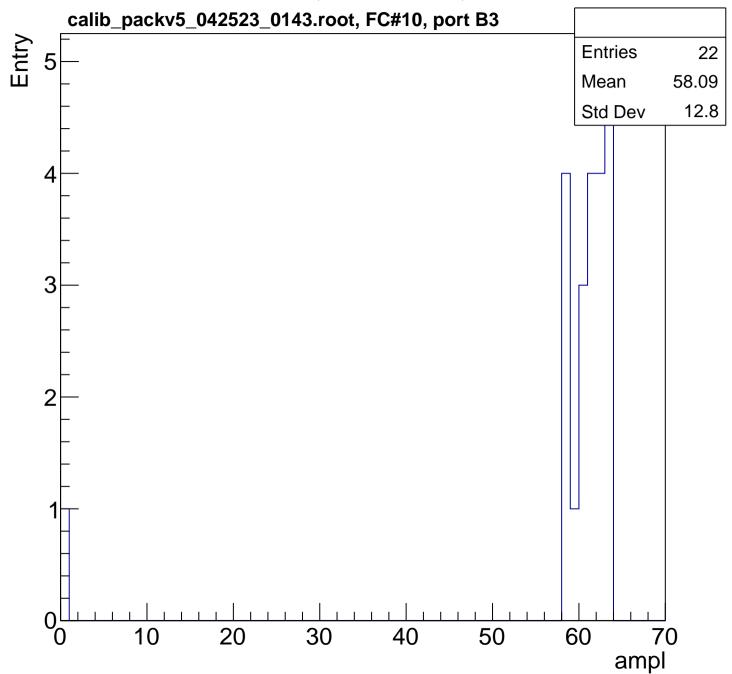


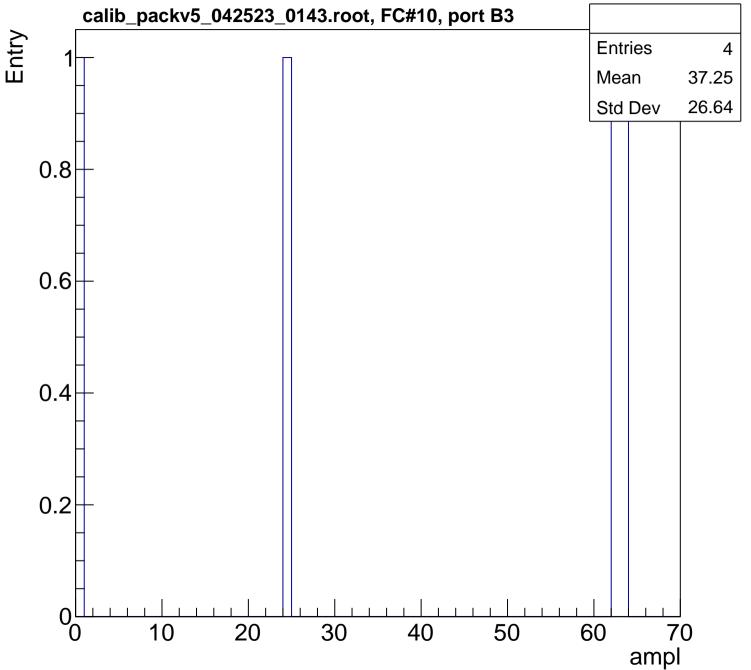


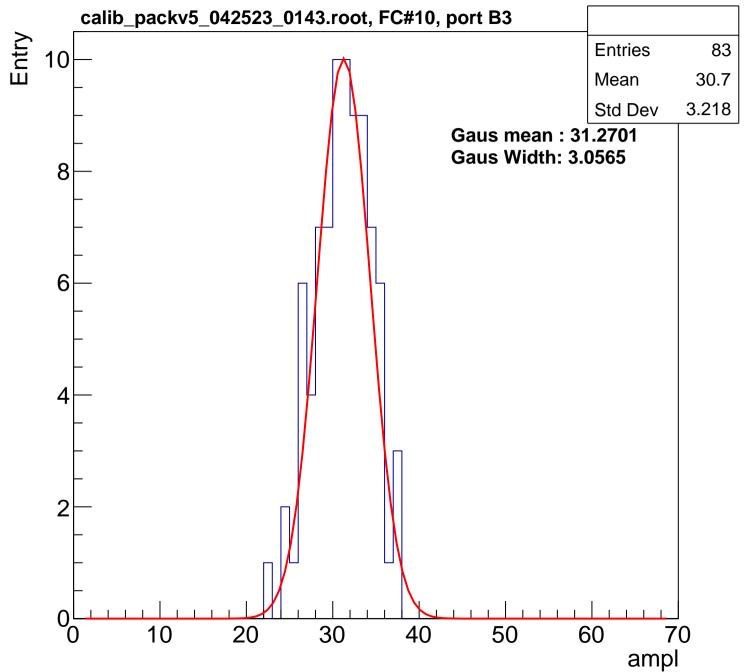


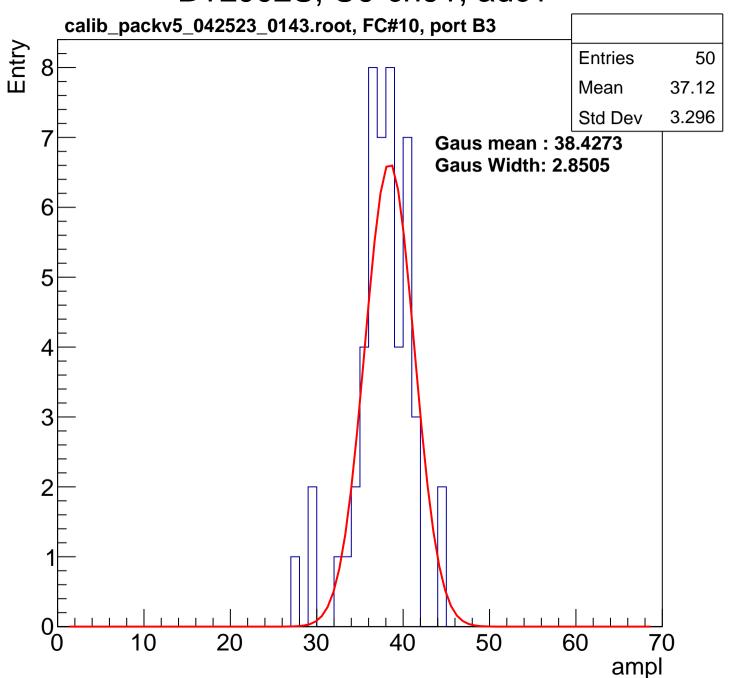


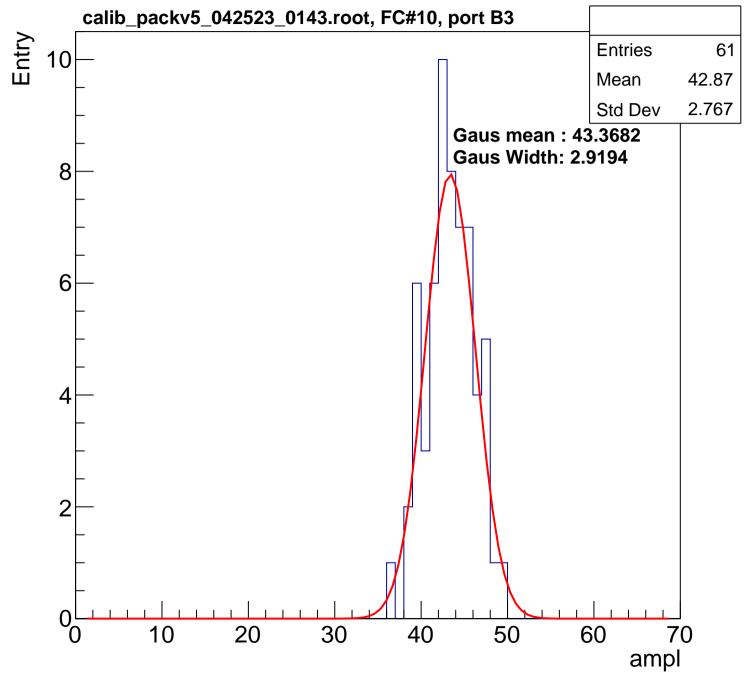


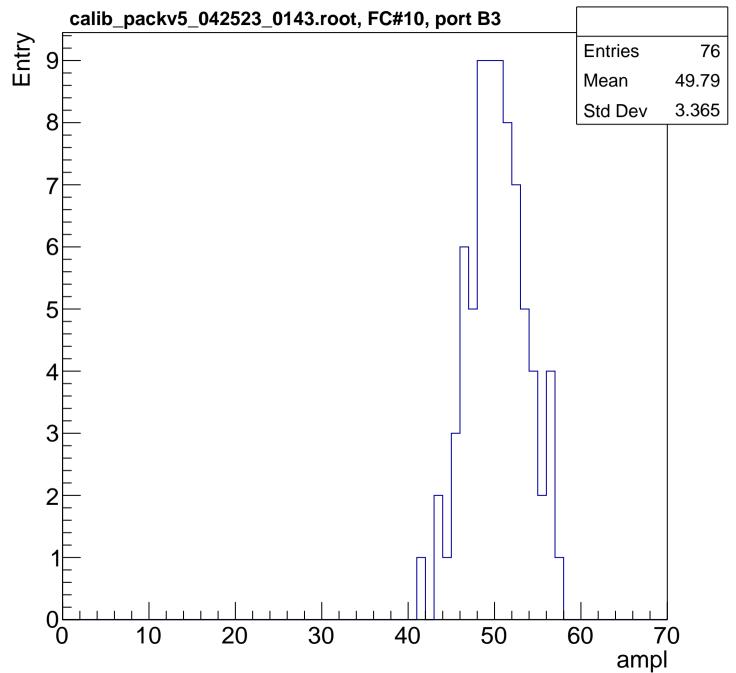


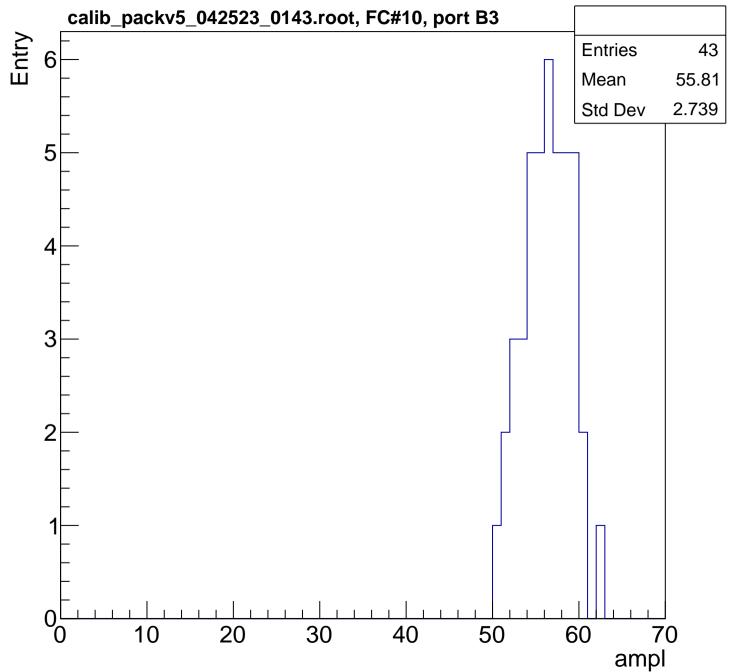


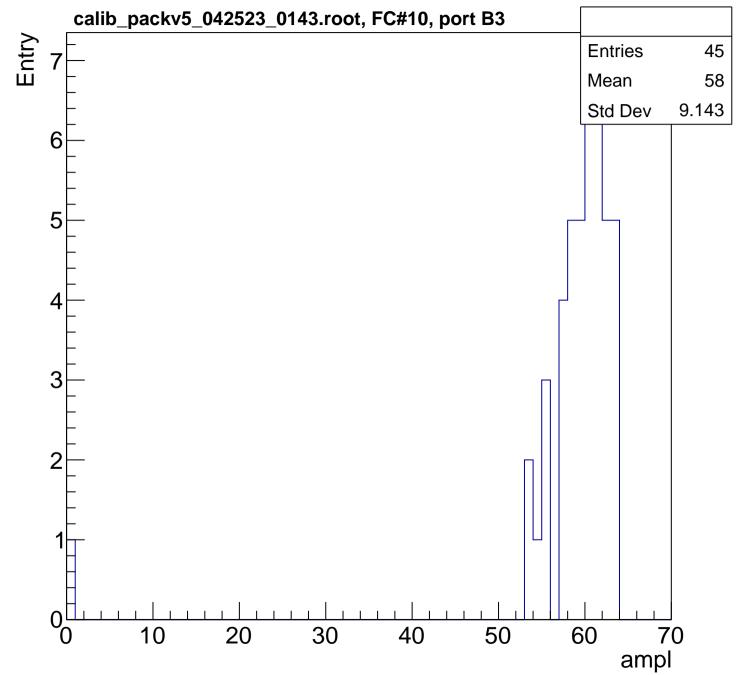


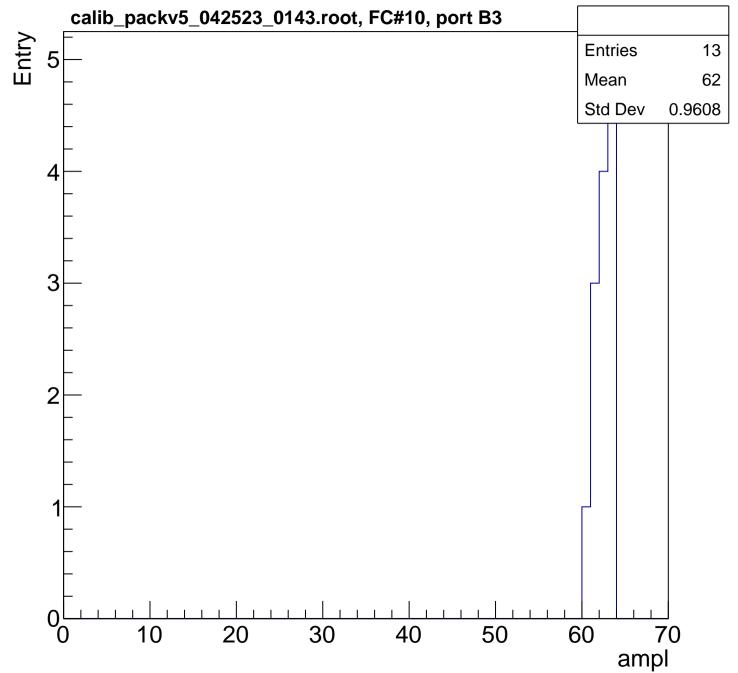




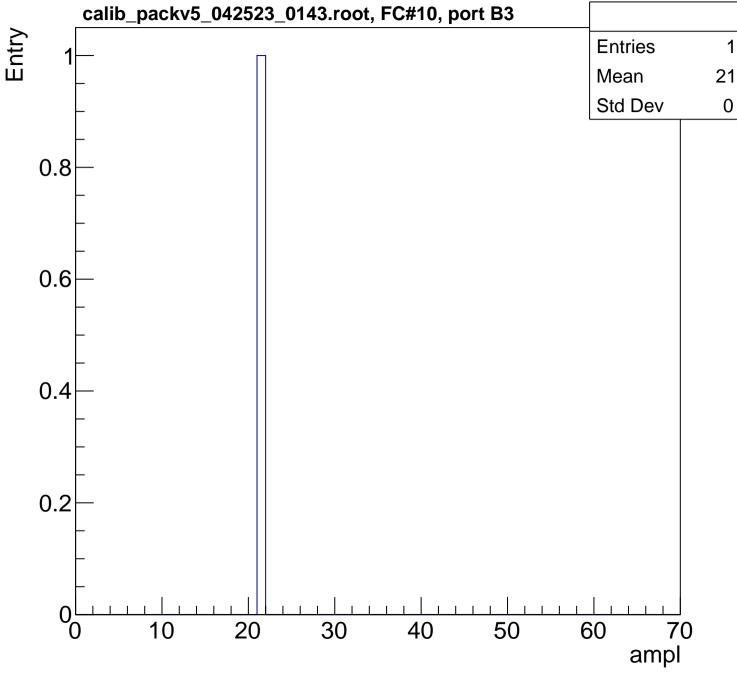


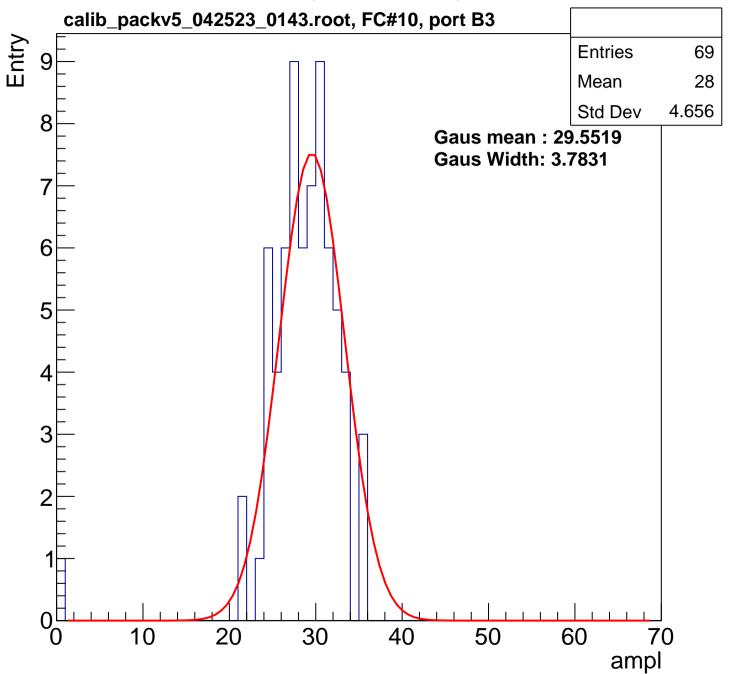


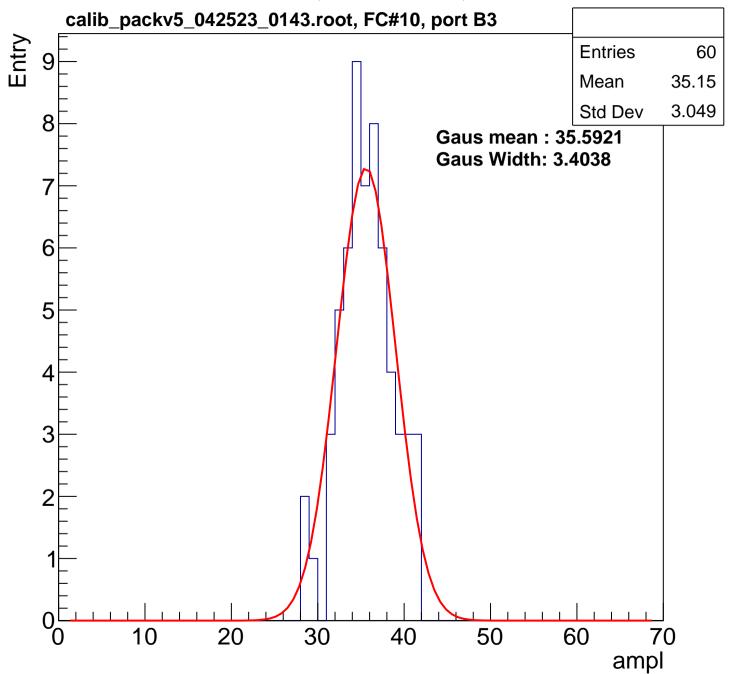


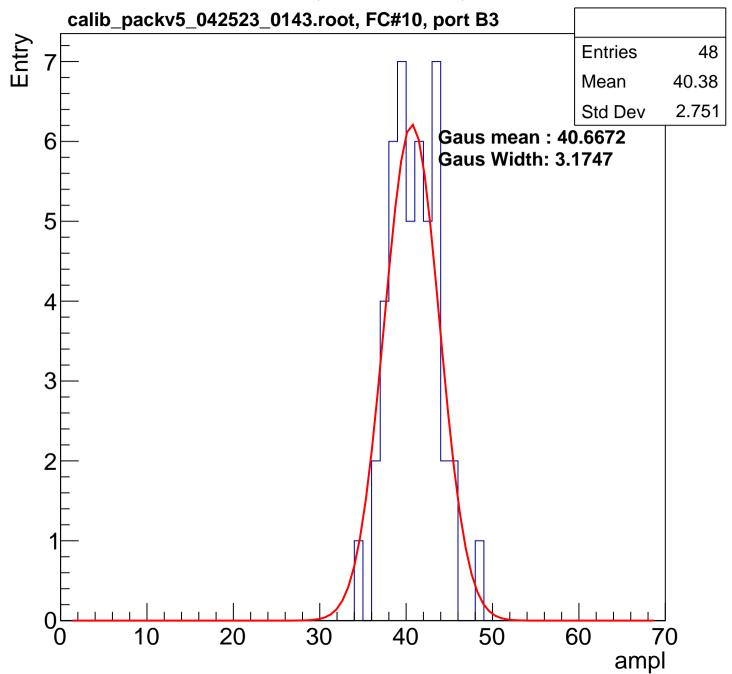


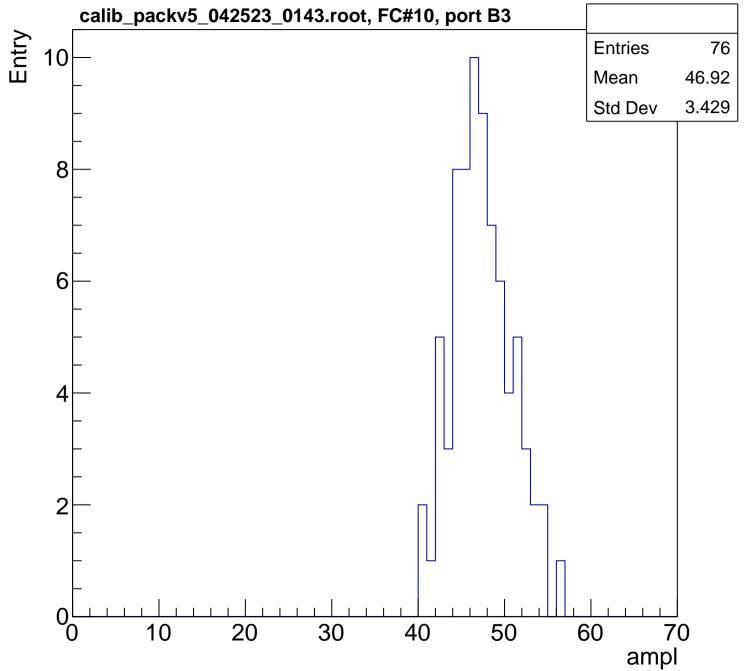
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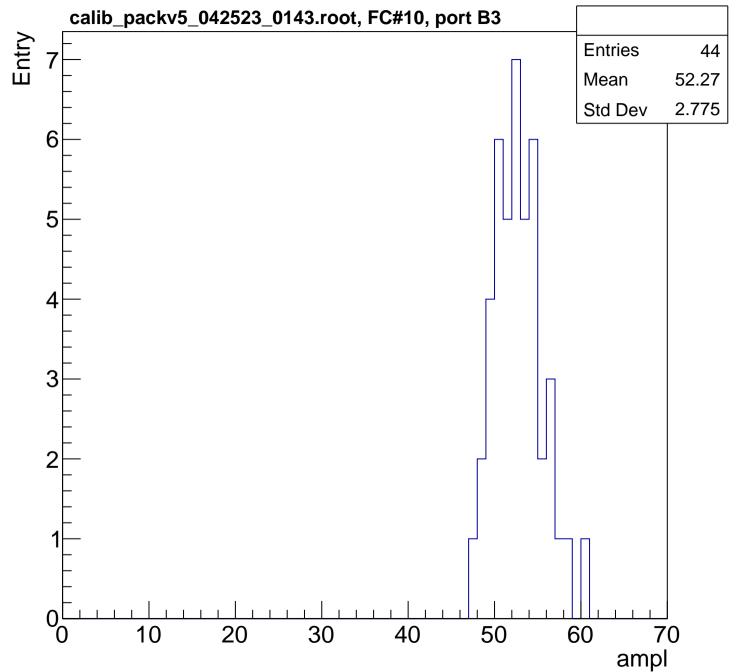


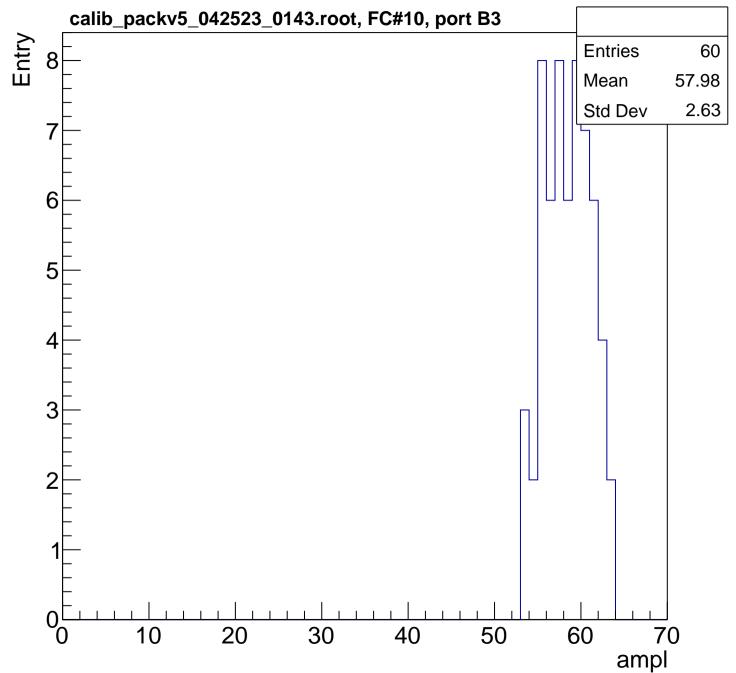


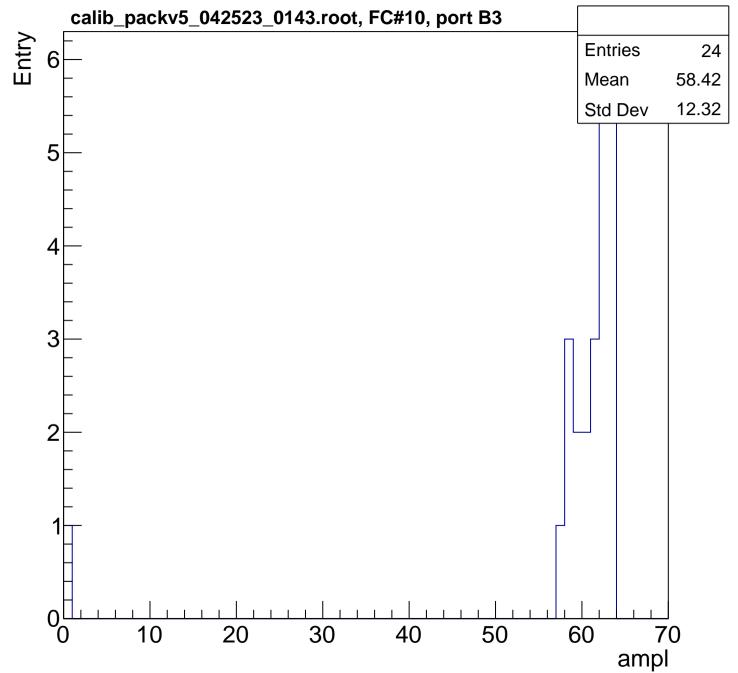


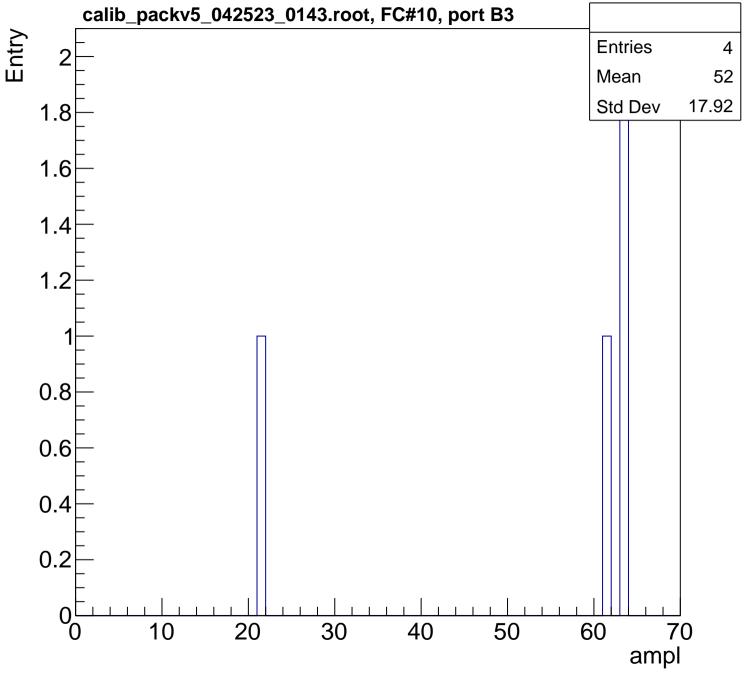


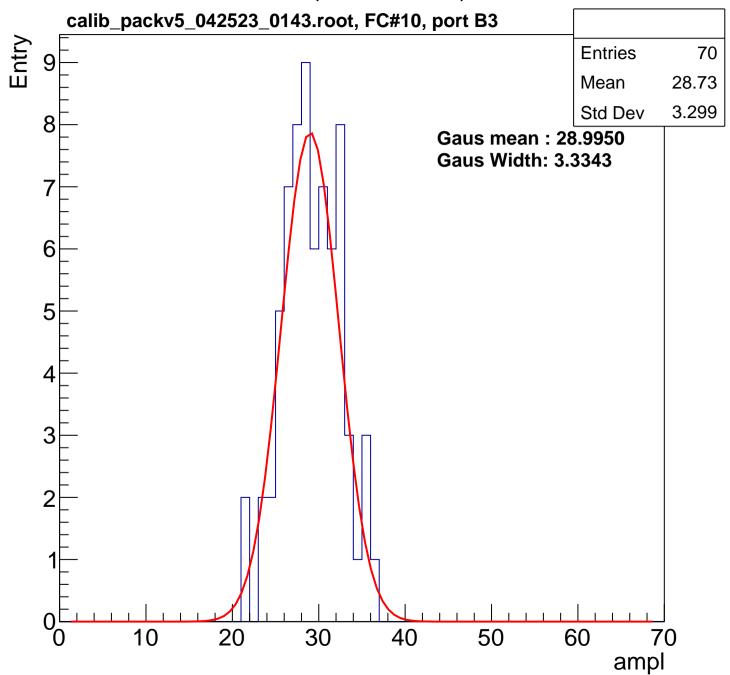


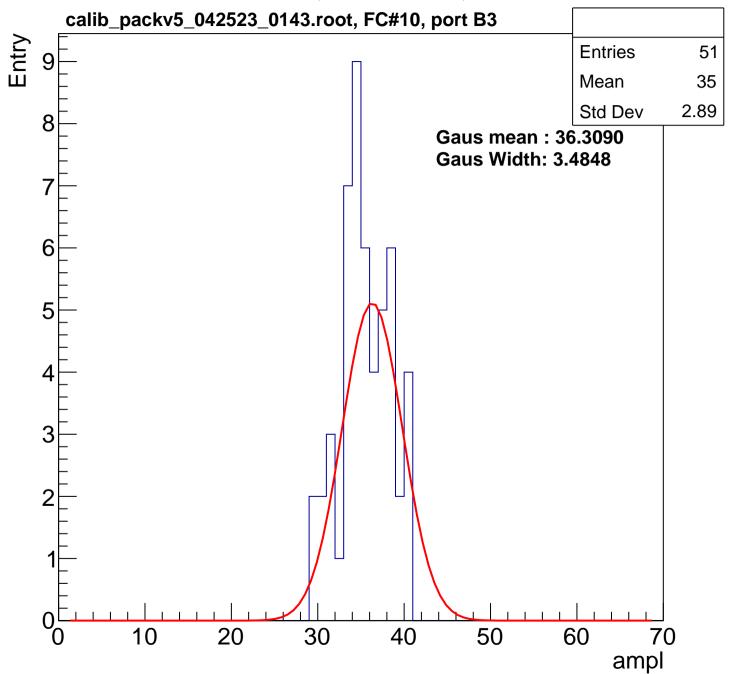


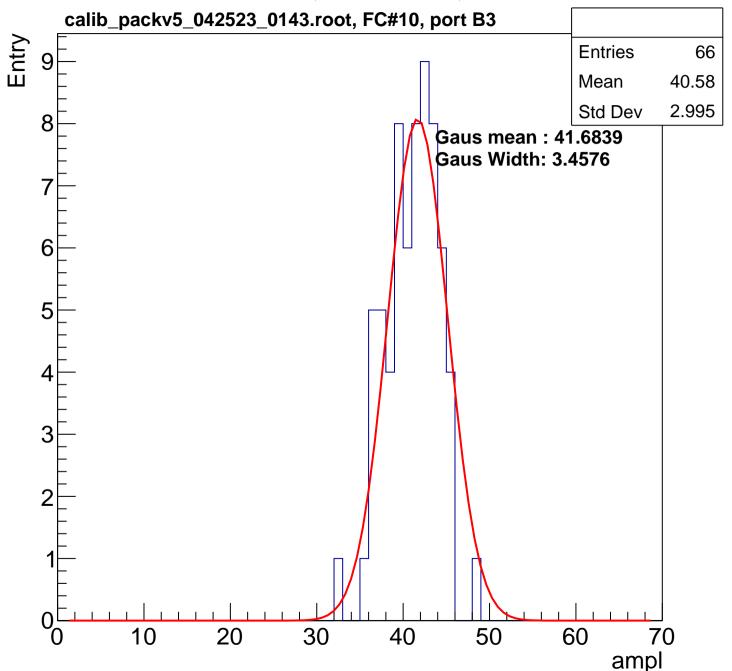


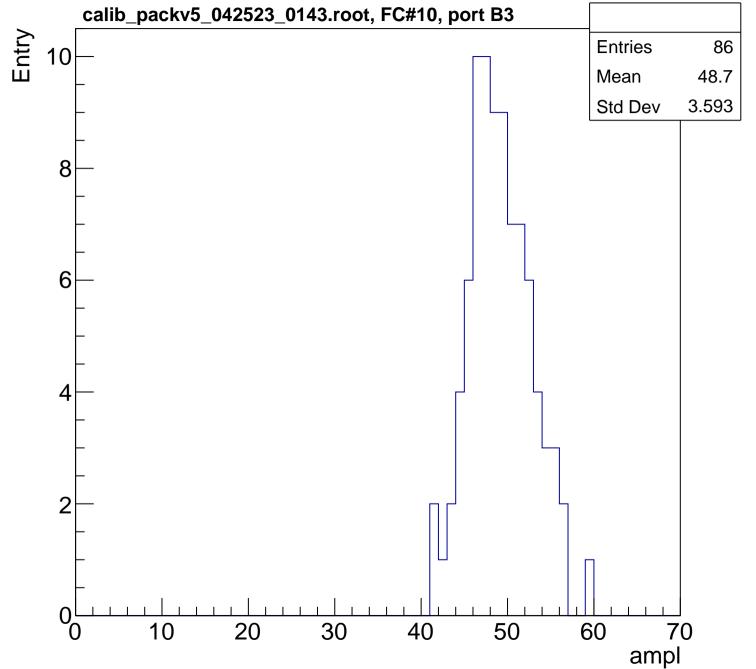


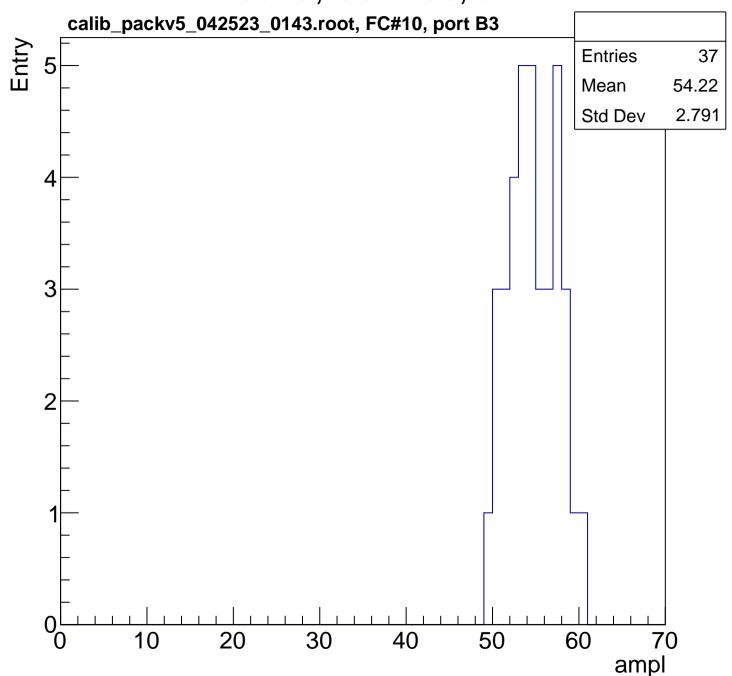


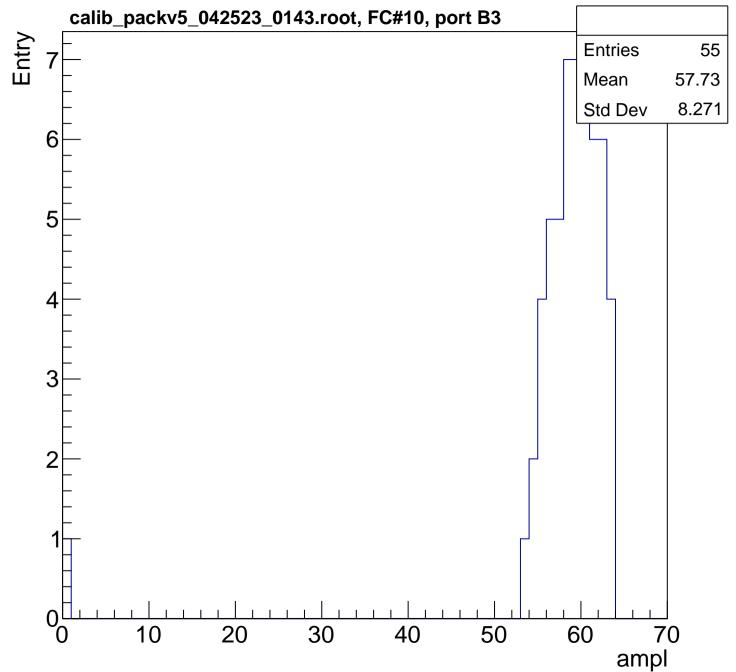


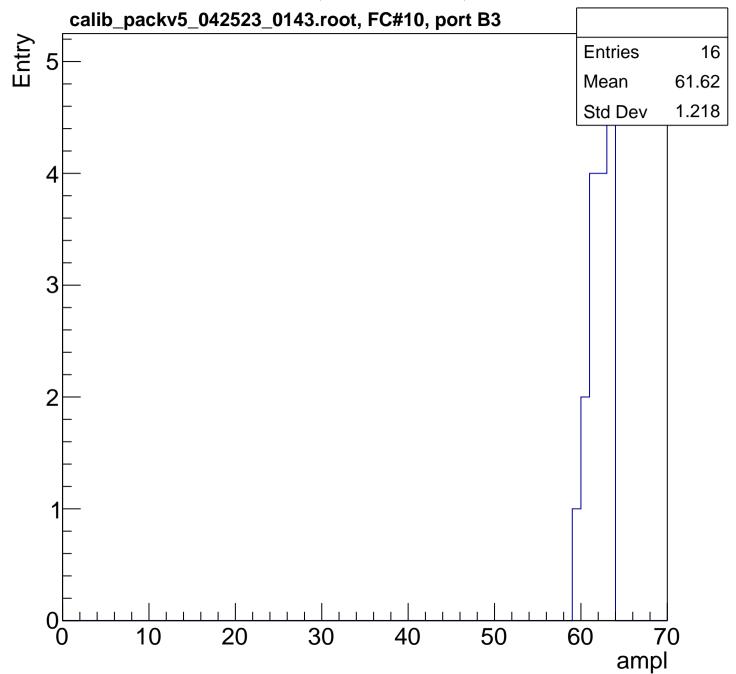




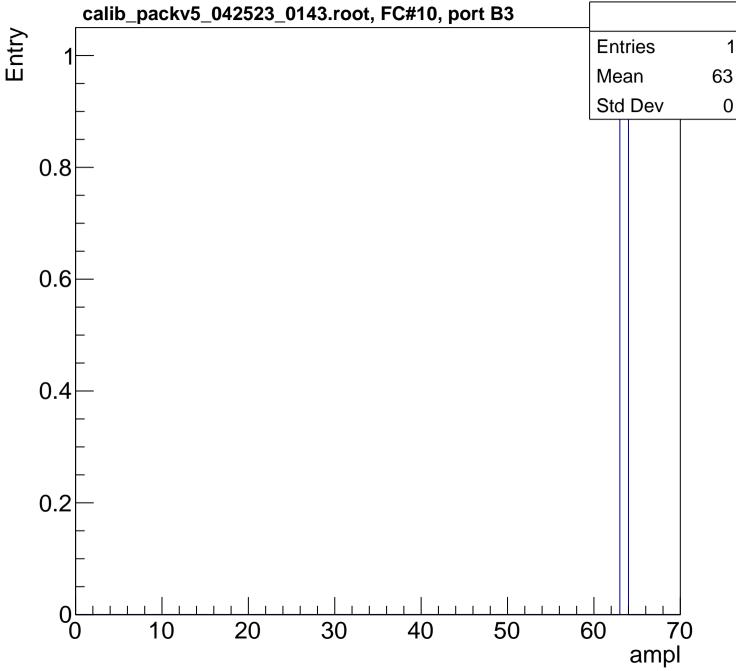


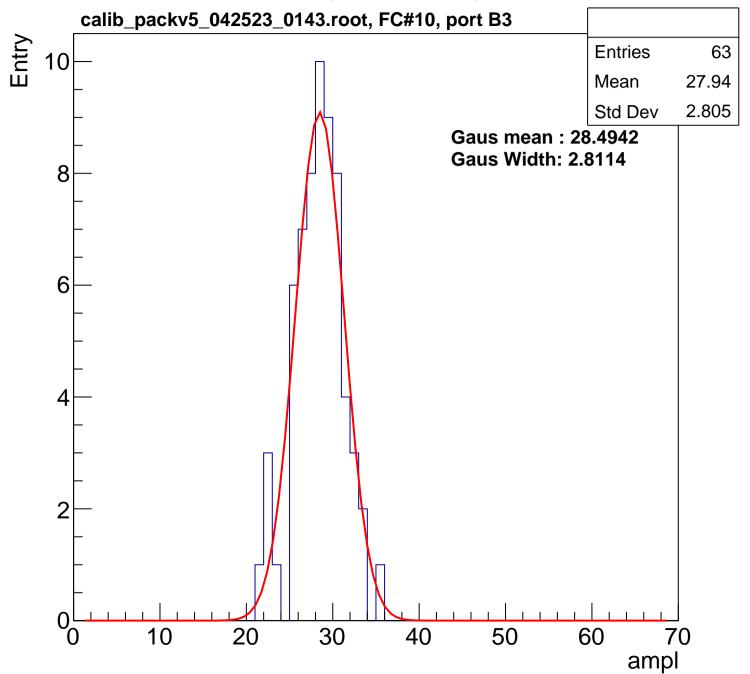


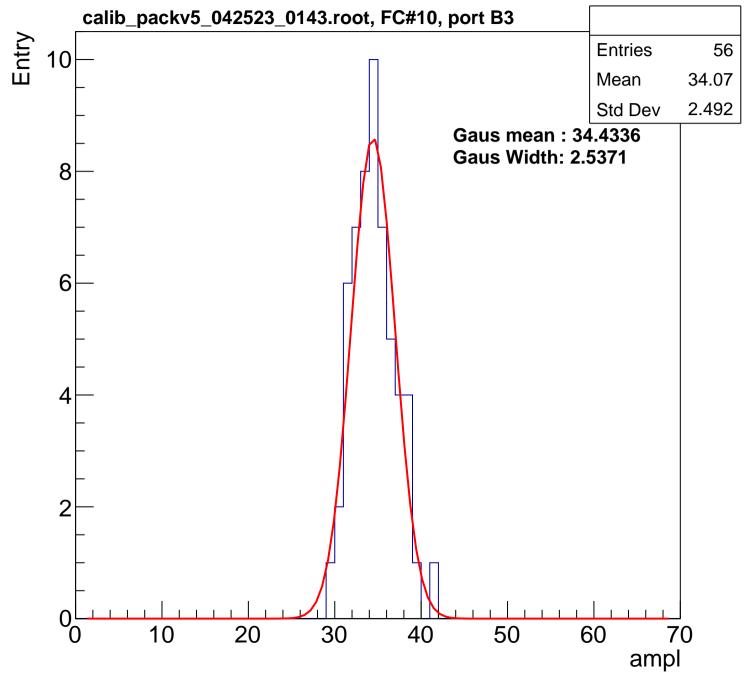


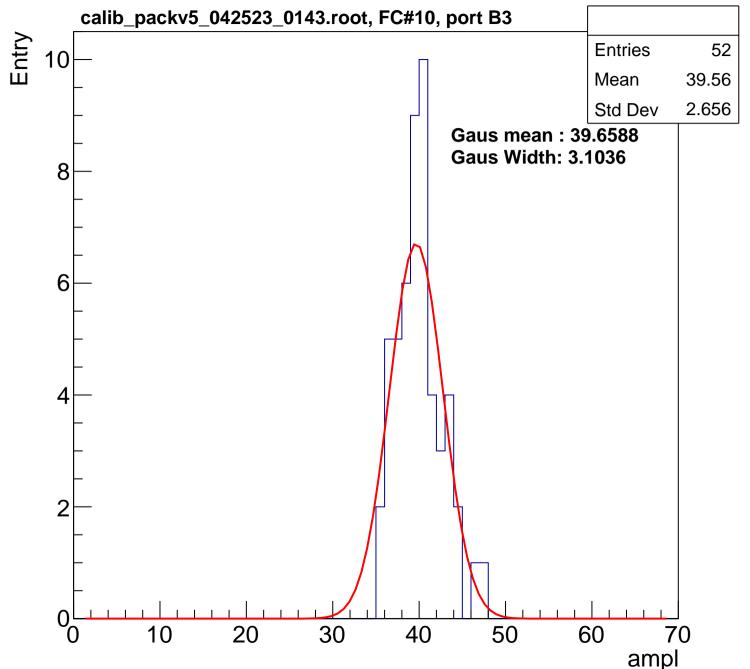


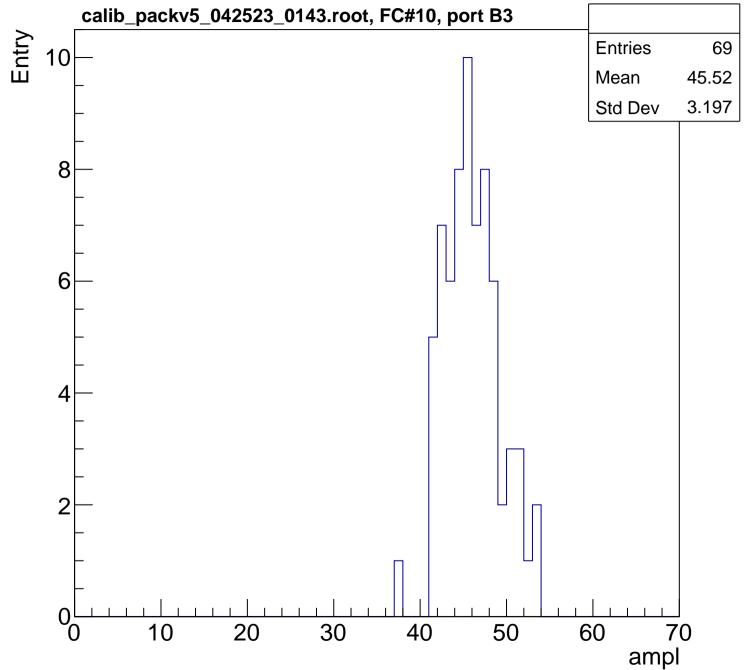
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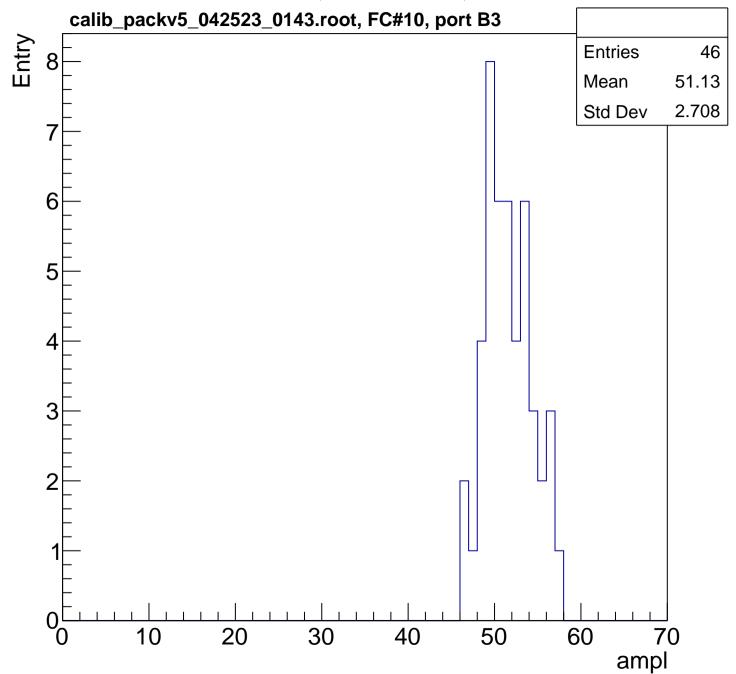


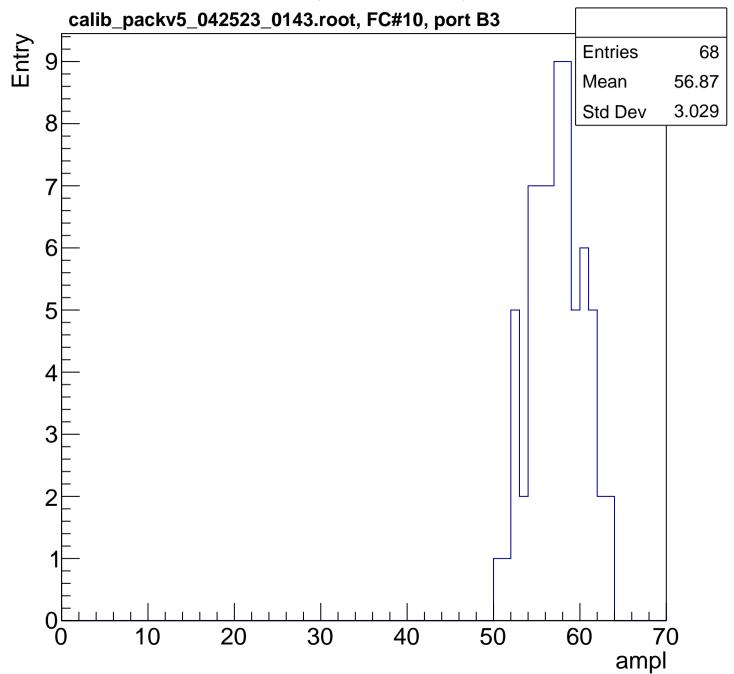


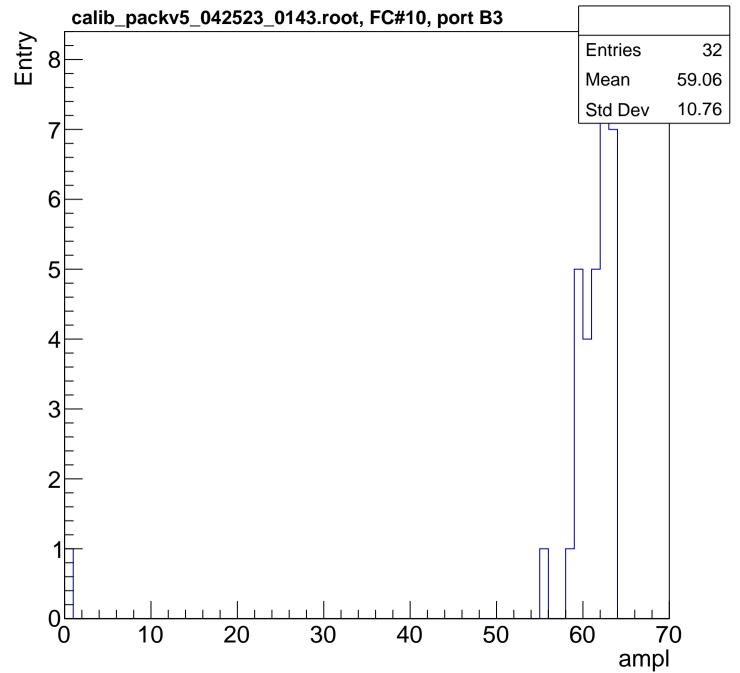


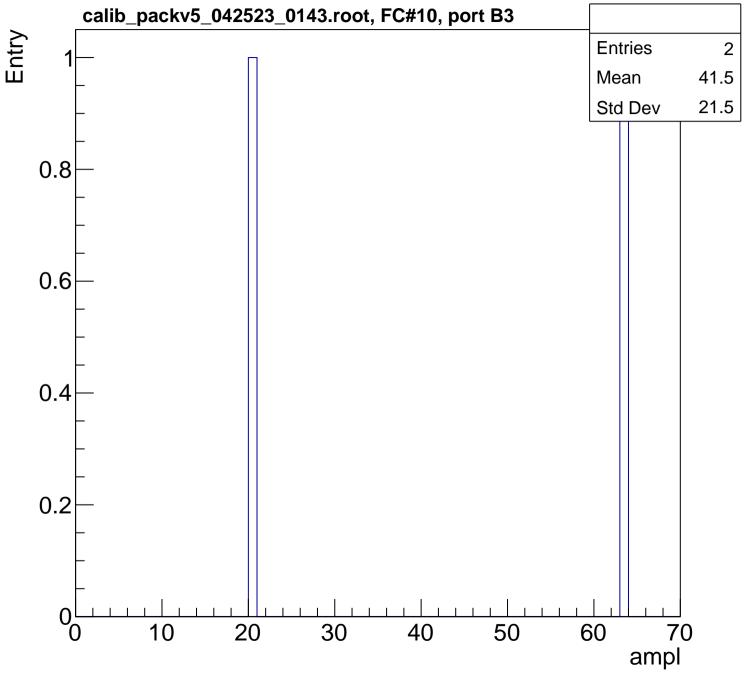


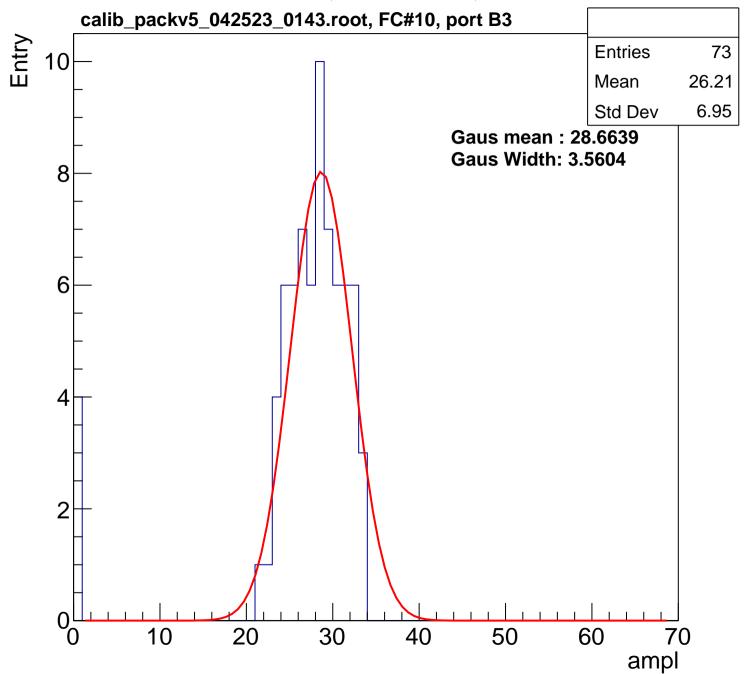


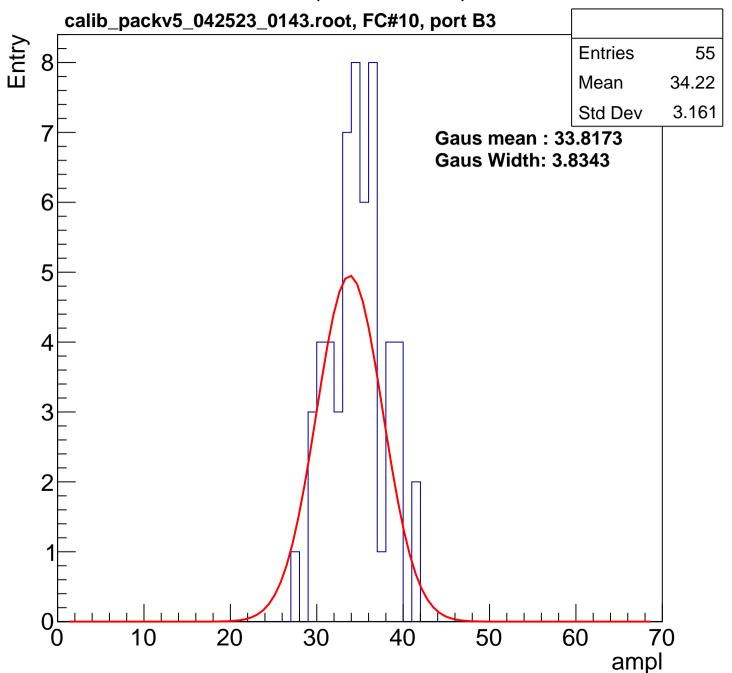


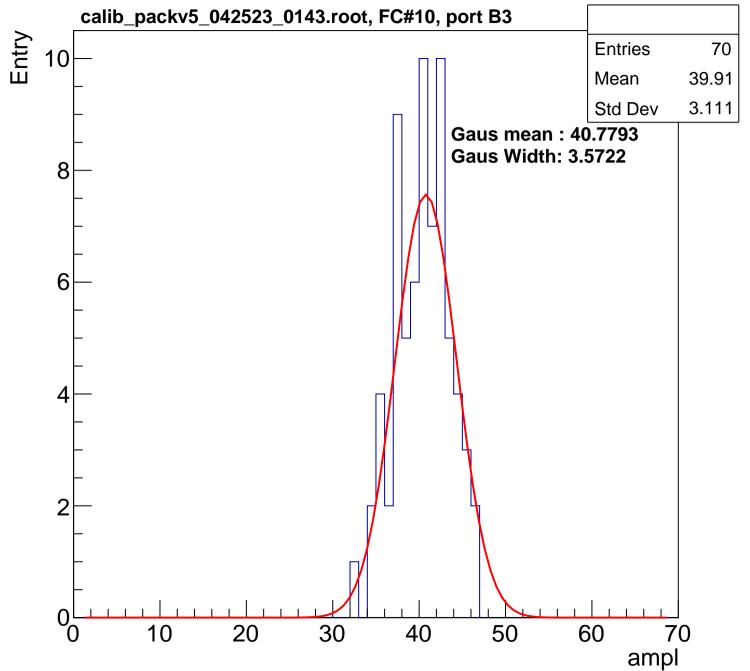


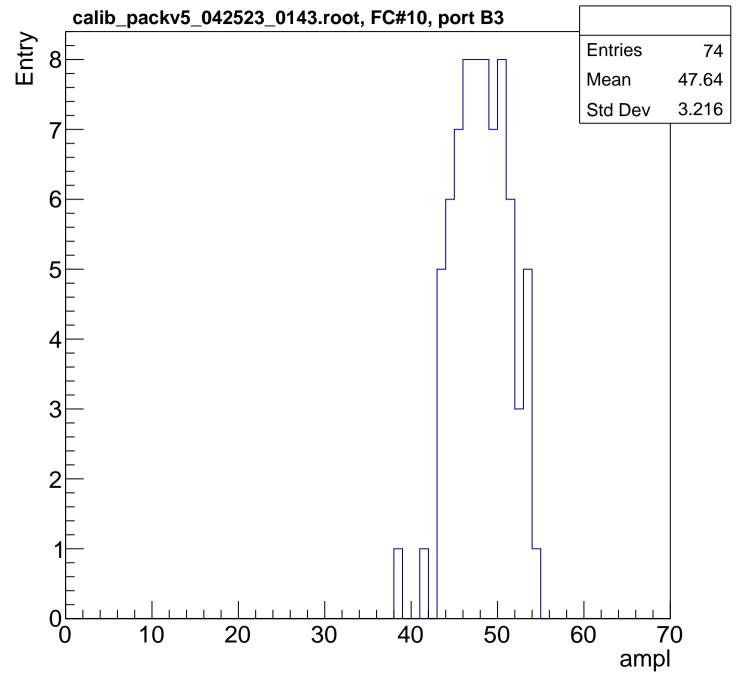


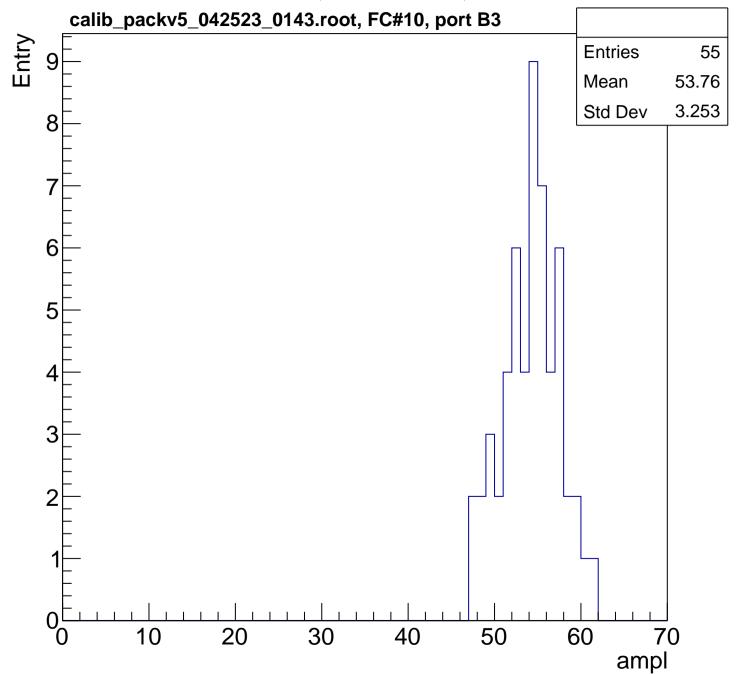


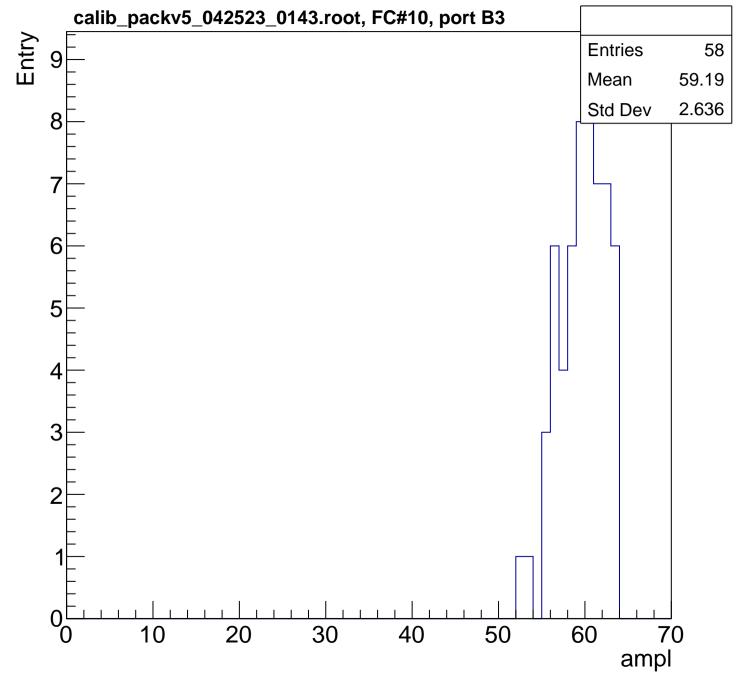


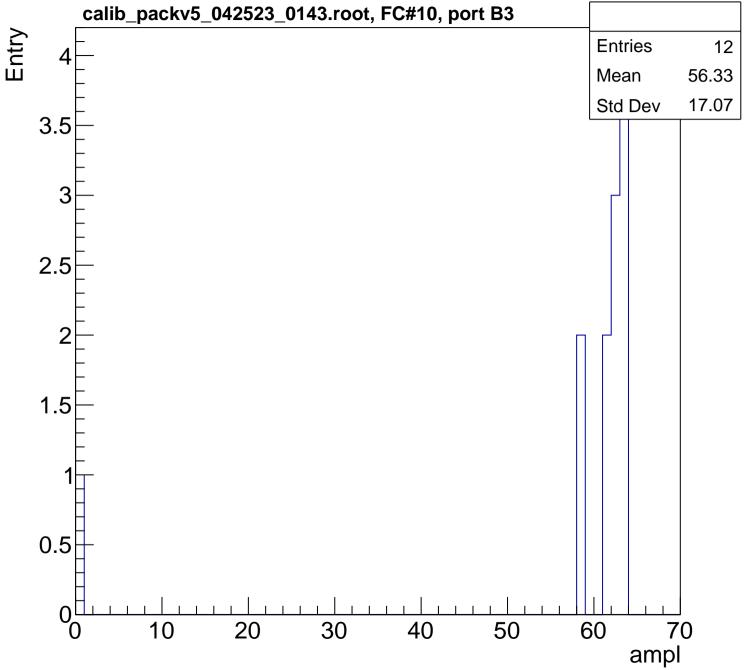




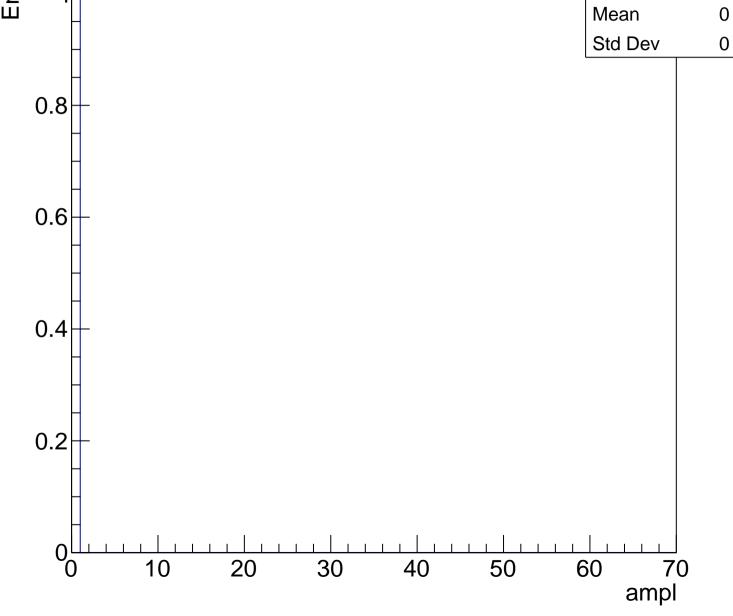


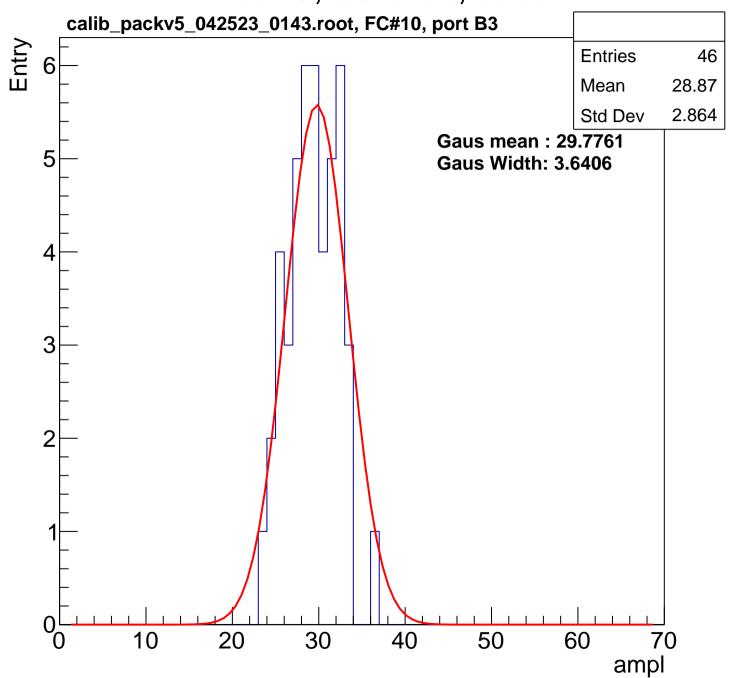


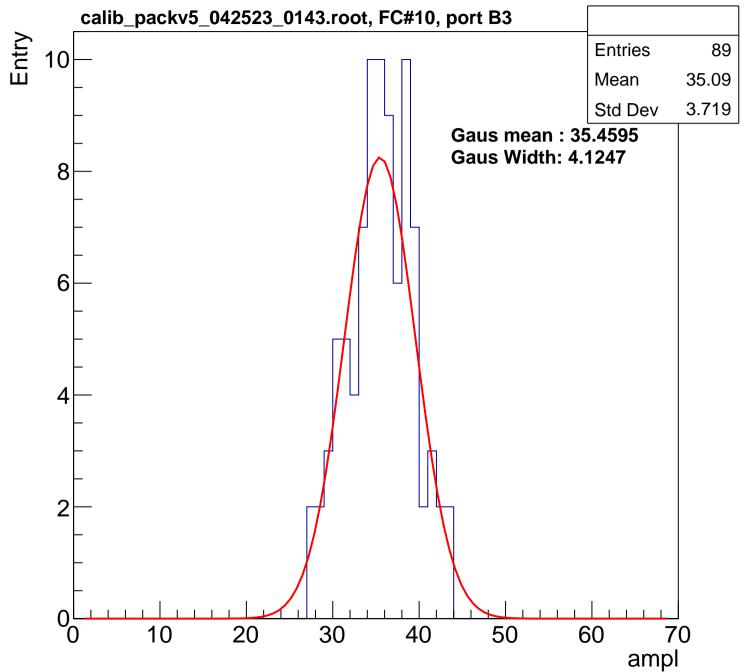


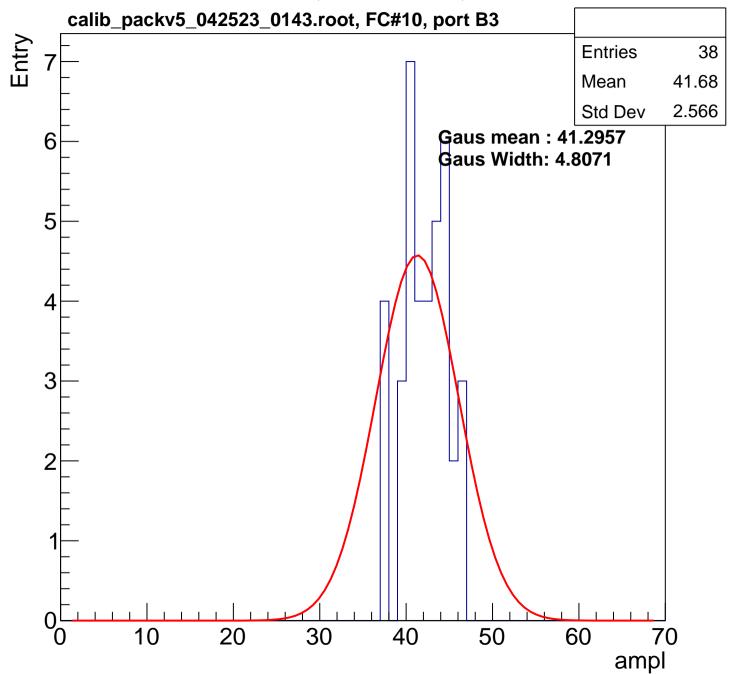


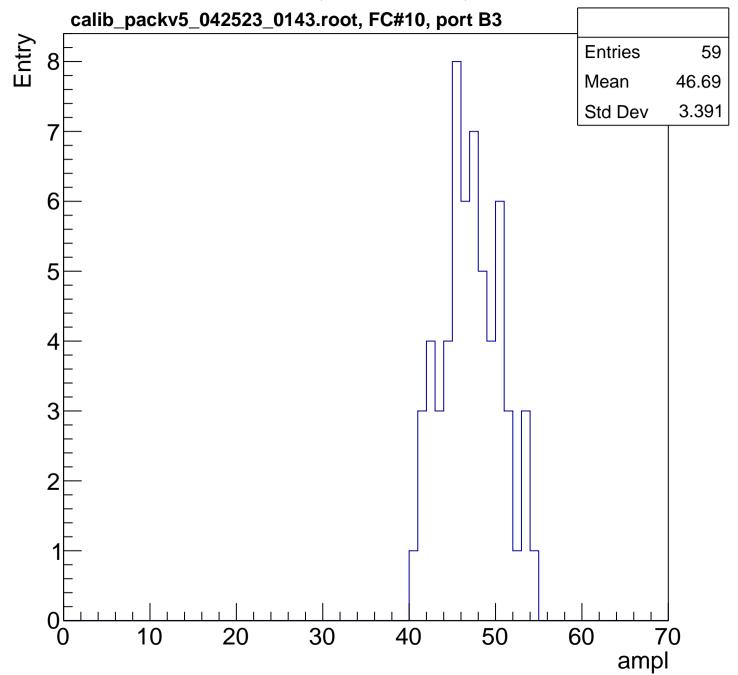
B1L002S, U9-ch95, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean Std Dev 8.0 0.6 0.4 0.2

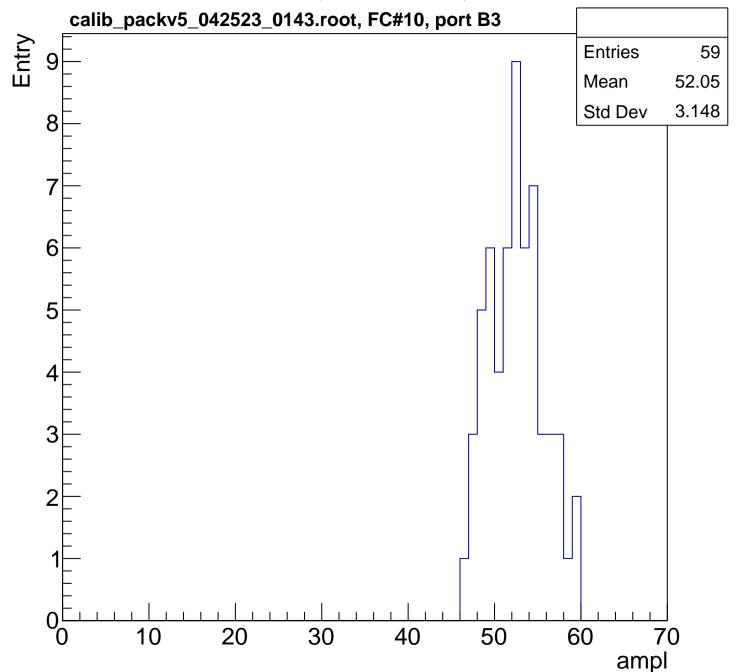


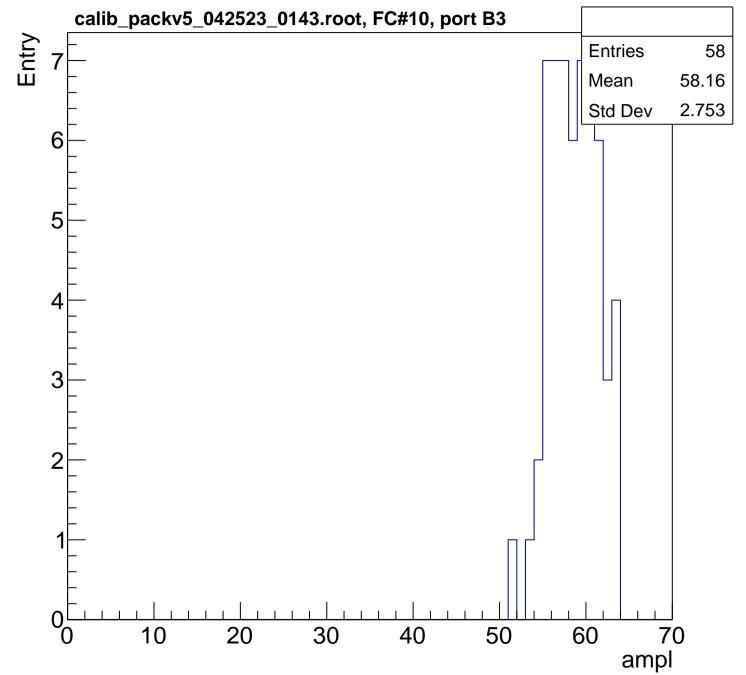


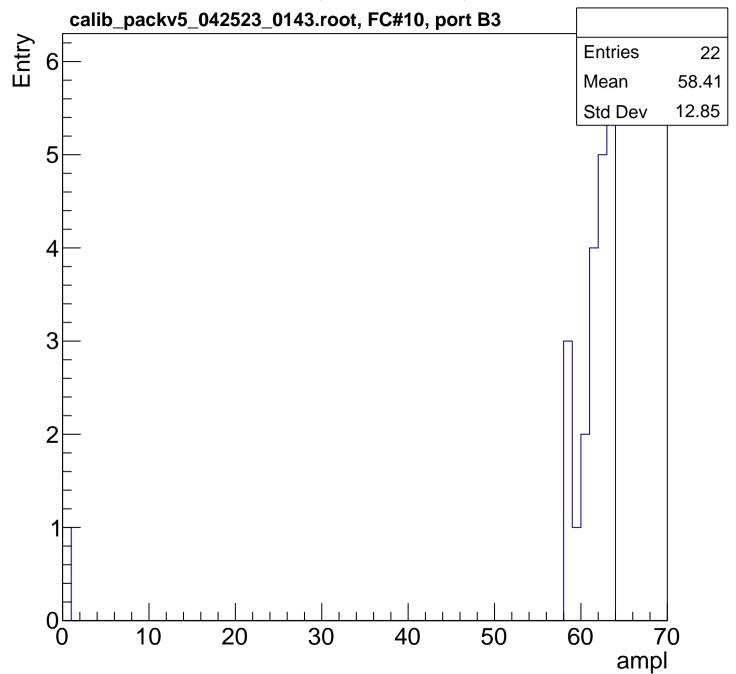


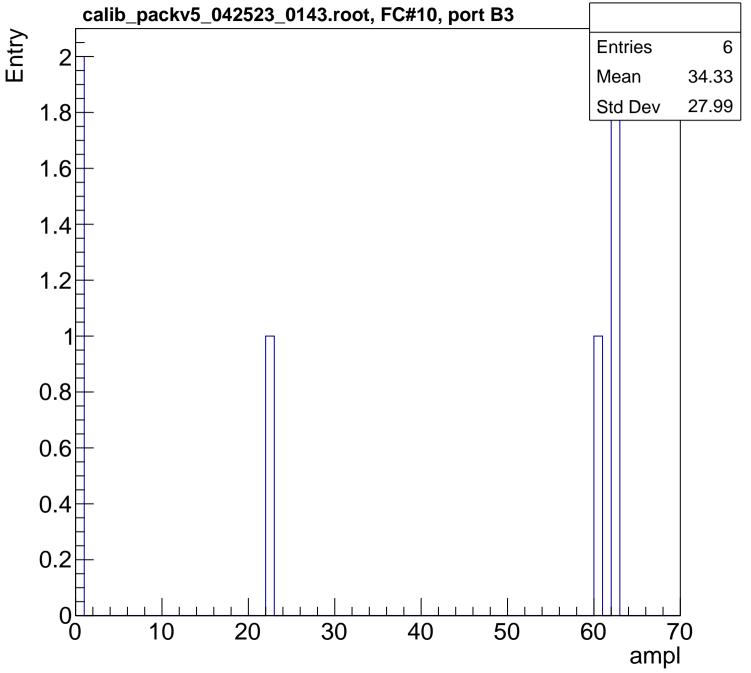


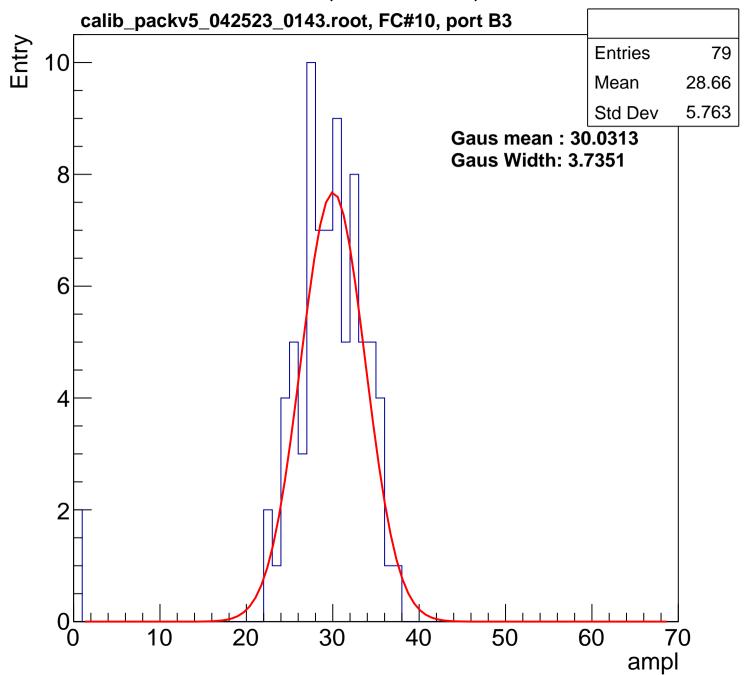


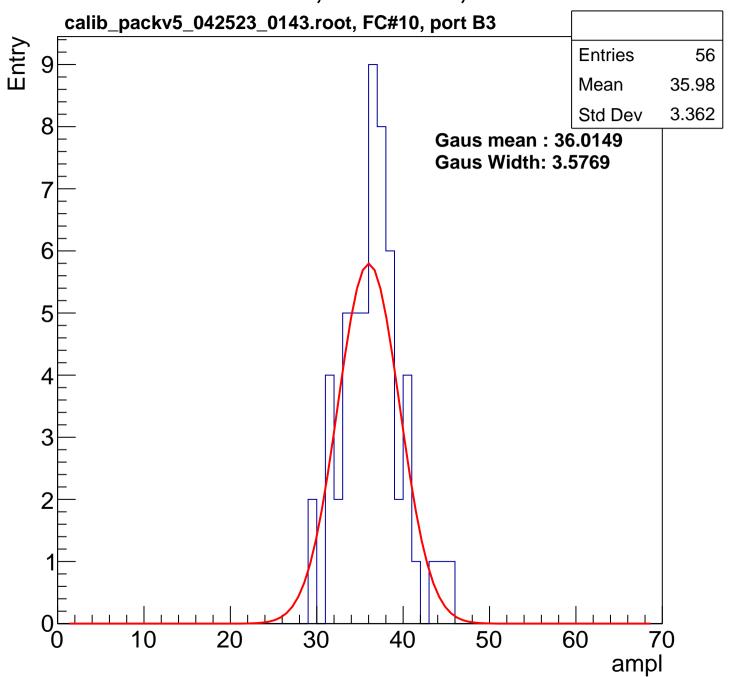


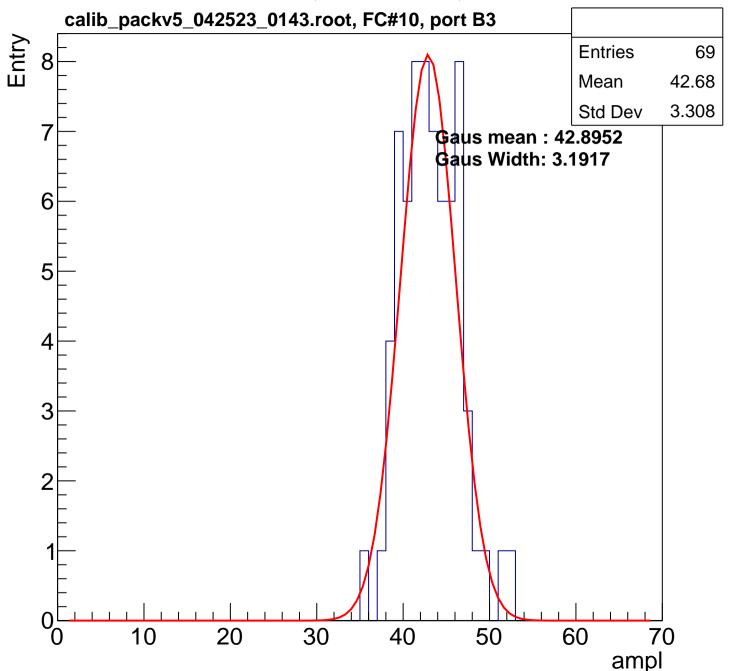


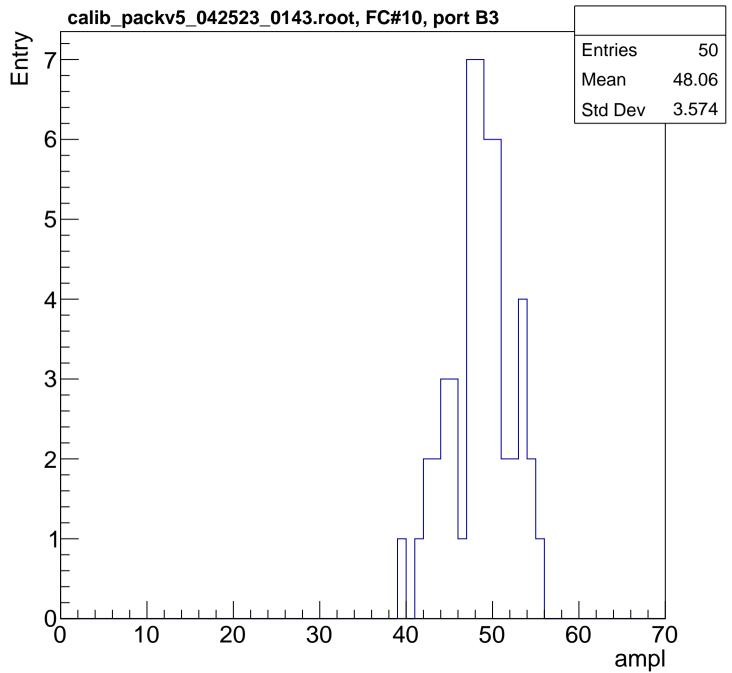


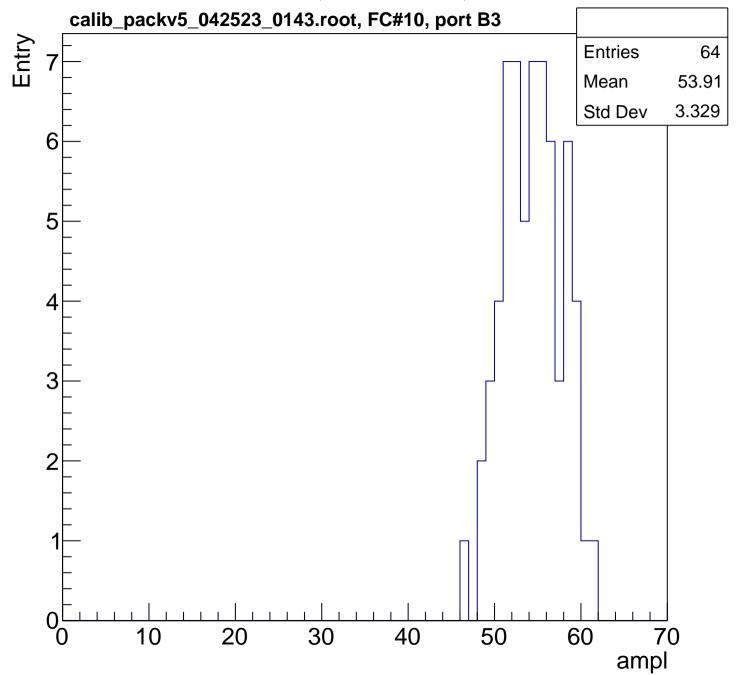


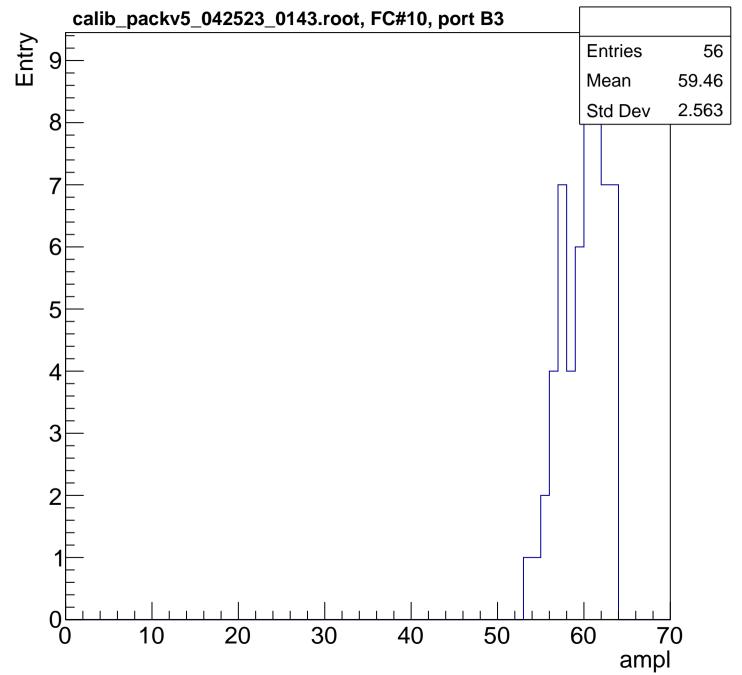


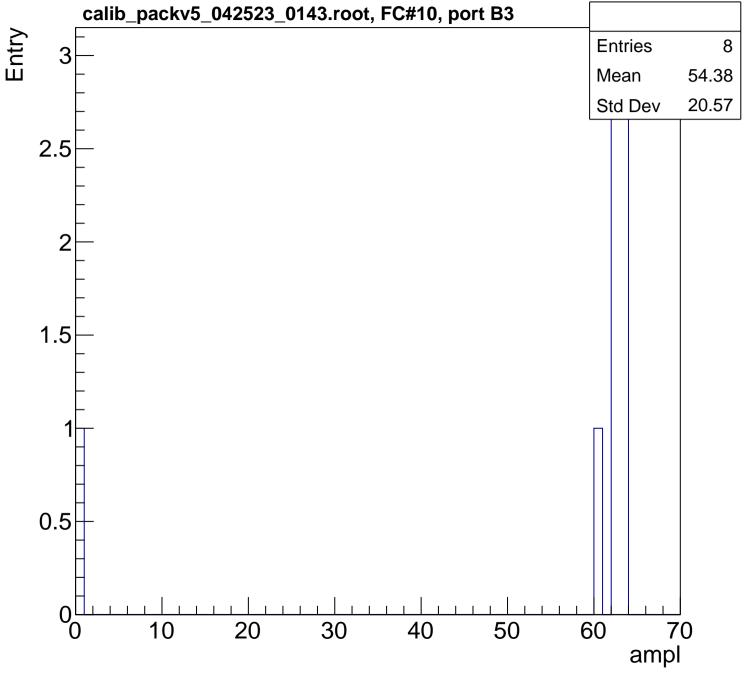




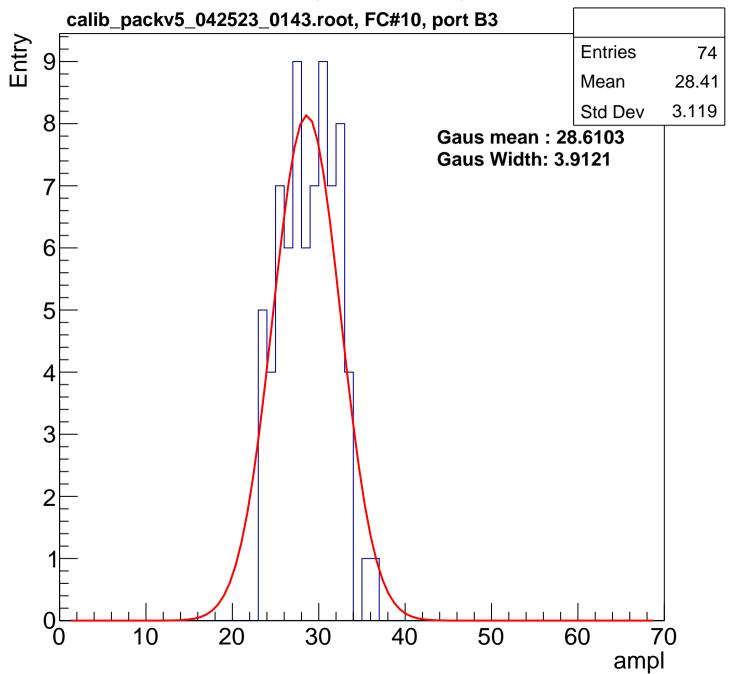


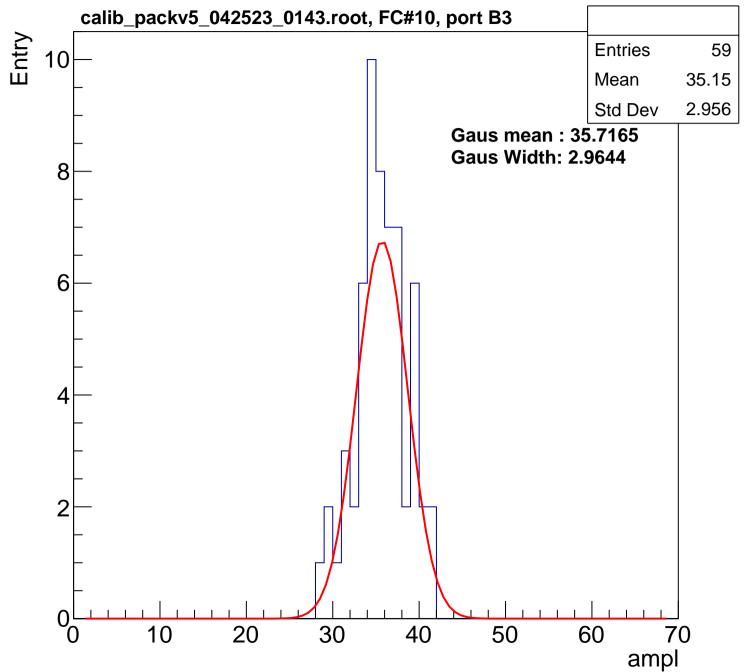


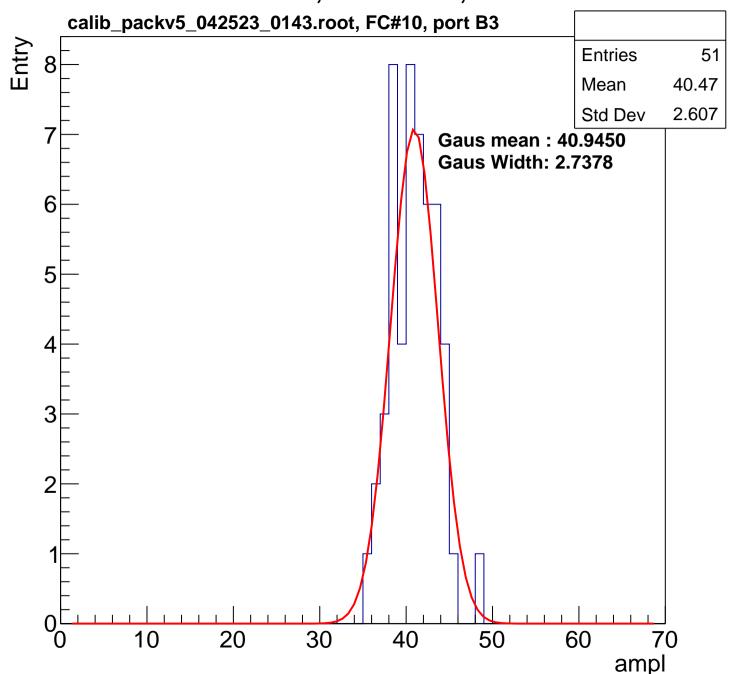


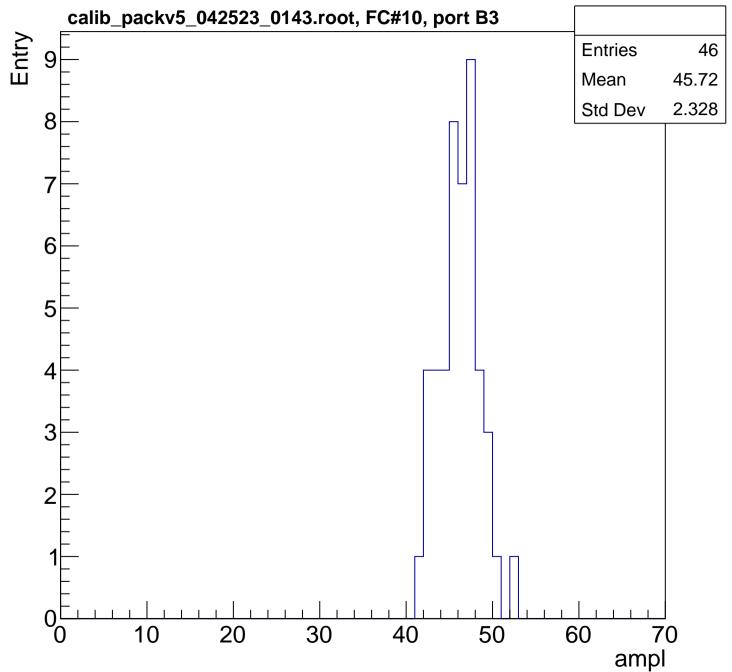


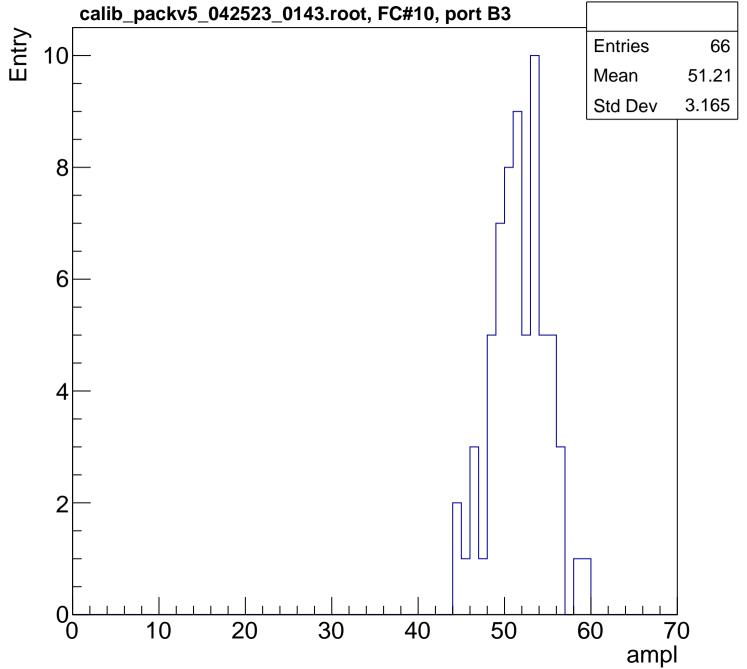
B1L002S, U9-ch97, adc7 calib_packv5_042523_0143.root, FC#10, port B3 Entry **Entries** 1 Mean 0 Std Dev 0 8.0 0.6 0.4 0.2 10 30 20 40 50 60 70 ampl

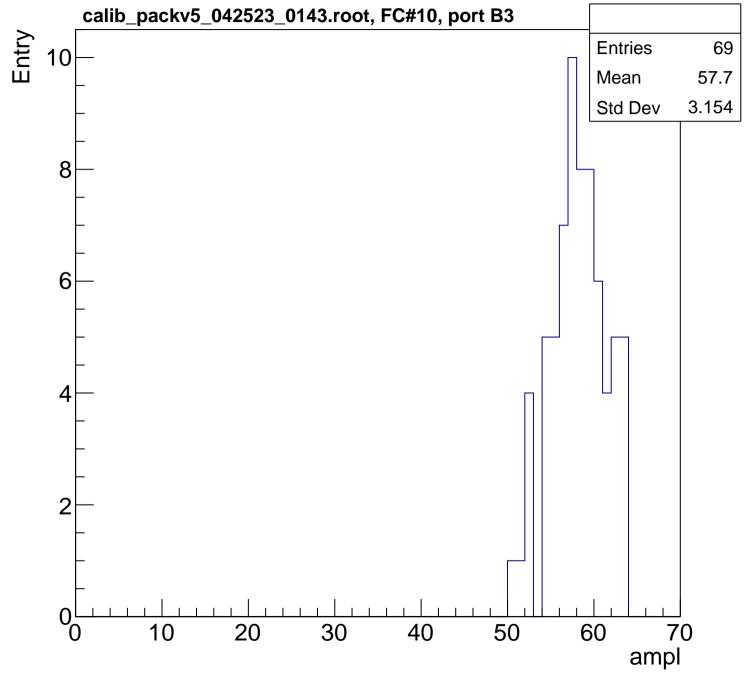


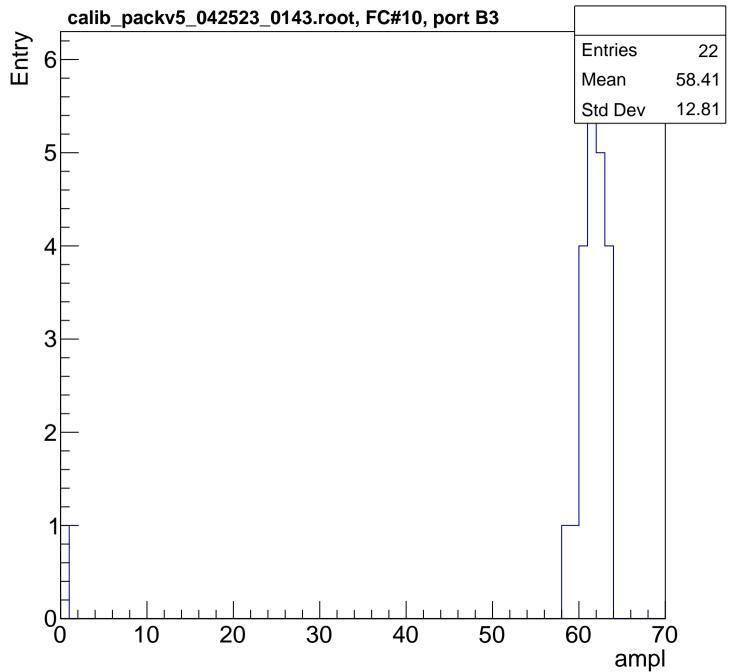




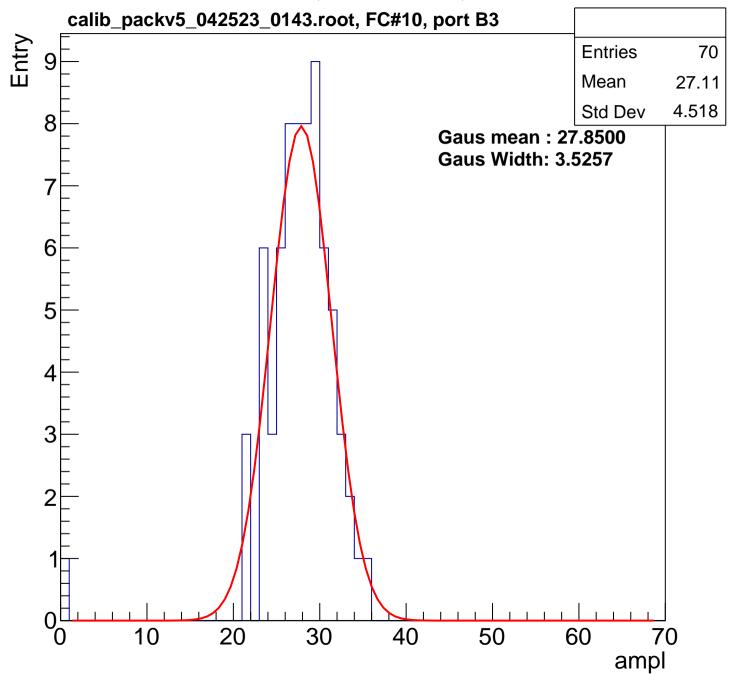


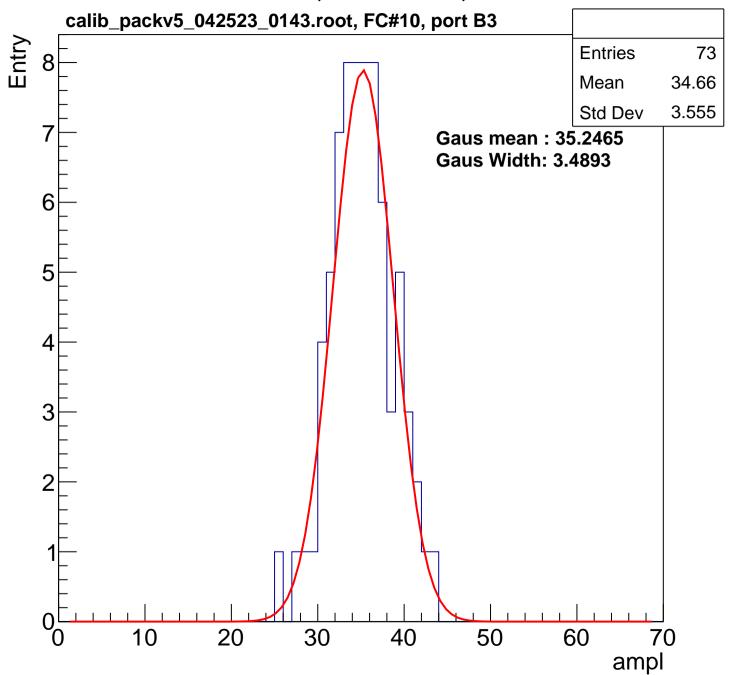


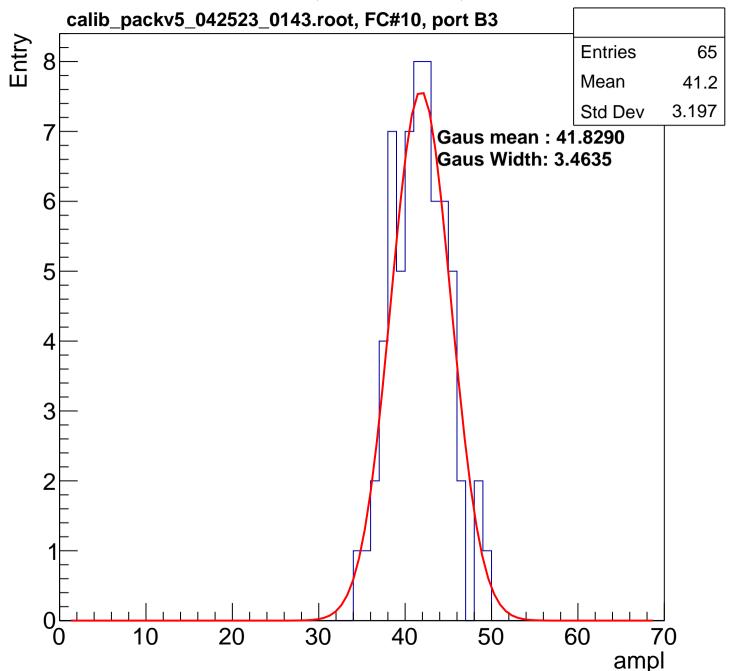


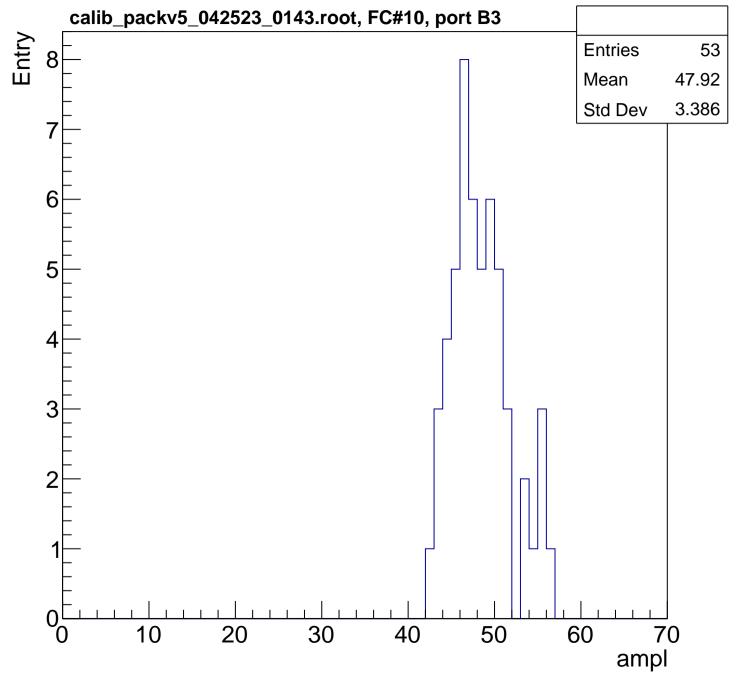


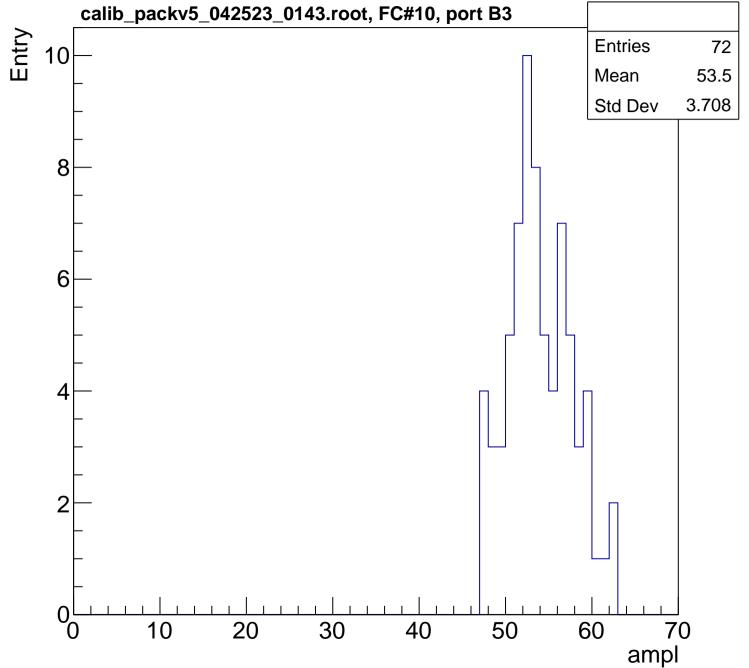


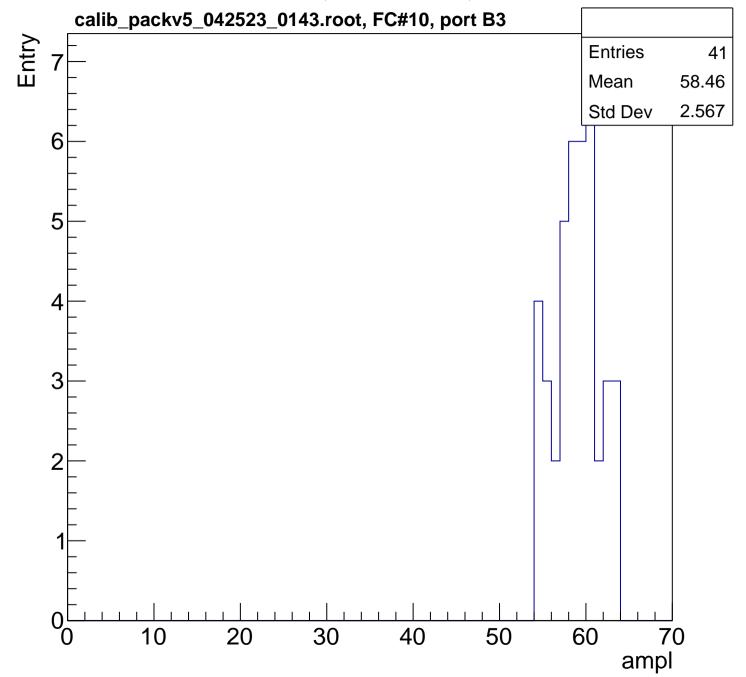


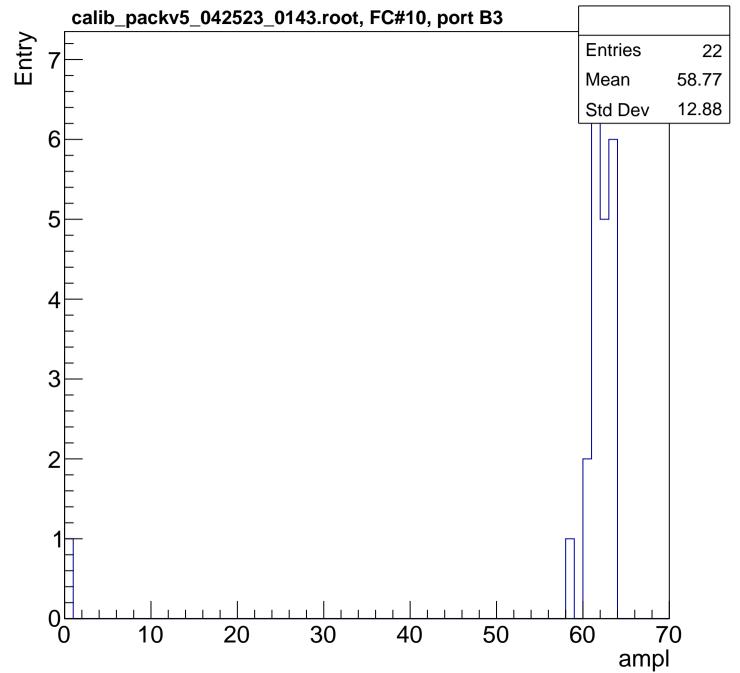




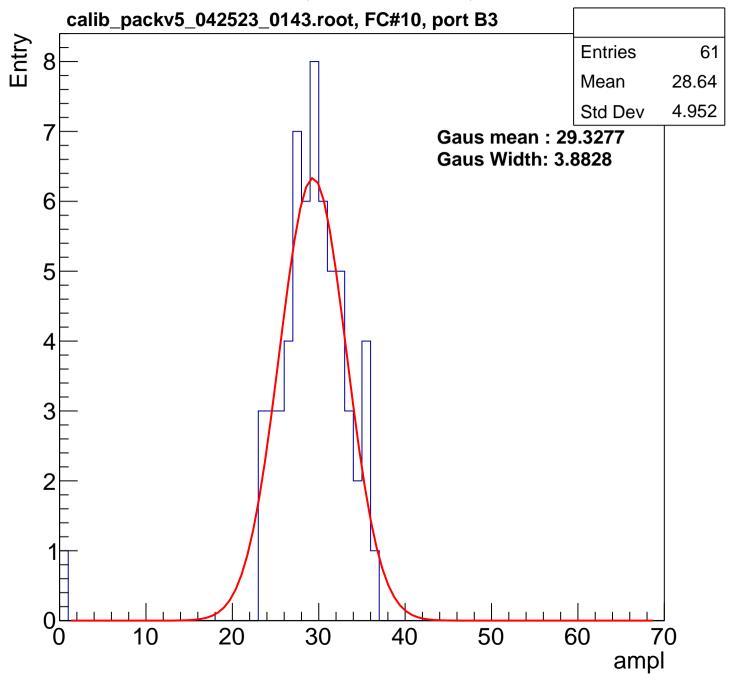


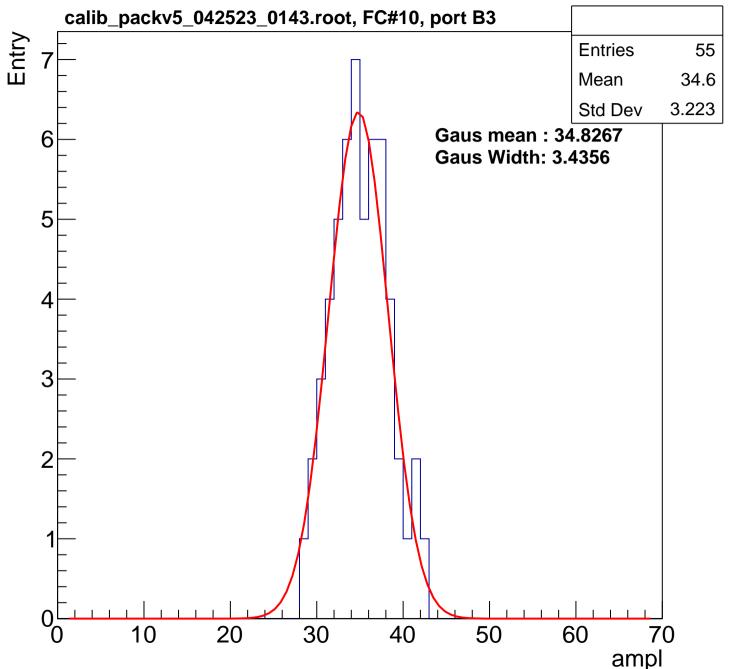


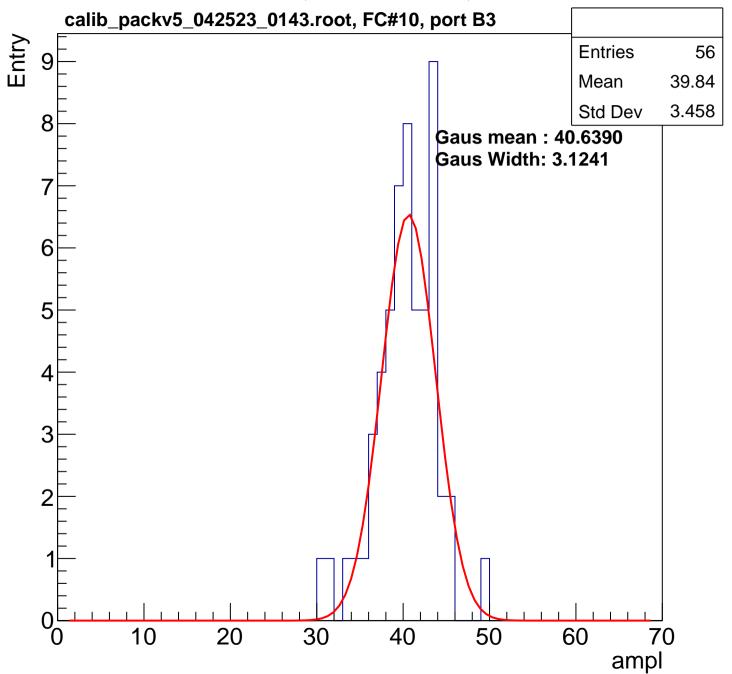


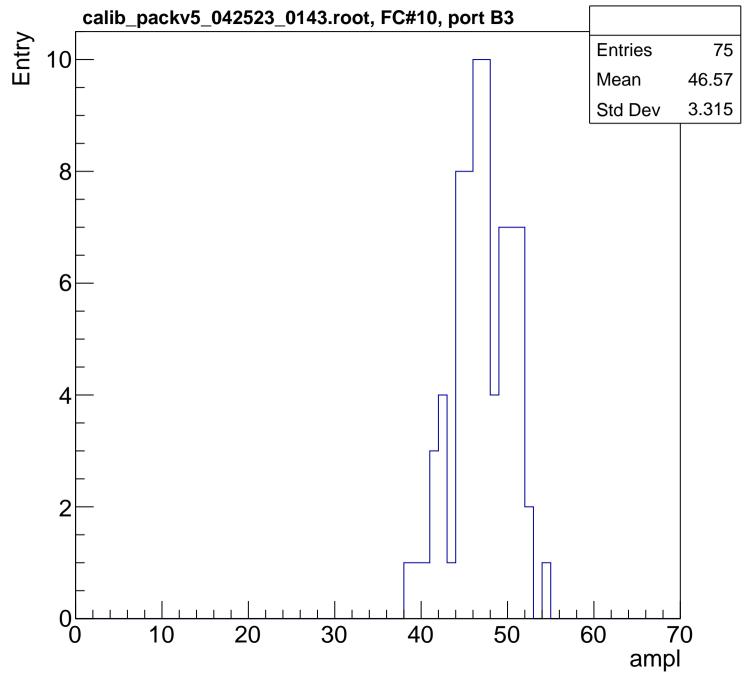


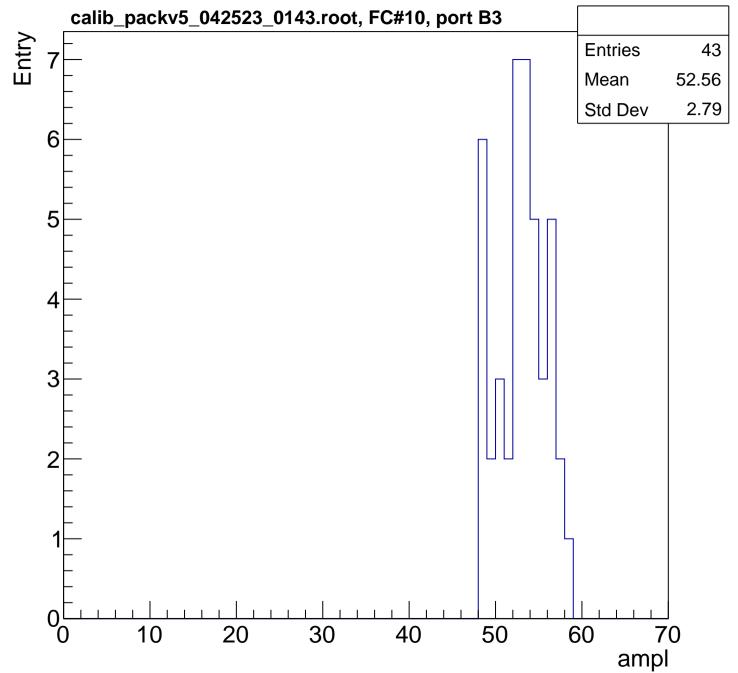


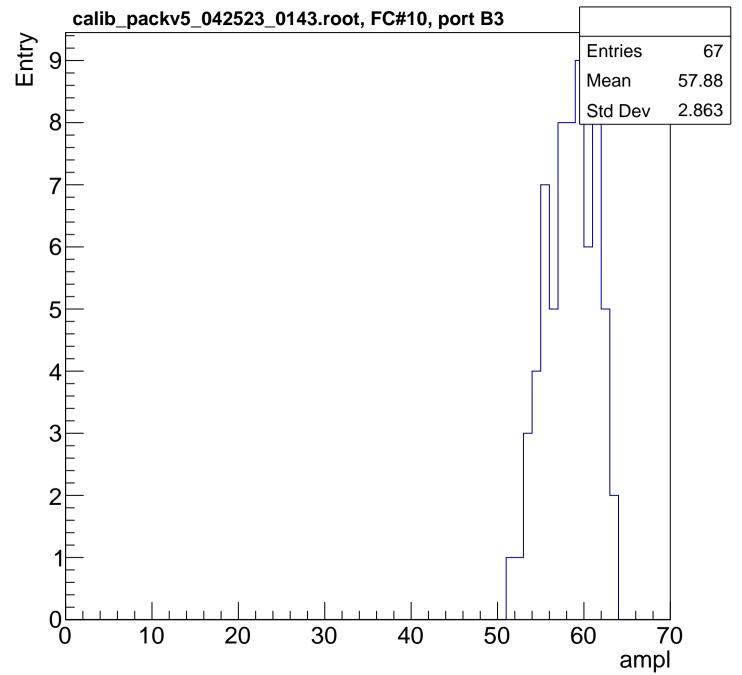


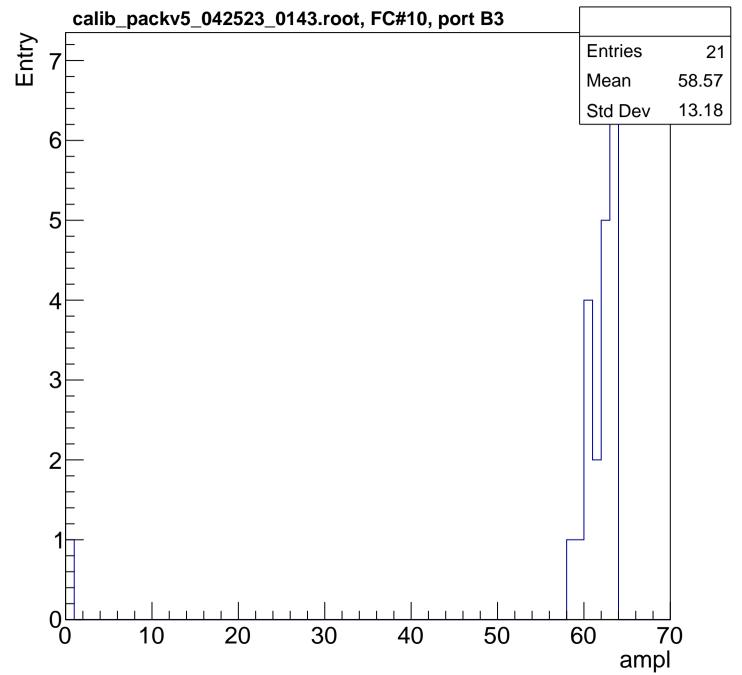


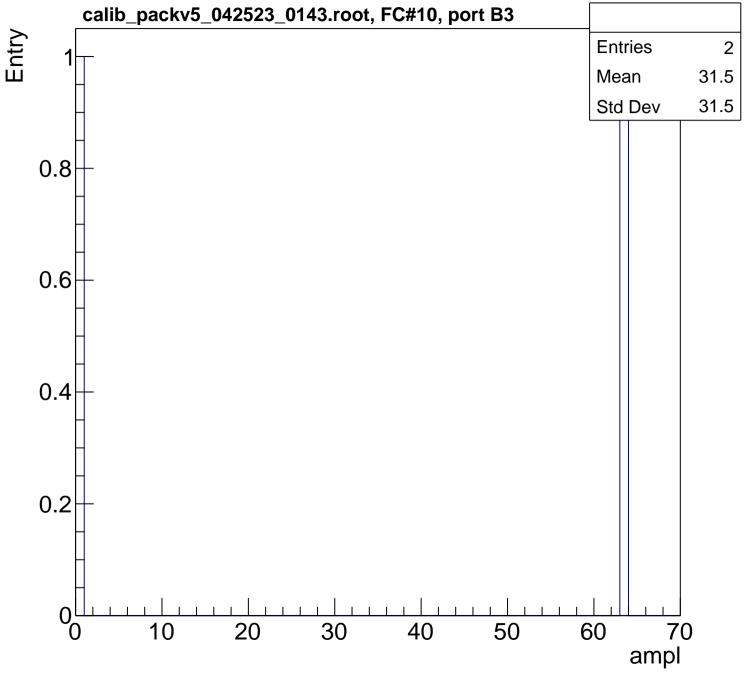


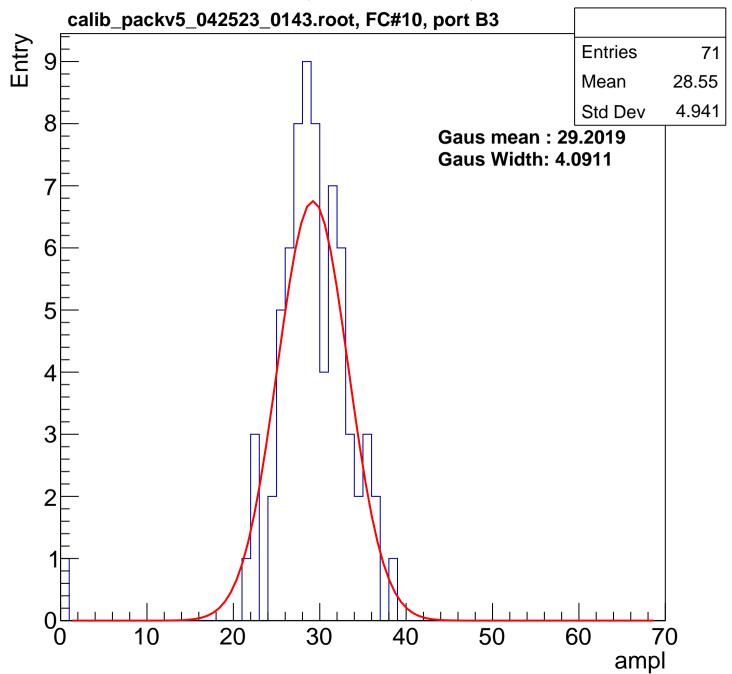


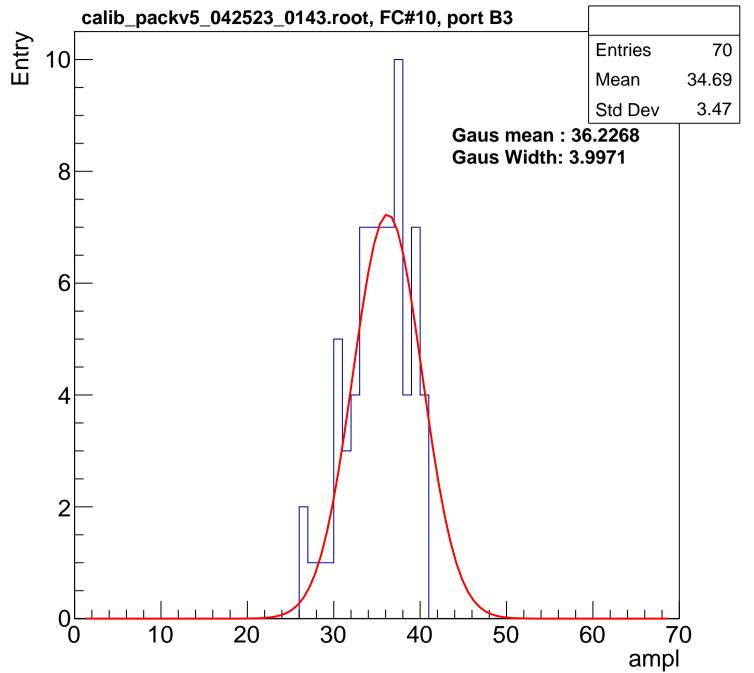


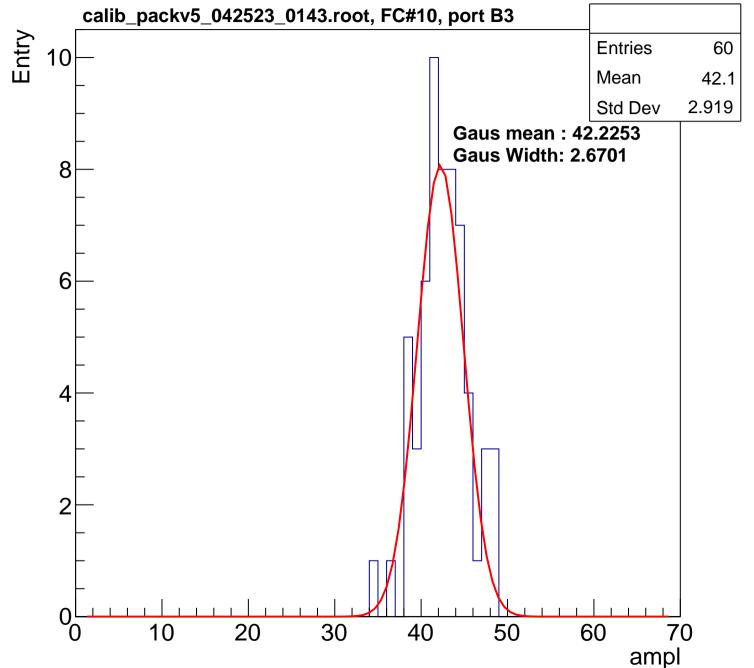


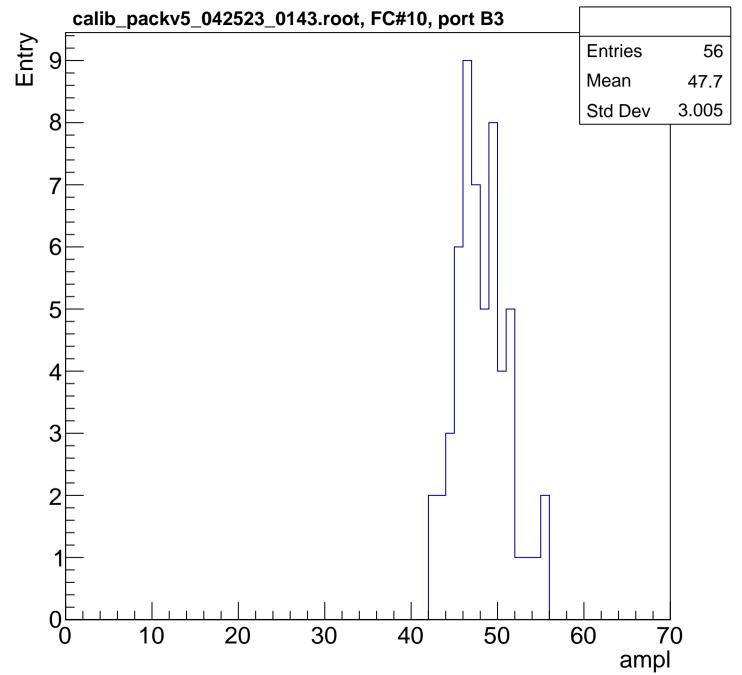


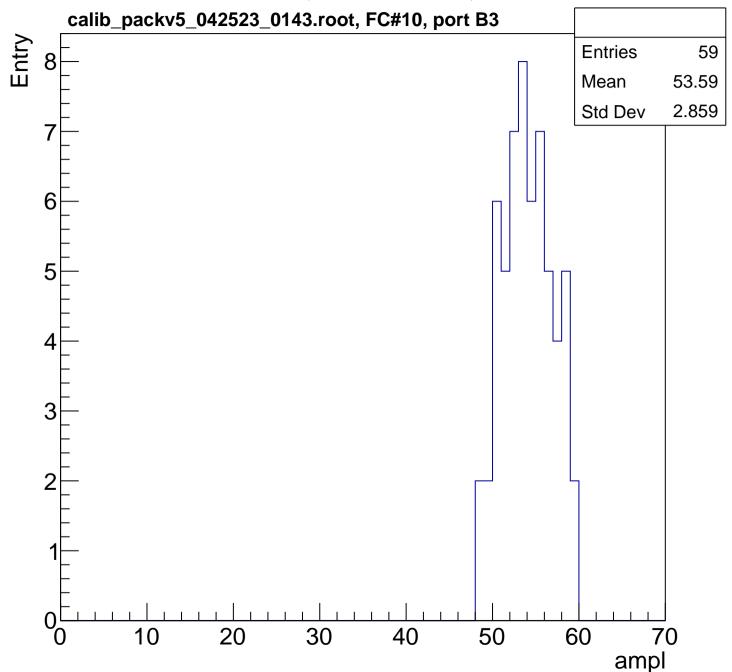


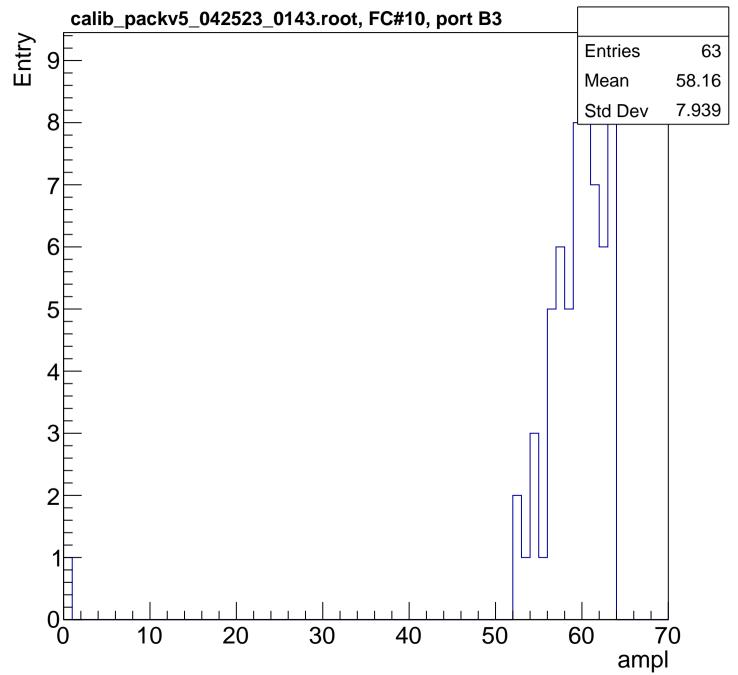


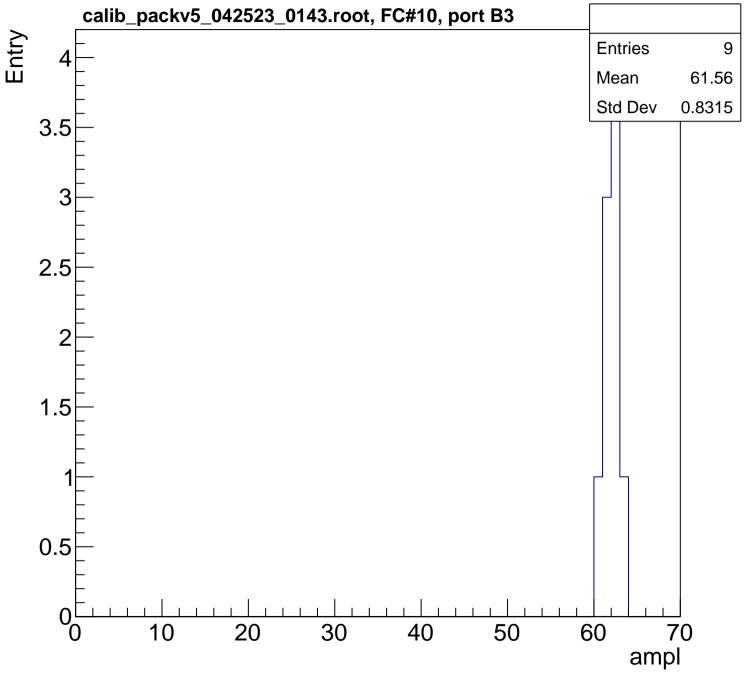


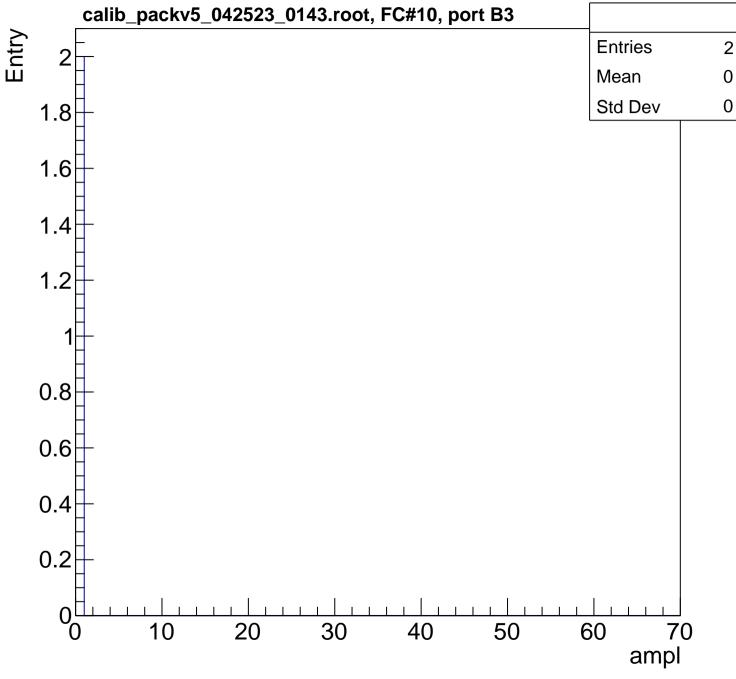


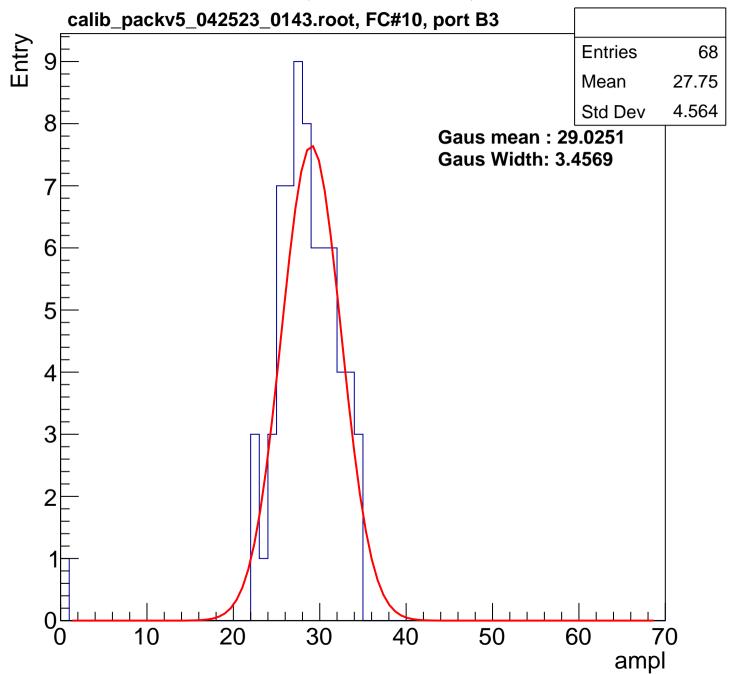


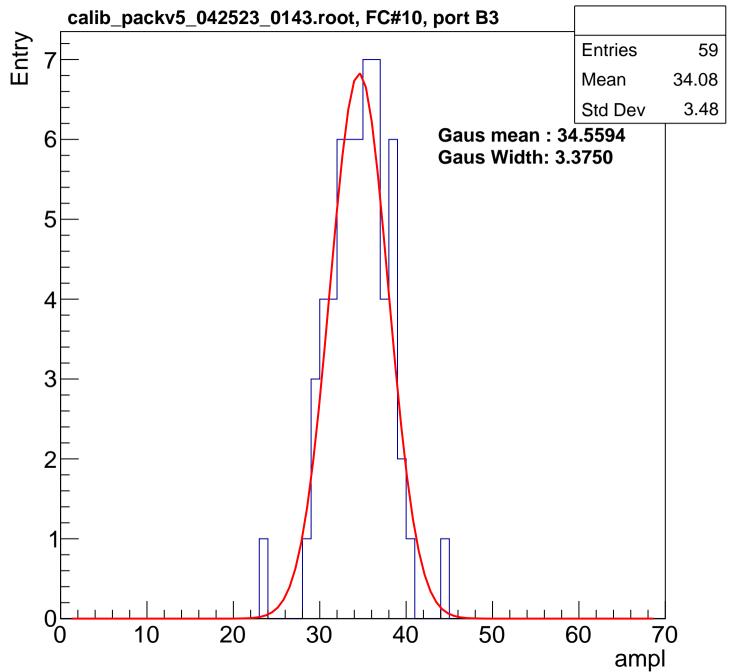


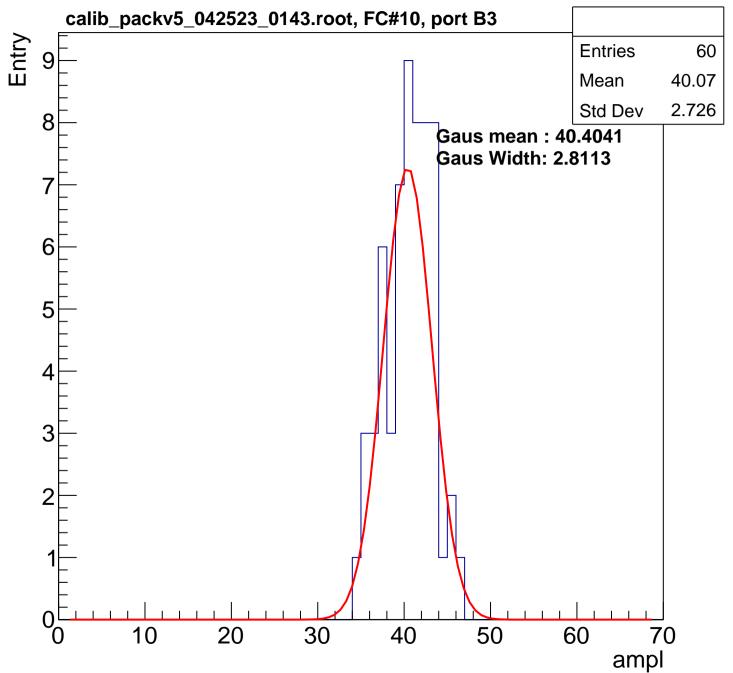


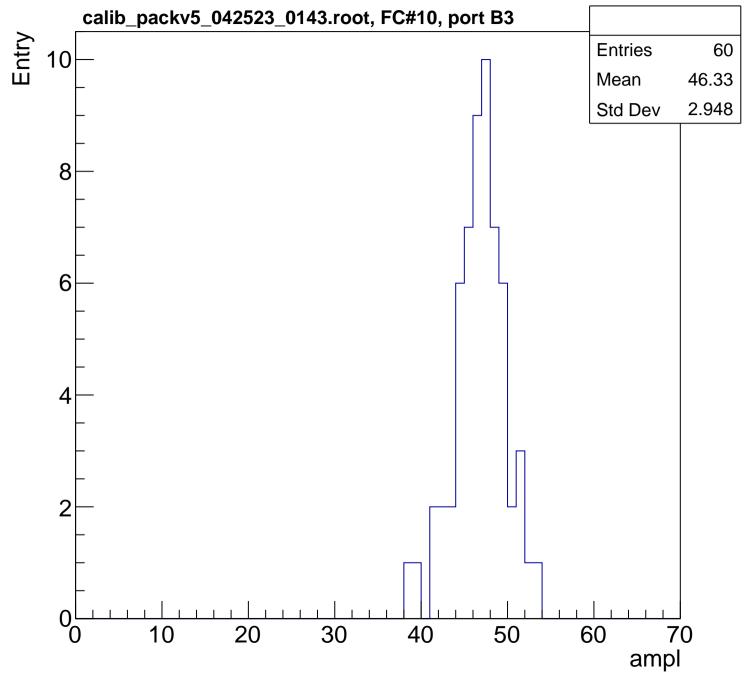


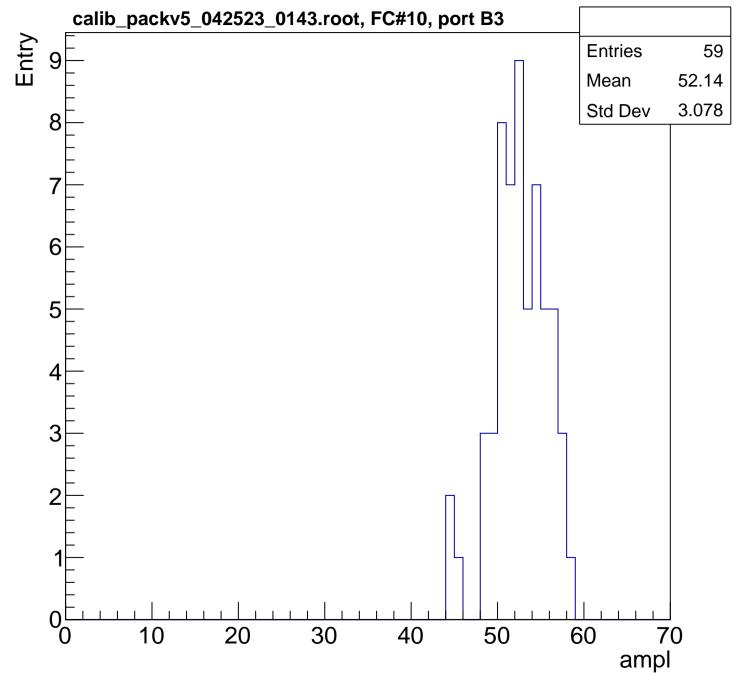


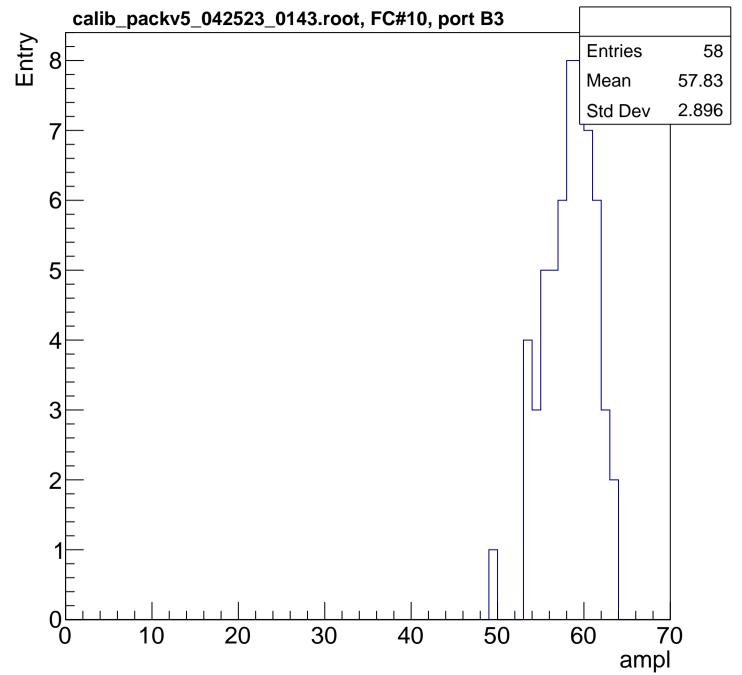


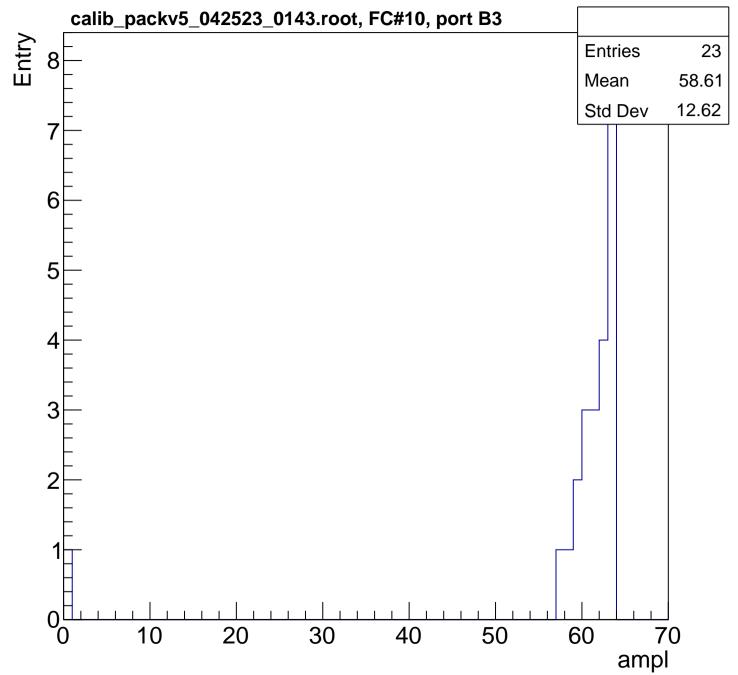


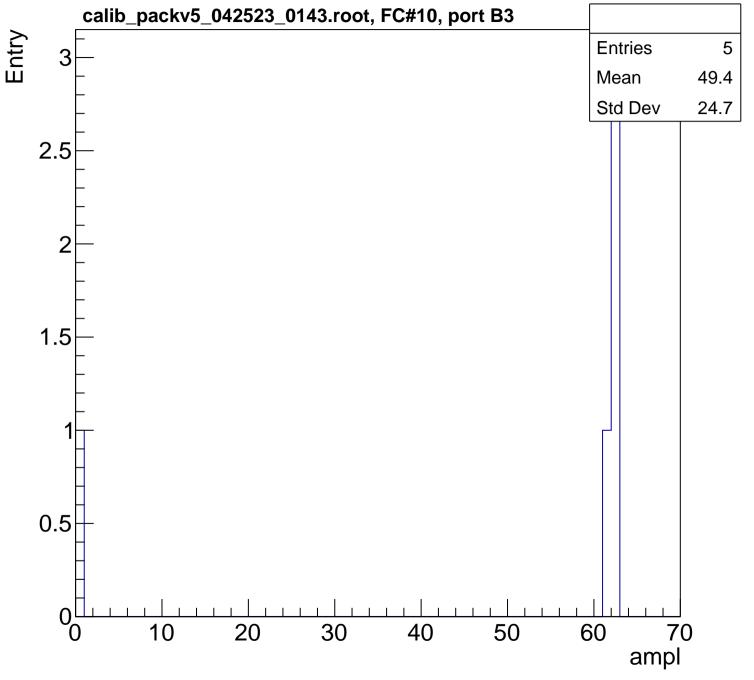


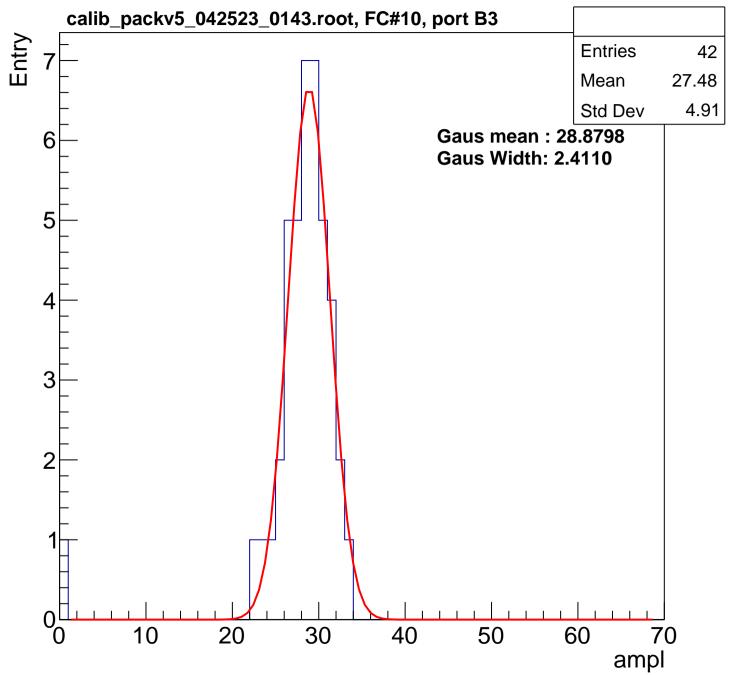


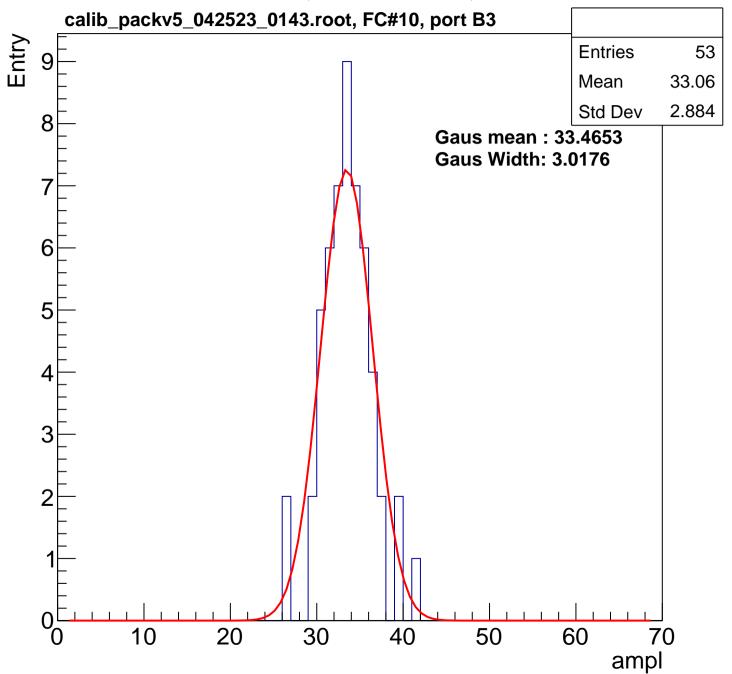


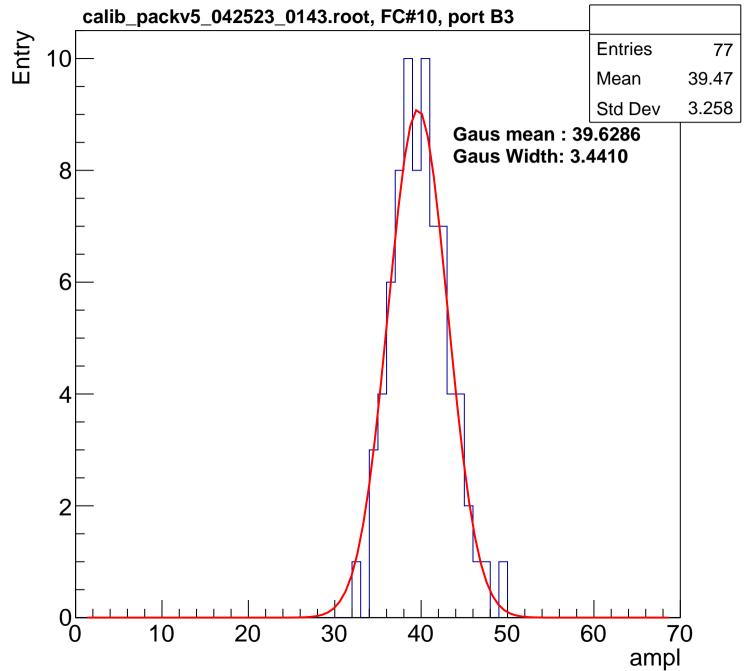


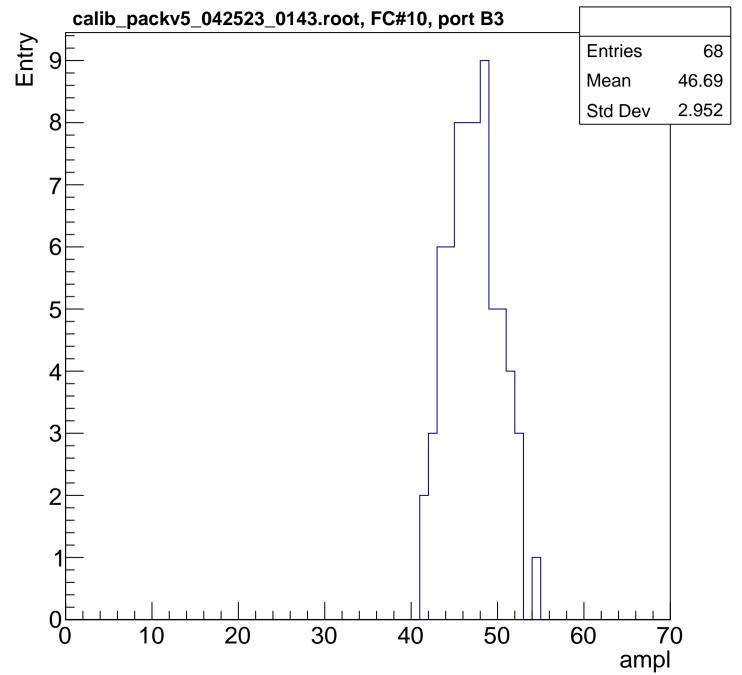


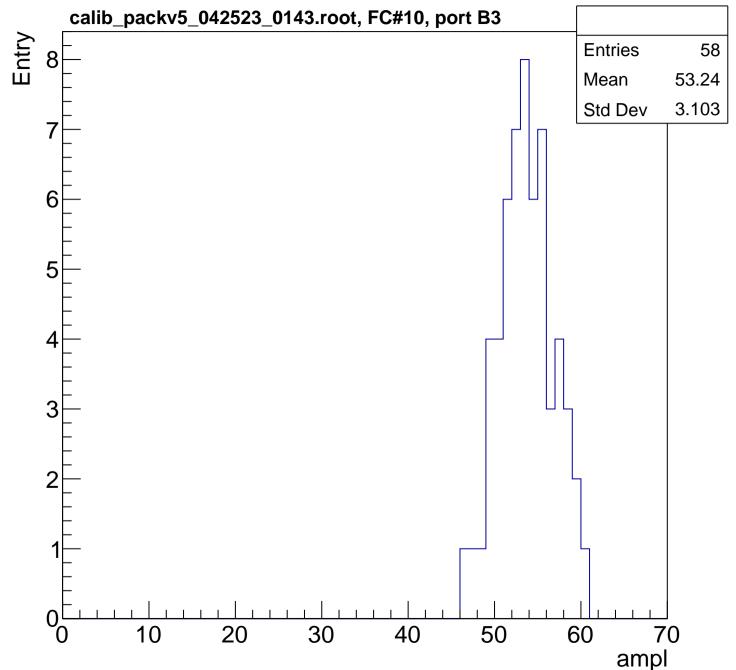


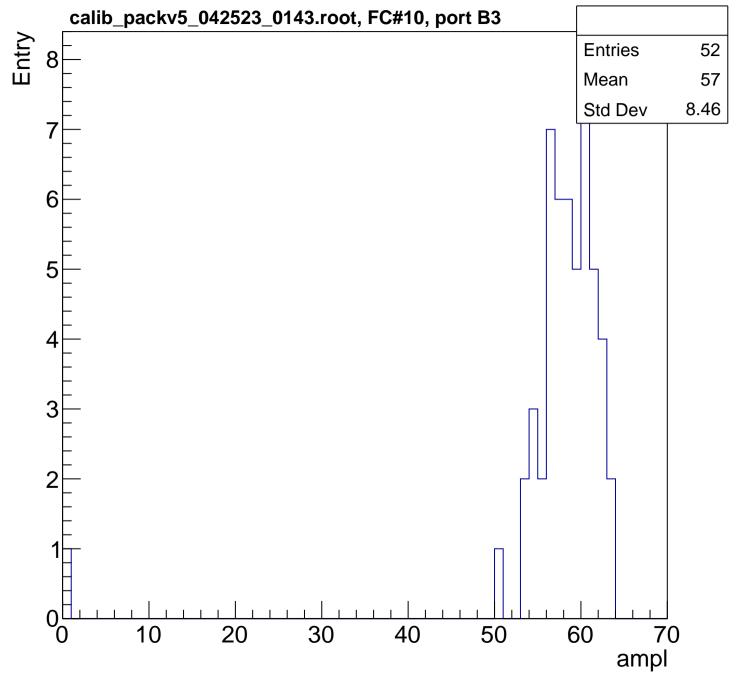


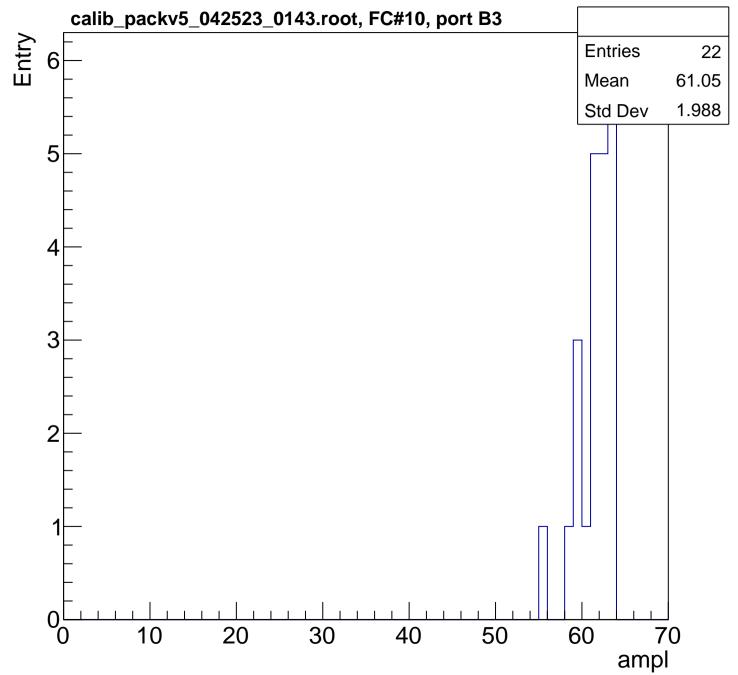


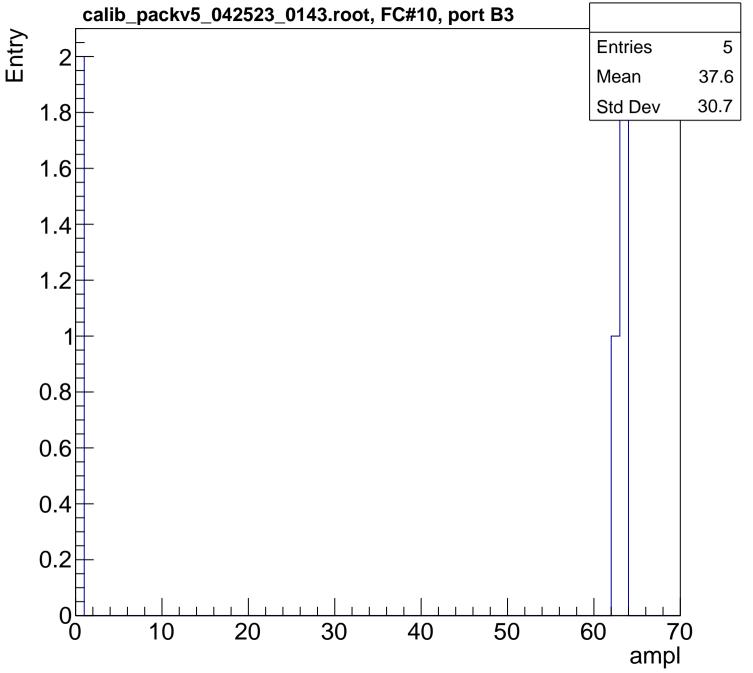


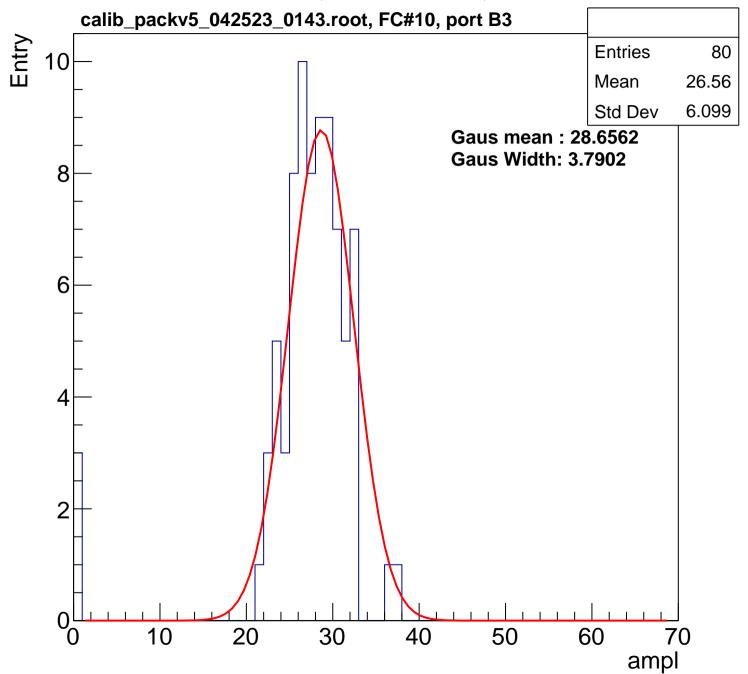


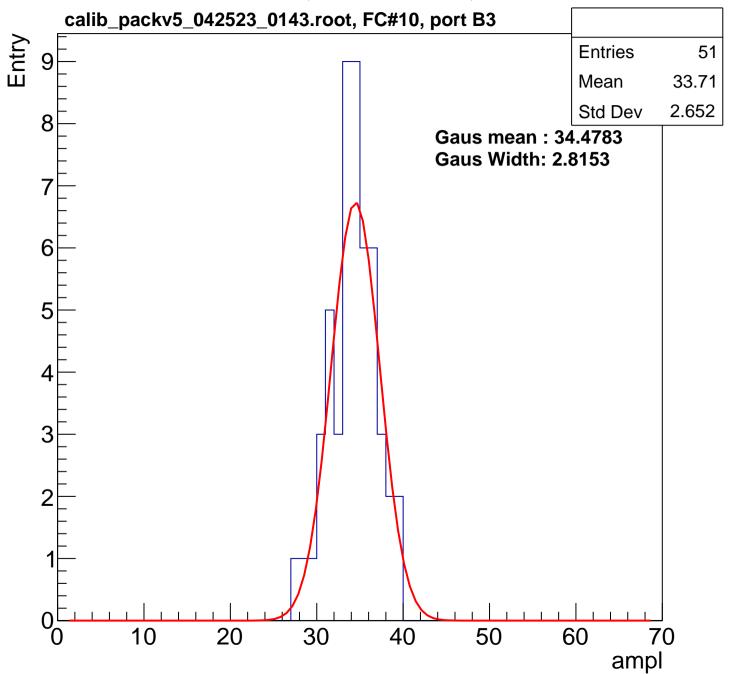


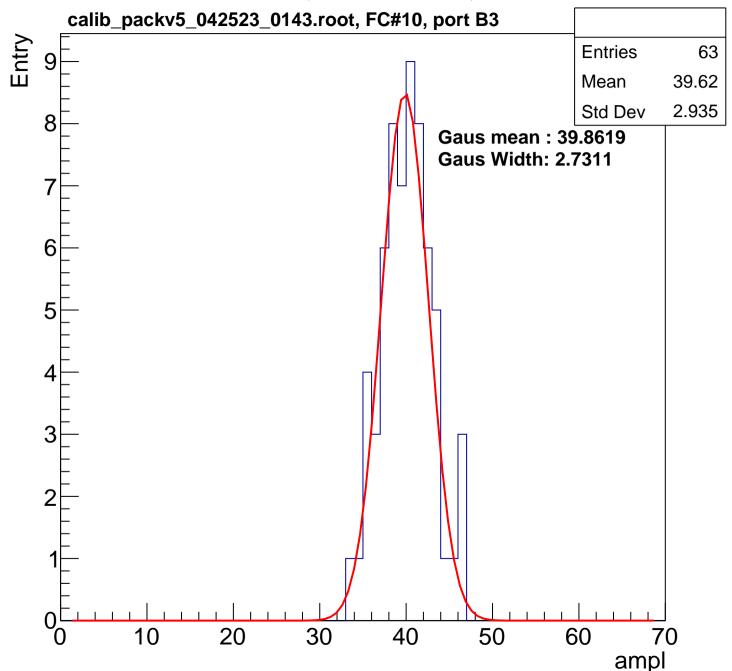


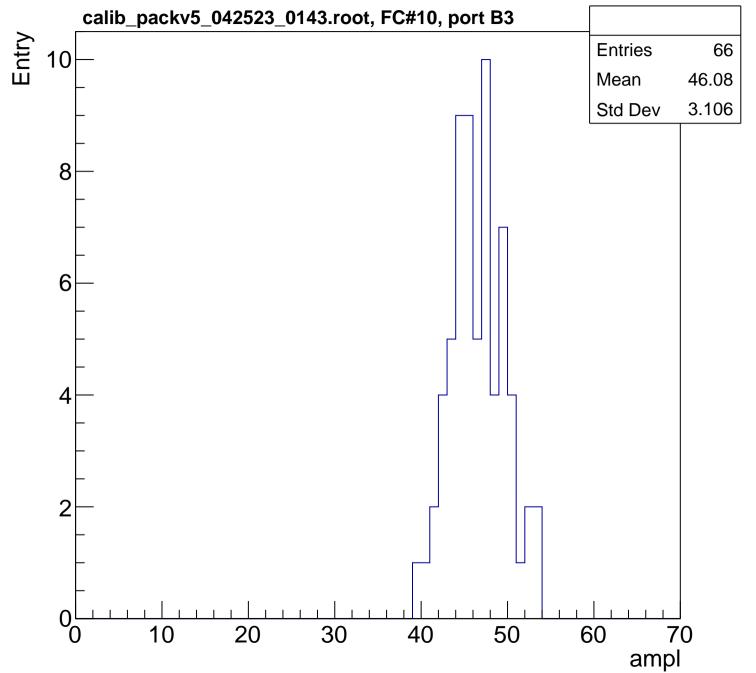


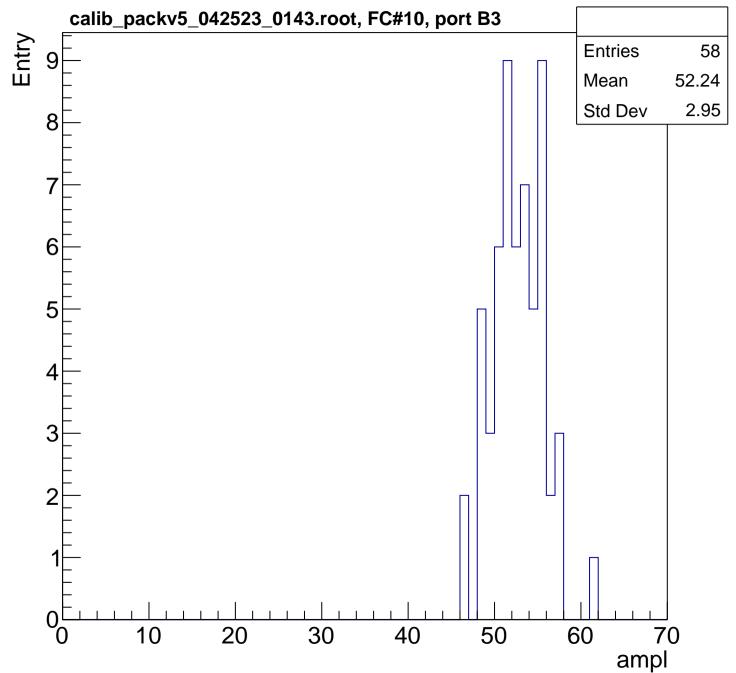


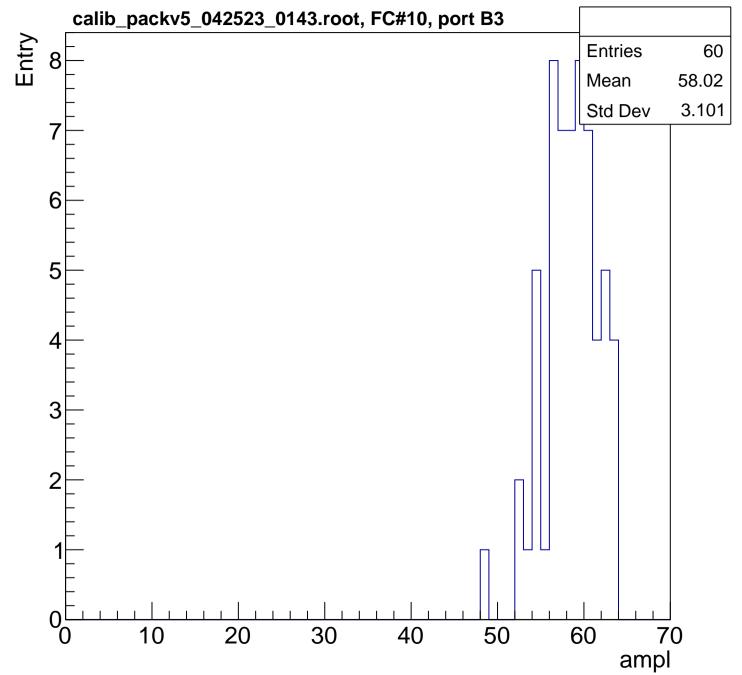


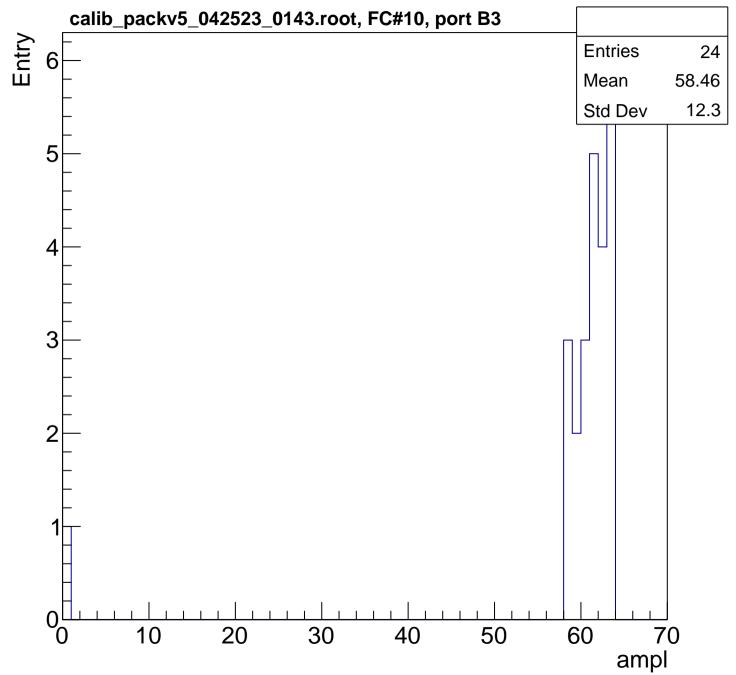


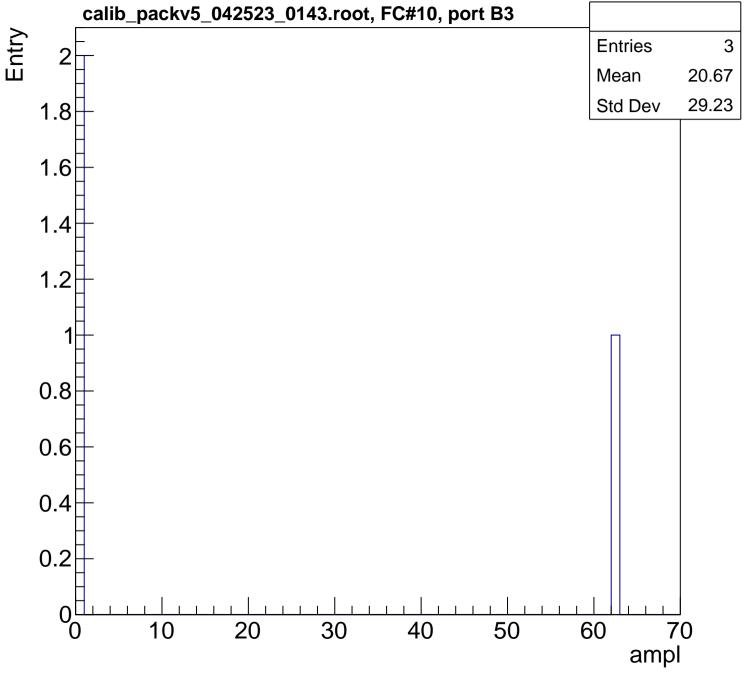


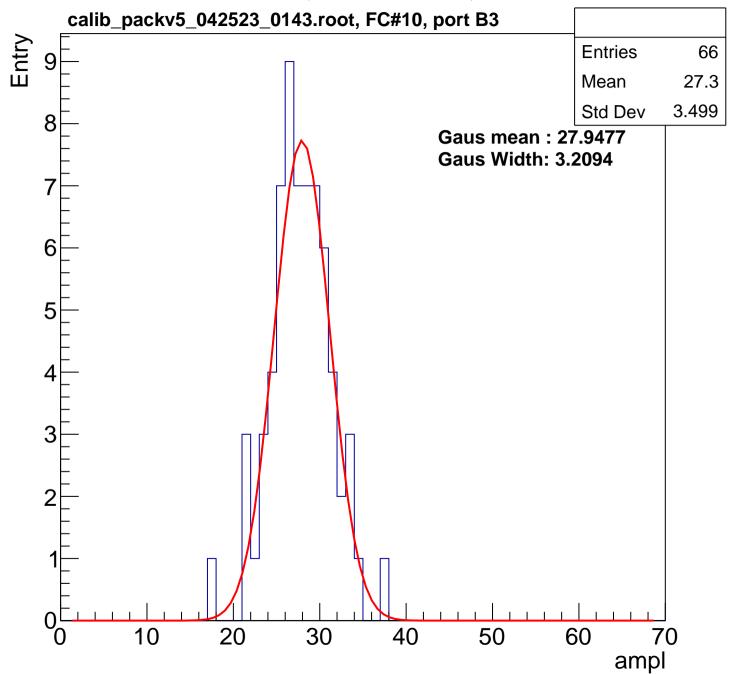


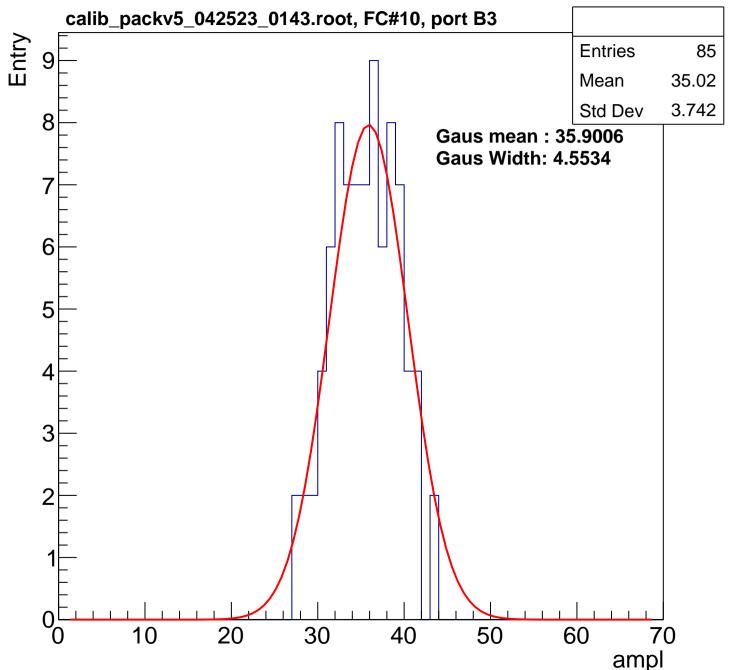


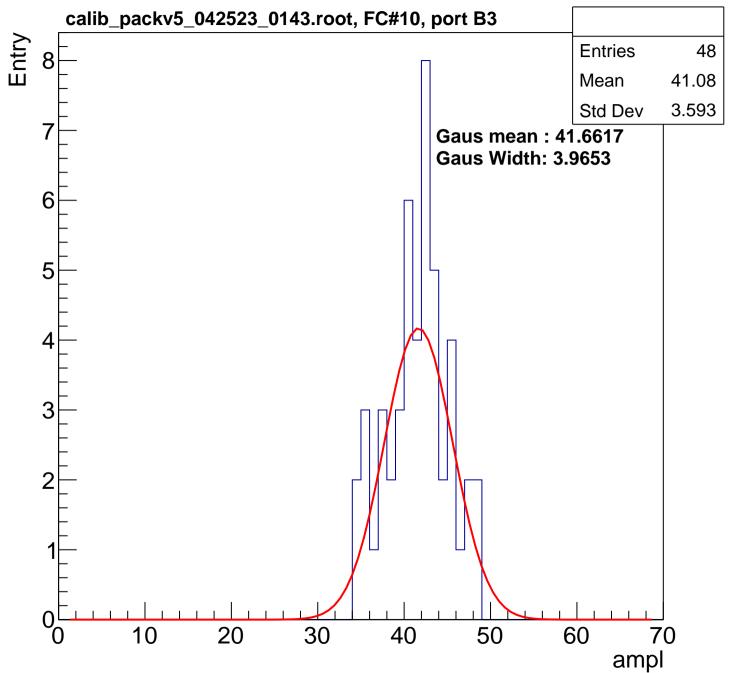


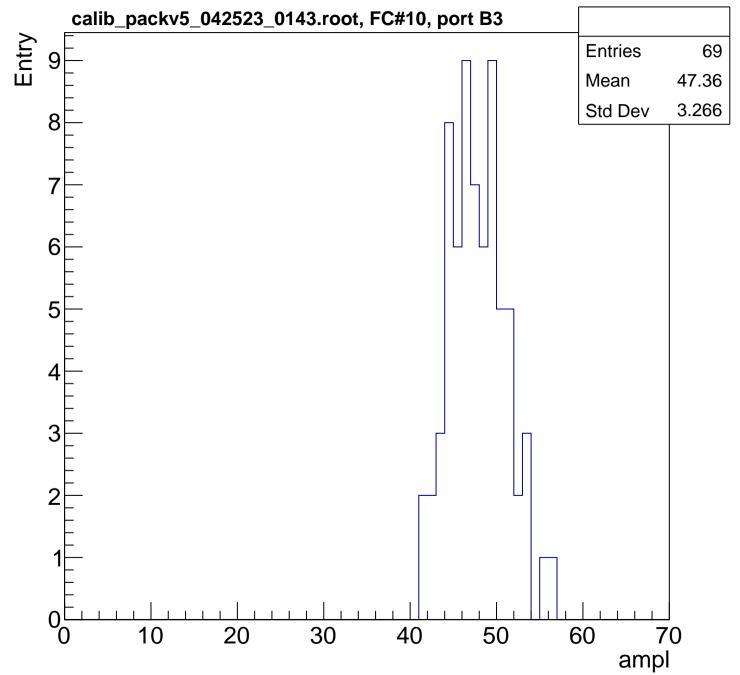


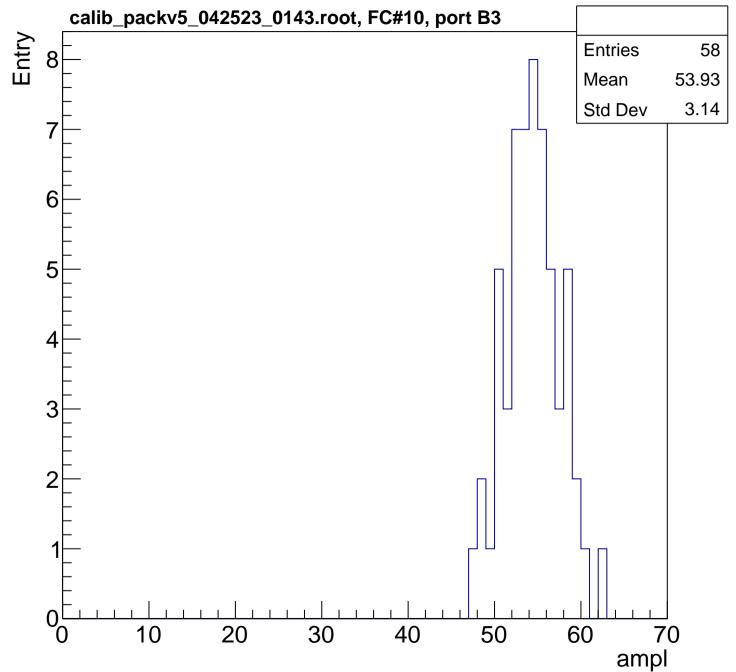


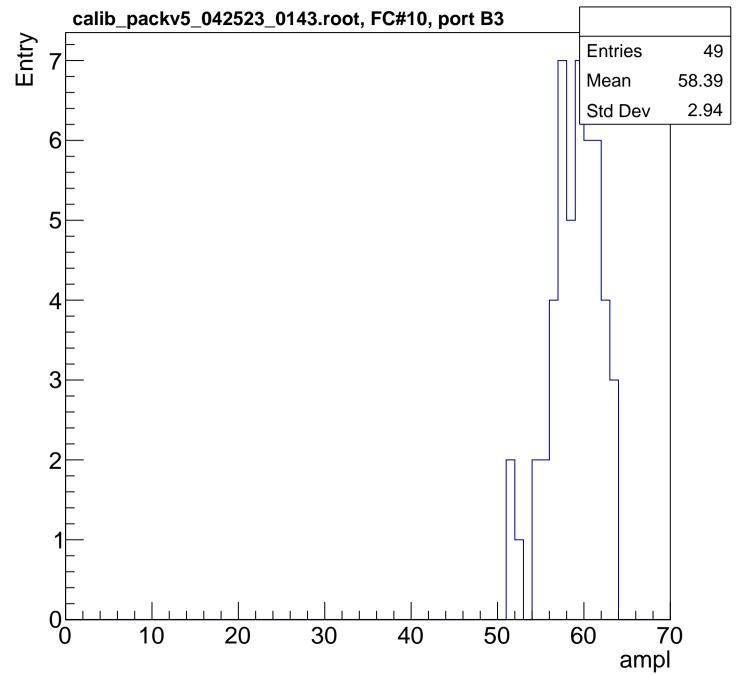


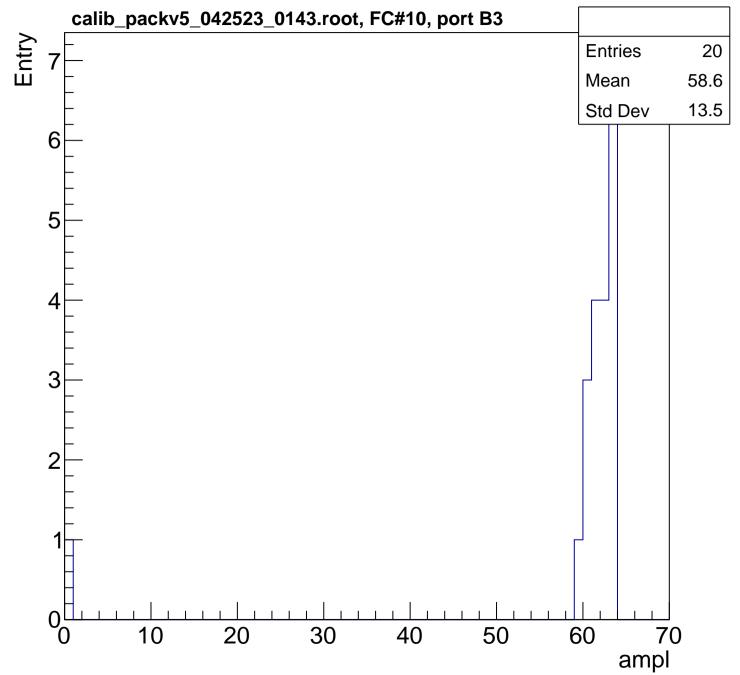


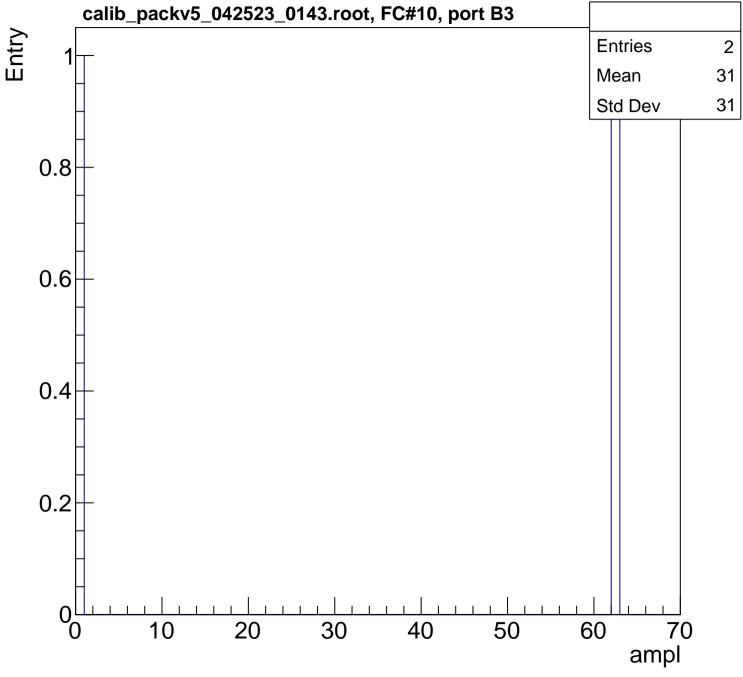


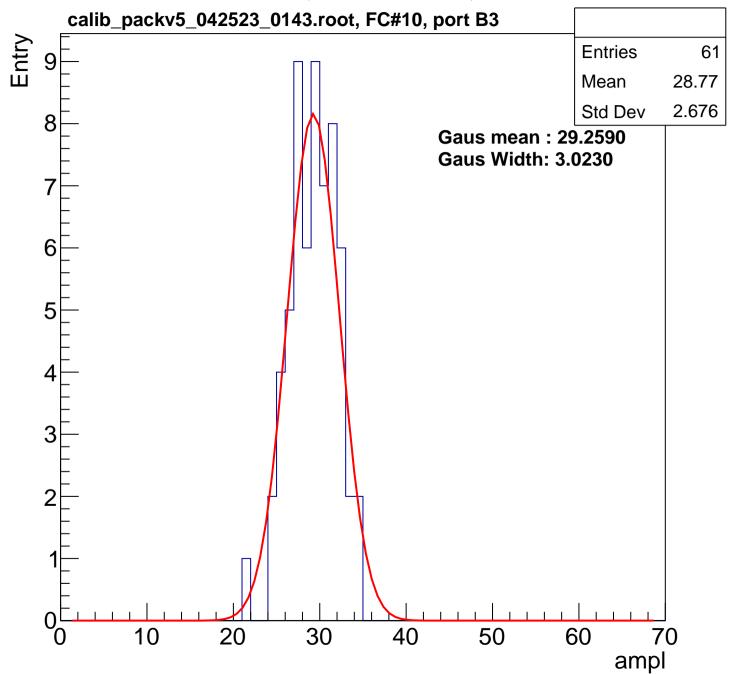


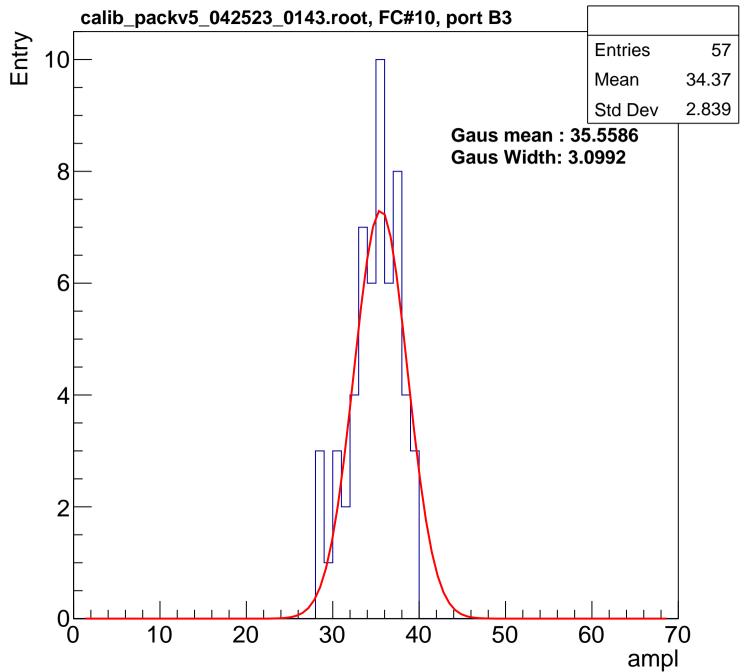


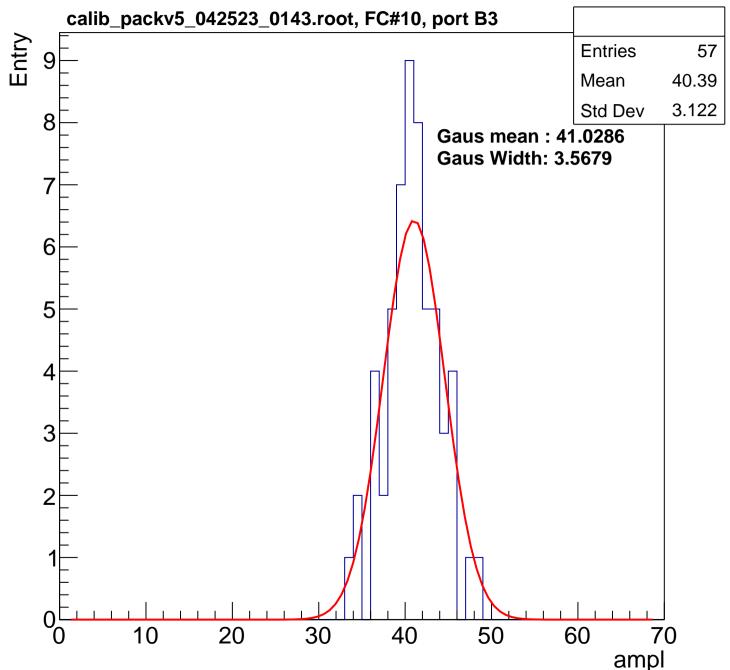


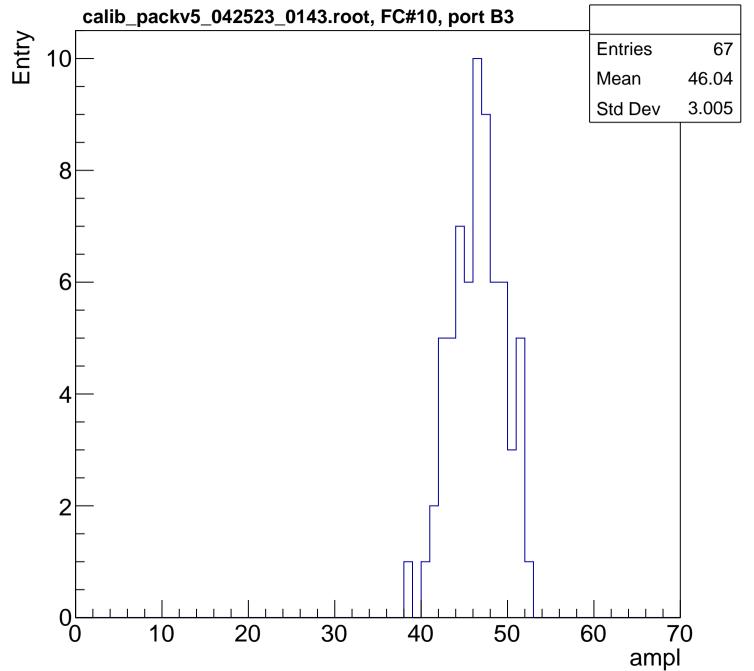


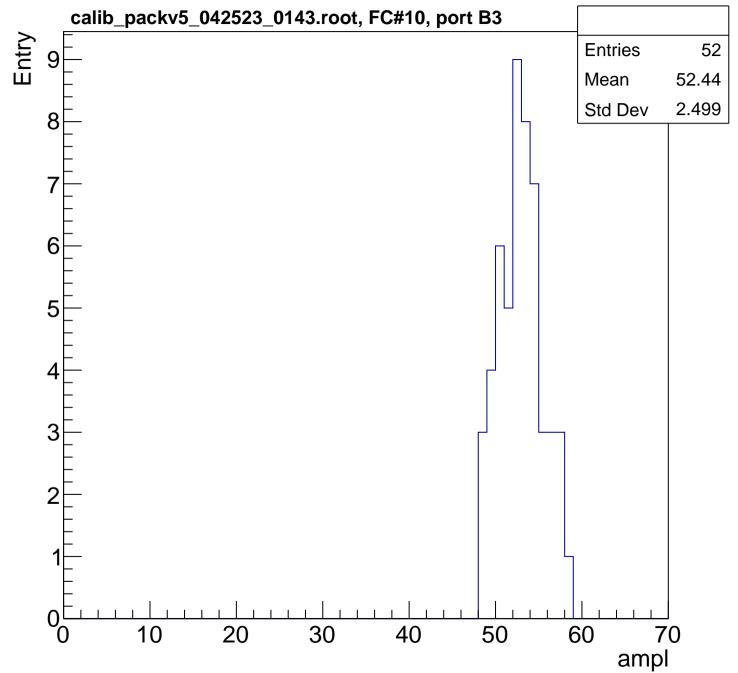


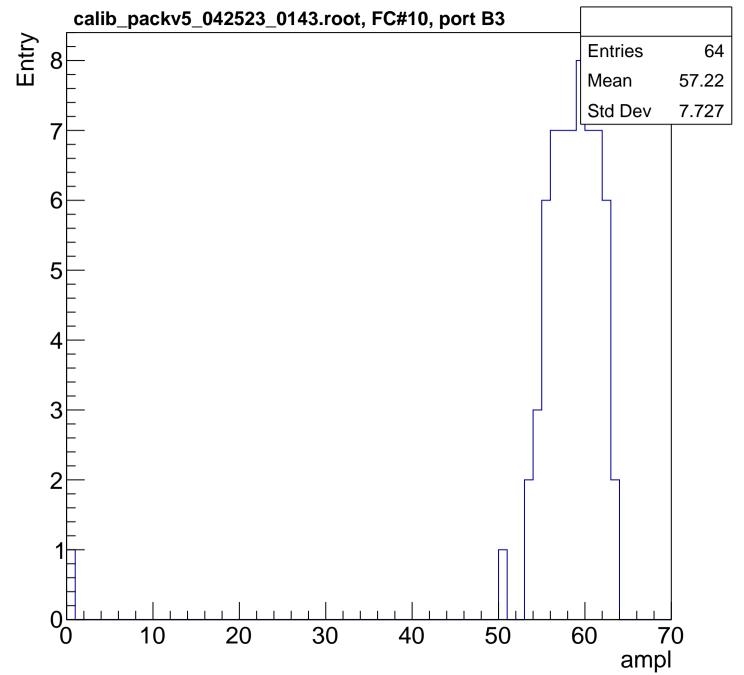


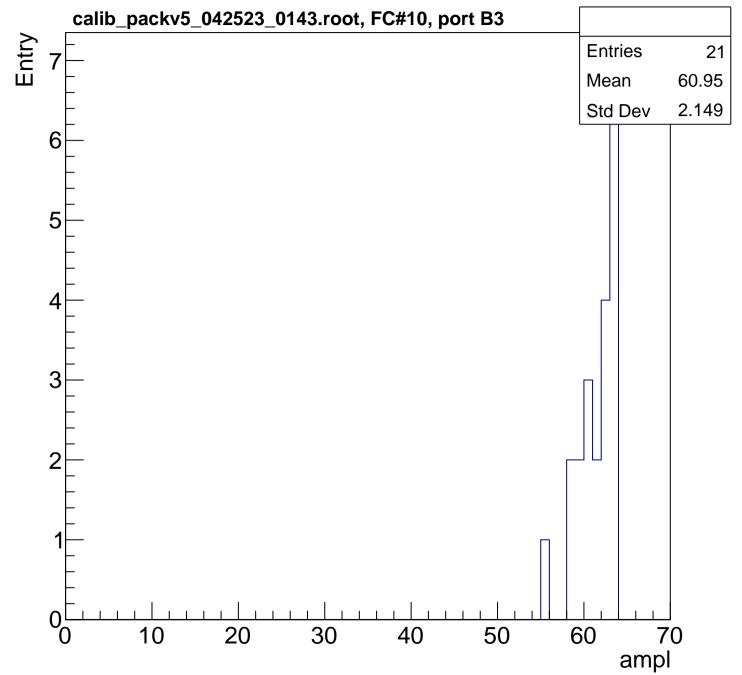


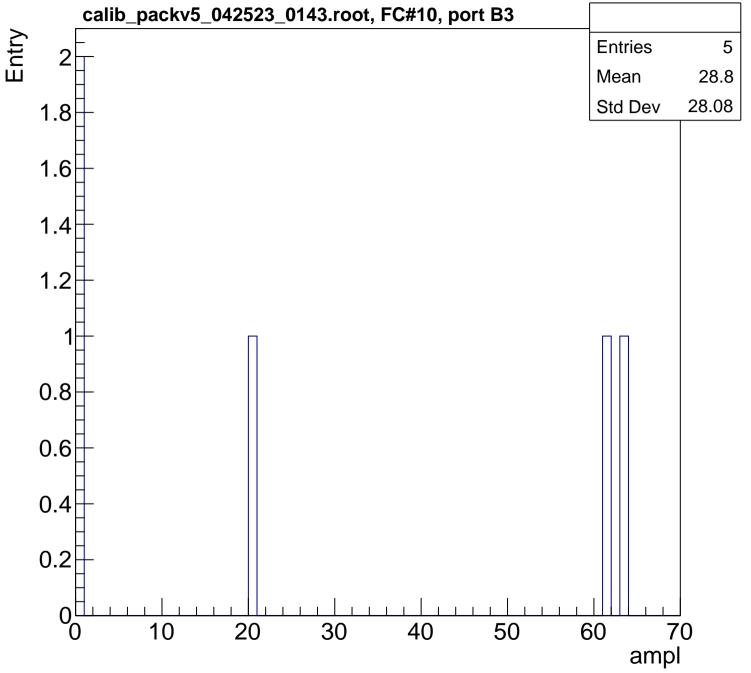


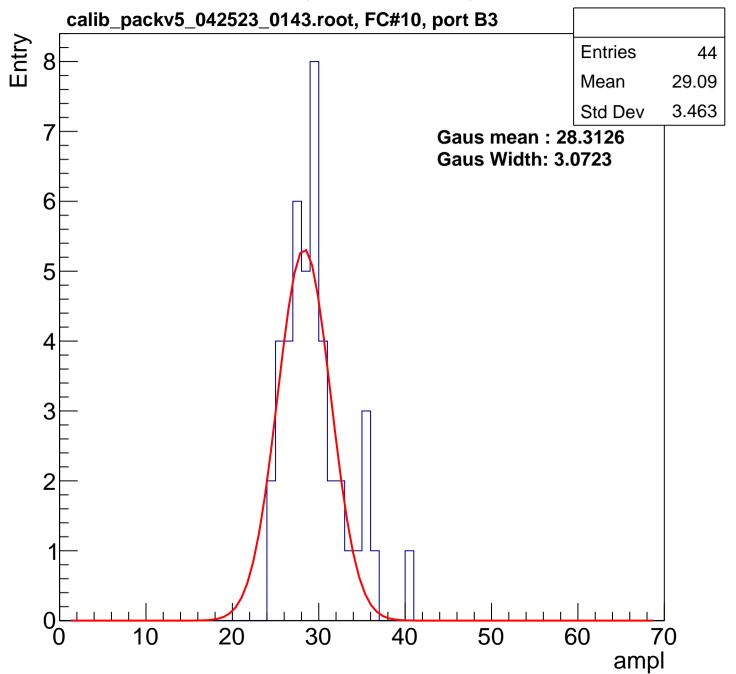


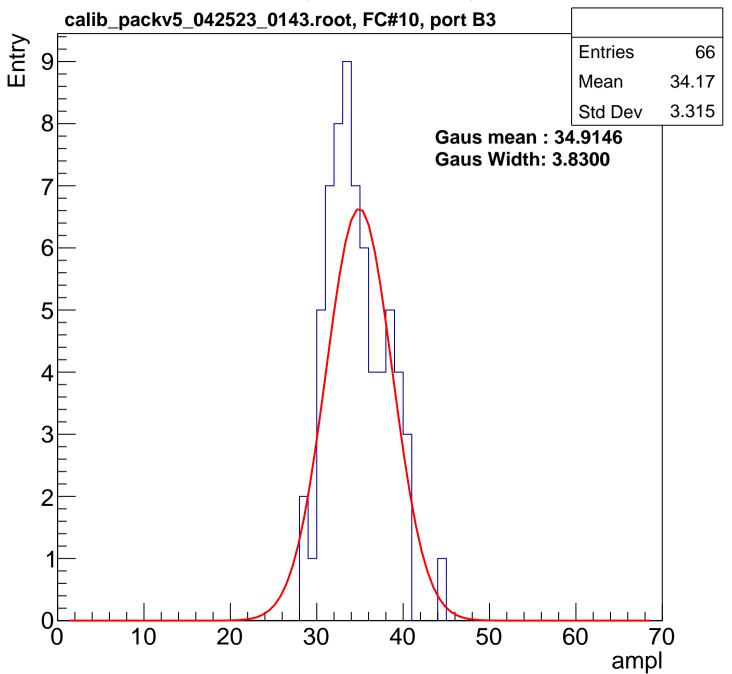


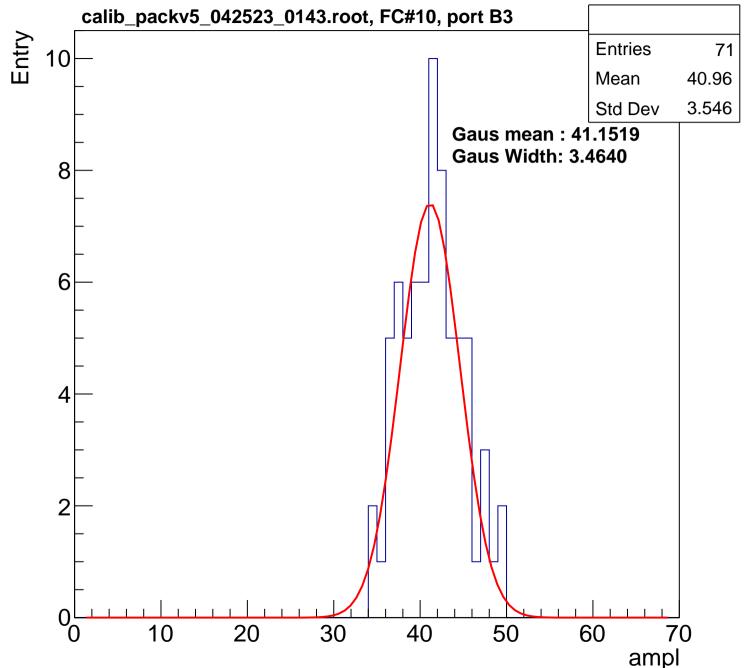


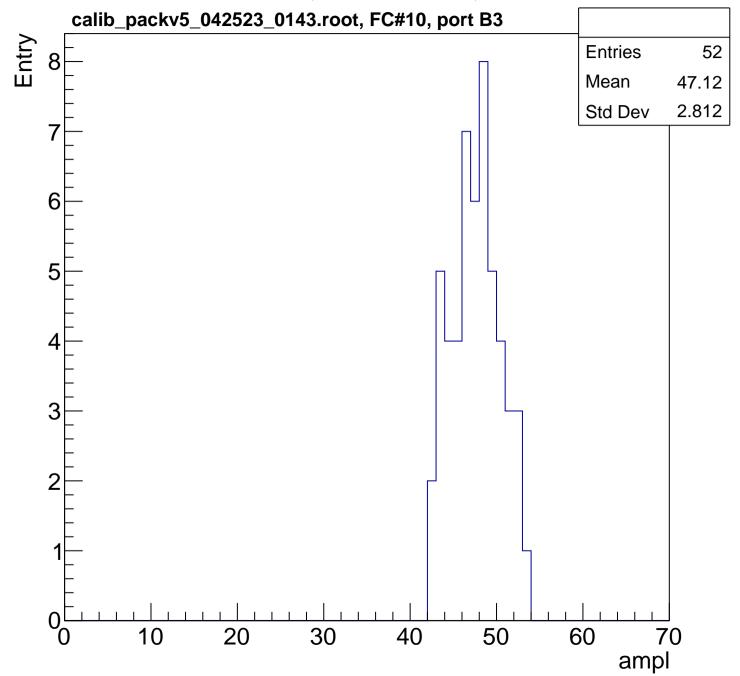


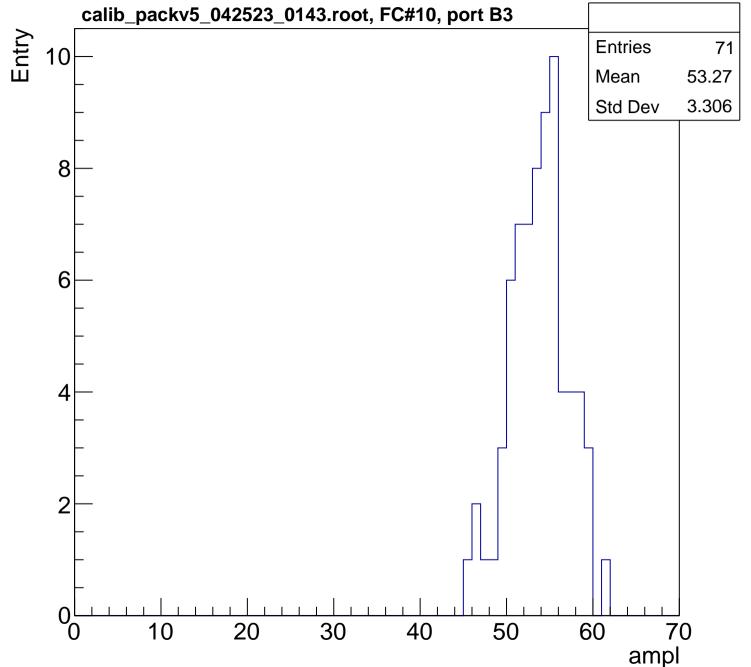


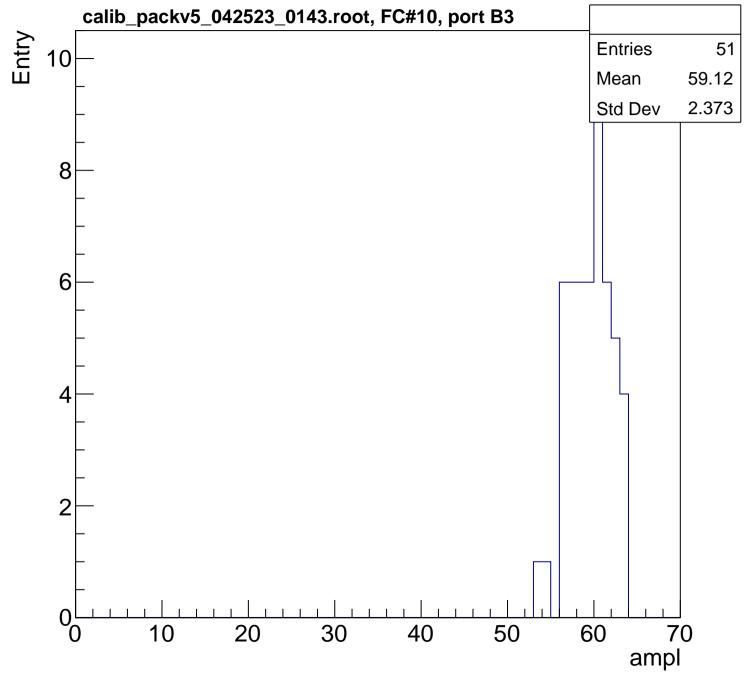


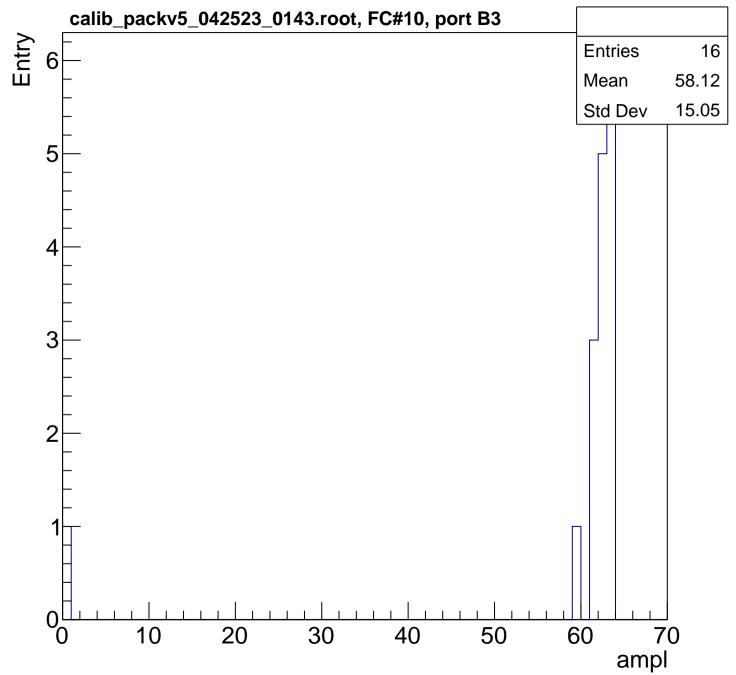


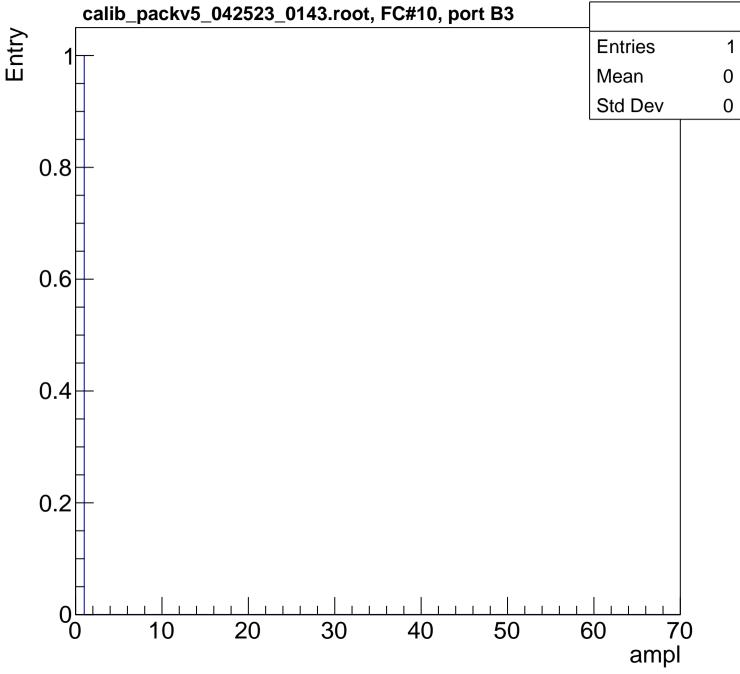


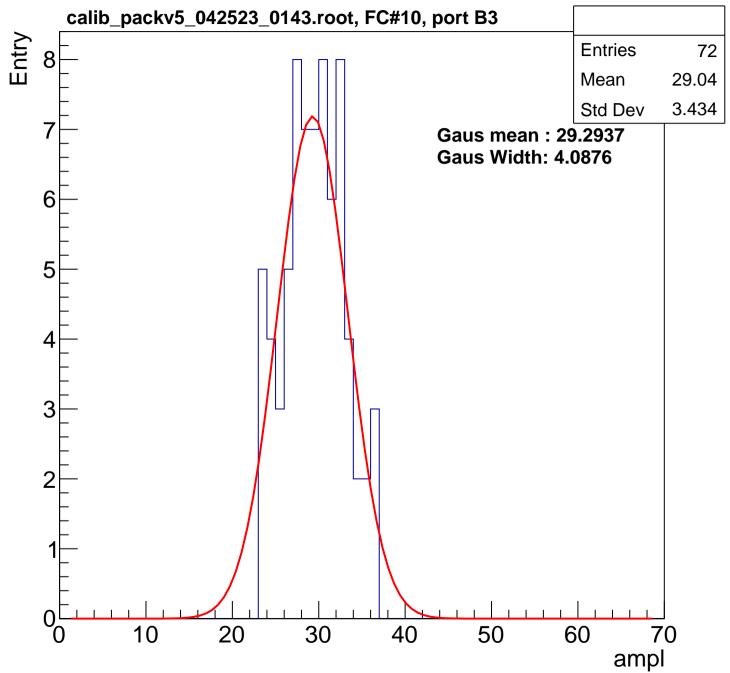


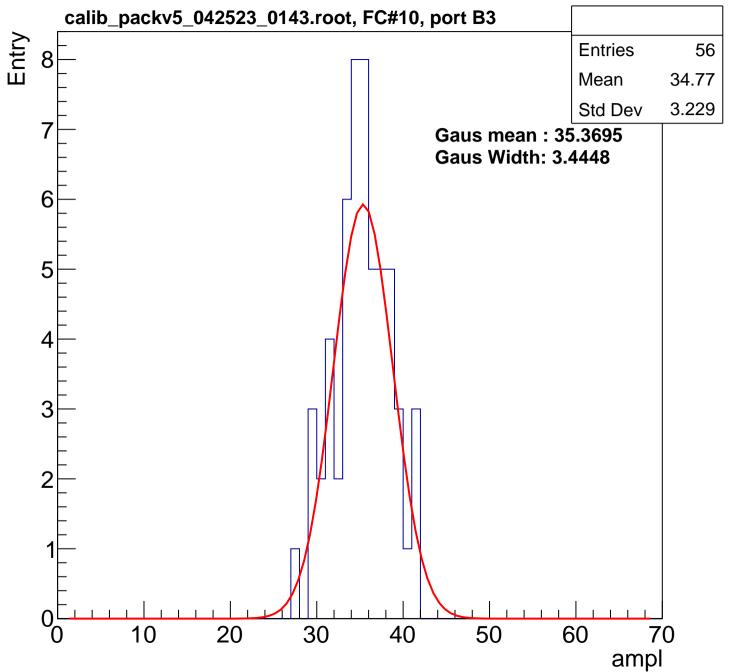


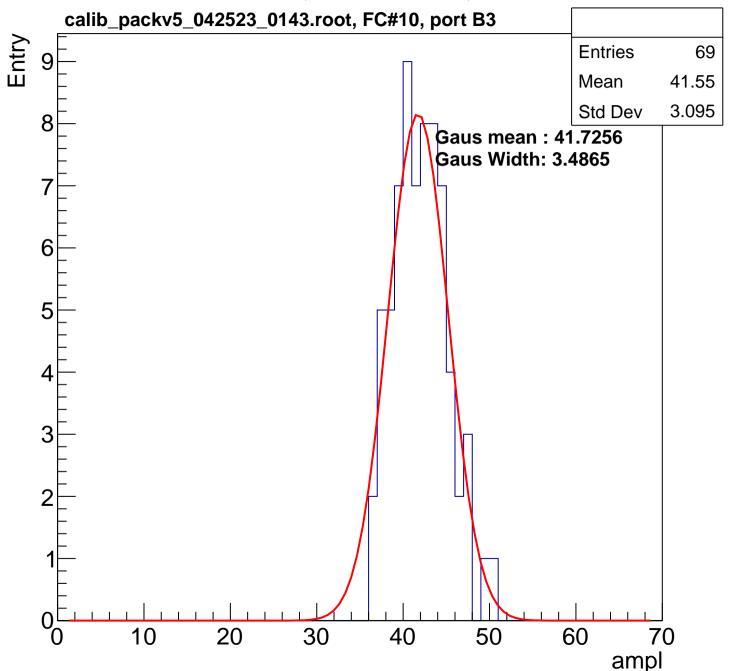


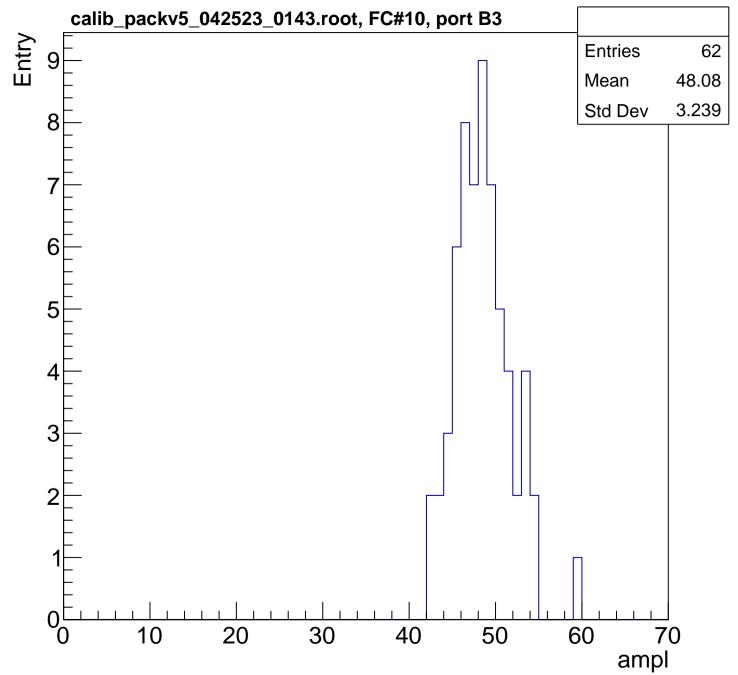


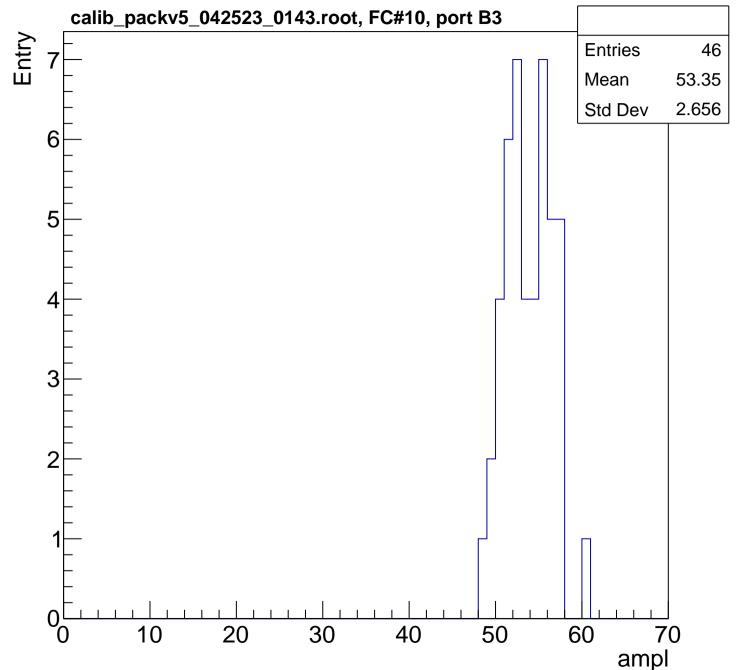


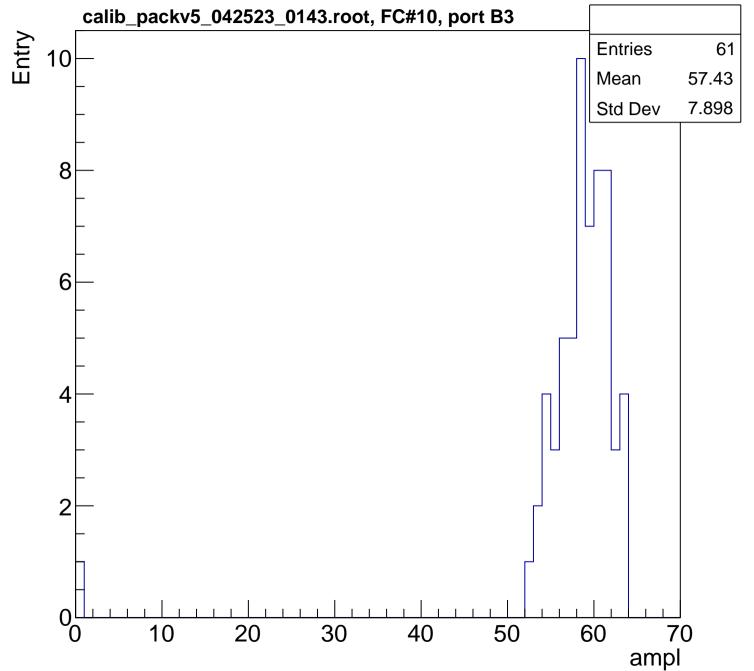


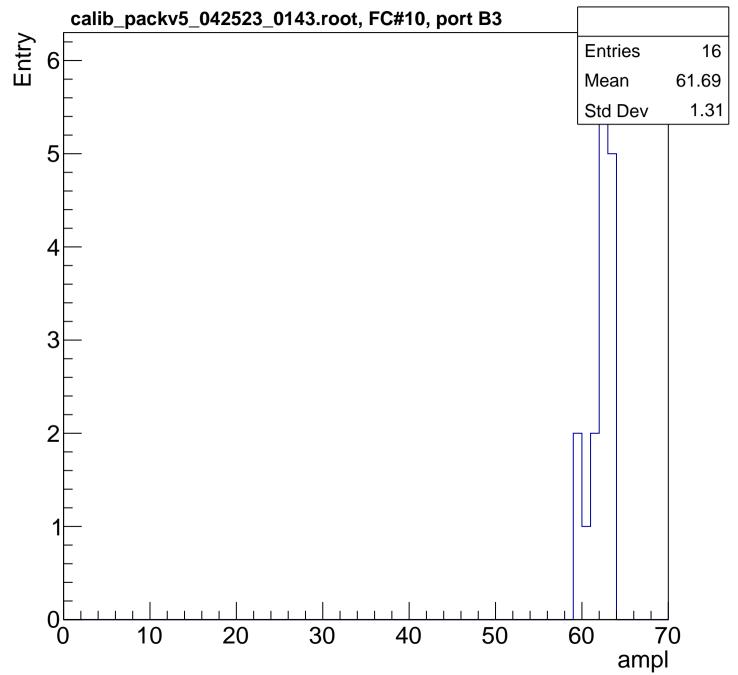


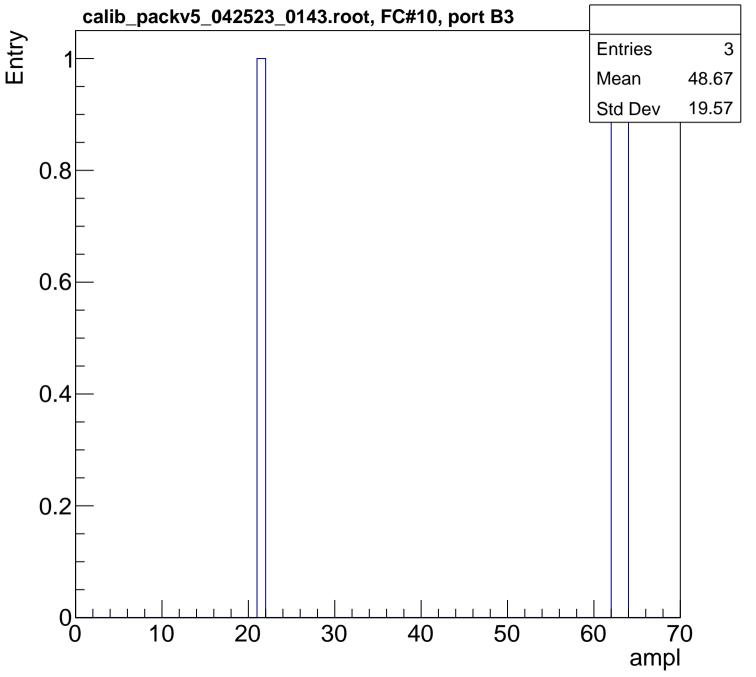


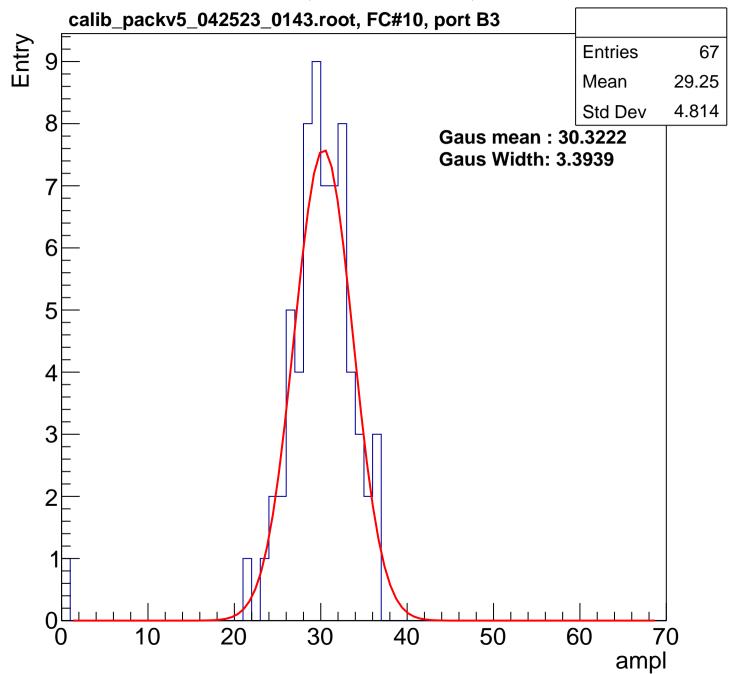


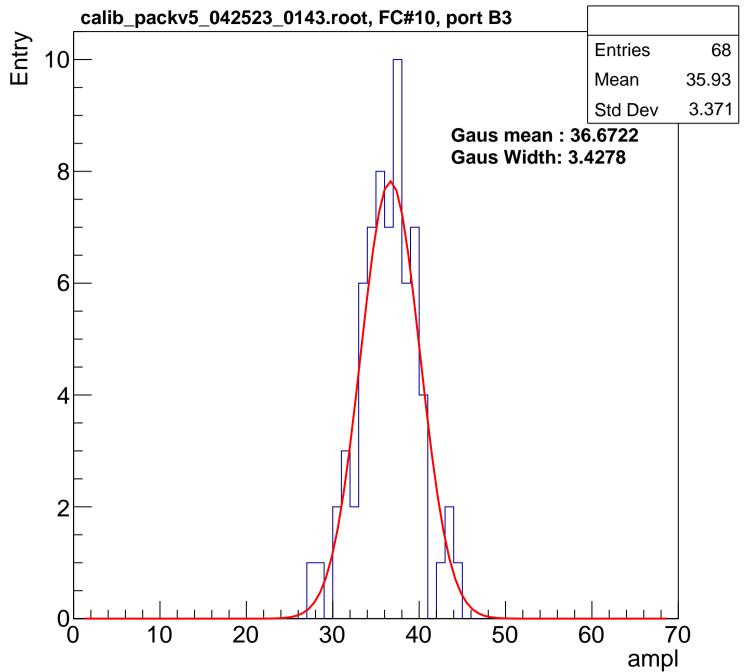


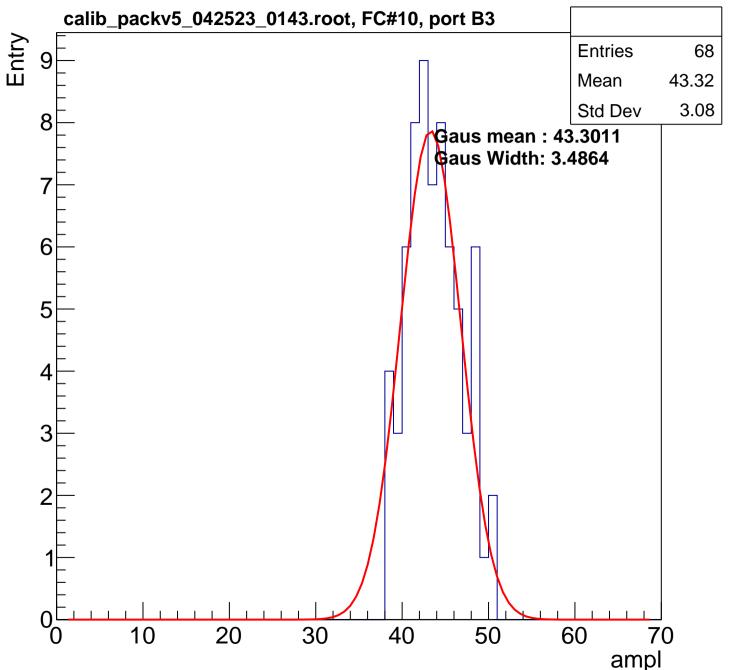


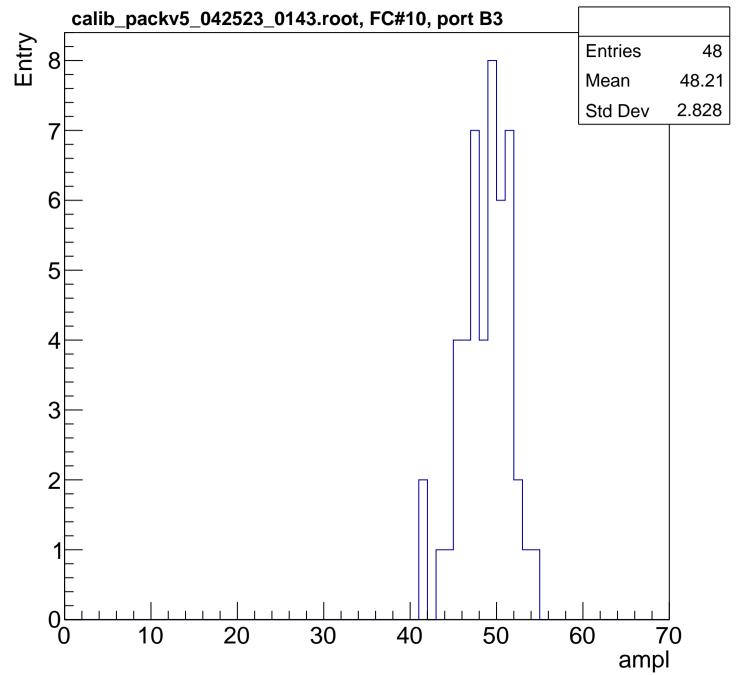


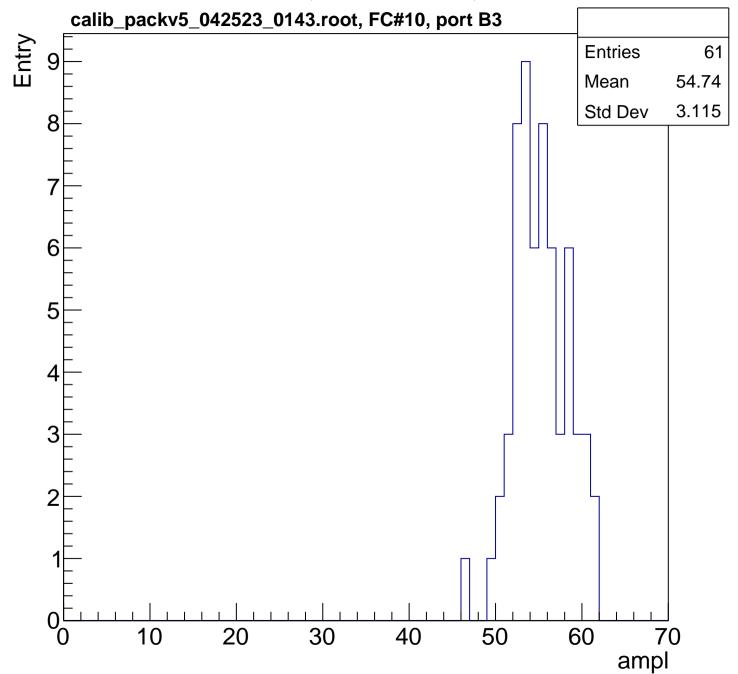


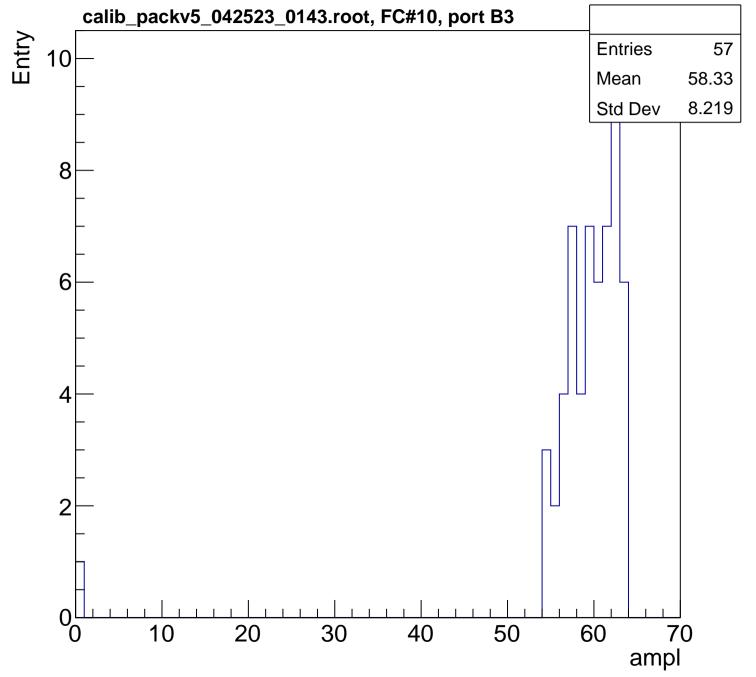


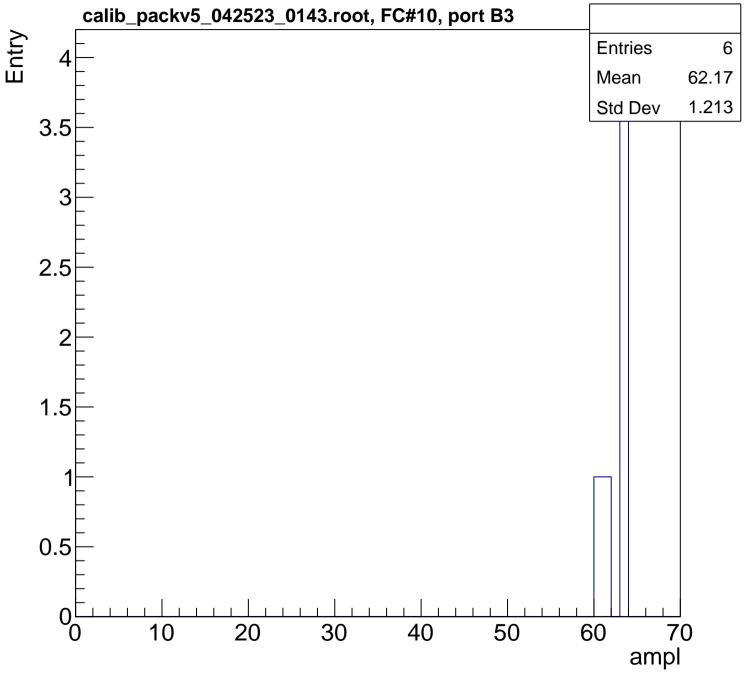


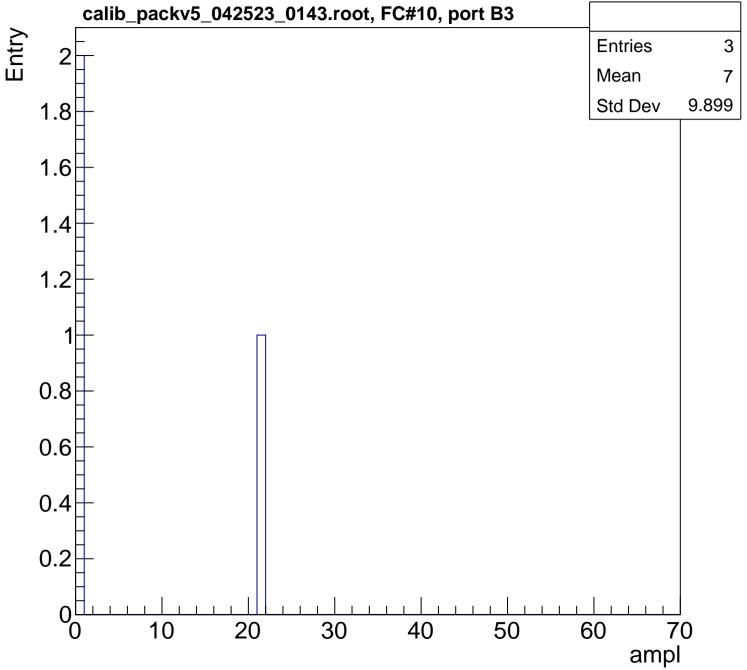


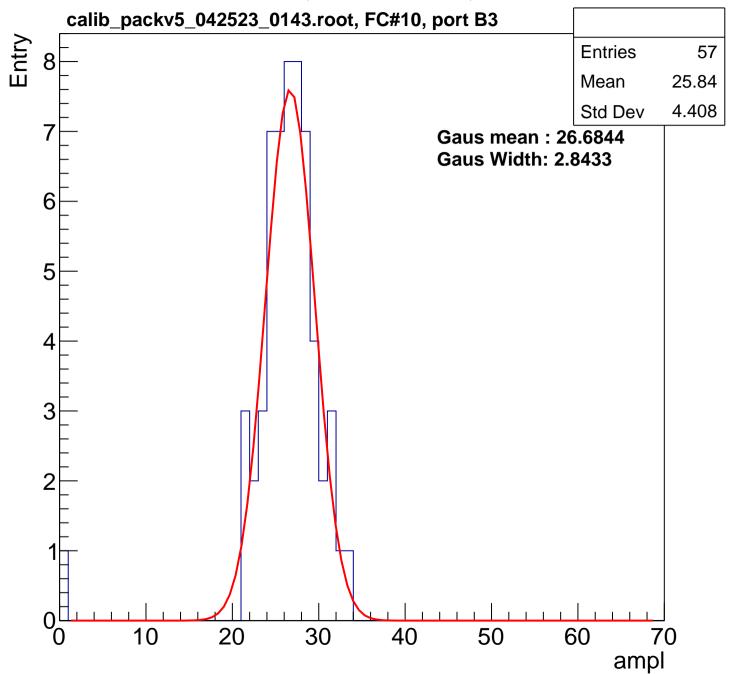


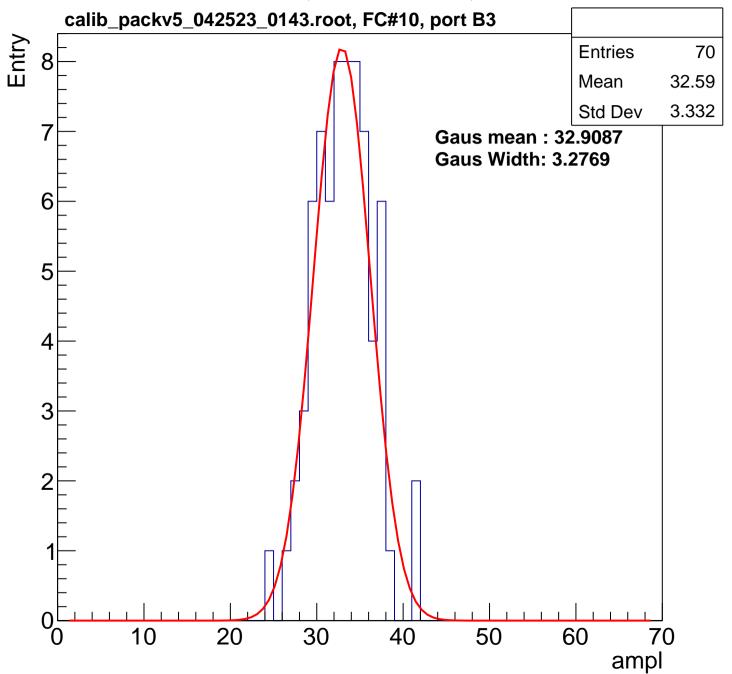


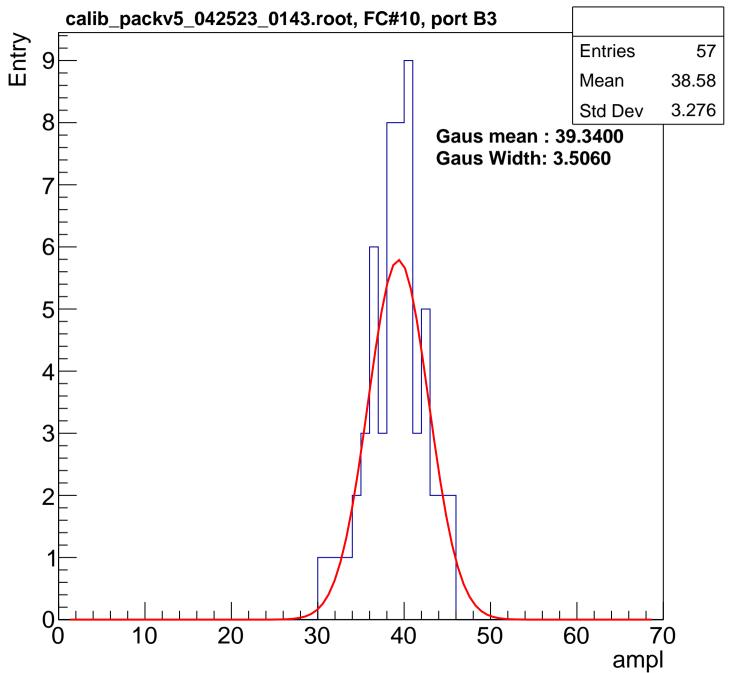


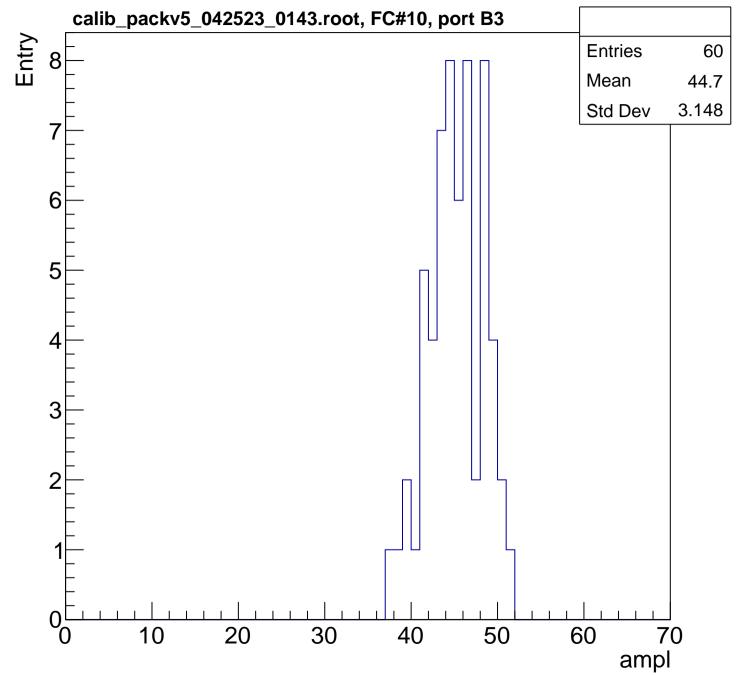


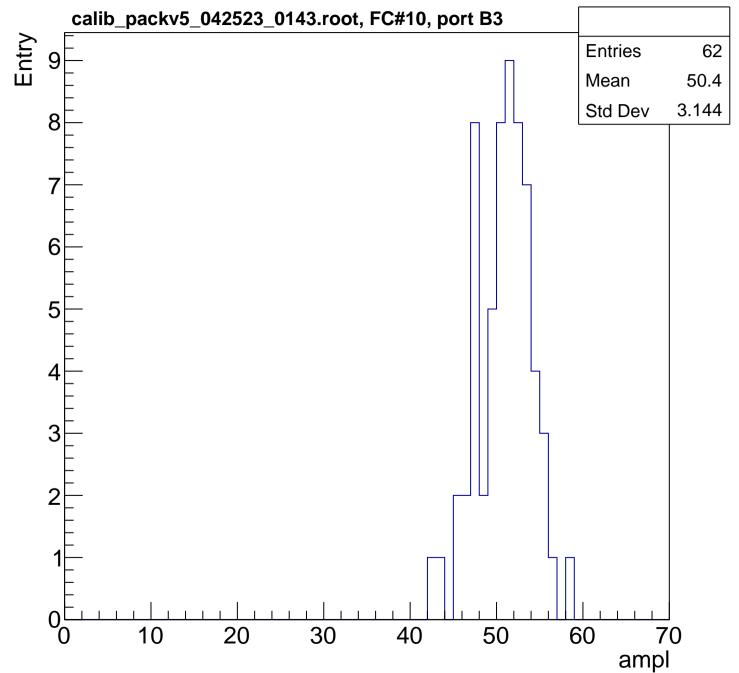


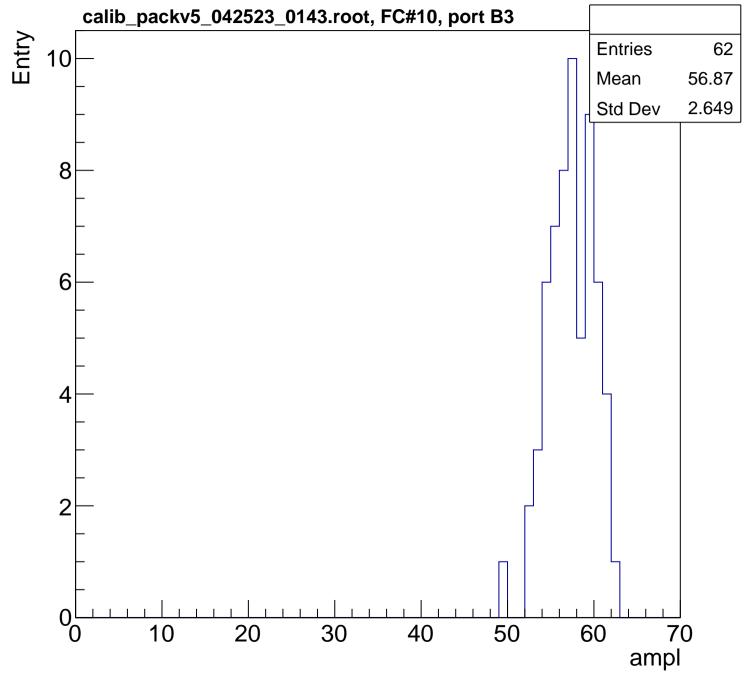


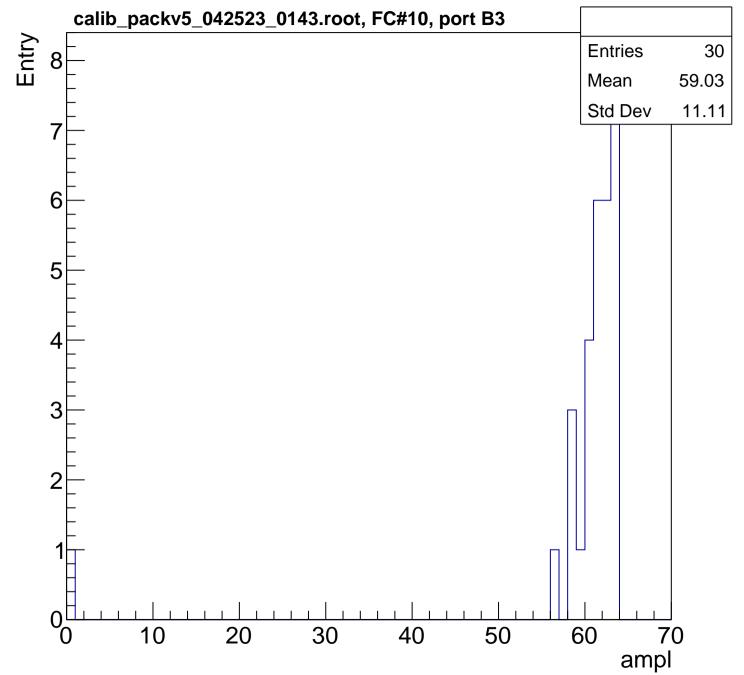


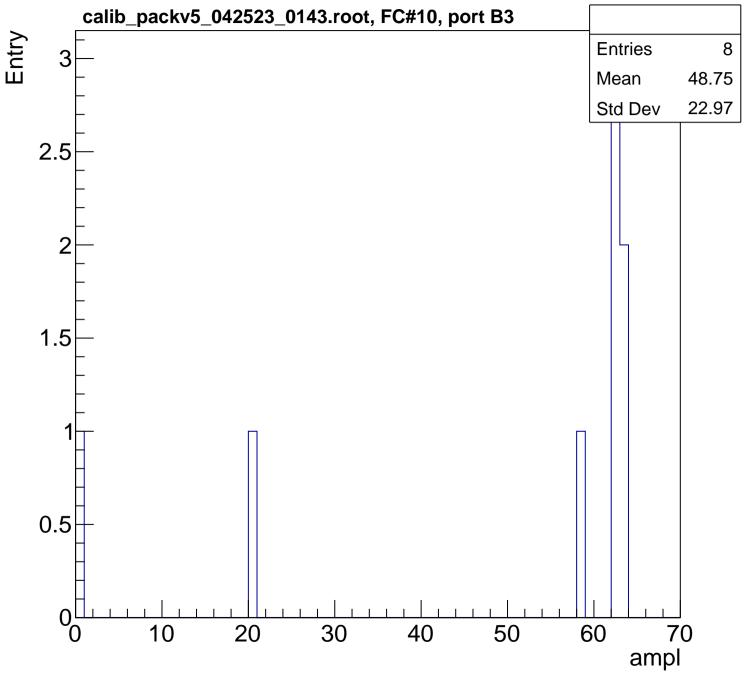


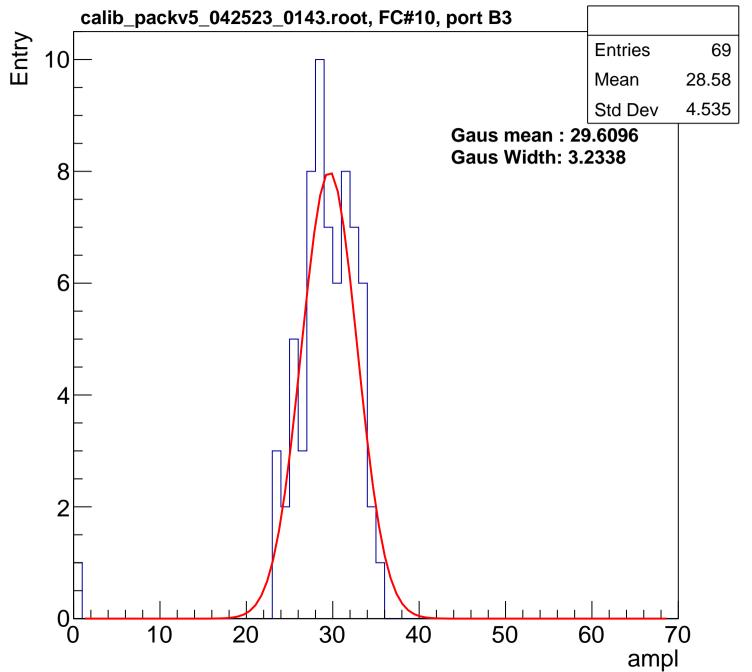


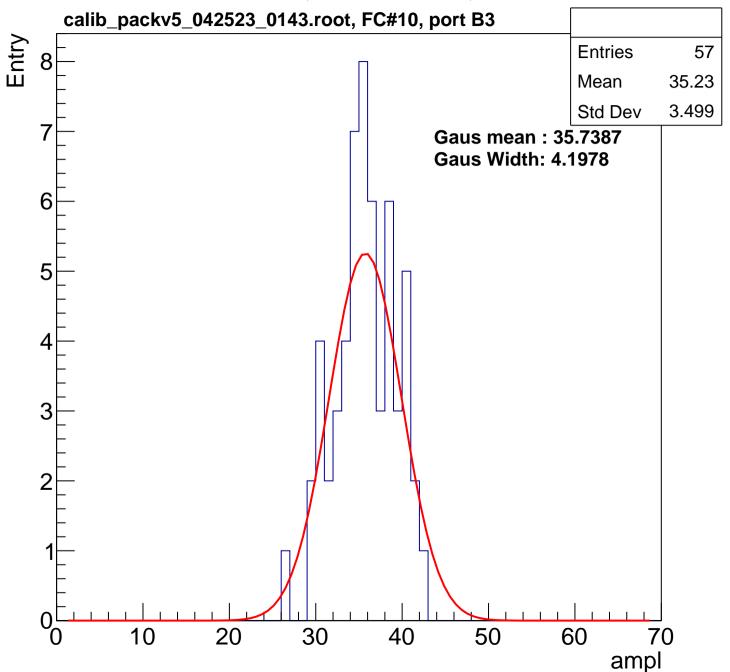


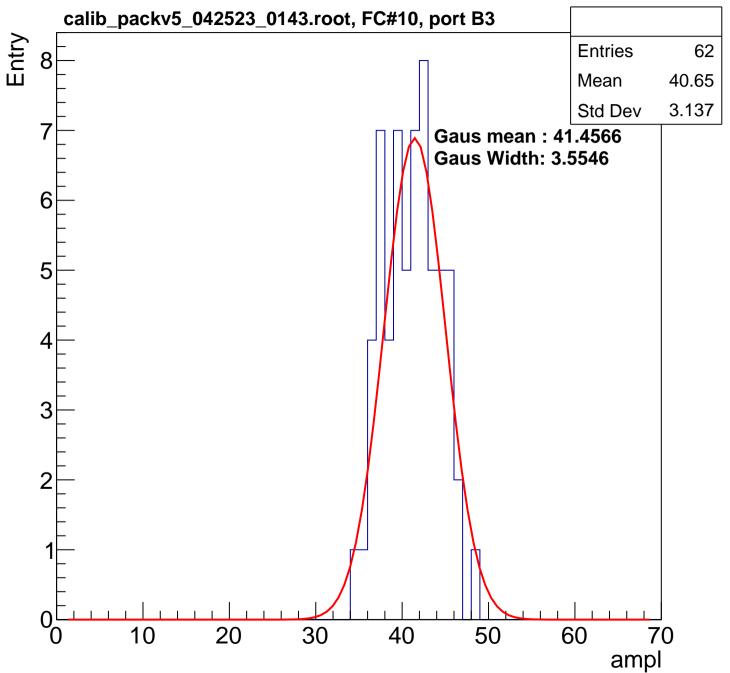


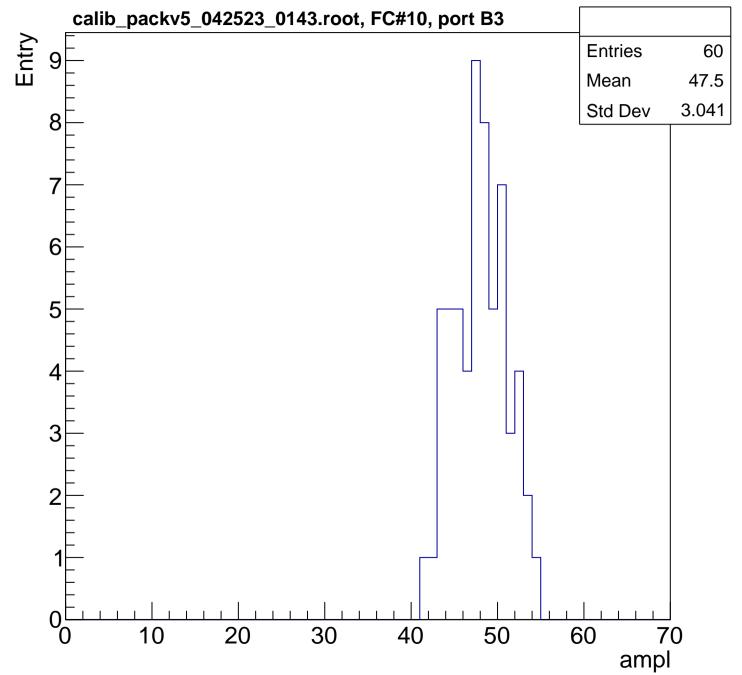


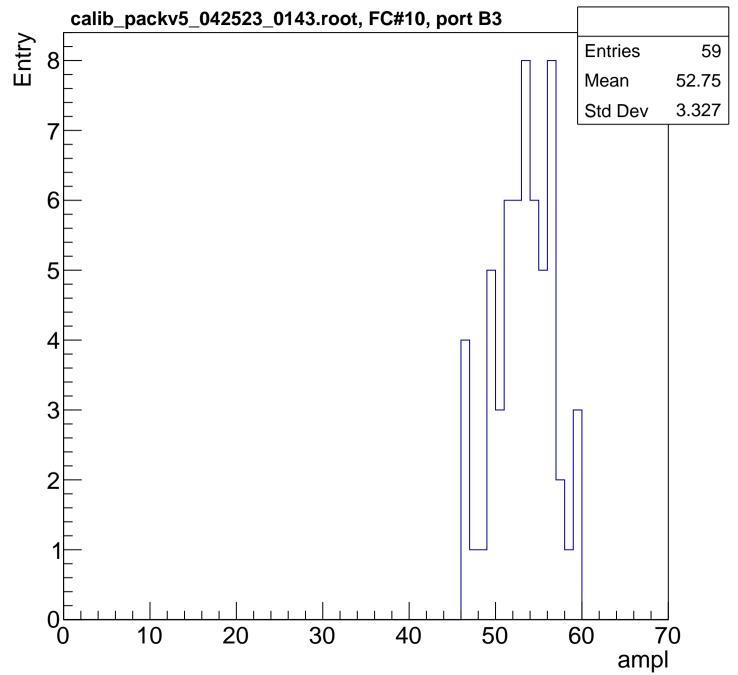


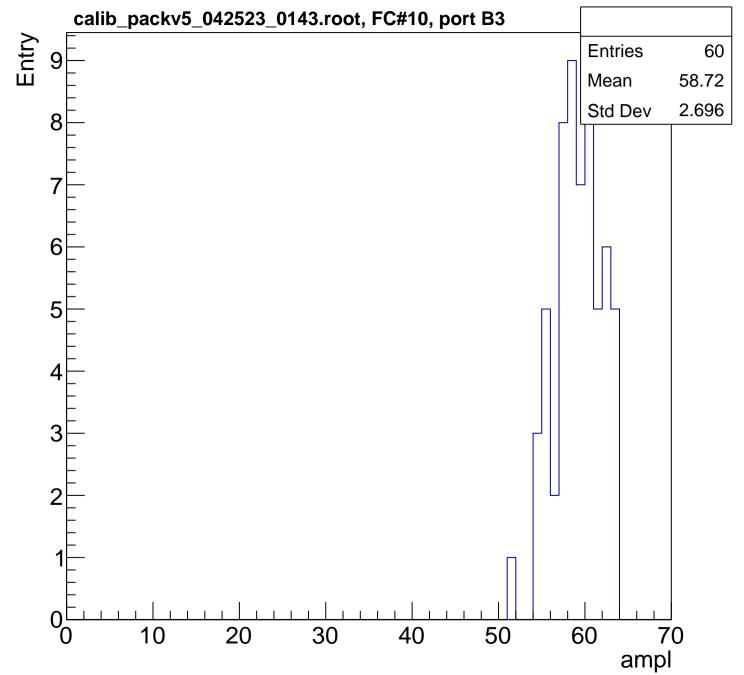


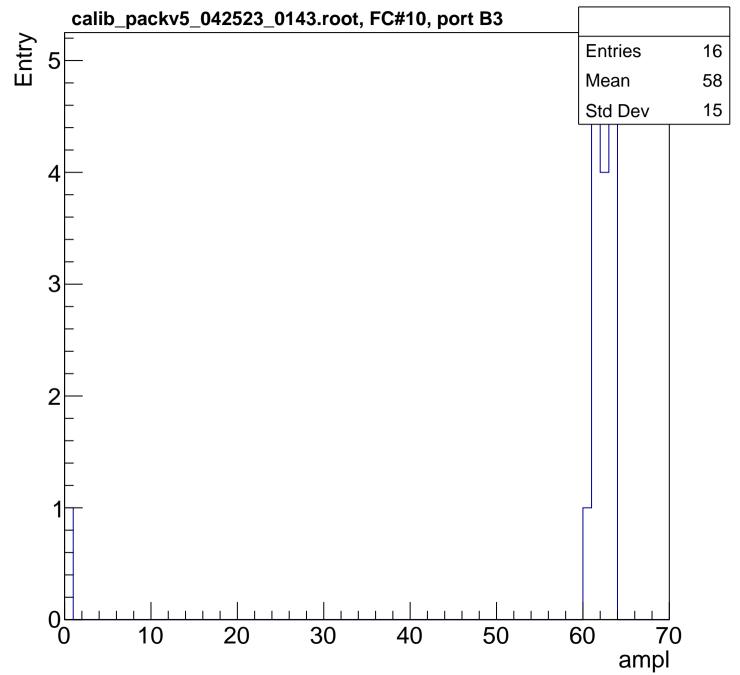


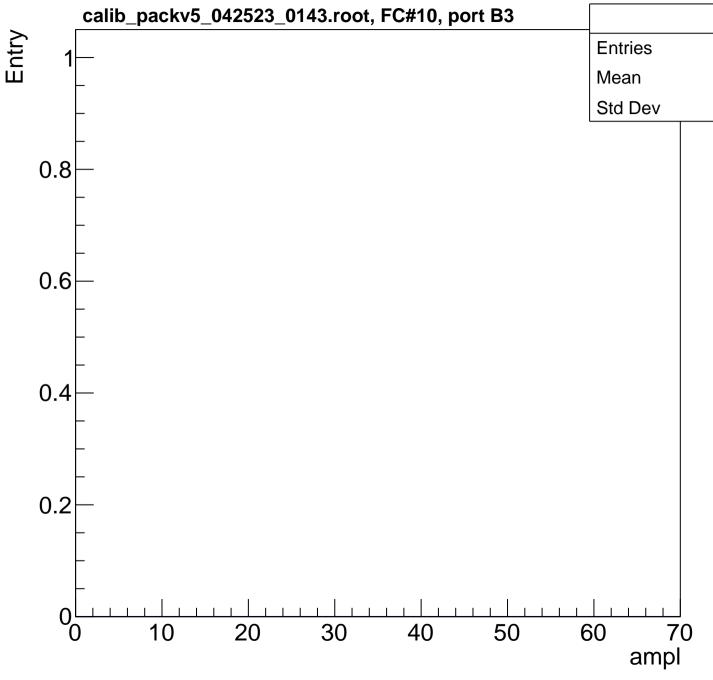


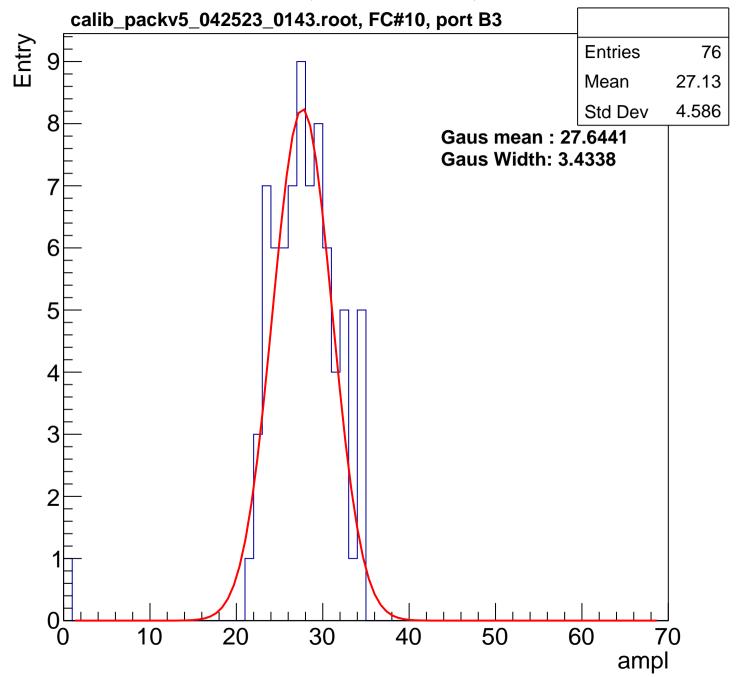


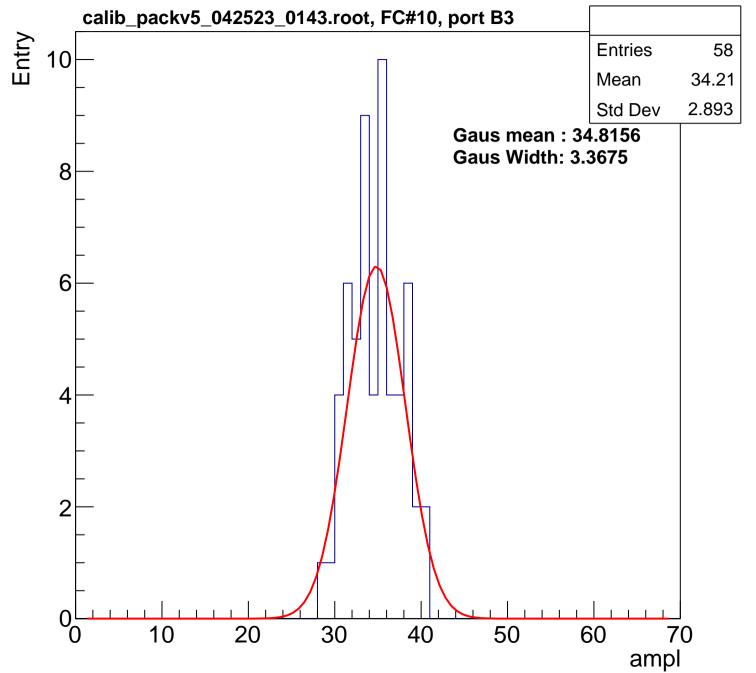


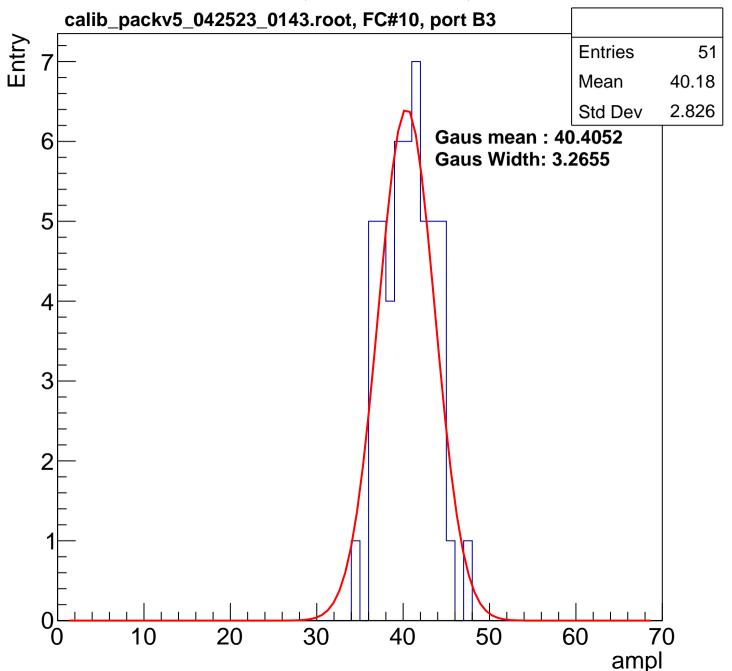


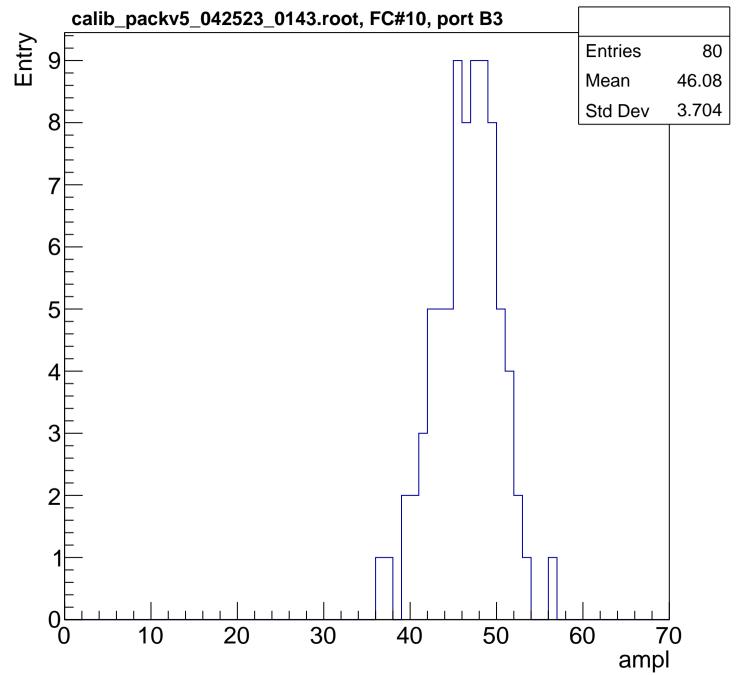


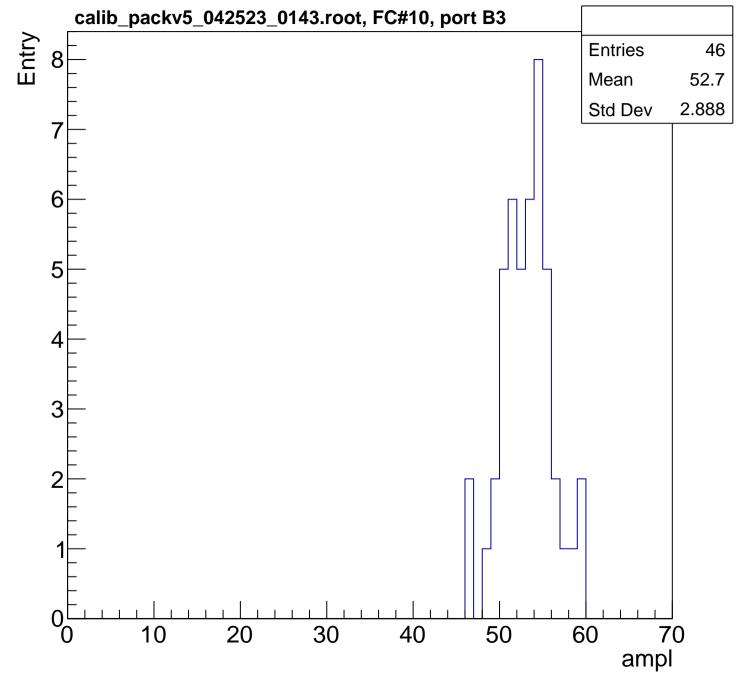


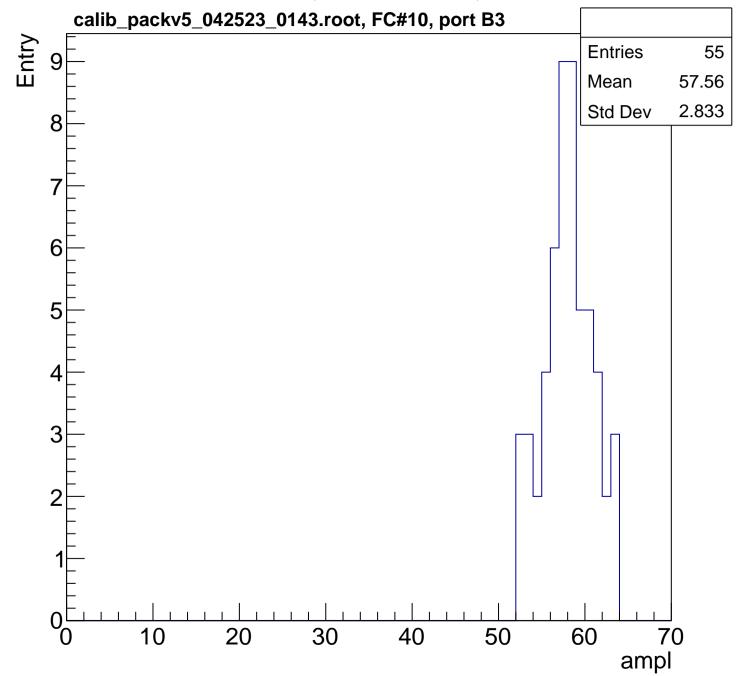


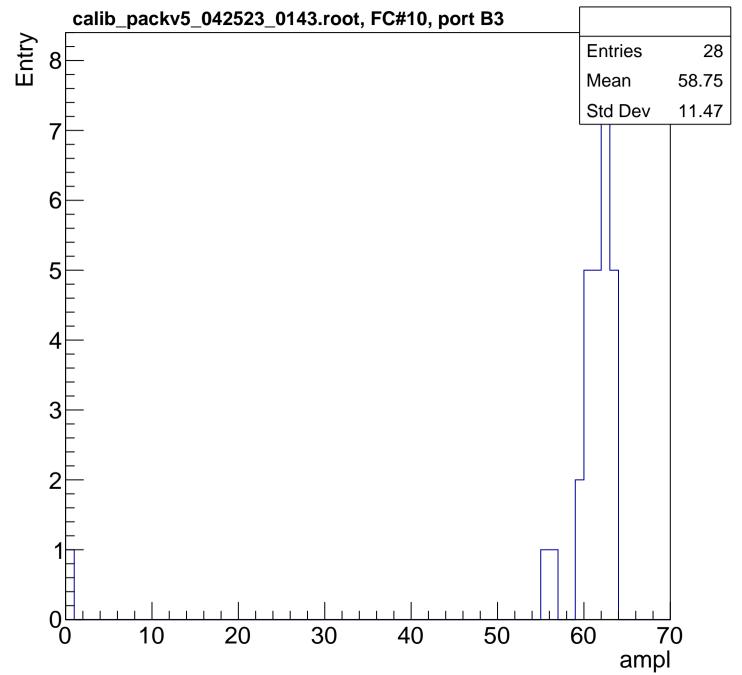


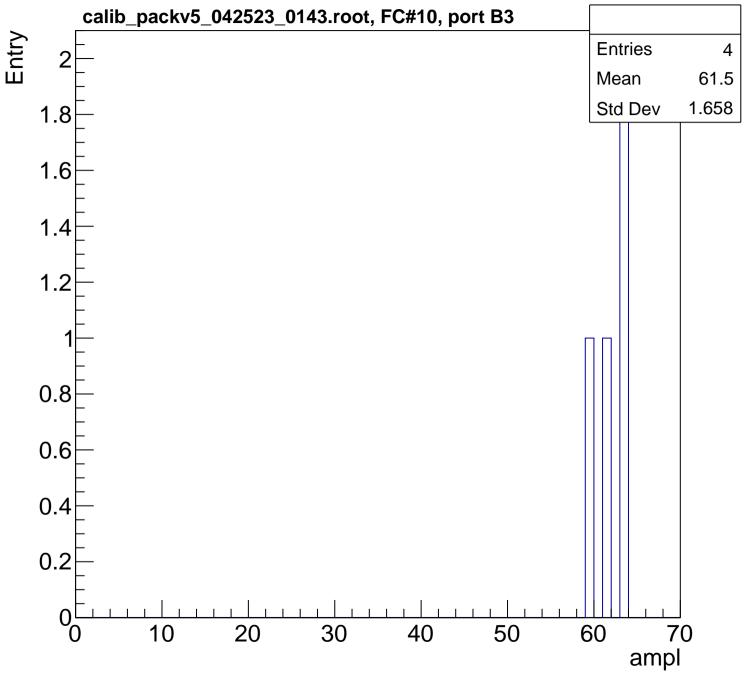


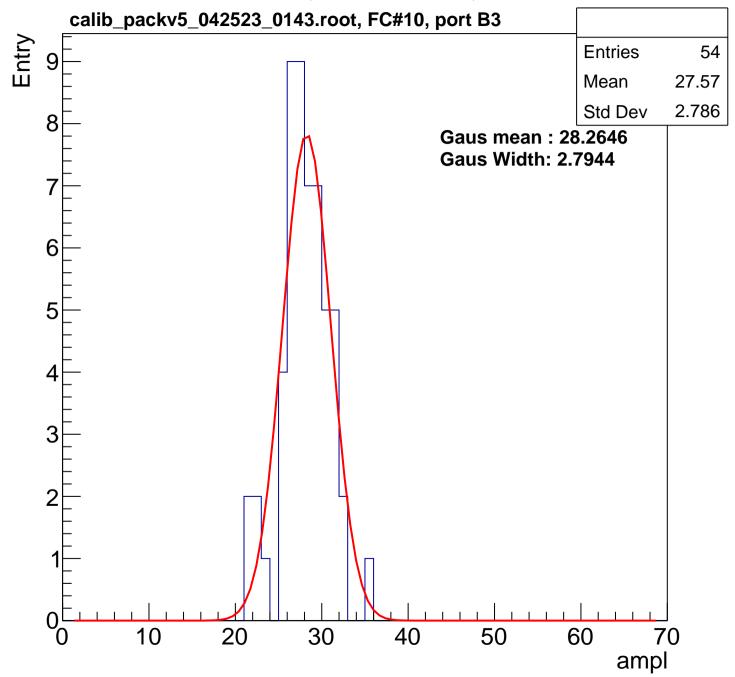


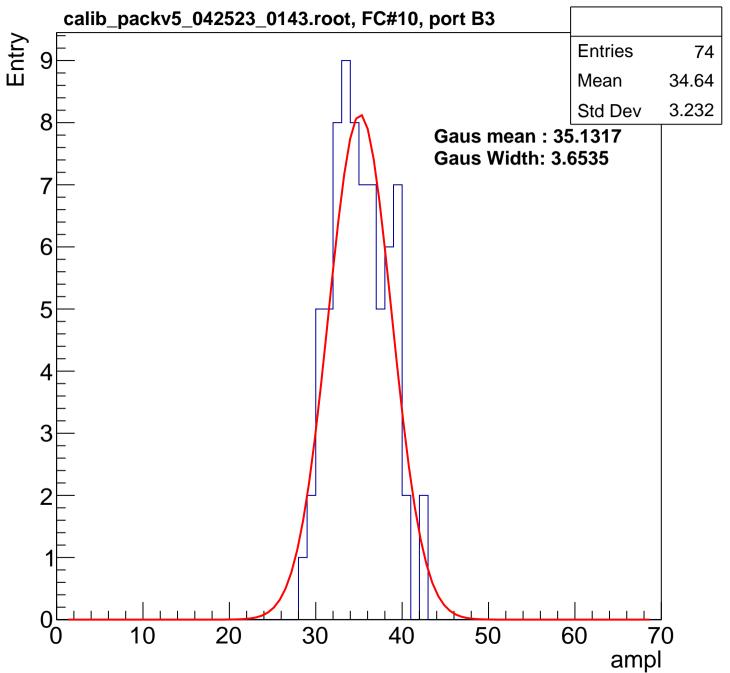


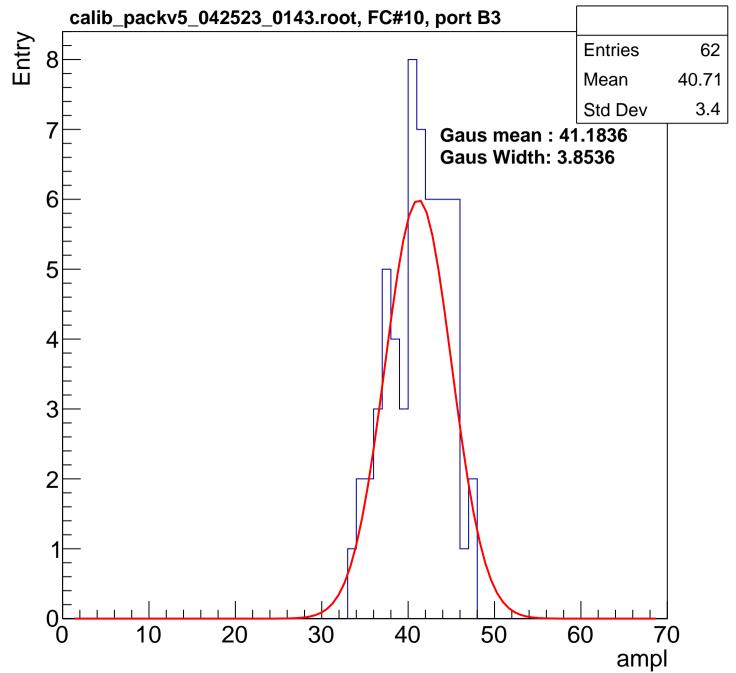


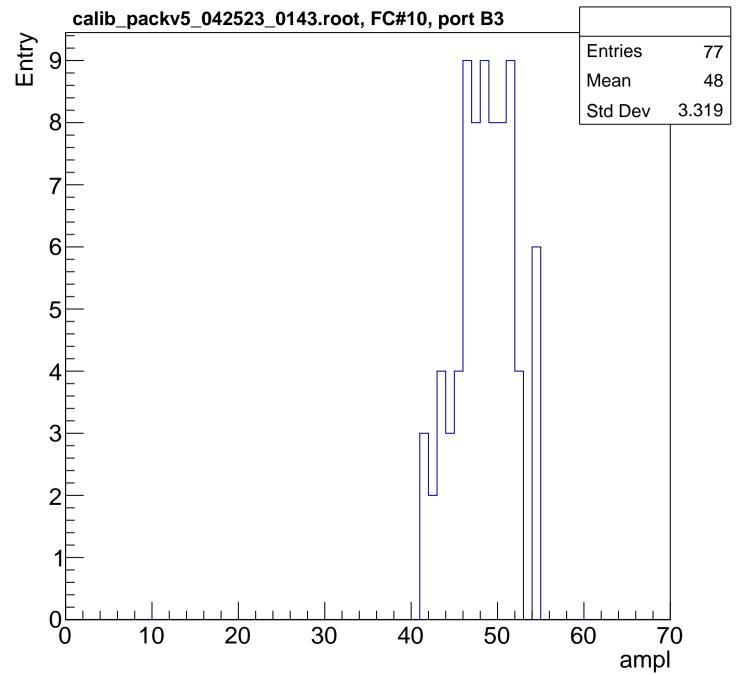


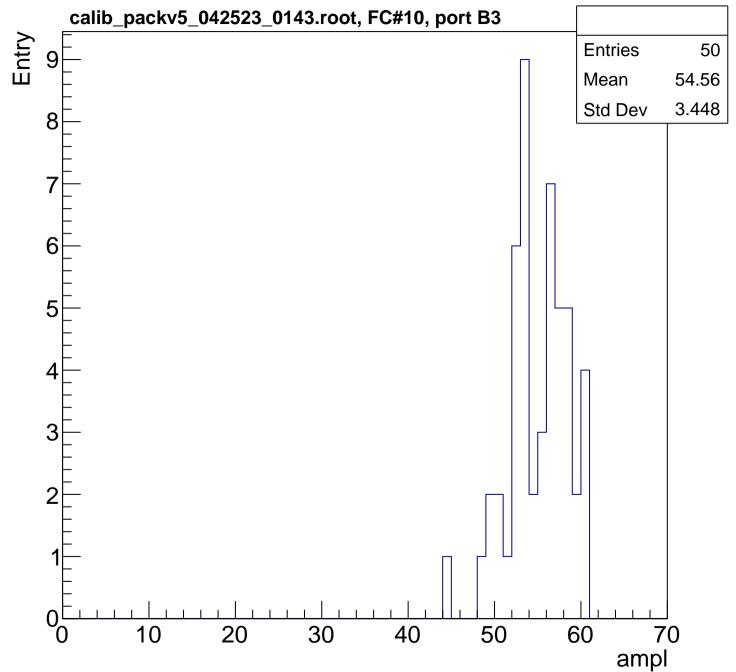


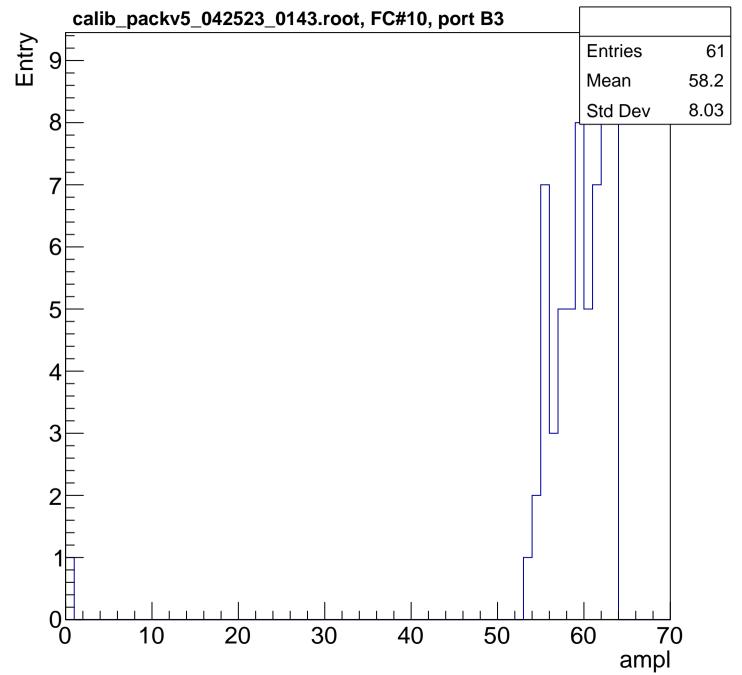


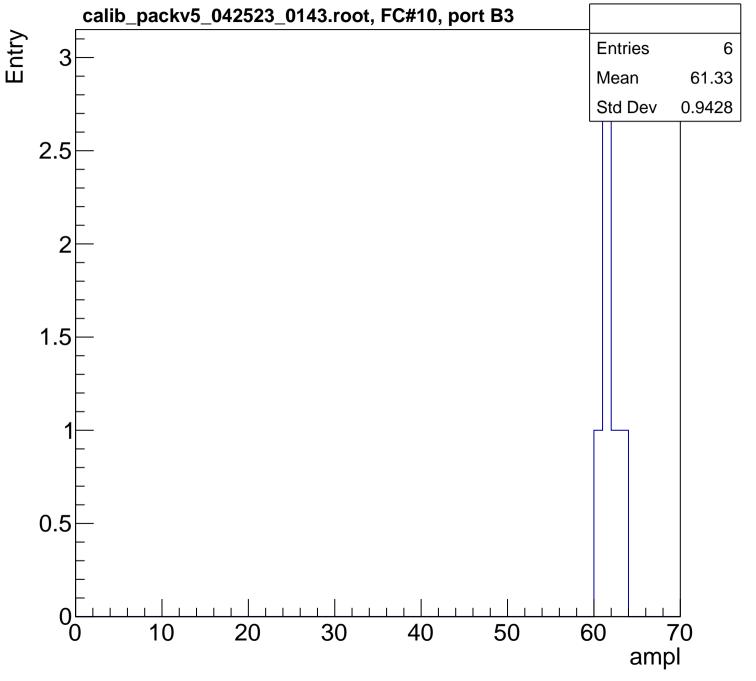












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