

B0L002S, U15-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.0968

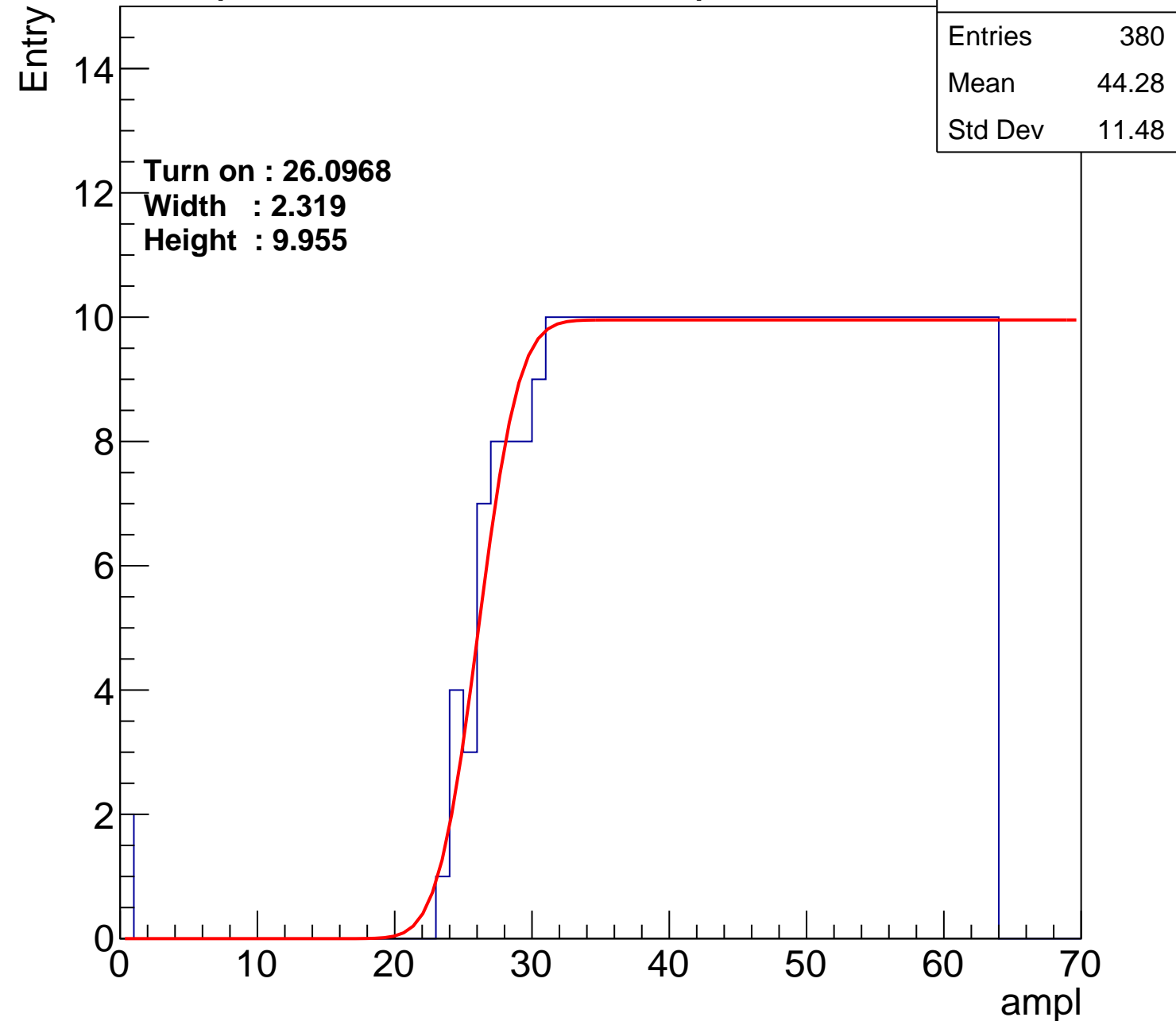
Width : 2.319

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.15
Std Dev	10.58

Turn on : 30.3260

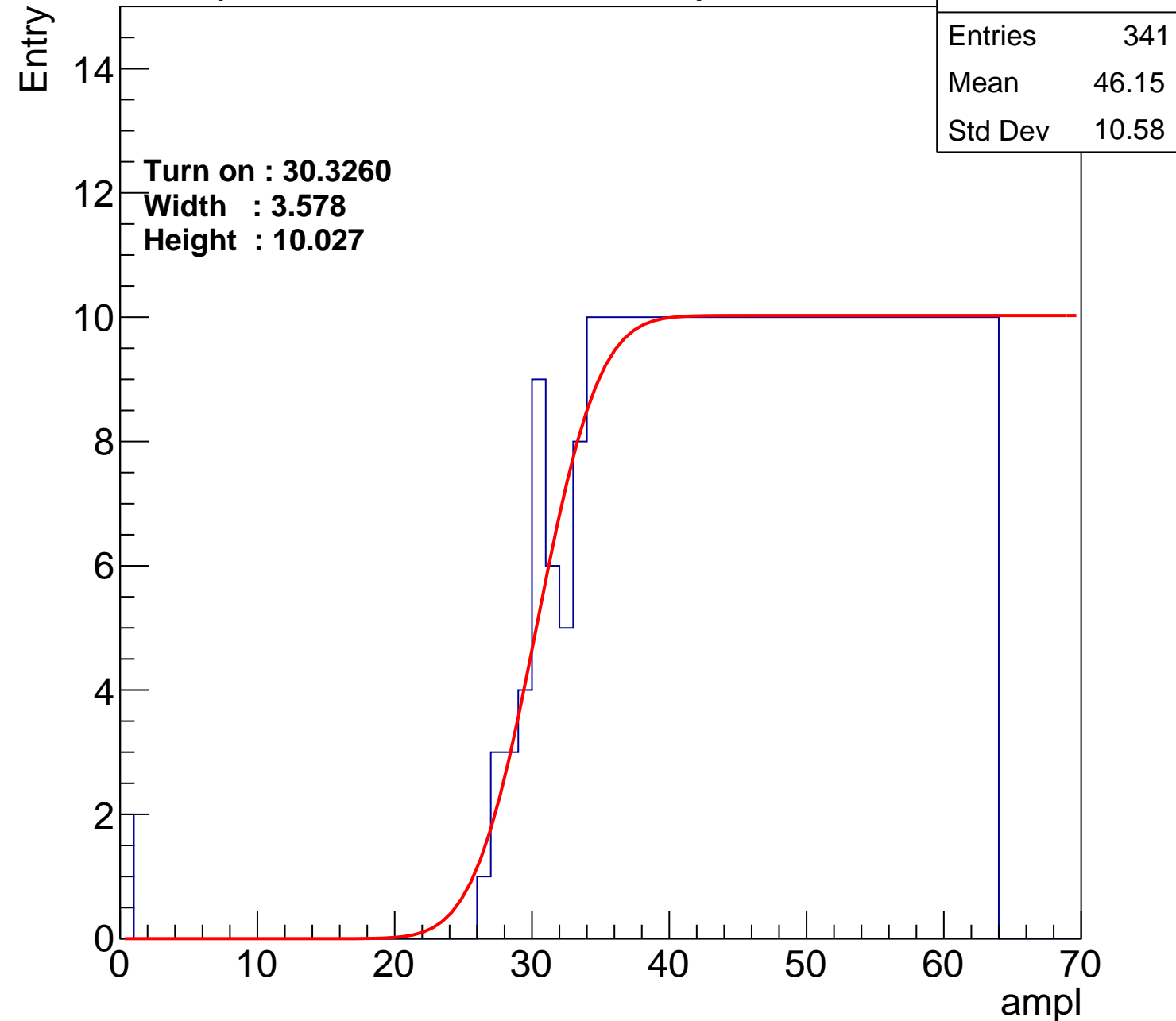
Width : 3.578

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch2

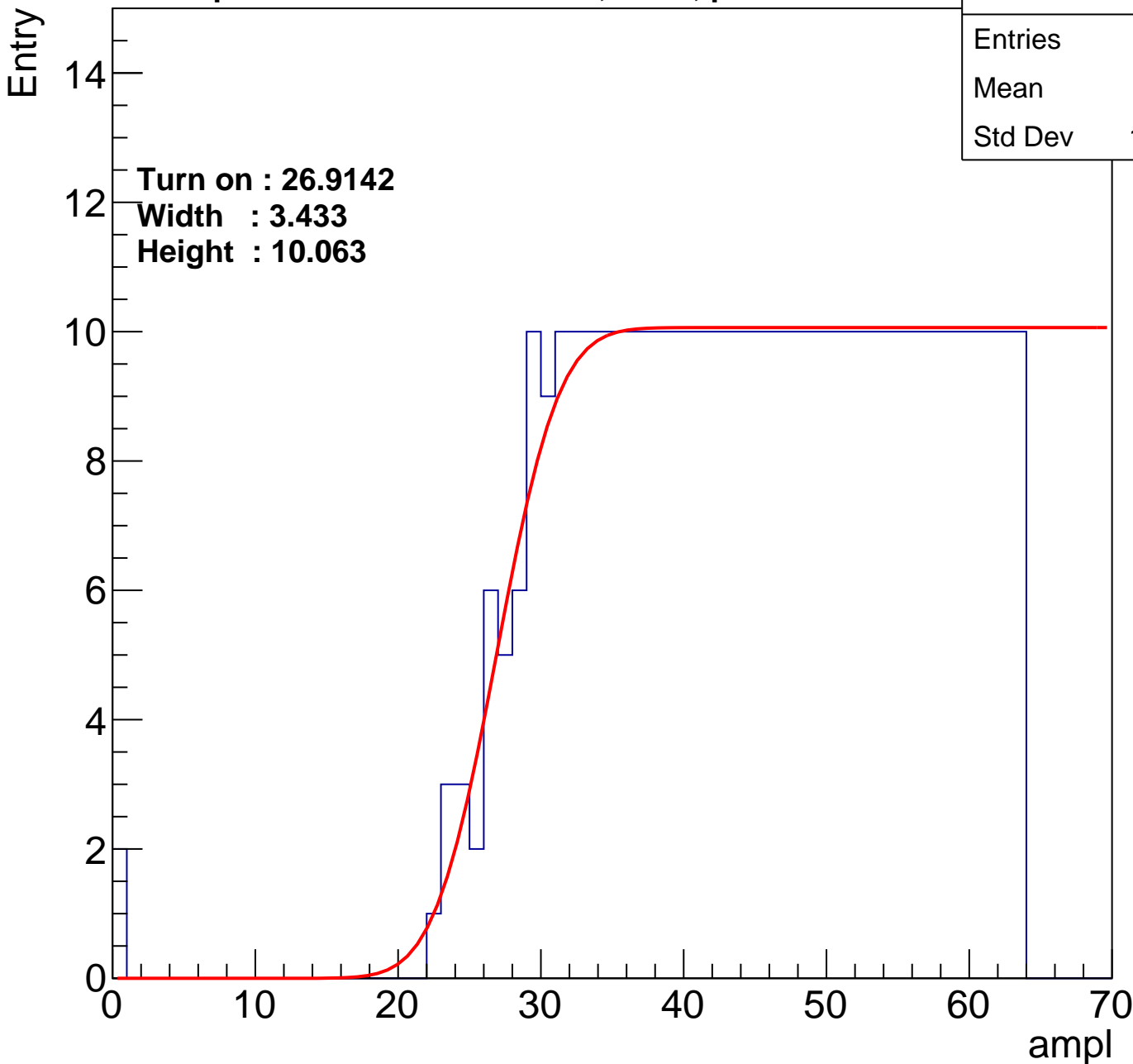
calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 26.9142

Width : 3.433

Height : 10.063



B0L002S, U15-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.38
Std Dev	11.48

Turn on : 26.8563

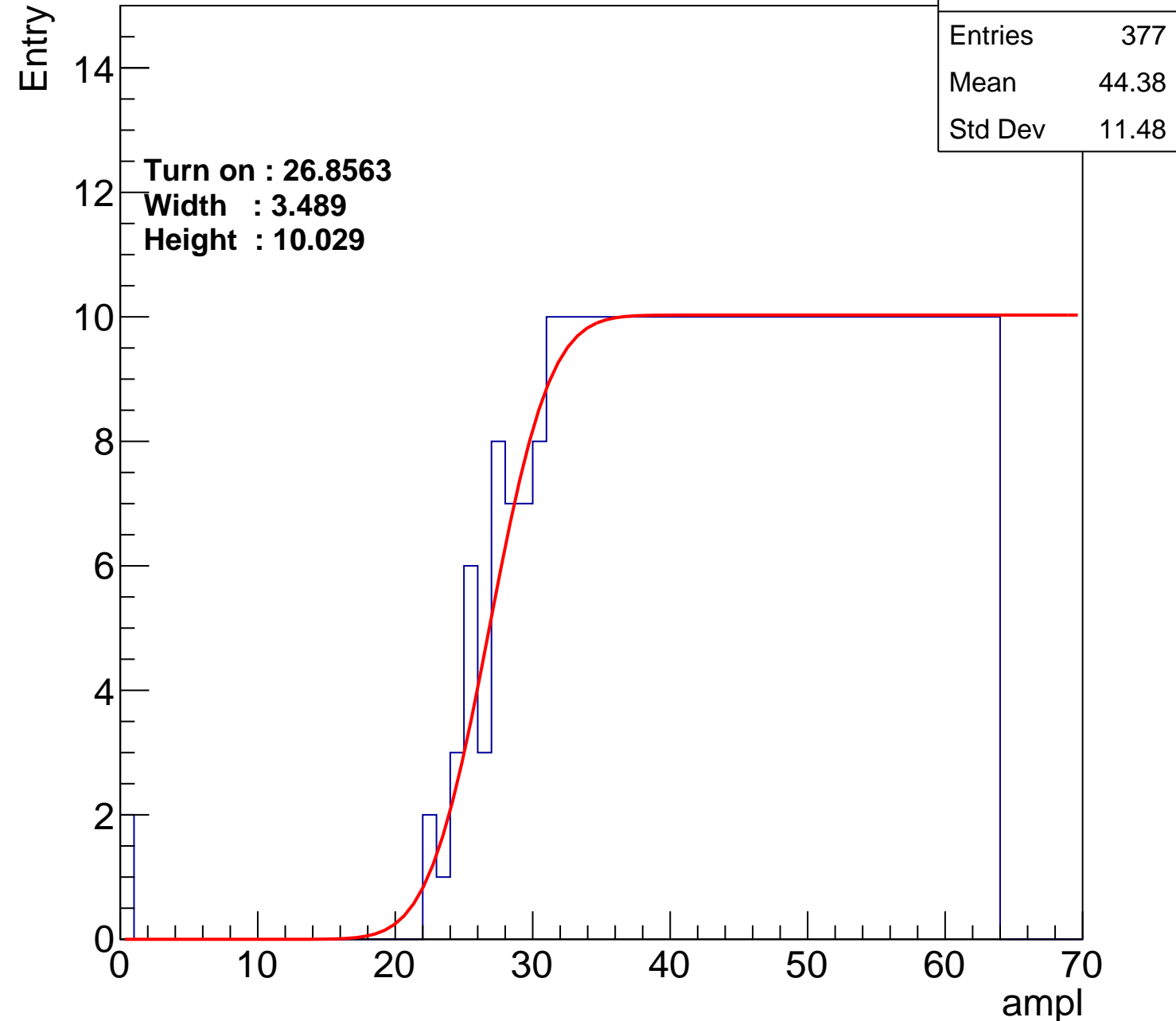
Width : 3.489

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.85
Std Dev	11.02

Turn on : 27.5210

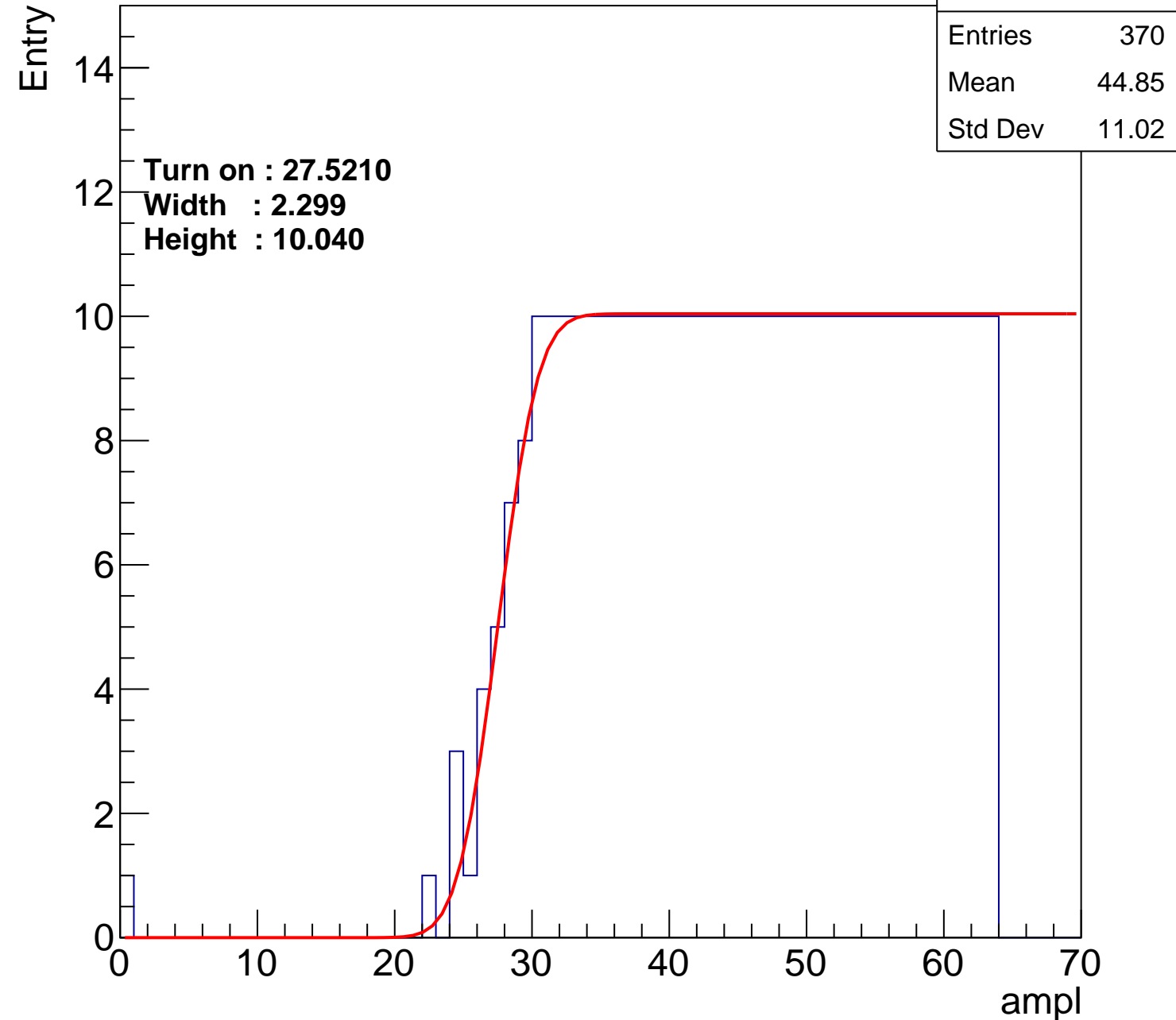
Width : 2.299

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.21
Std Dev	11.85

Turn on : 26.3212

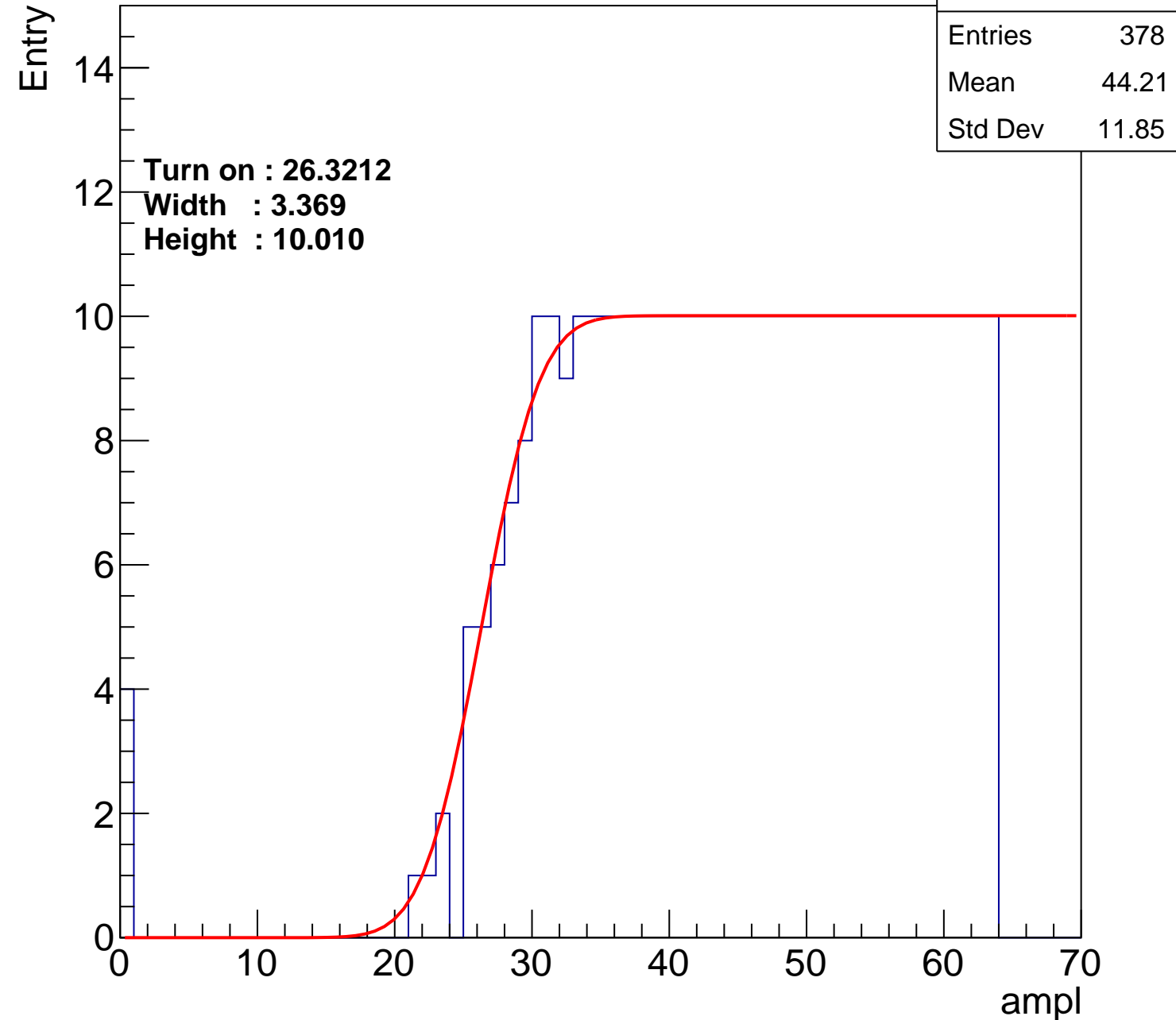
Width : 3.369

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.42
Std Dev	11.09

Turn on : 28.8295

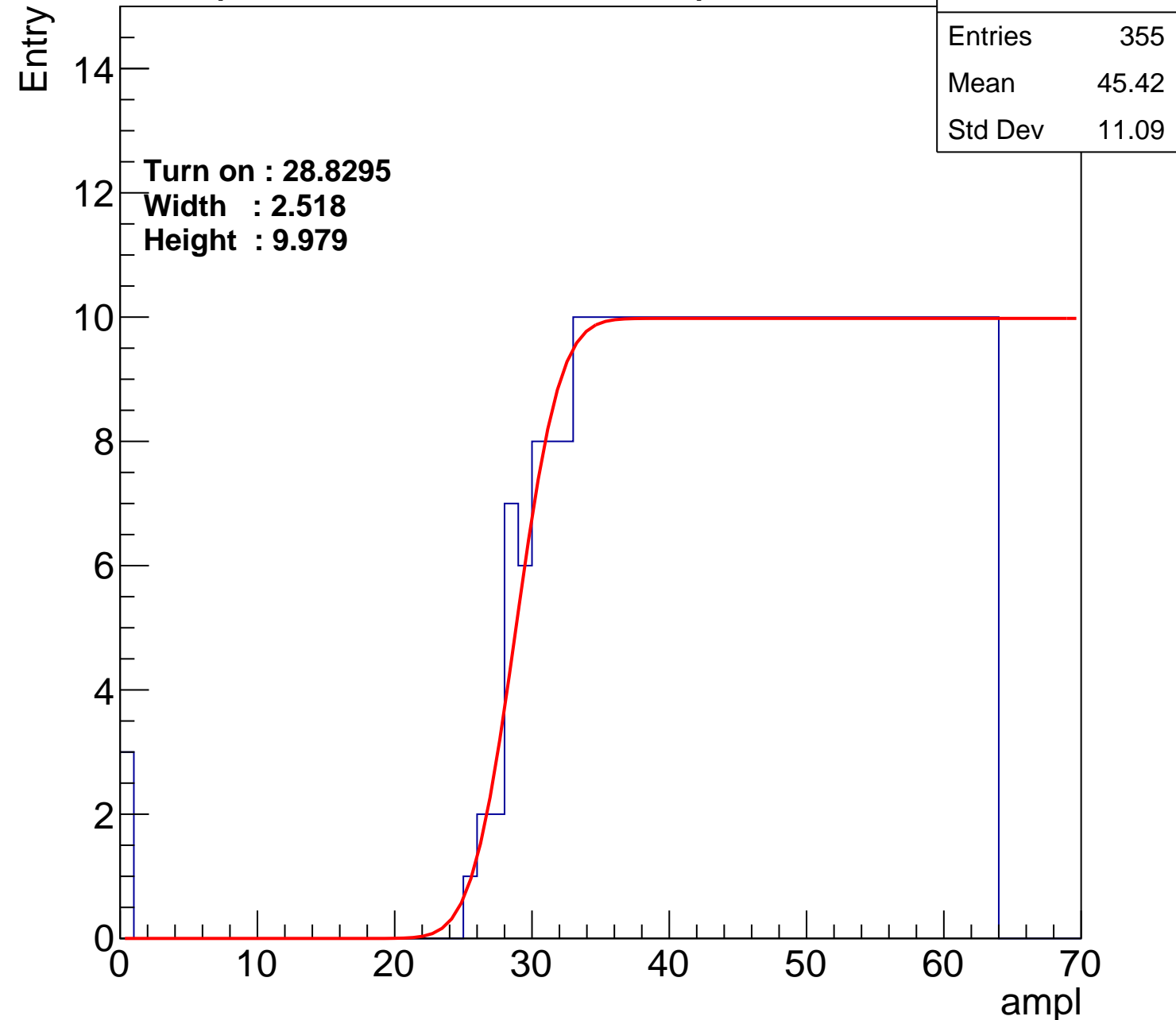
Width : 2.518

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.62
Std Dev	11.64

Turn on : 26.8655

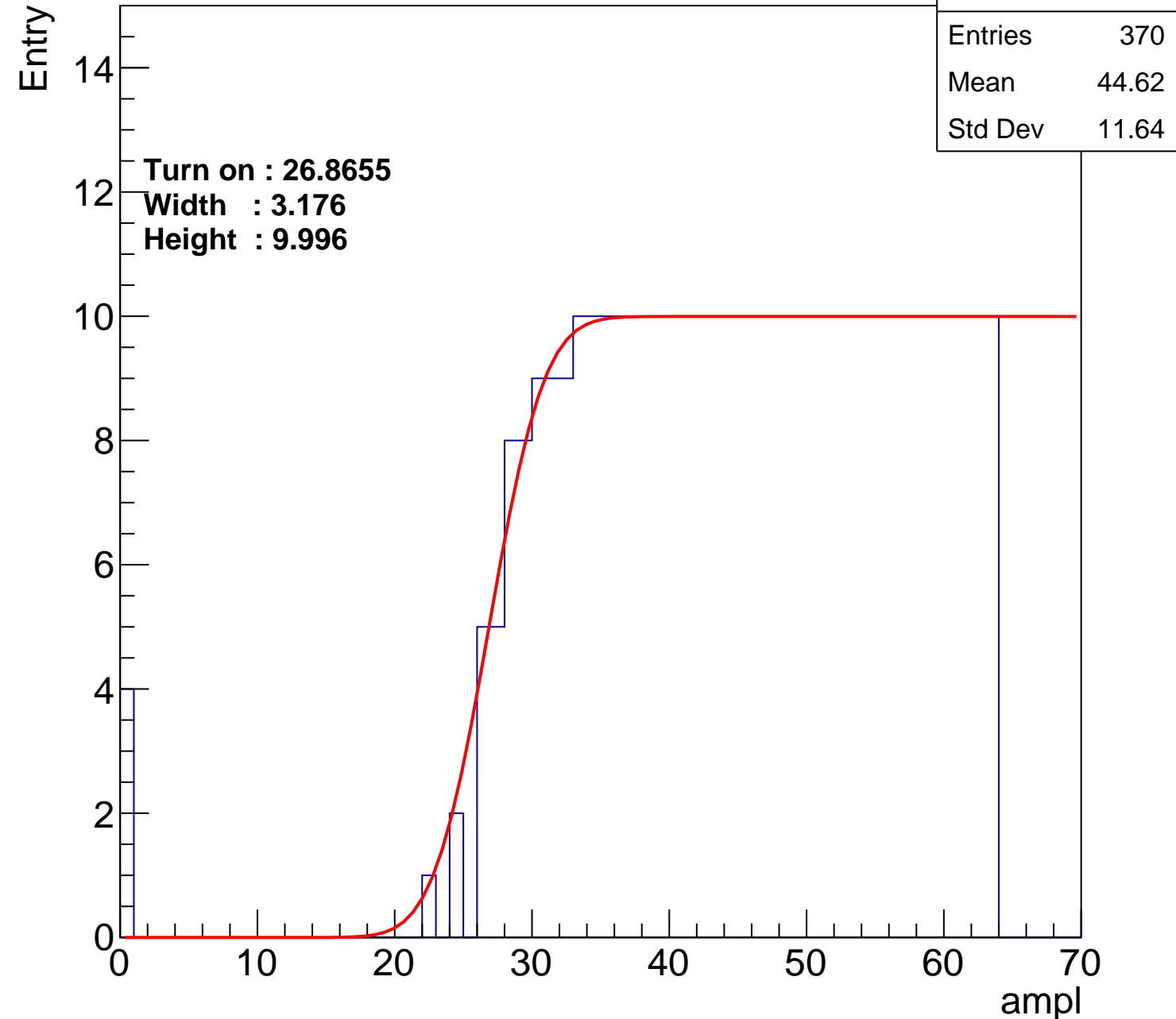
Width : 3.176

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.03
Std Dev	11.74

Turn on : 25.8781

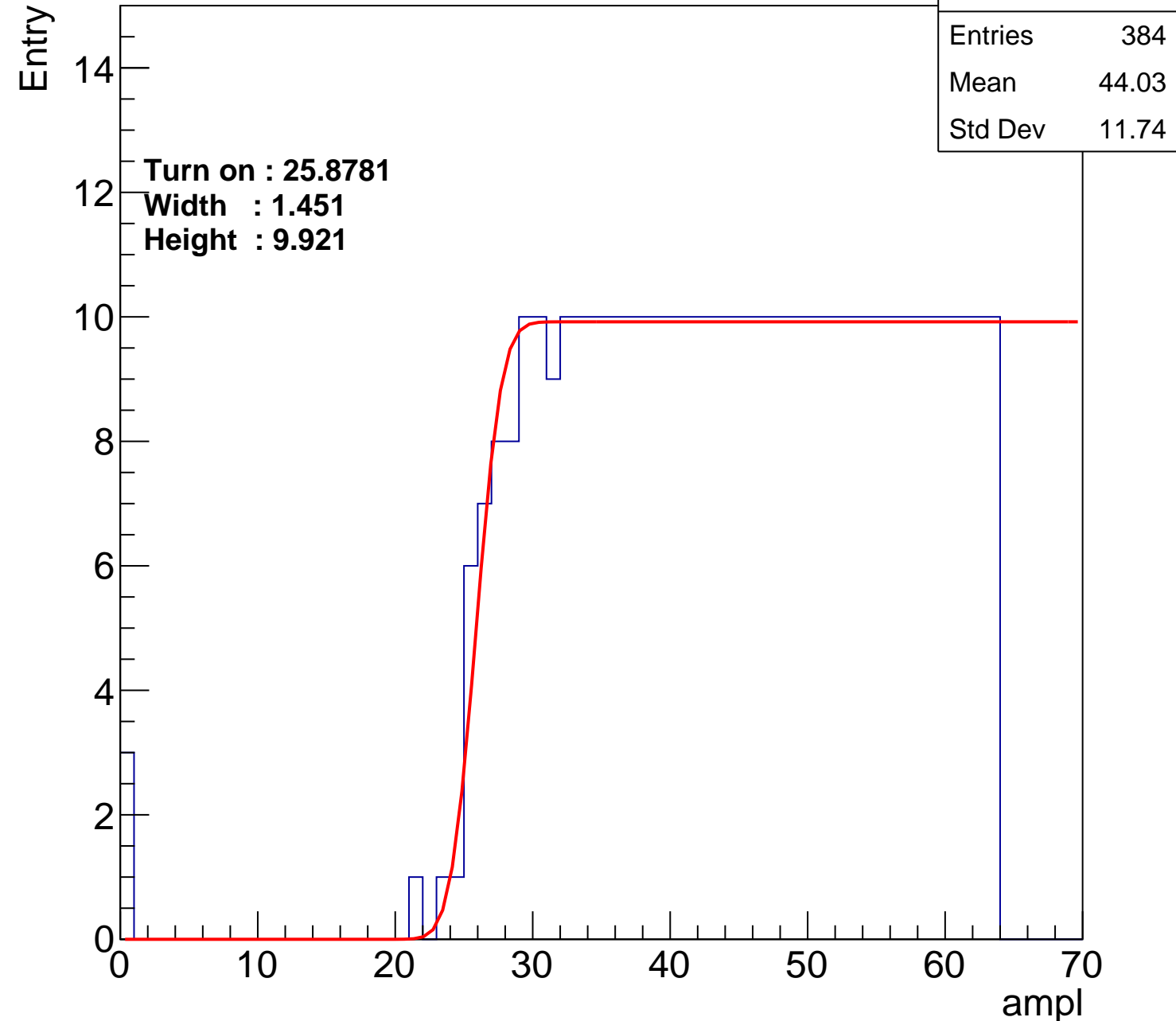
Width : 1.451

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.09
Std Dev	11.59

Turn on : 26.2874

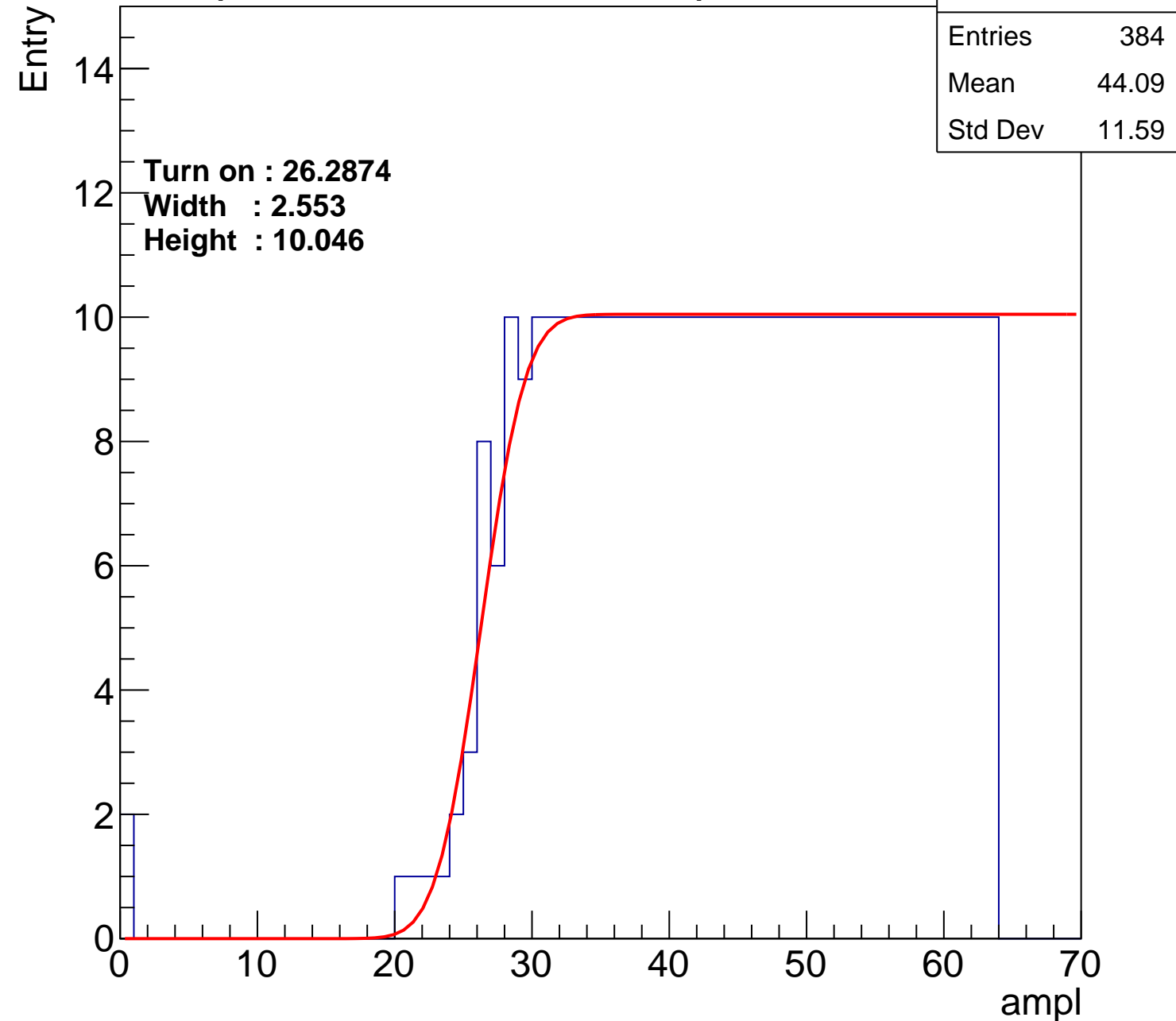
Width : 2.553

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.42
Std Dev	11.47

Turn on : 26.9299

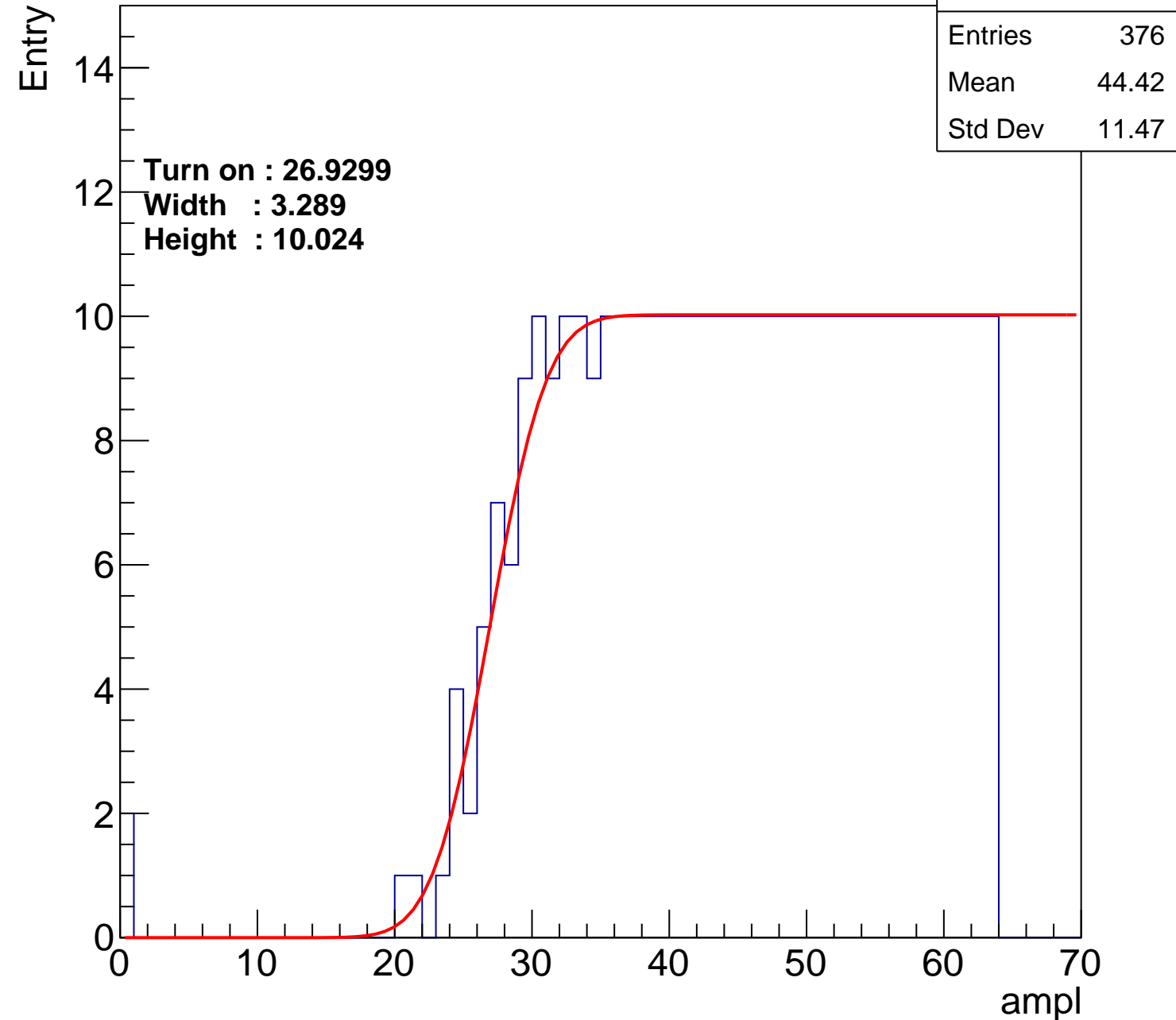
Width : 3.289

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.51
Std Dev	10.75

Turn on : 28.7557

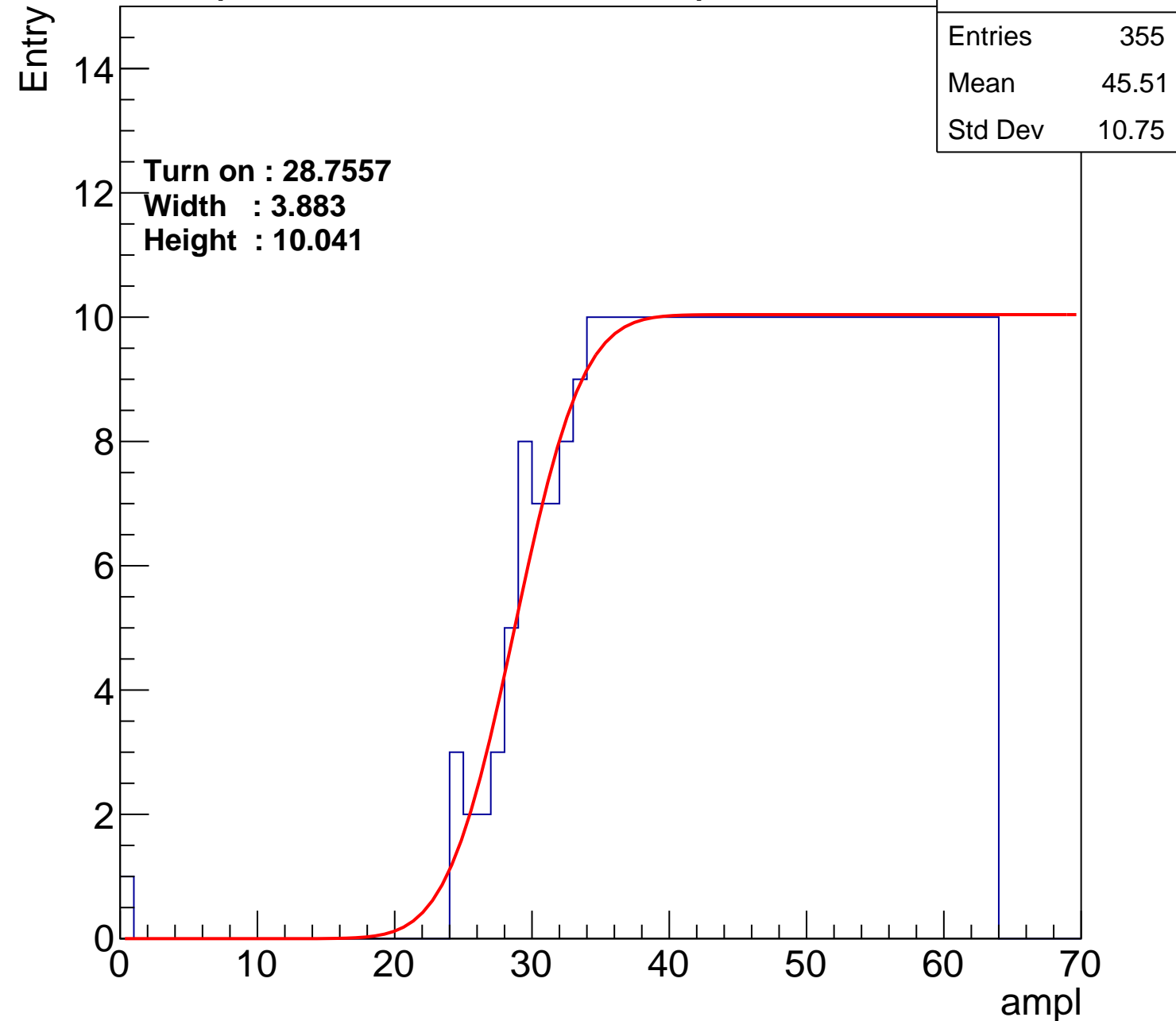
Width : 3.883

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch12

calib_packv5_042523_0143.root, FC#8, port C1

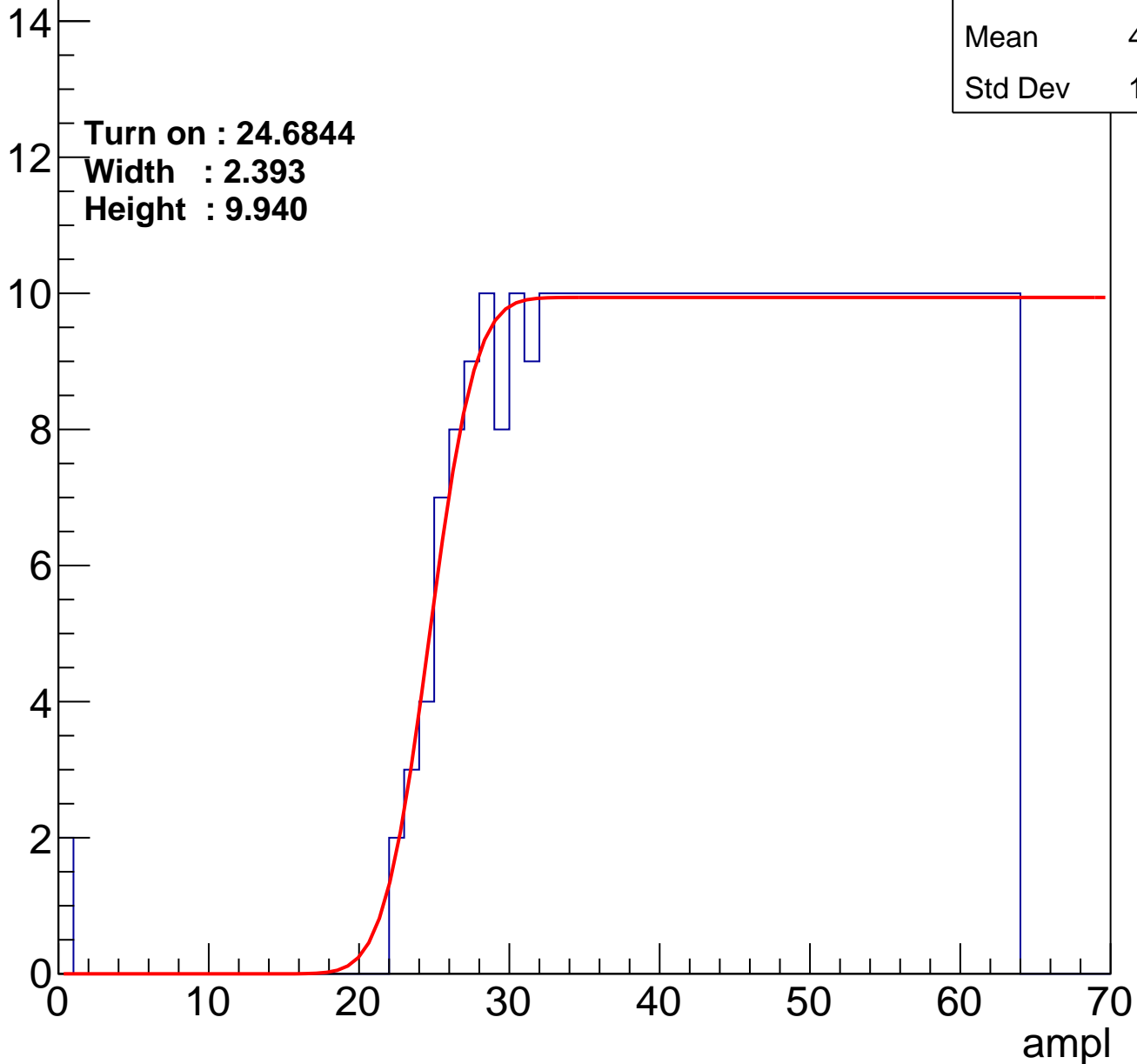
Entries	392
Mean	43.69
Std Dev	11.79

Turn on : 24.6844

Width : 2.393

Height : 9.940

Entry



B0L002S, U15-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 26.1815

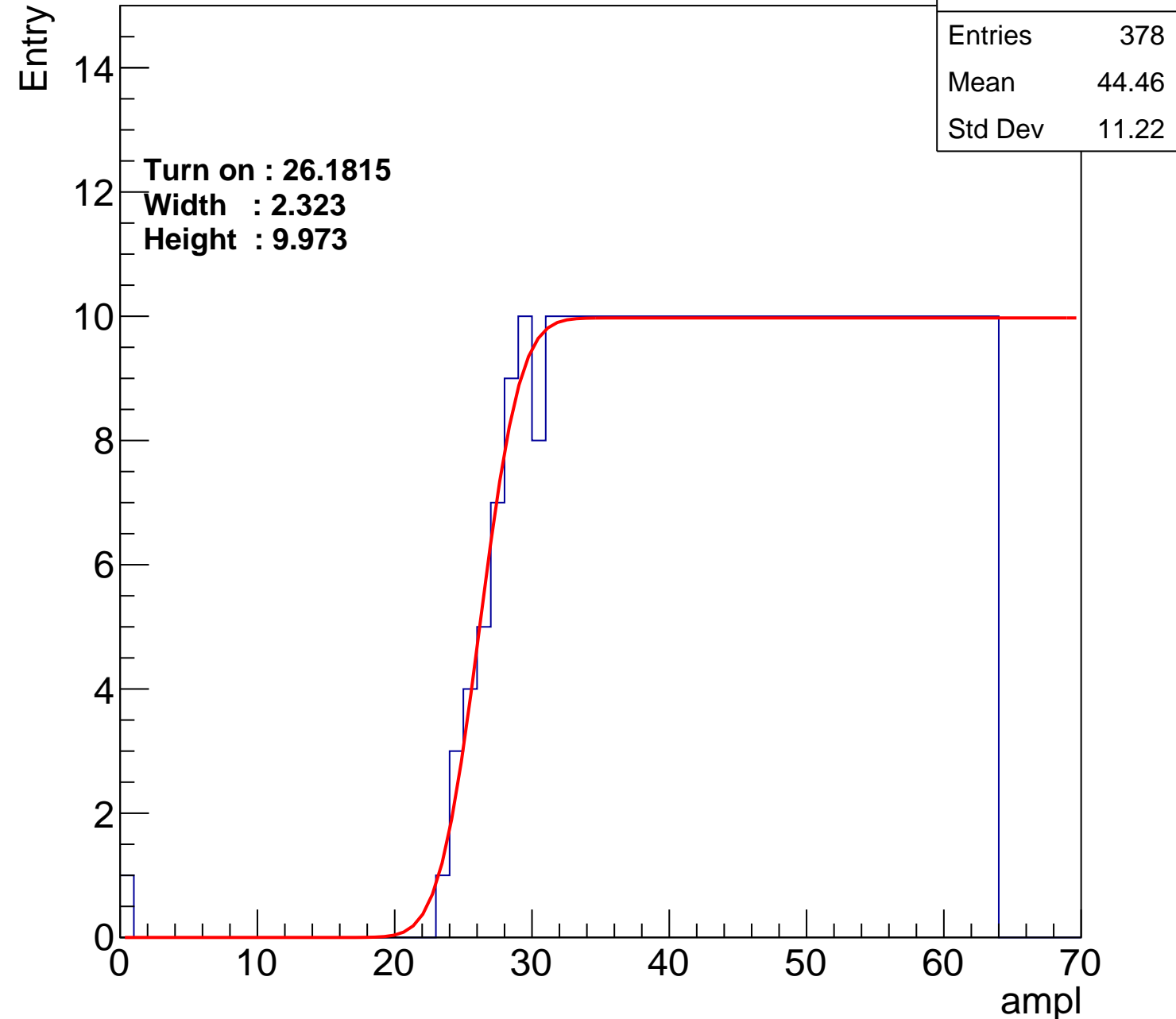
Width : 2.323

Height : 9.973

Entry

14
12
10
8
6
4
2
0

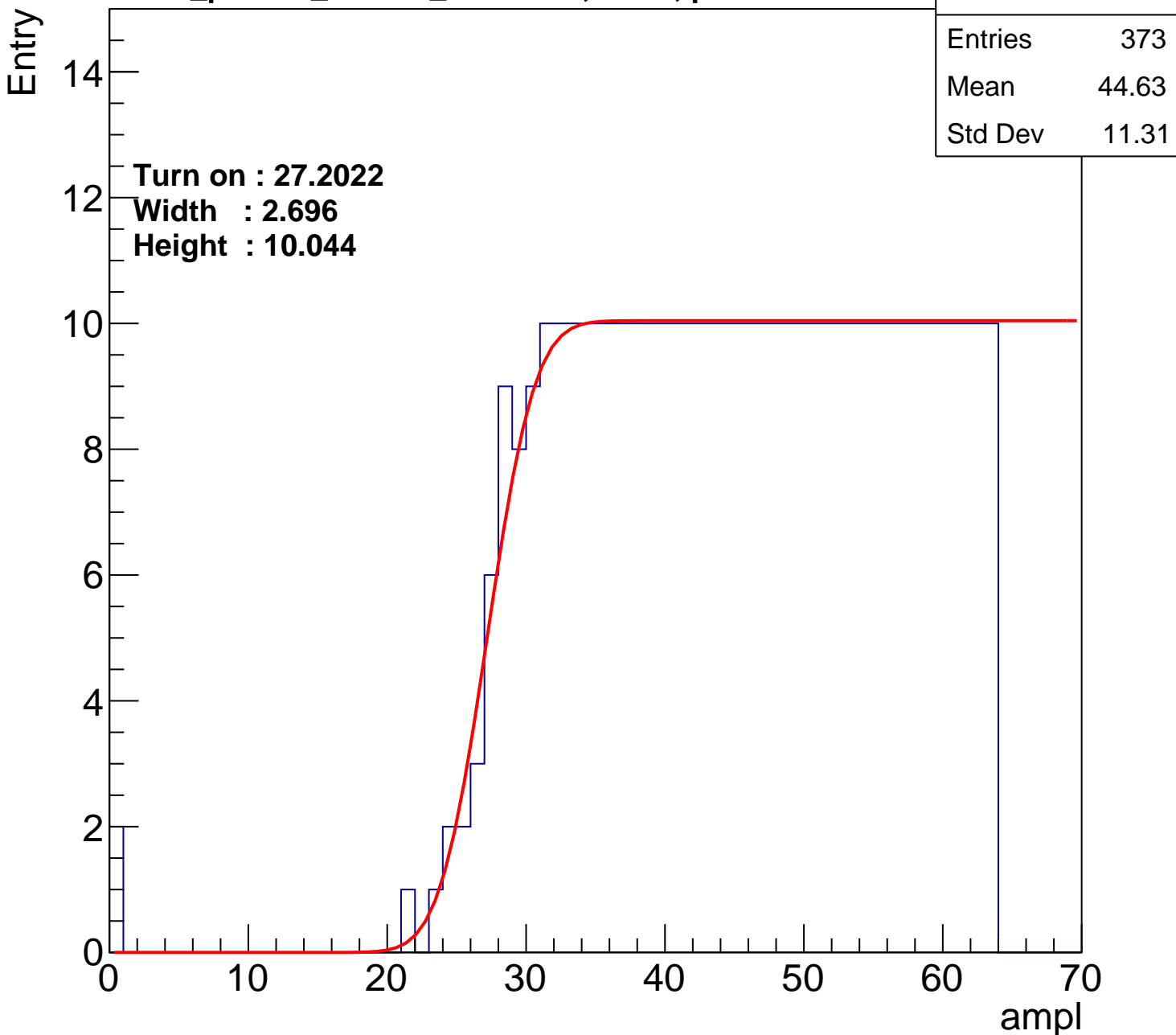
ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.2022
Width : 2.696
Height : 10.044



B0L002S, U15-ch15

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.43
Std Dev	10.87

Turn on : 28.4773

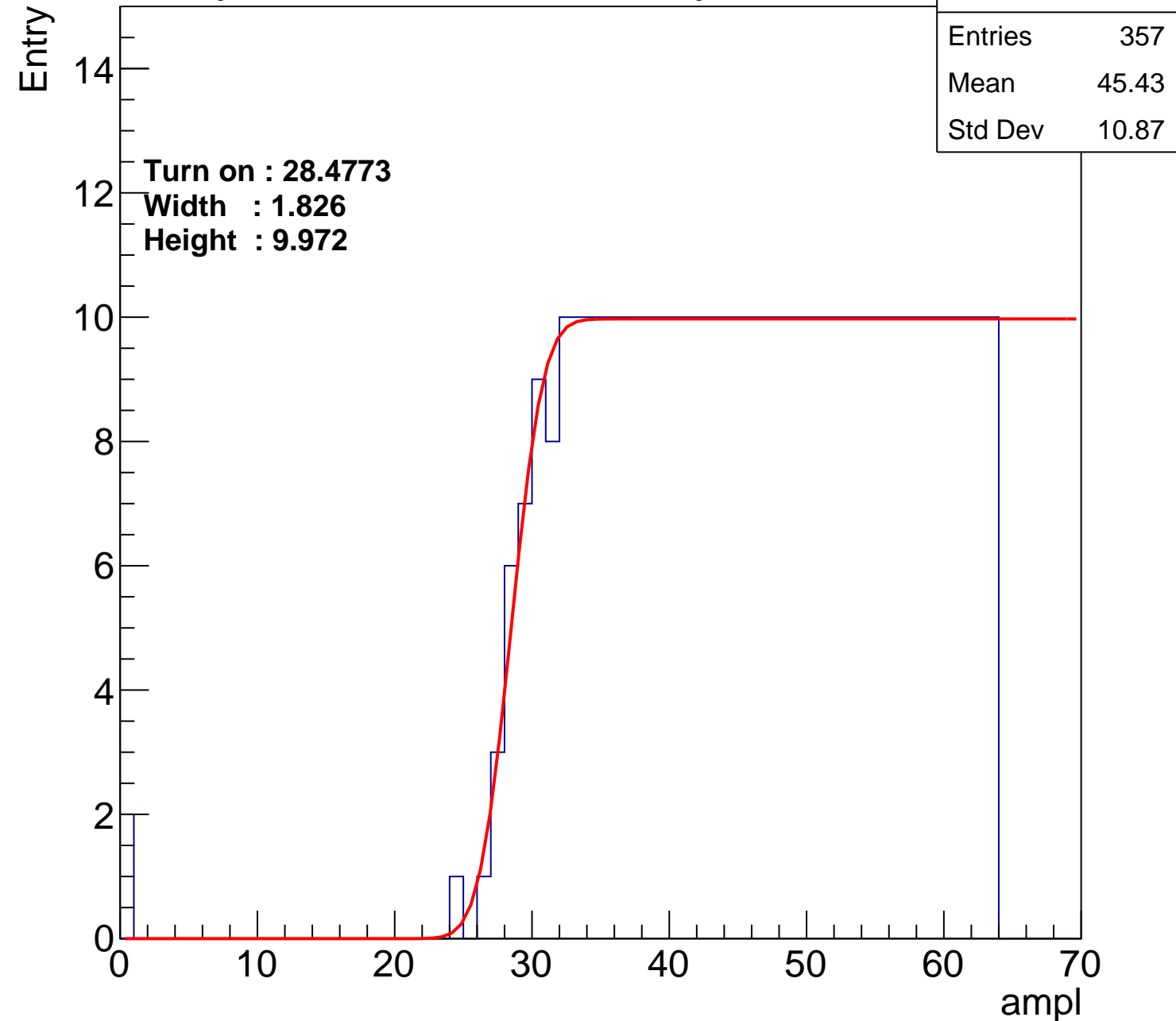
Width : 1.826

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.35
Std Dev	11.49

Turn on : 26.7989

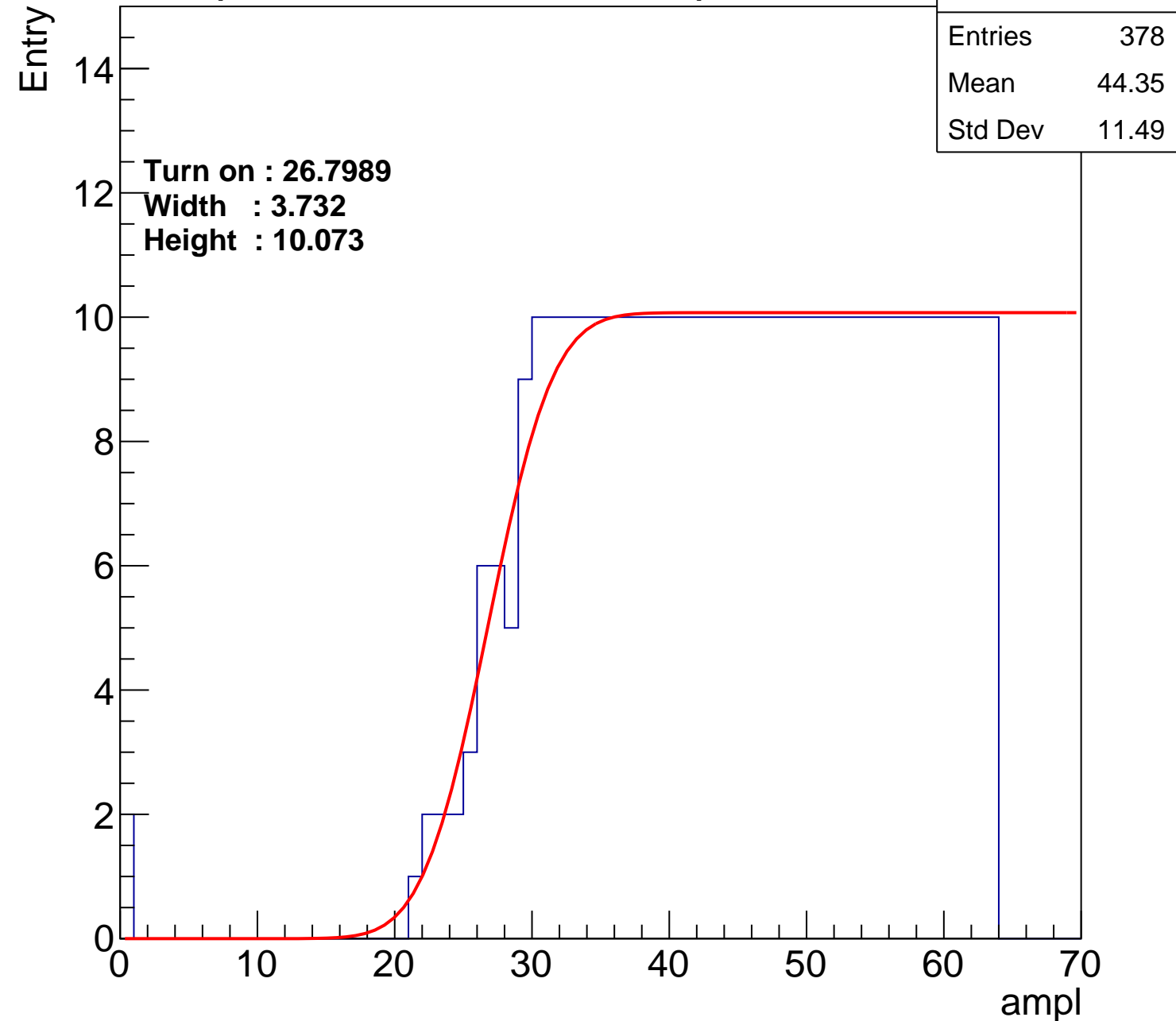
Width : 3.732

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.18
Std Dev	11.34

Turn on : 28.2033

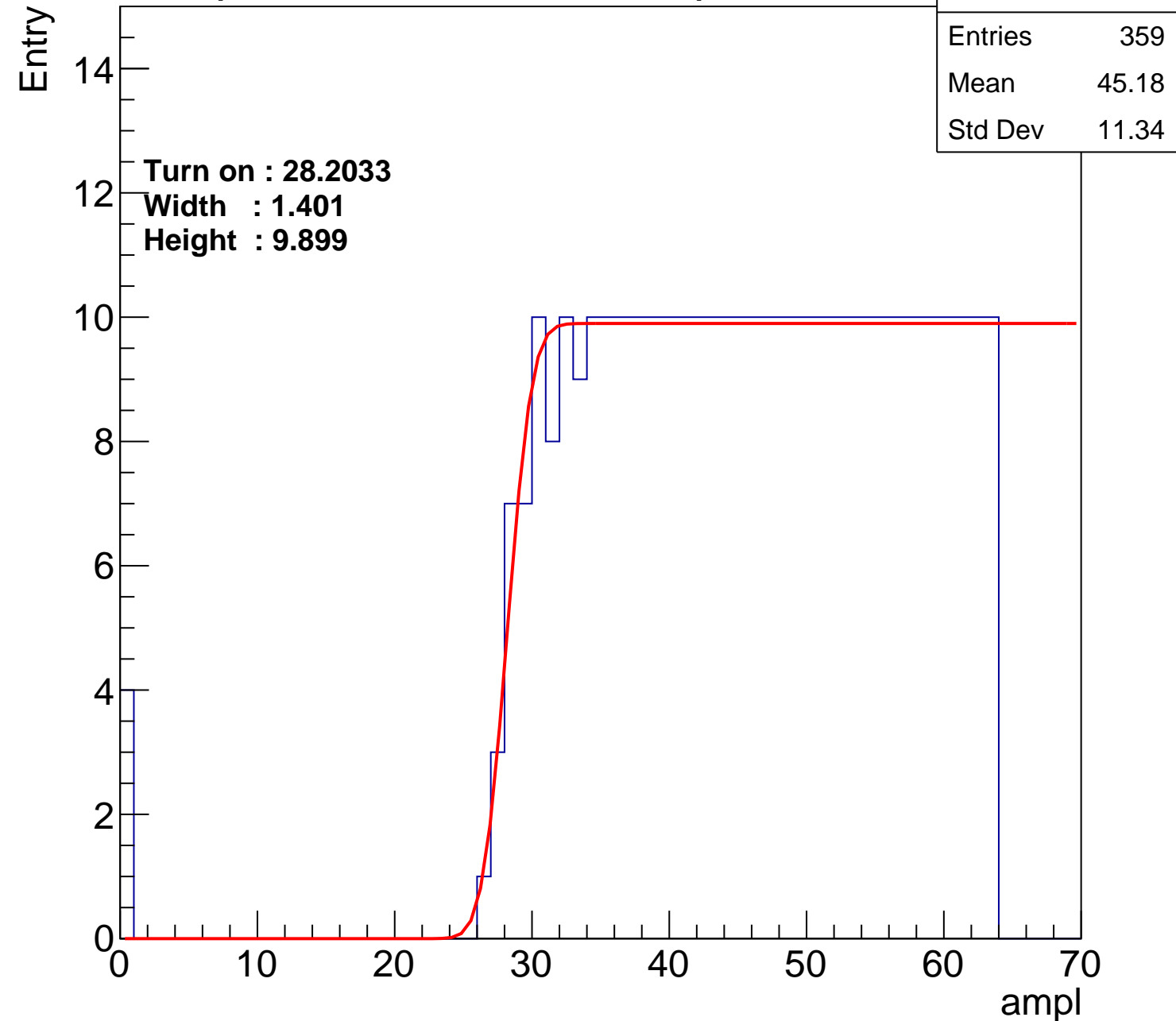
Width : 1.401

Height : 9.899

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.34
Std Dev	11.32

Turn on : 26.6206

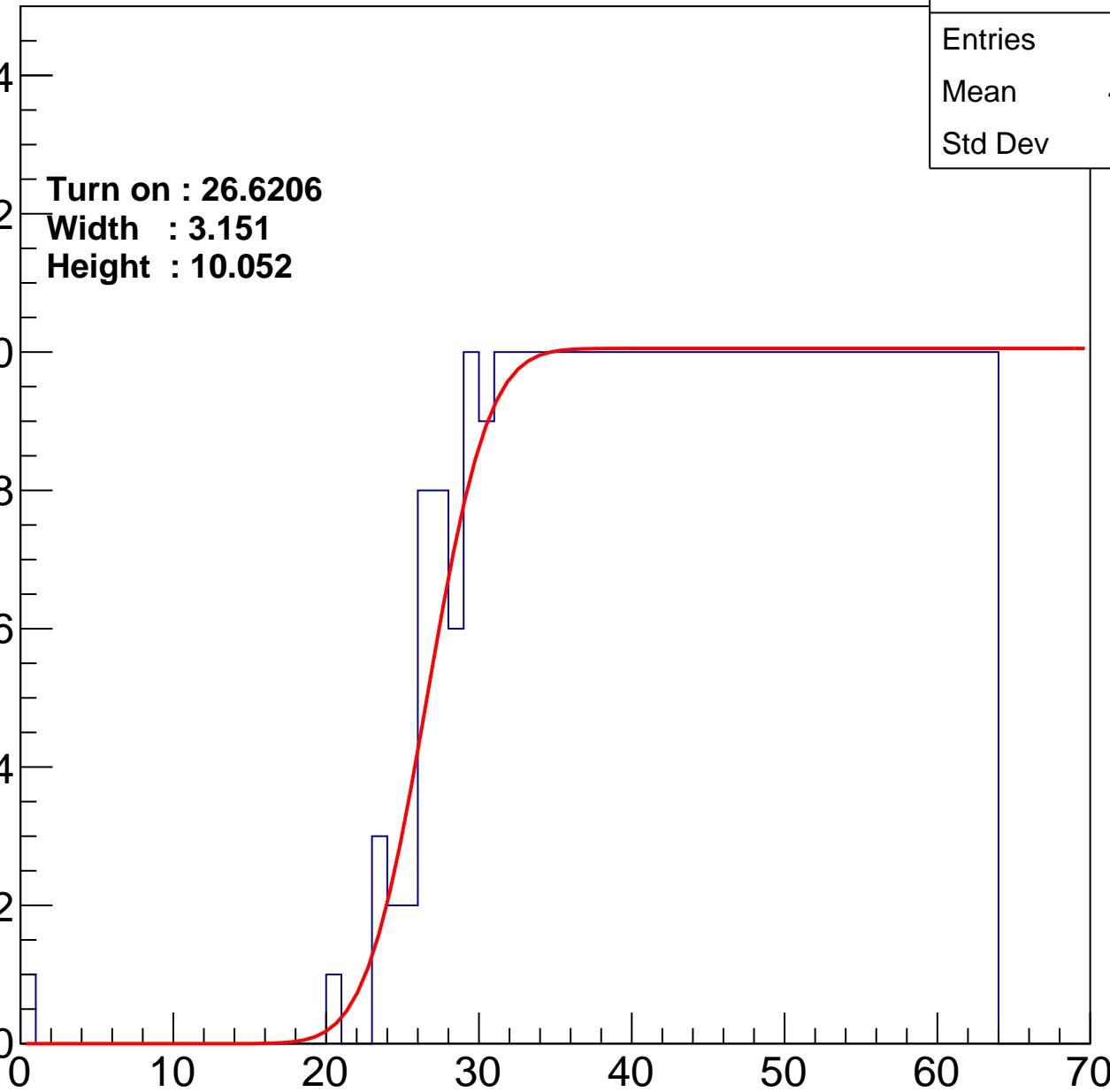
Width : 3.151

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.84
Std Dev	11.96

Turn on : 25.6733

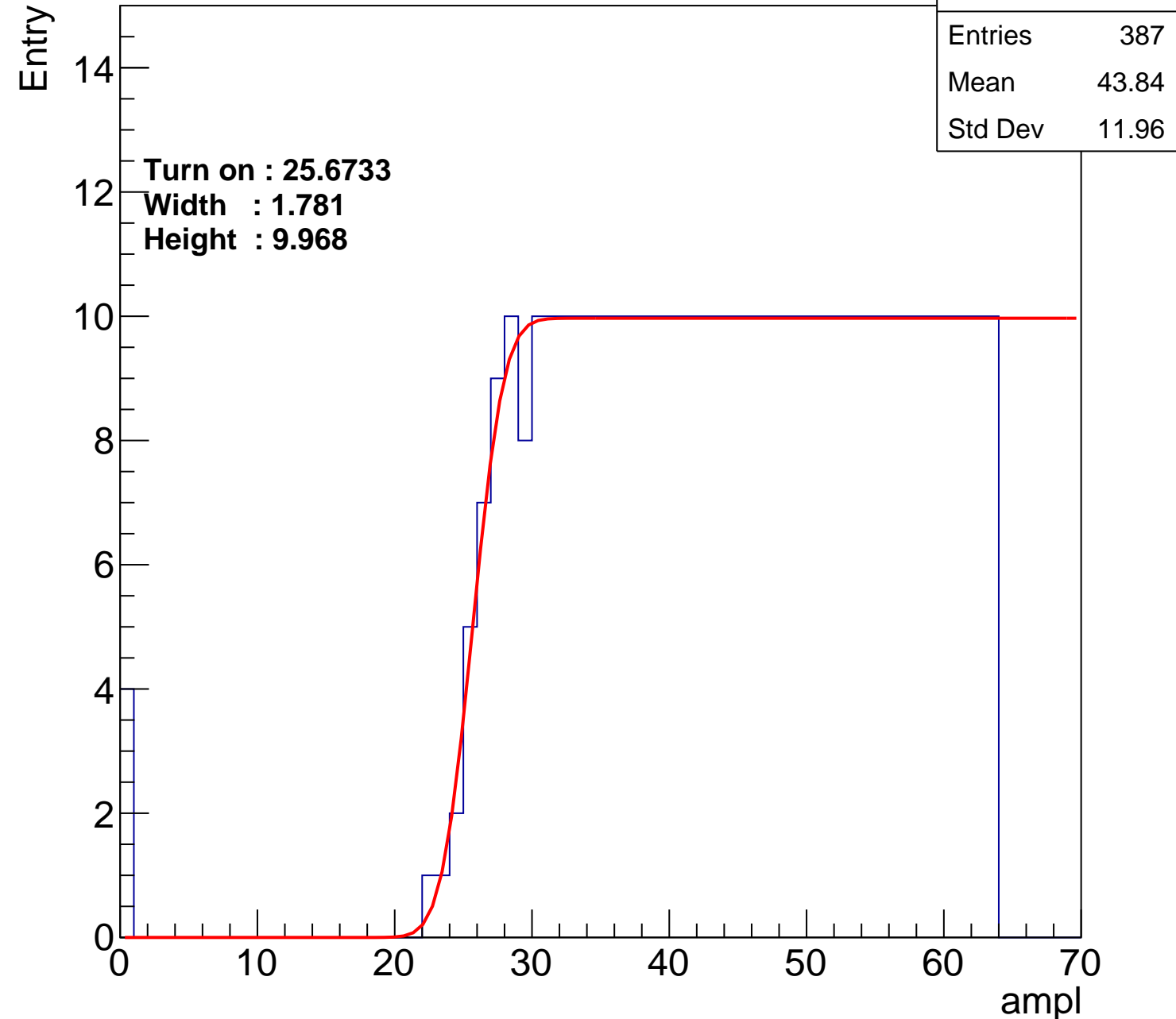
Width : 1.781

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch20

calib_packv5_042523_0143.root, FC#8, port C1

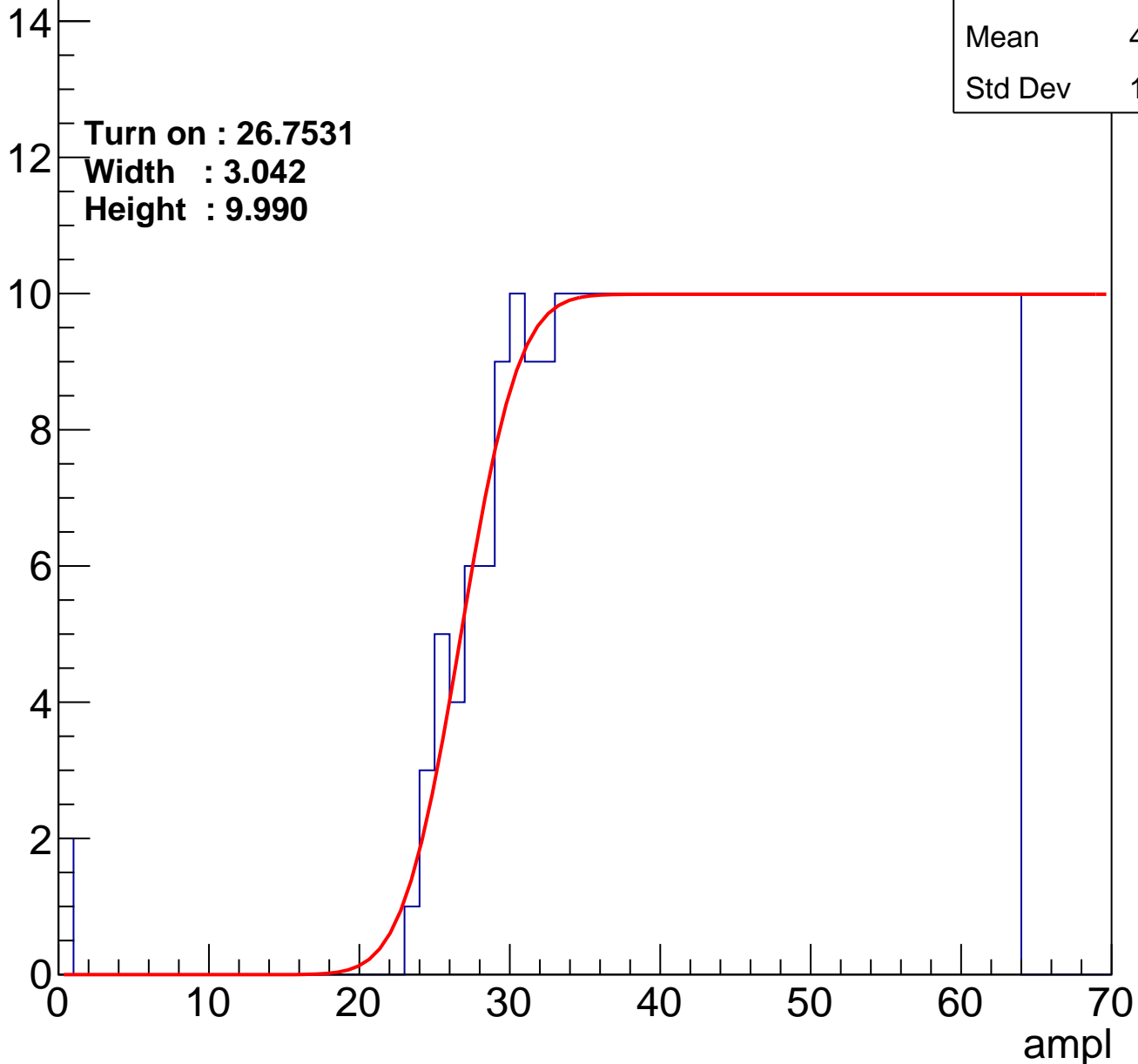
Entries	374
Mean	44.55
Std Dev	11.37

Turn on : 26.7531

Width : 3.042

Height : 9.990

Entry



B0L002S, U15-ch21

calib_packv5_042523_0143.root, FC#8, port C1

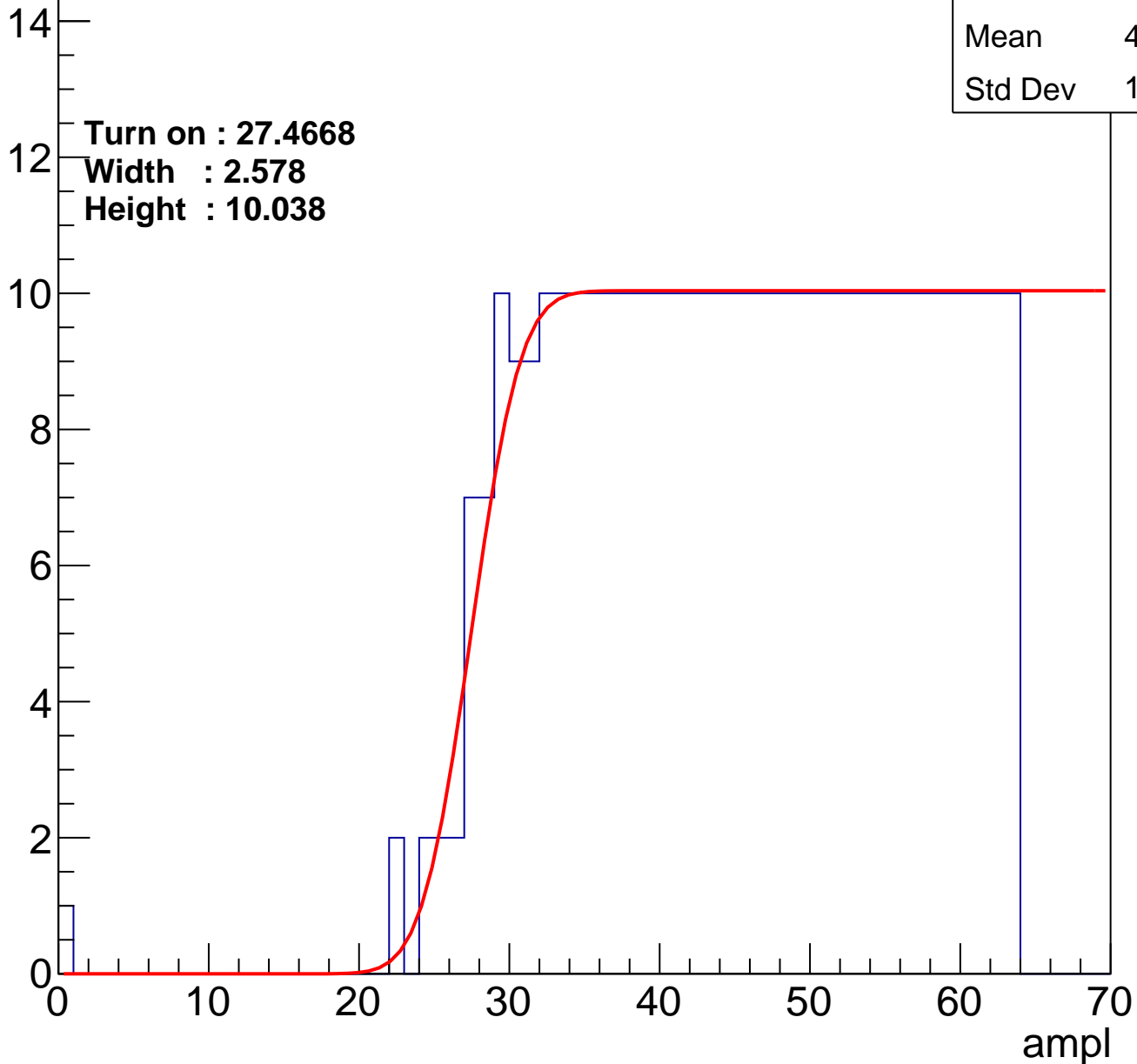
Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.4668

Width : 2.578

Height : 10.038

Entry



B0L002S, U15-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	43.88
Std Dev	11.95

Turn on : 26.2835

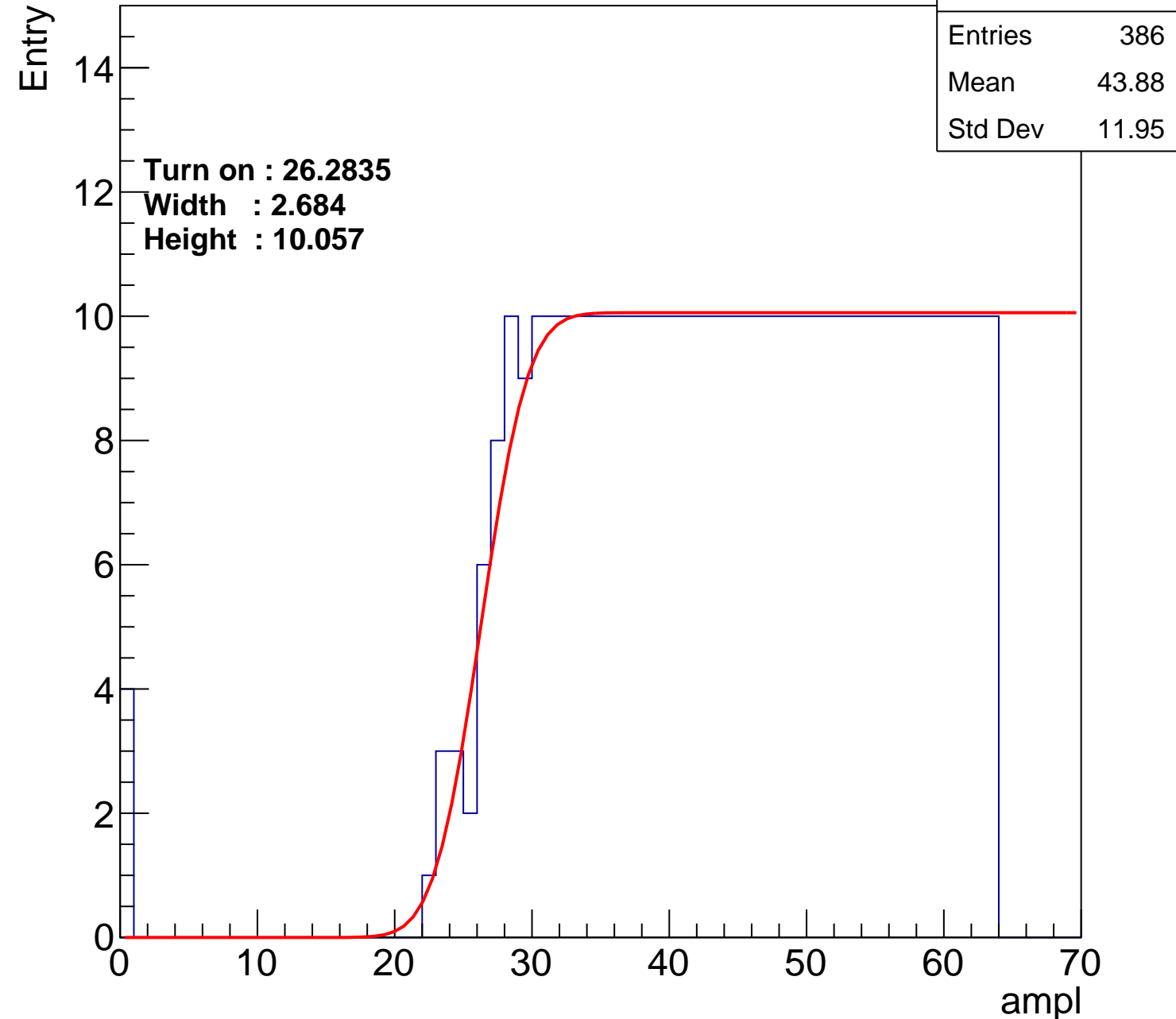
Width : 2.684

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.03
Std Dev	10.94

Turn on : 27.5383

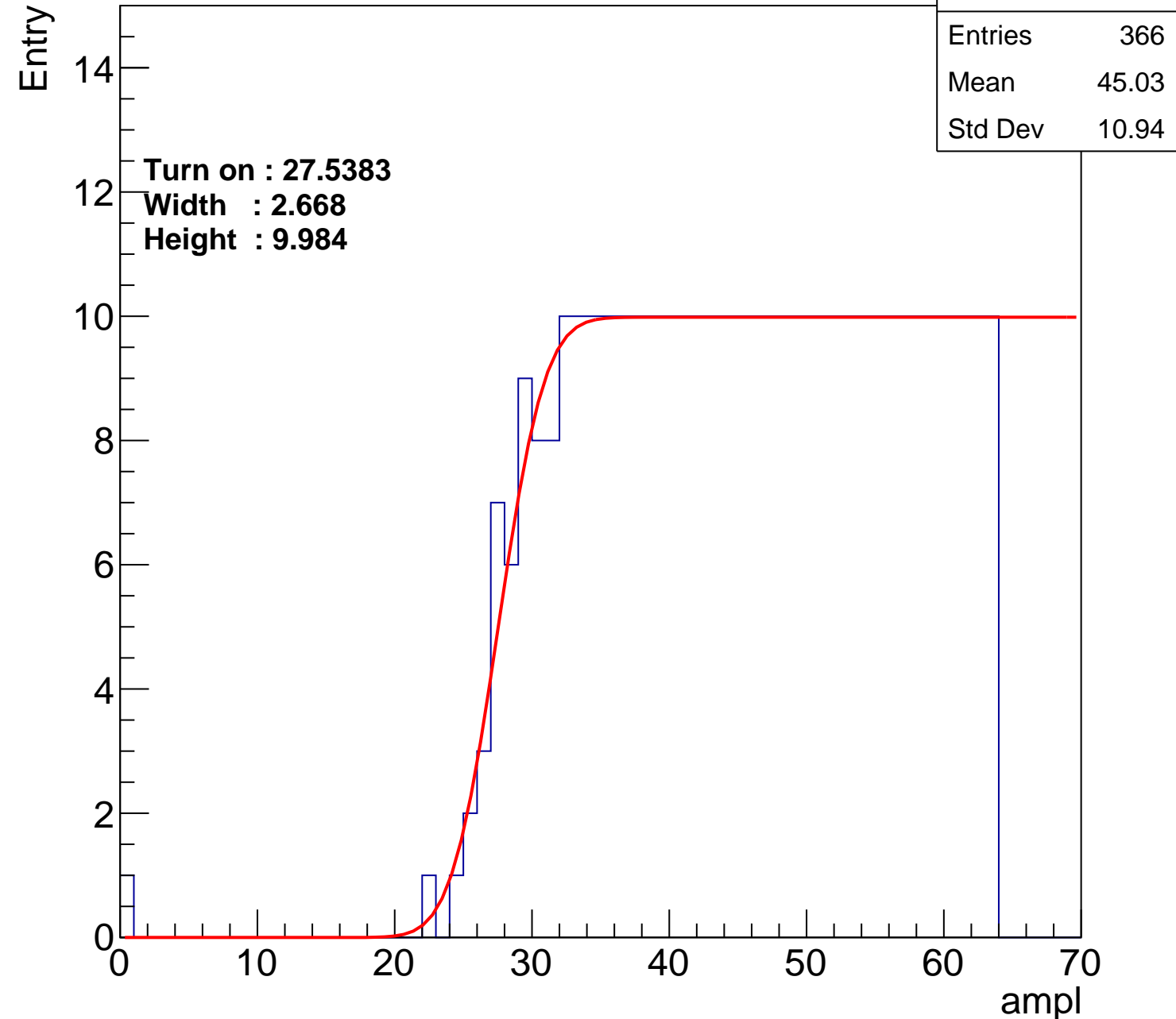
Width : 2.668

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch24

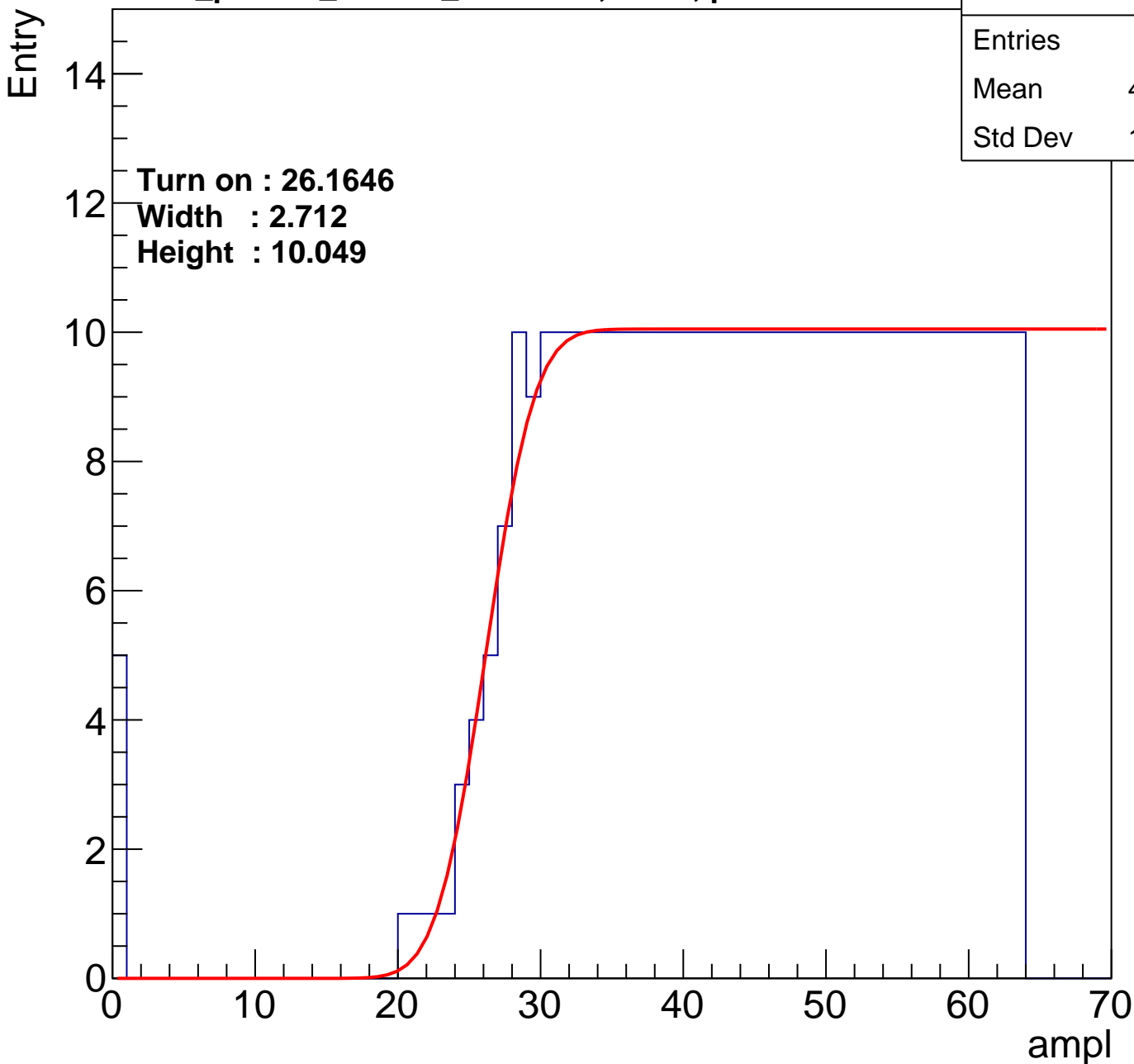
calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.74
Std Dev	12.18

Turn on : 26.1646

Width : 2.712

Height : 10.049



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 26.3296
Width : 3.561
Height : 10.066



B0L002S, U15-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 27.0110

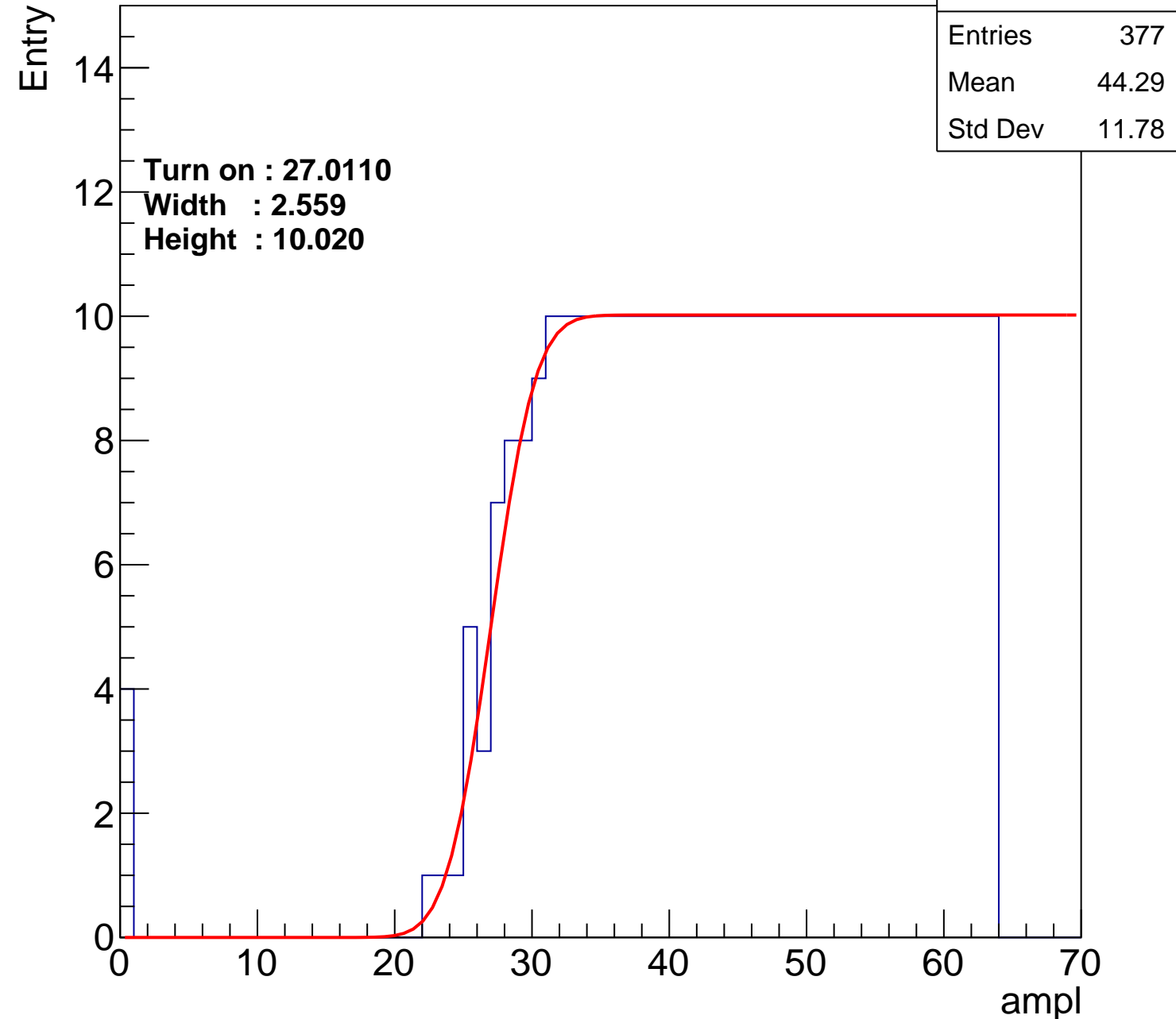
Width : 2.559

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.67
Std Dev	11.8

Turn on : 27.8532

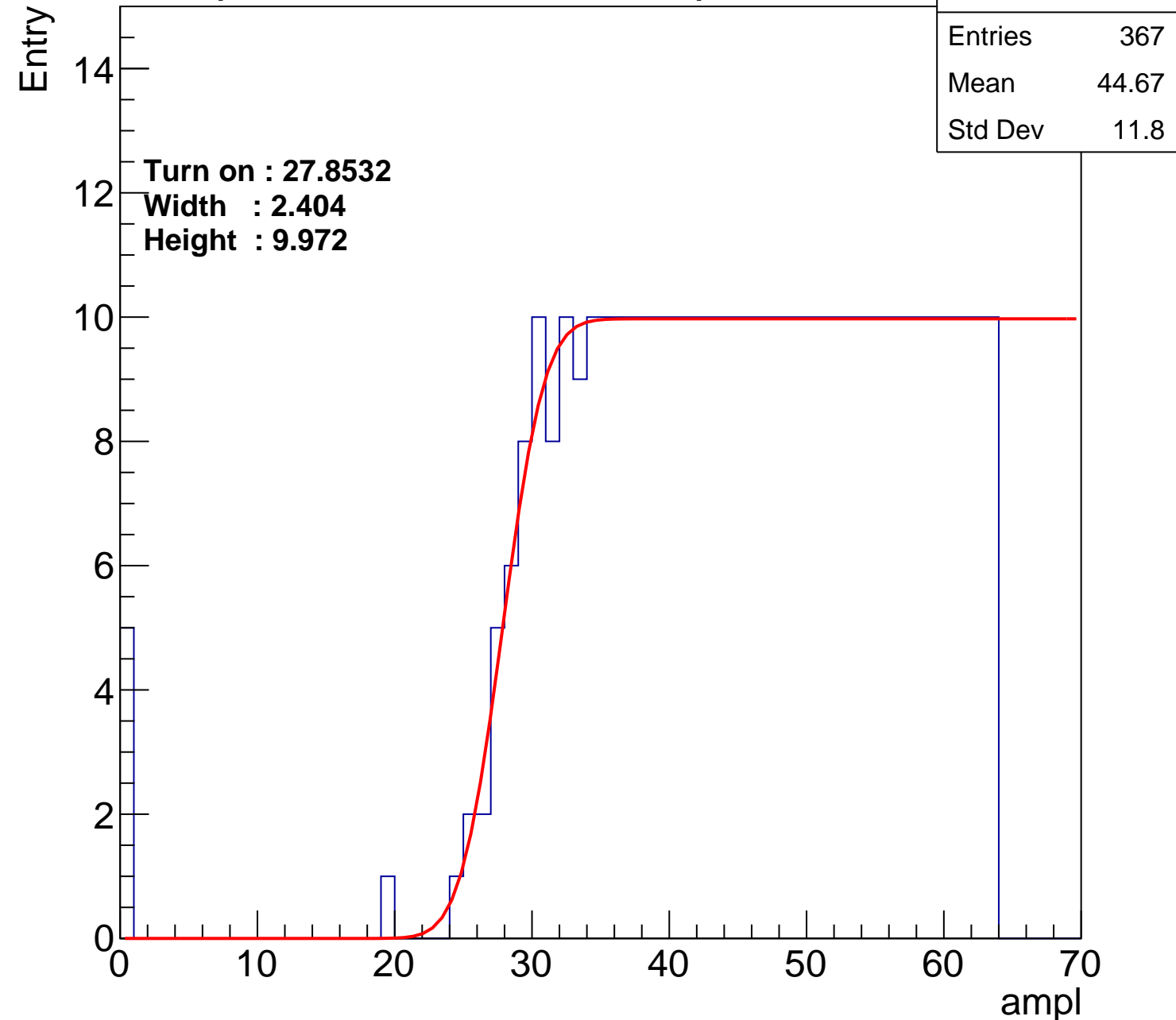
Width : 2.404

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.42
Std Dev	11.61

Turn on : 27.6192

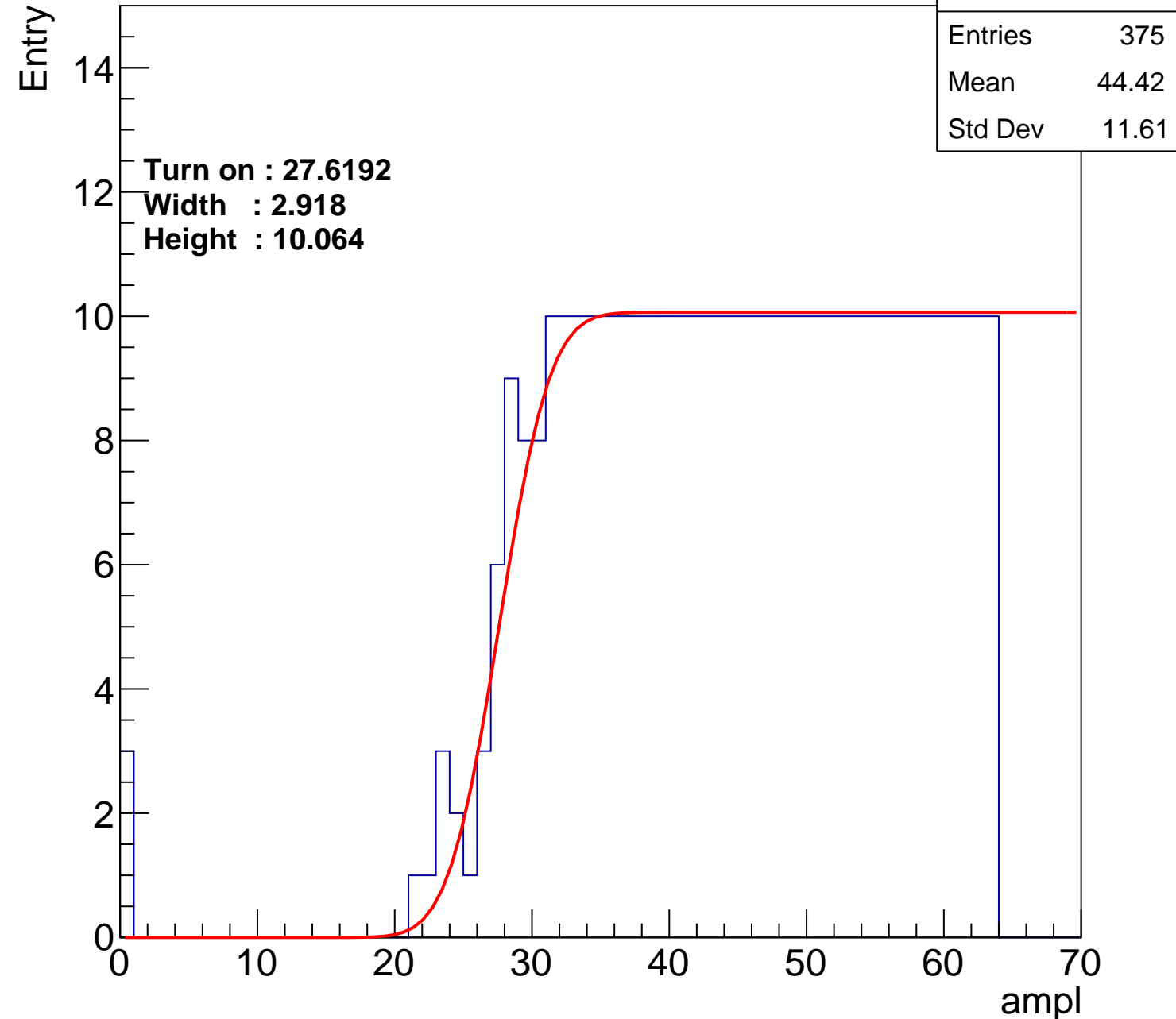
Width : 2.918

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch29

calib_packv5_042523_0143.root, FC#8, port C1

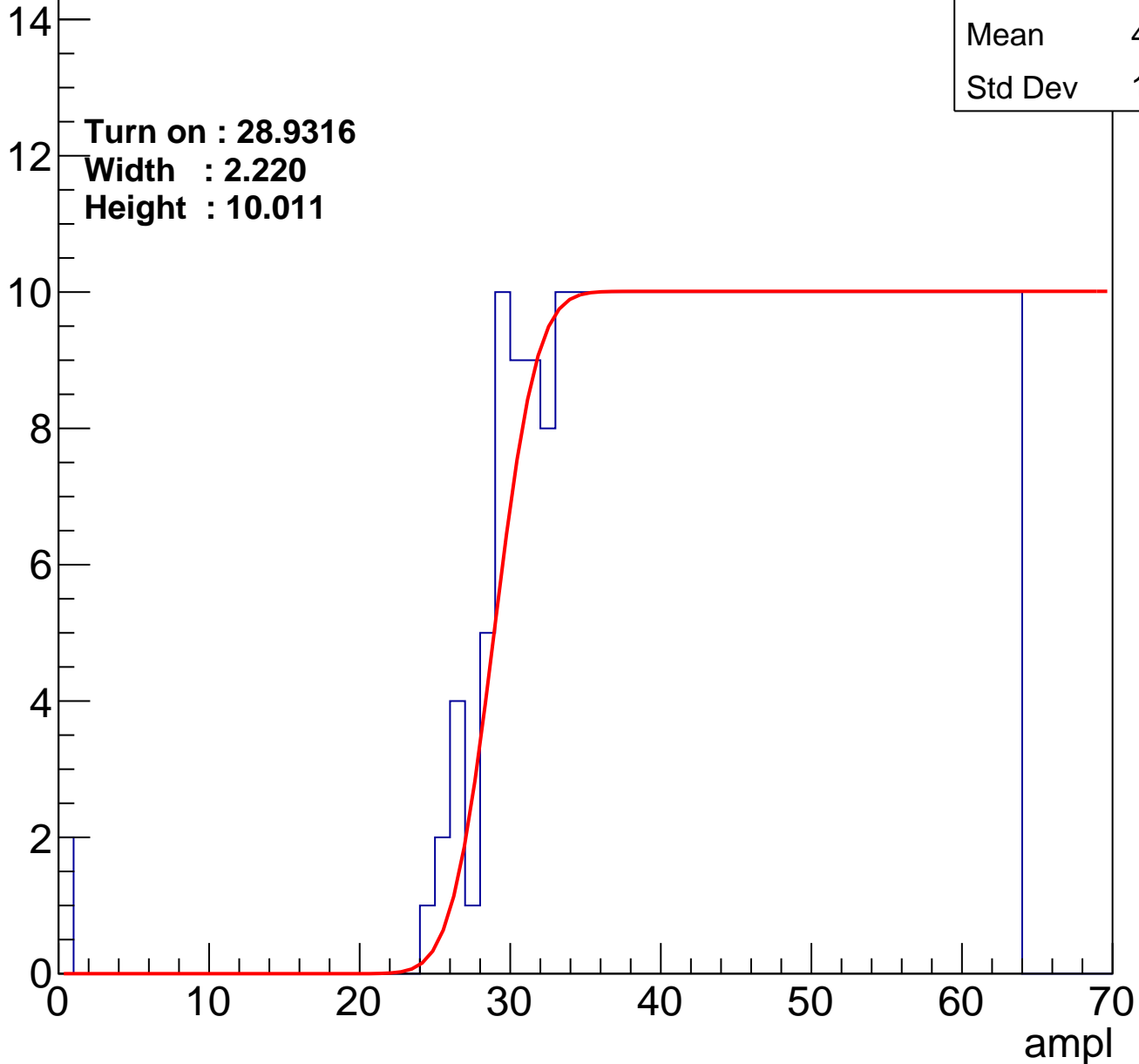
Entries	361
Mean	45.21
Std Dev	11.01

Turn on : 28.9316

Width : 2.220

Height : 10.011

Entry



B0L002S, U15-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	43.83
Std Dev	12.01

Turn on : 25.8676

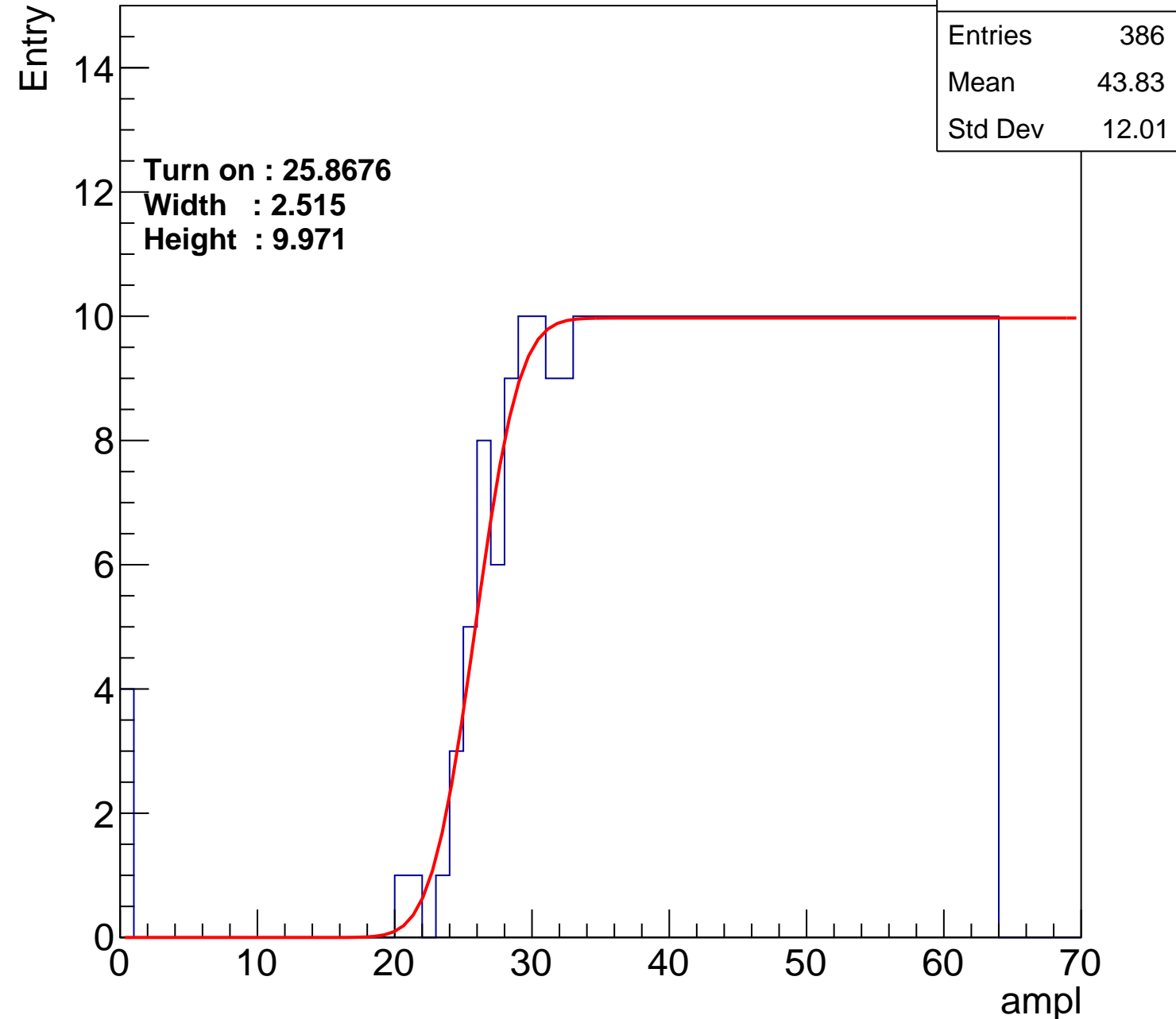
Width : 2.515

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	392
Mean	43.65
Std Dev	11.86

Turn on : 25.1174

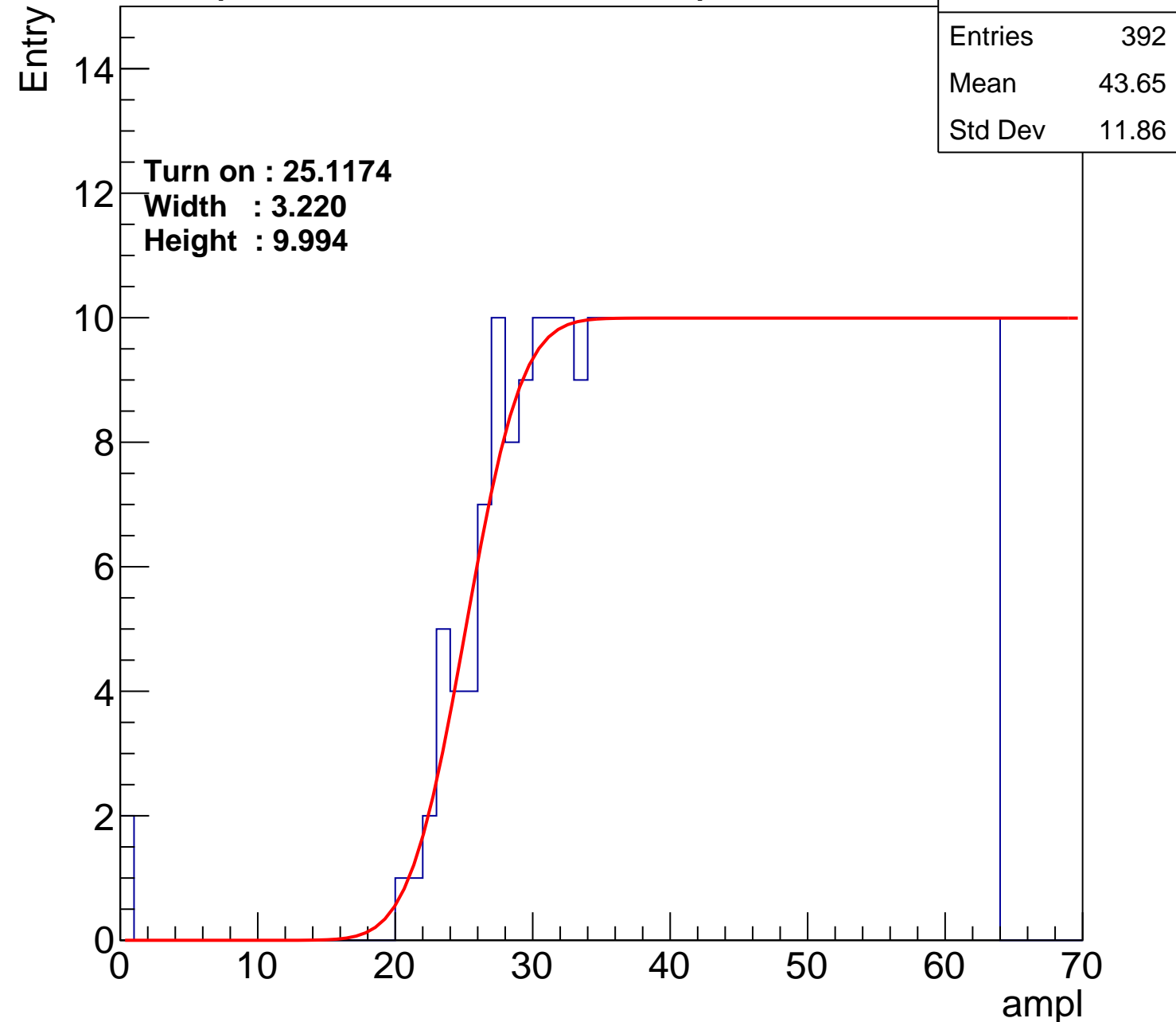
Width : 3.220

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch32

calib_packv5_042523_0143.root, FC#8, port C1

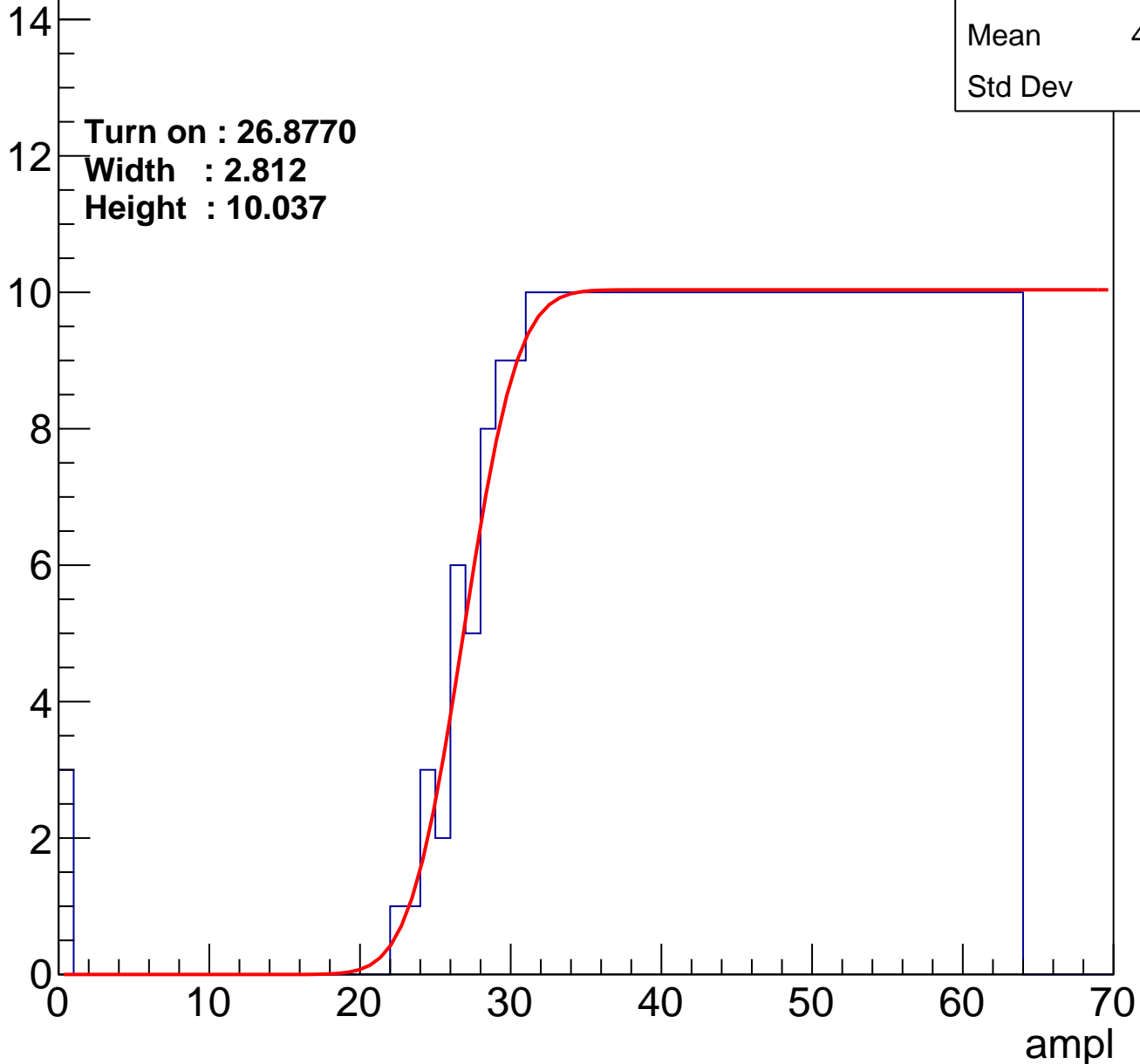
Entries	377
Mean	44.36
Std Dev	11.6

Turn on : 26.8770

Width : 2.812

Height : 10.037

Entry



B0L002S, U15-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.97
Std Dev	11.19

Turn on : 28.0461

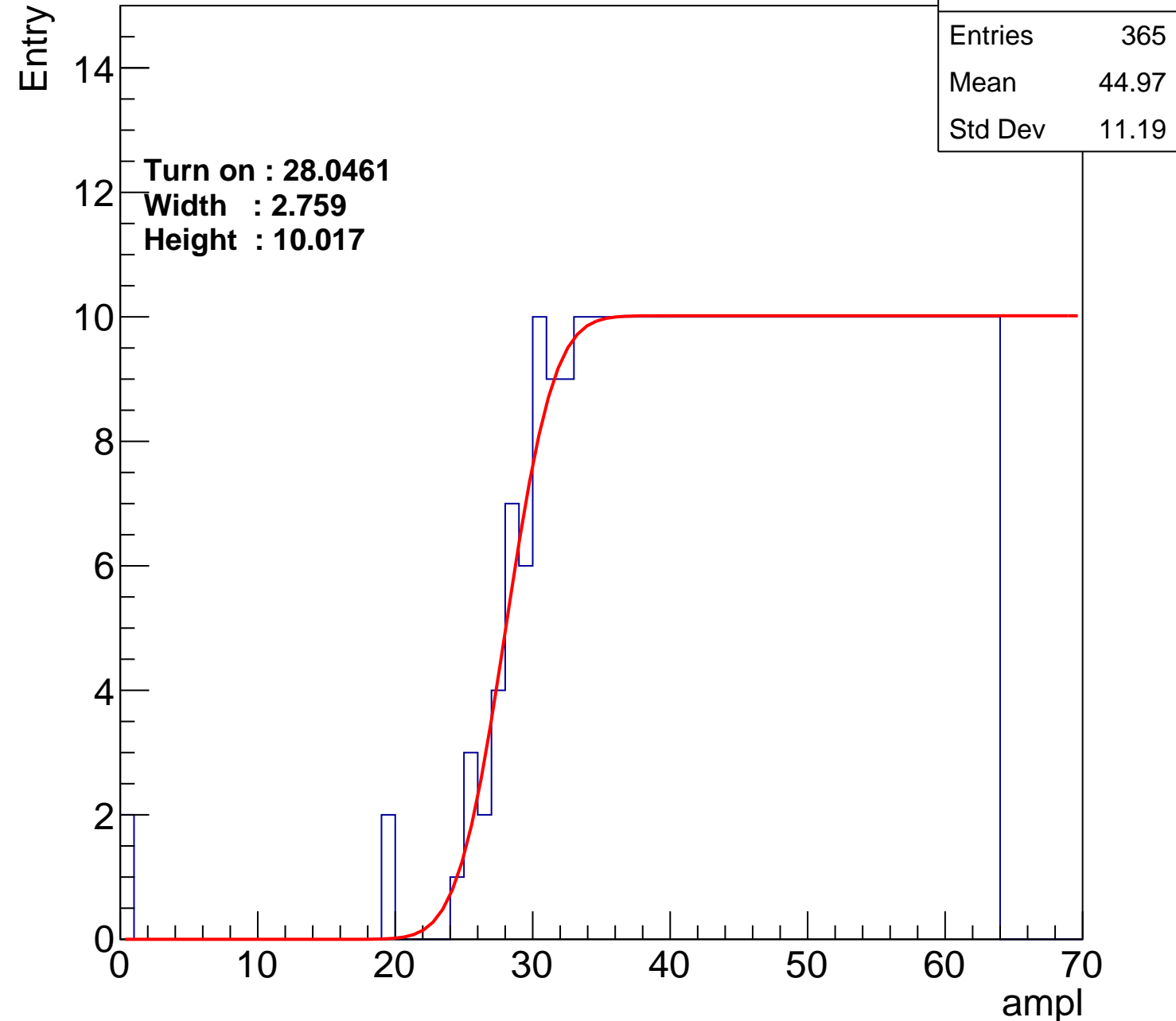
Width : 2.759

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch34

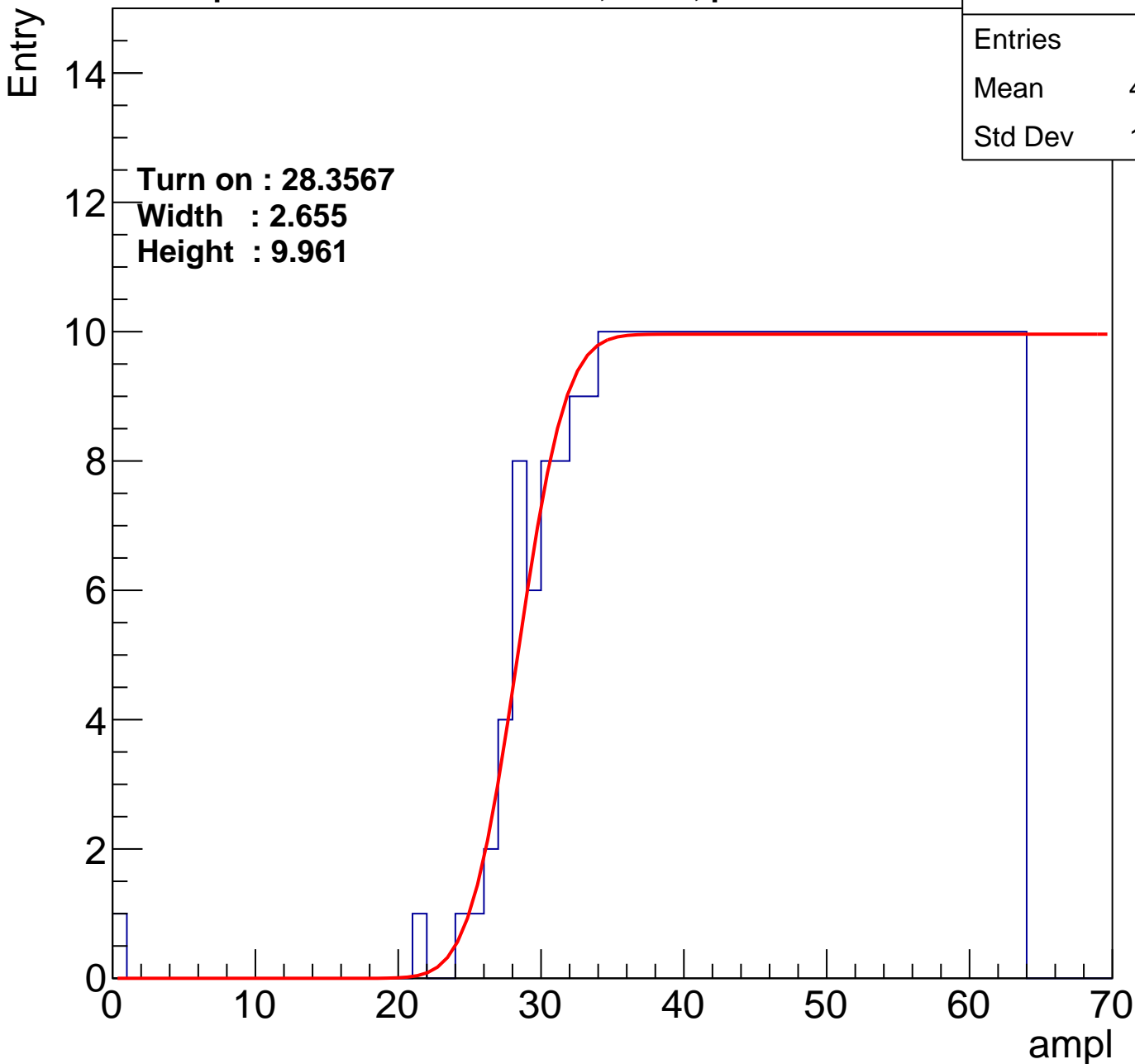
calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.39
Std Dev	10.78

Turn on : 28.3567

Width : 2.655

Height : 9.961



B0L002S, U15-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.89
Std Dev	11.35

Turn on : 27.8617

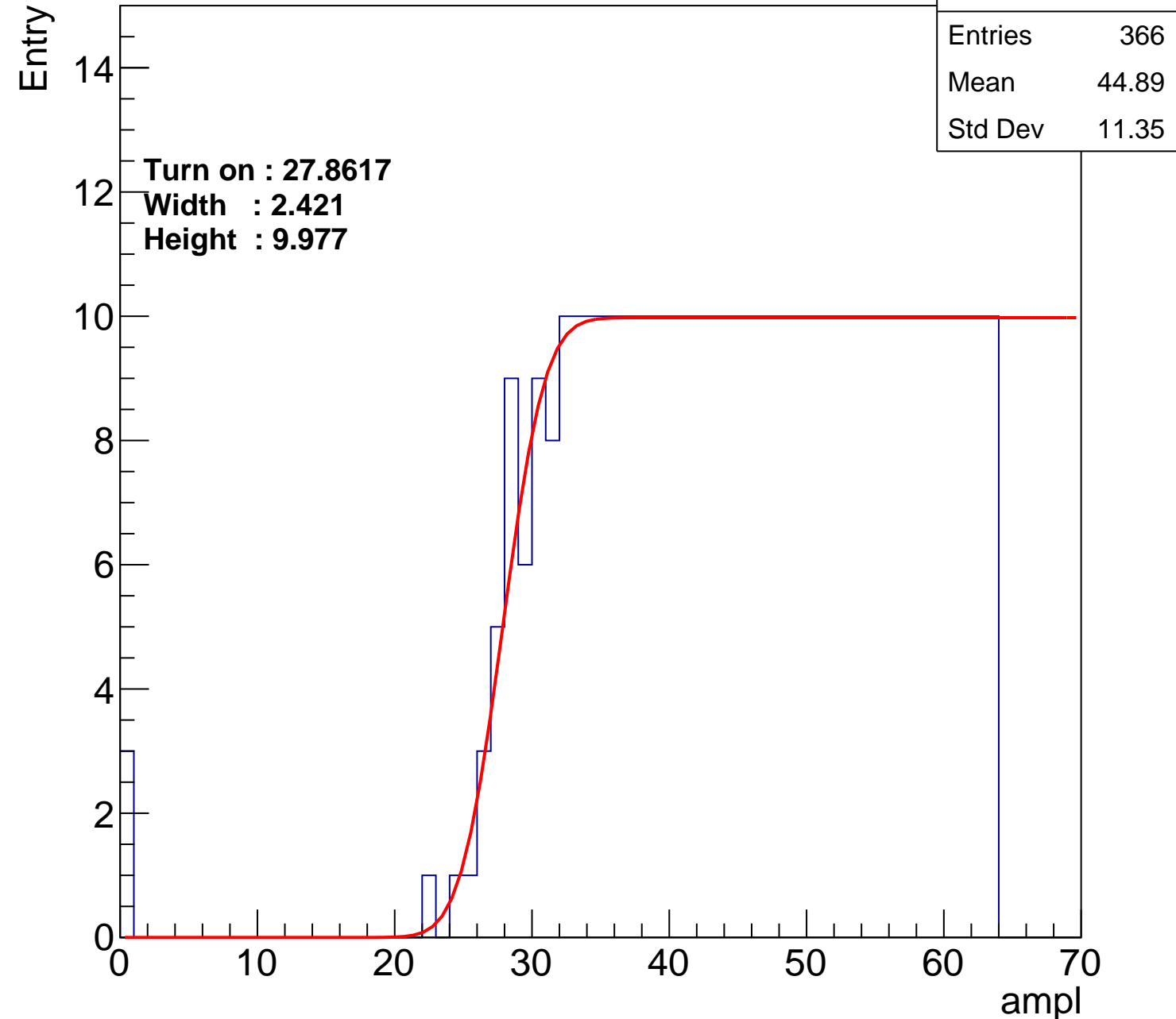
Width : 2.421

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch36

calib_packv5_042523_0143.root, FC#8, port C1

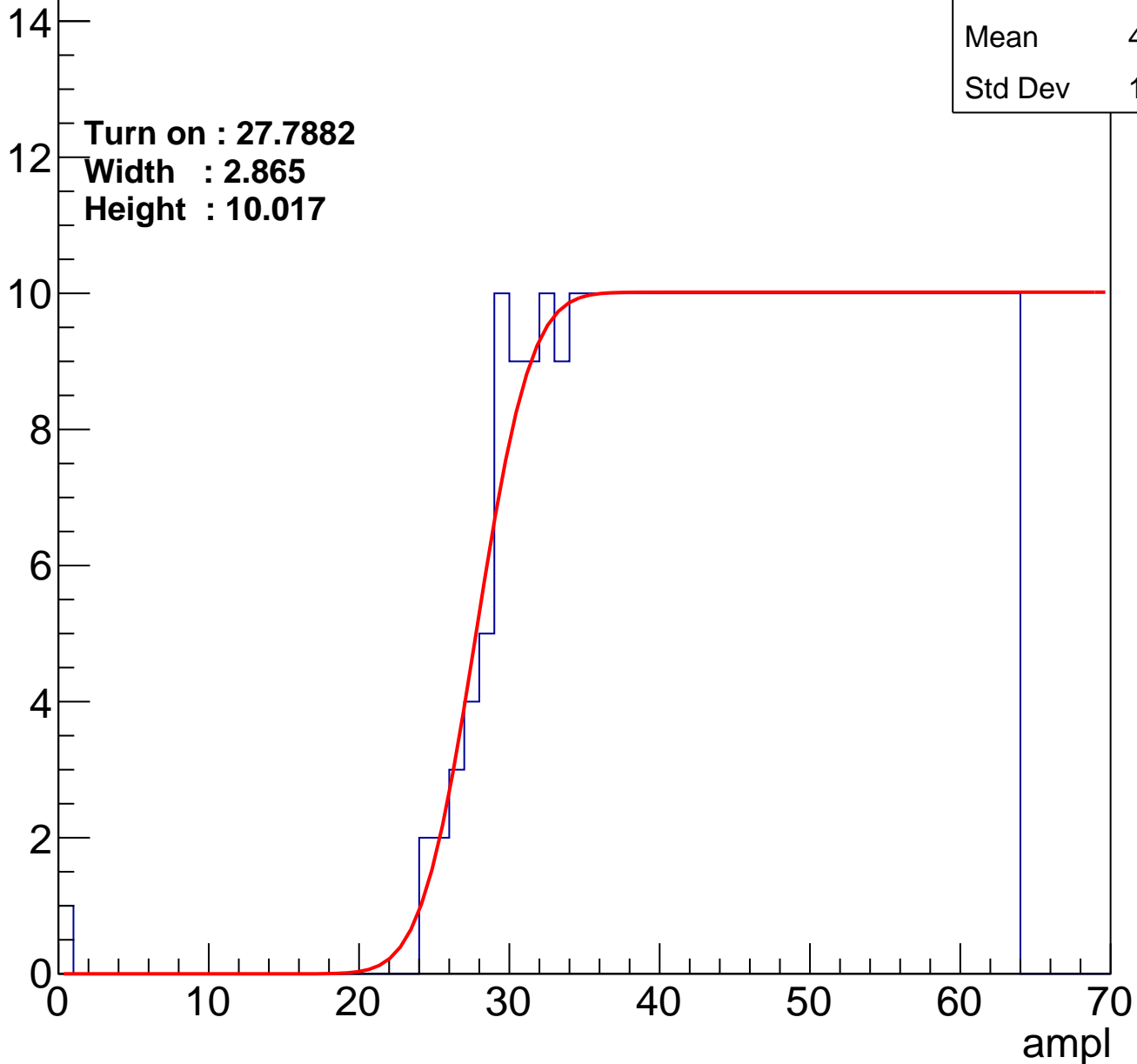
Entries	364
Mean	45.14
Std Dev	10.87

Turn on : 27.7882

Width : 2.865

Height : 10.017

Entry



B0L002S, U15-ch37

calib_packv5_042523_0143.root, FC#8, port C1

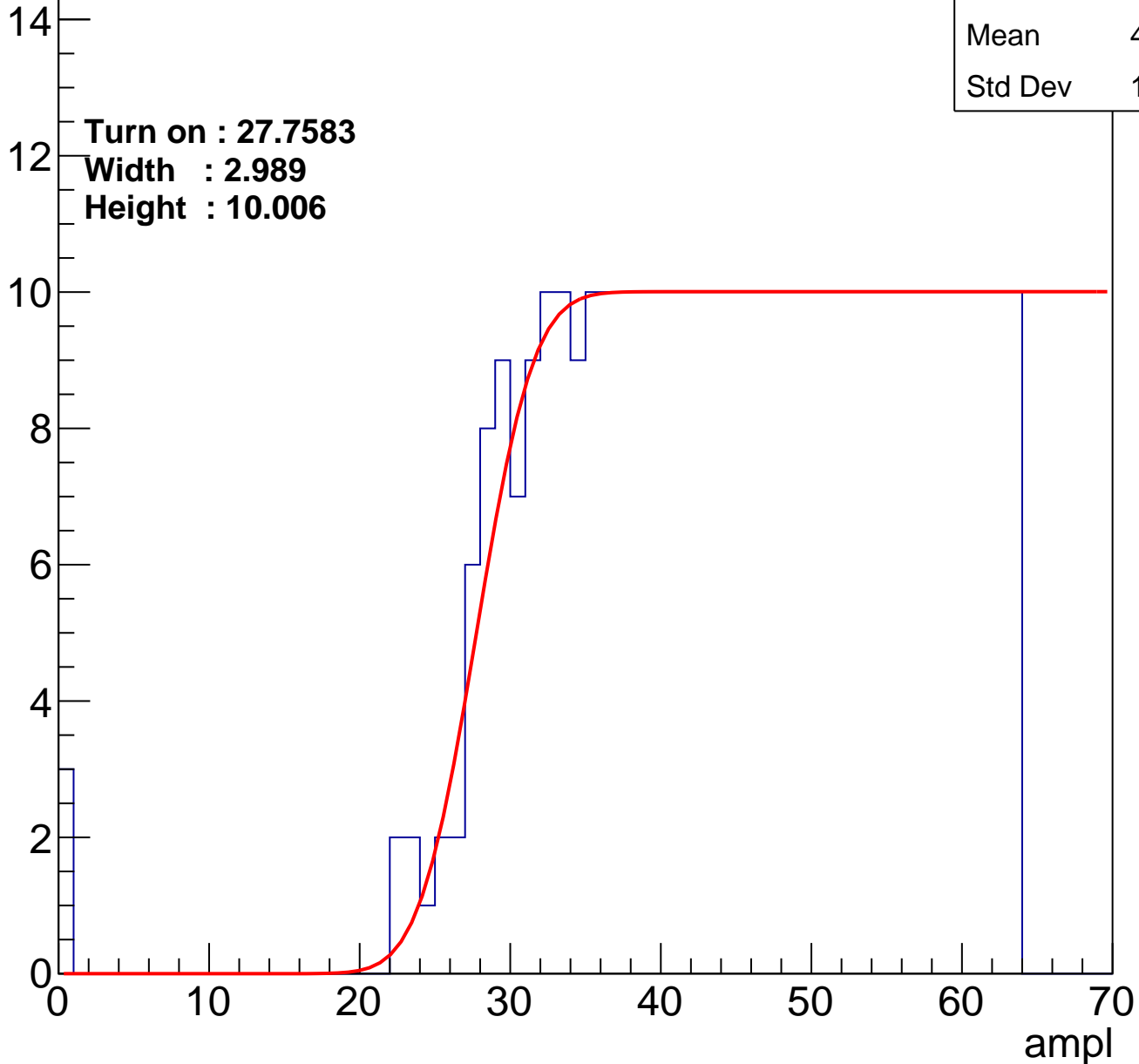
Entries	370
Mean	44.64
Std Dev	11.52

Turn on : 27.7583

Width : 2.989

Height : 10.006

Entry



B0L002S, U15-ch38

calib_packv5_042523_0143.root, FC#8, port C1

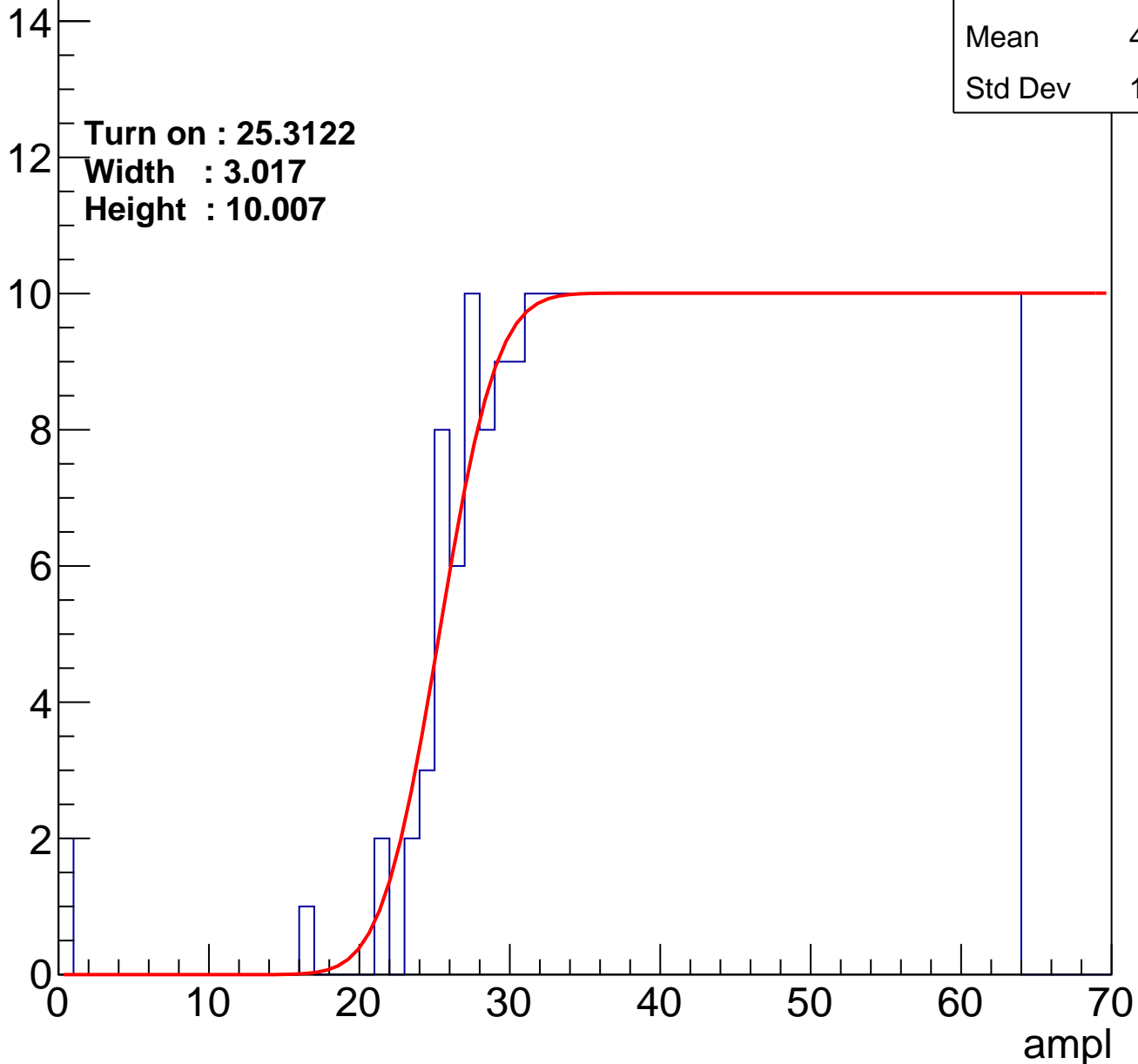
Entries	390
Mean	43.76
Std Dev	11.79

Turn on : 25.3122

Width : 3.017

Height : 10.007

Entry



B0L002S, U15-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.86
Std Dev	11.8

Turn on : 26.0147

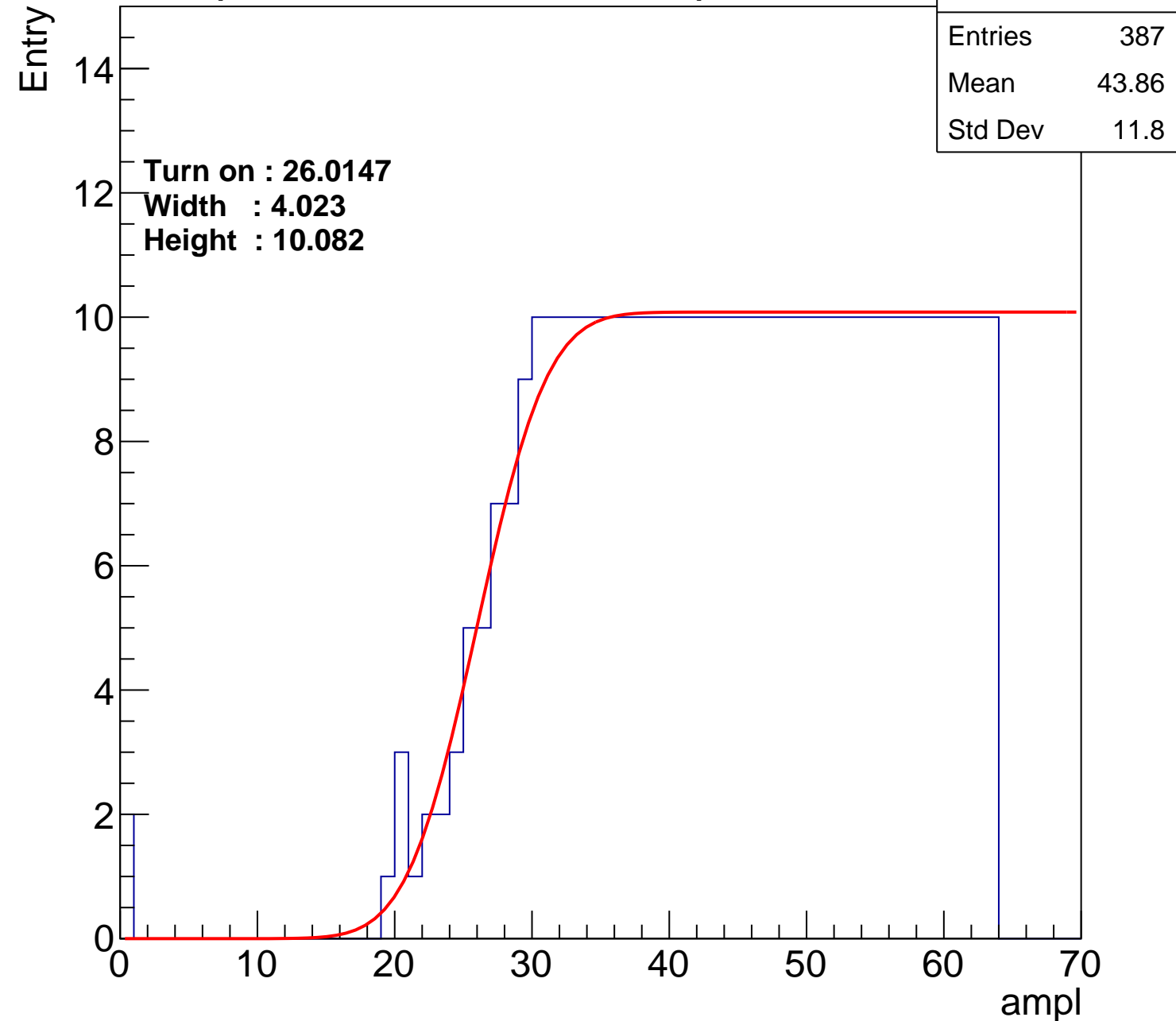
Width : 4.023

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.14
Std Dev	11.22

Turn on : 28.2518

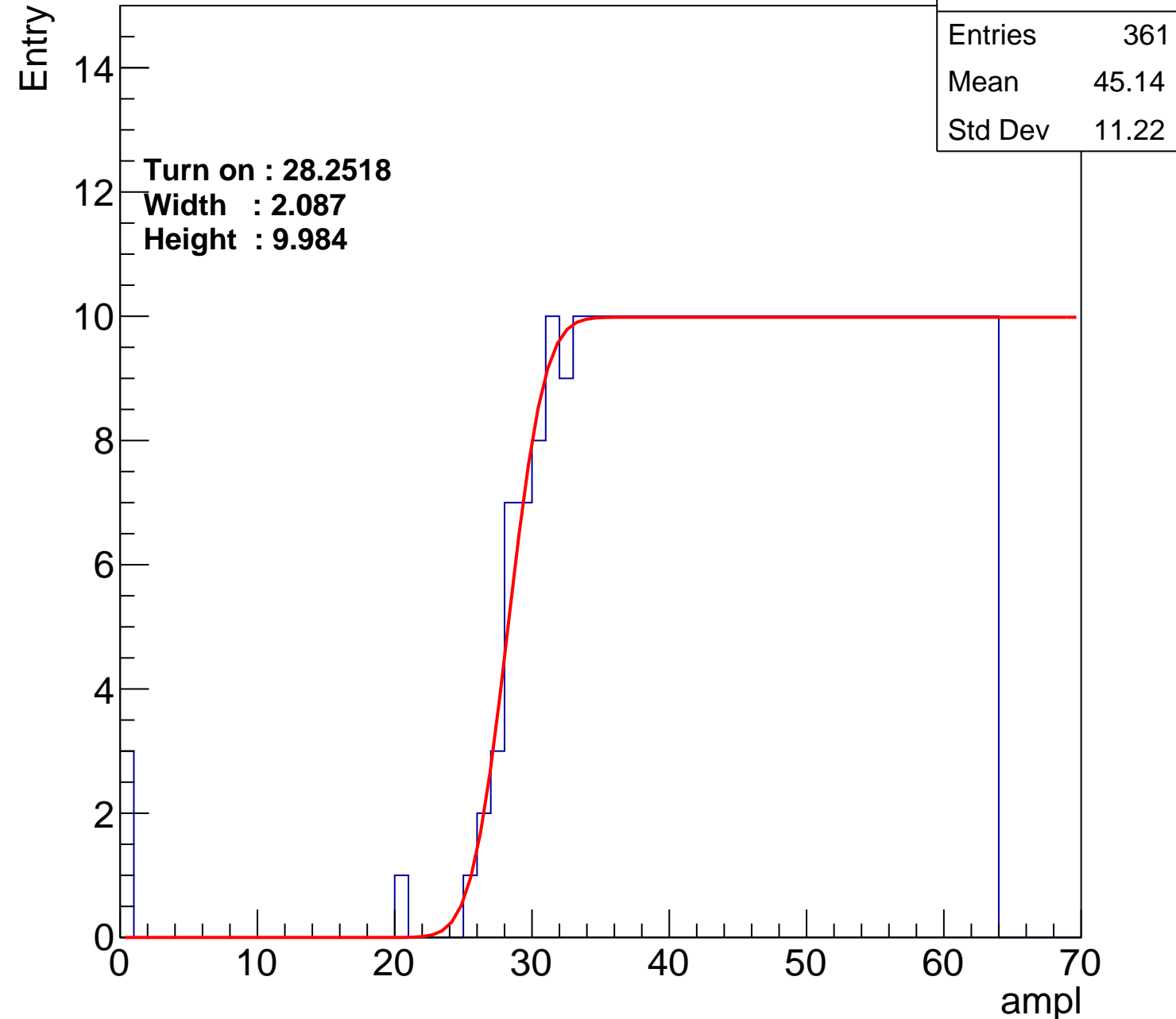
Width : 2.087

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 25.8461

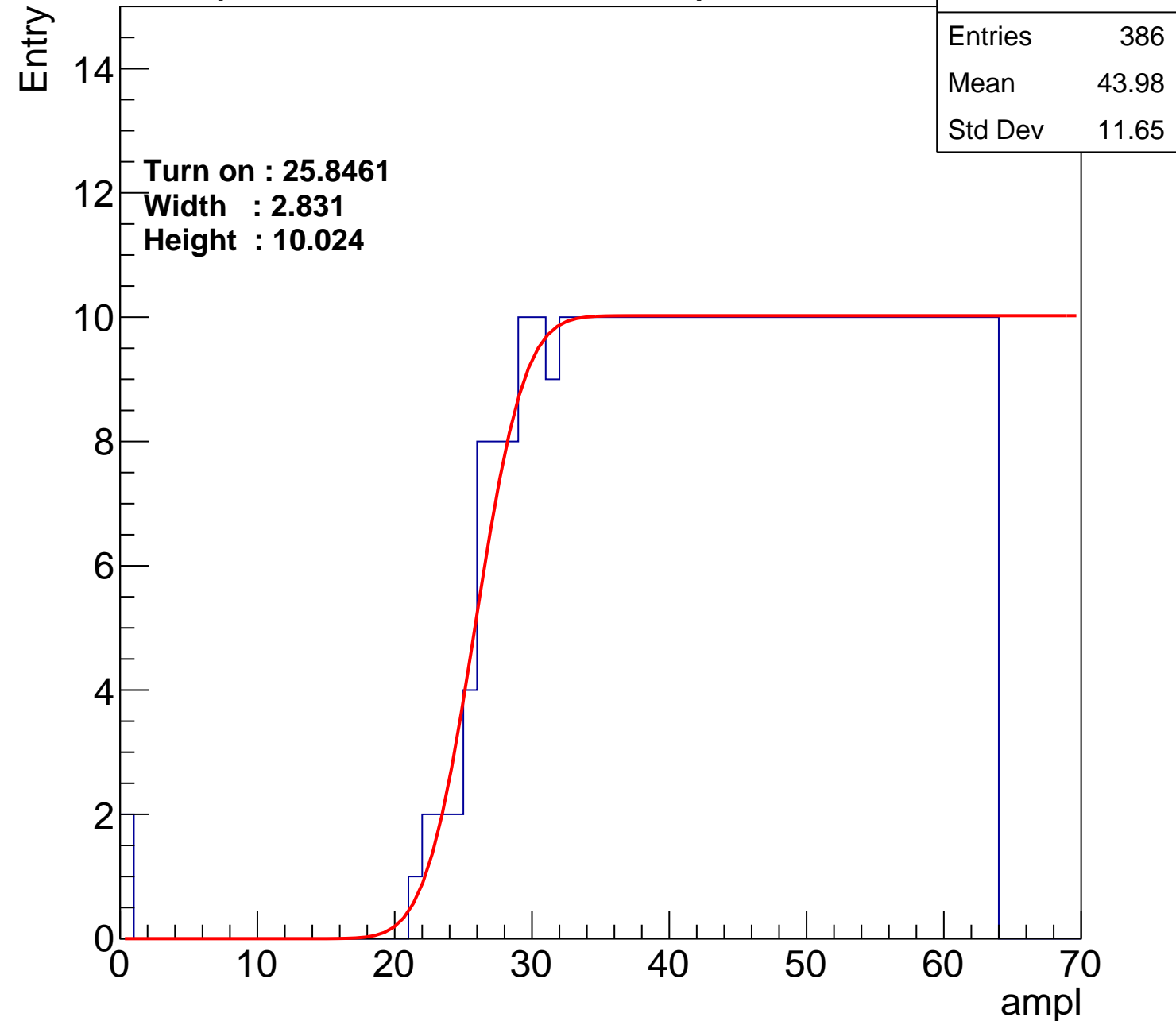
Width : 2.831

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.72
Std Dev	11.27

Turn on : 27.9745

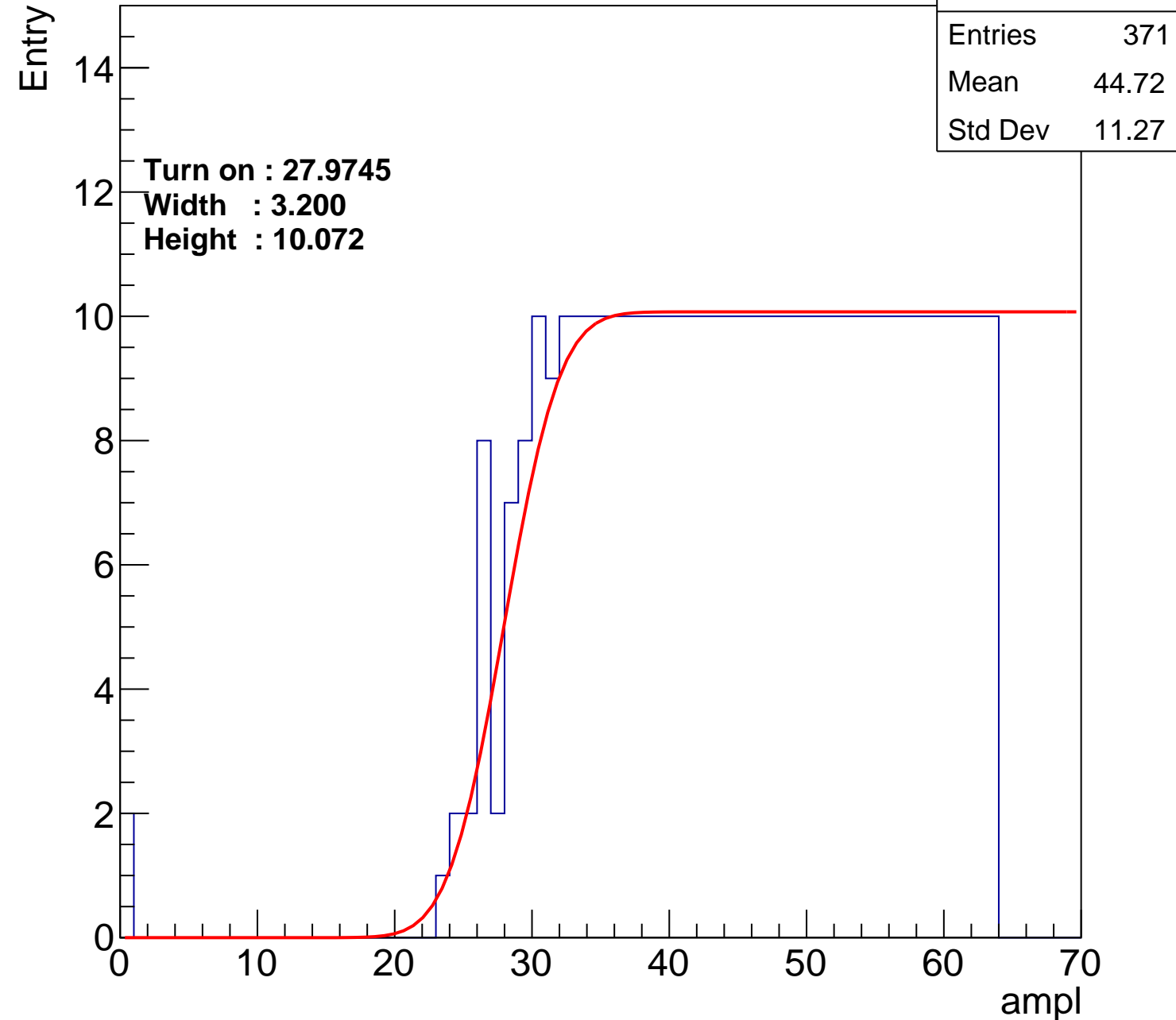
Width : 3.200

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.3
Std Dev	10.98

Turn on : 28.3490

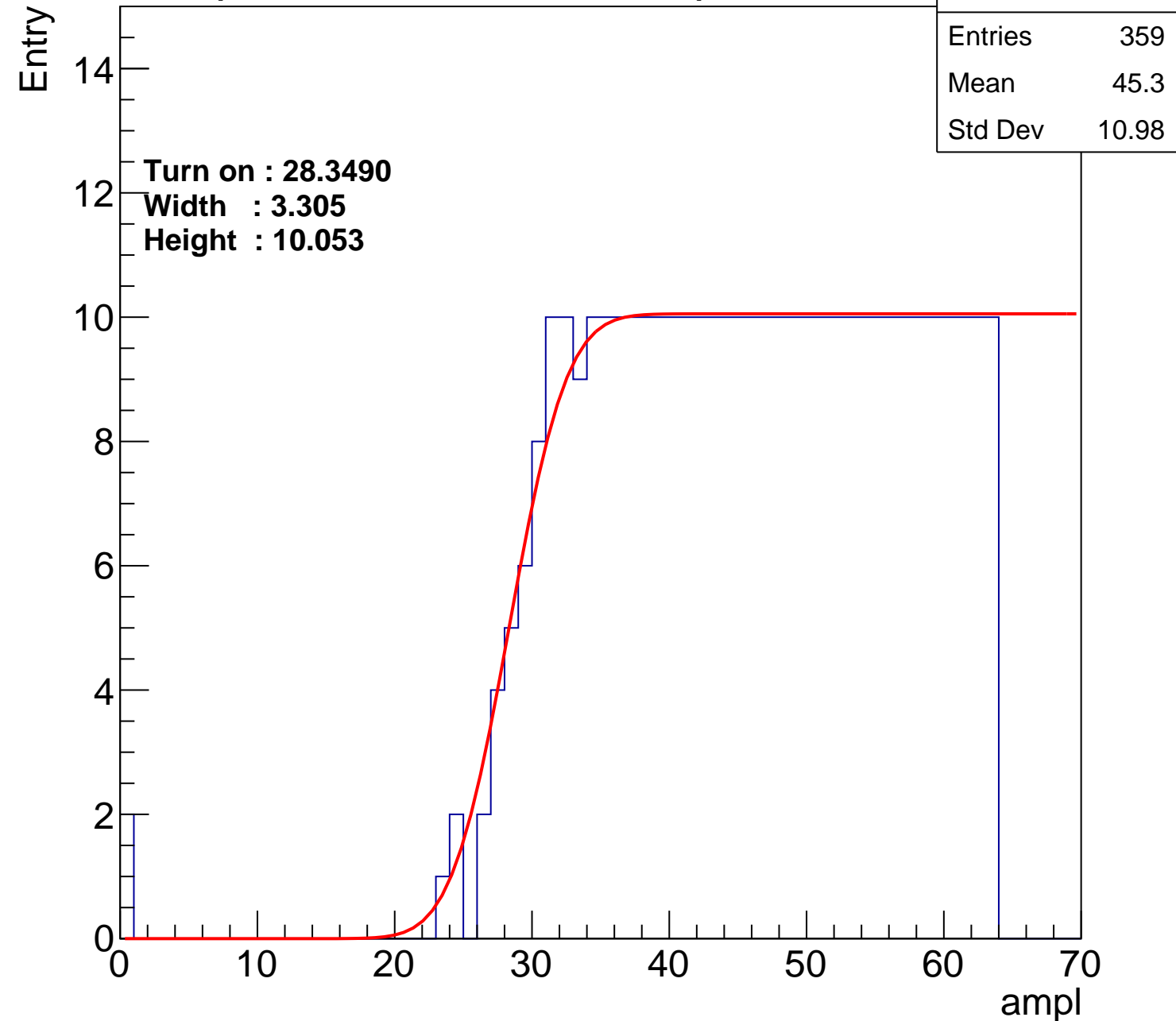
Width : 3.305

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.33
Std Dev	10.98

Turn on : 28.6092

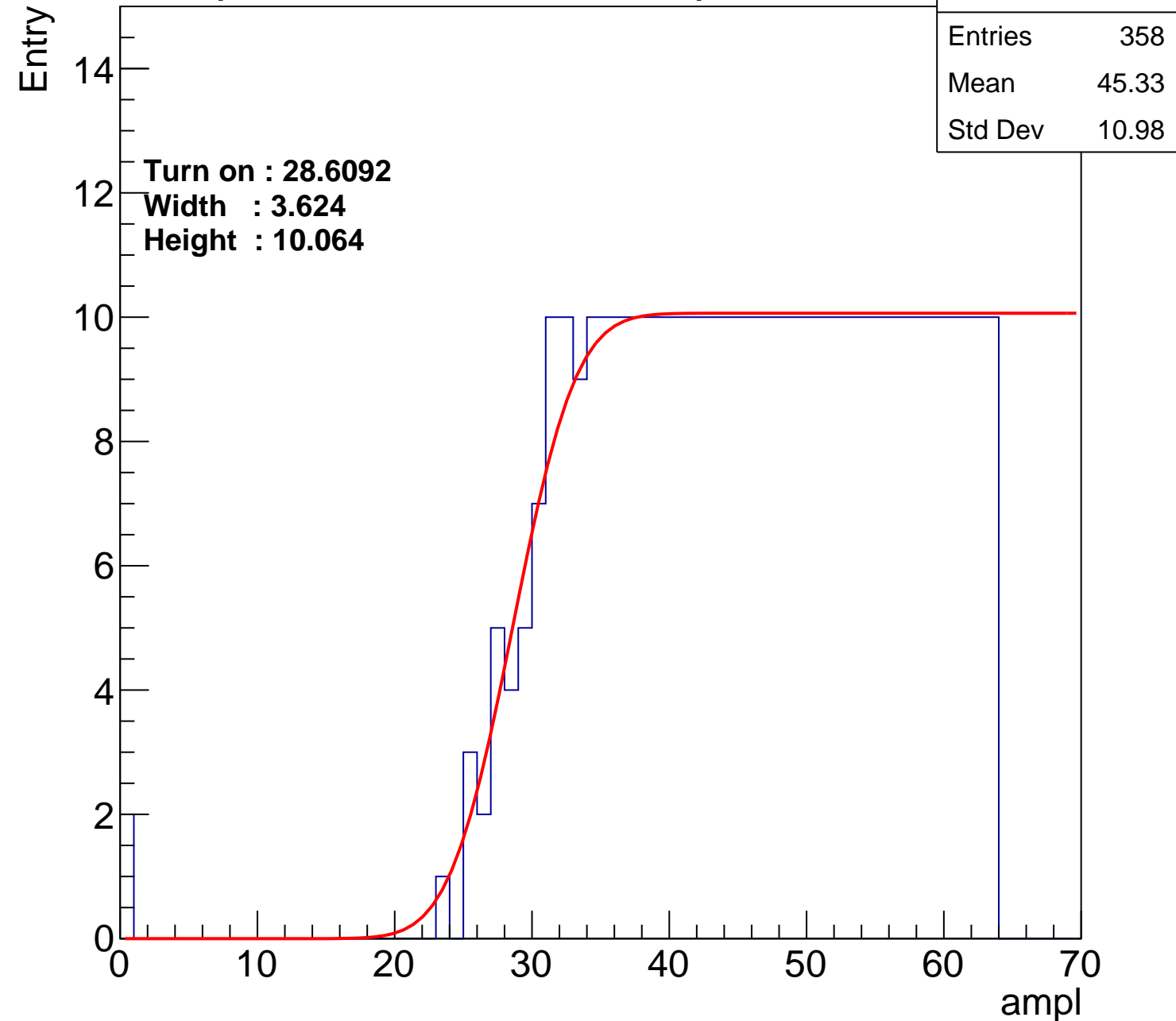
Width : 3.624

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch45

calib_packv5_042523_0143.root, FC#8, port C1

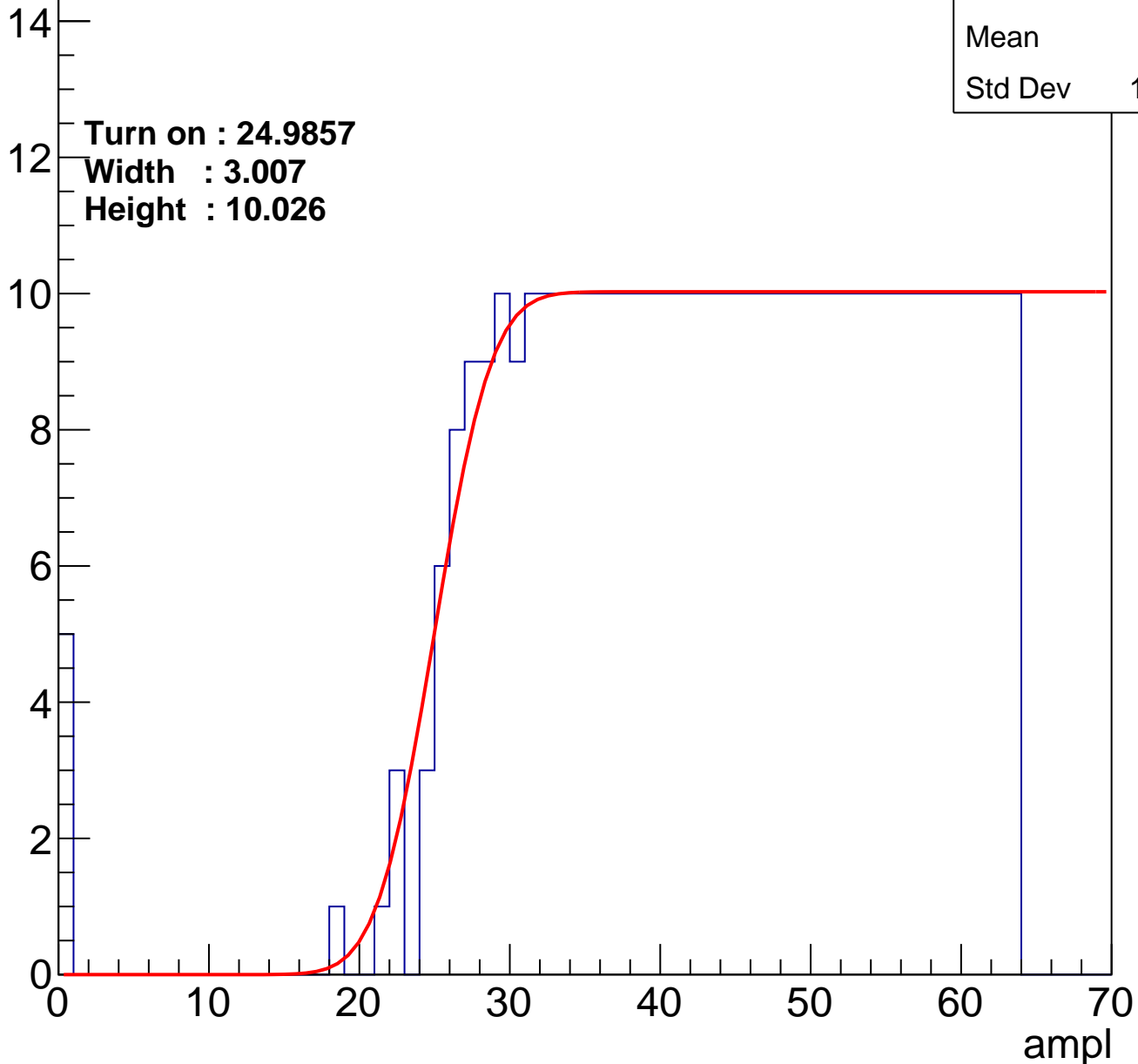
Entries	394
Mean	43.4
Std Dev	12.34

Turn on : 24.9857

Width : 3.007

Height : 10.026

Entry



B0L002S, U15-ch46

calib_packv5_042523_0143.root, FC#8, port C1

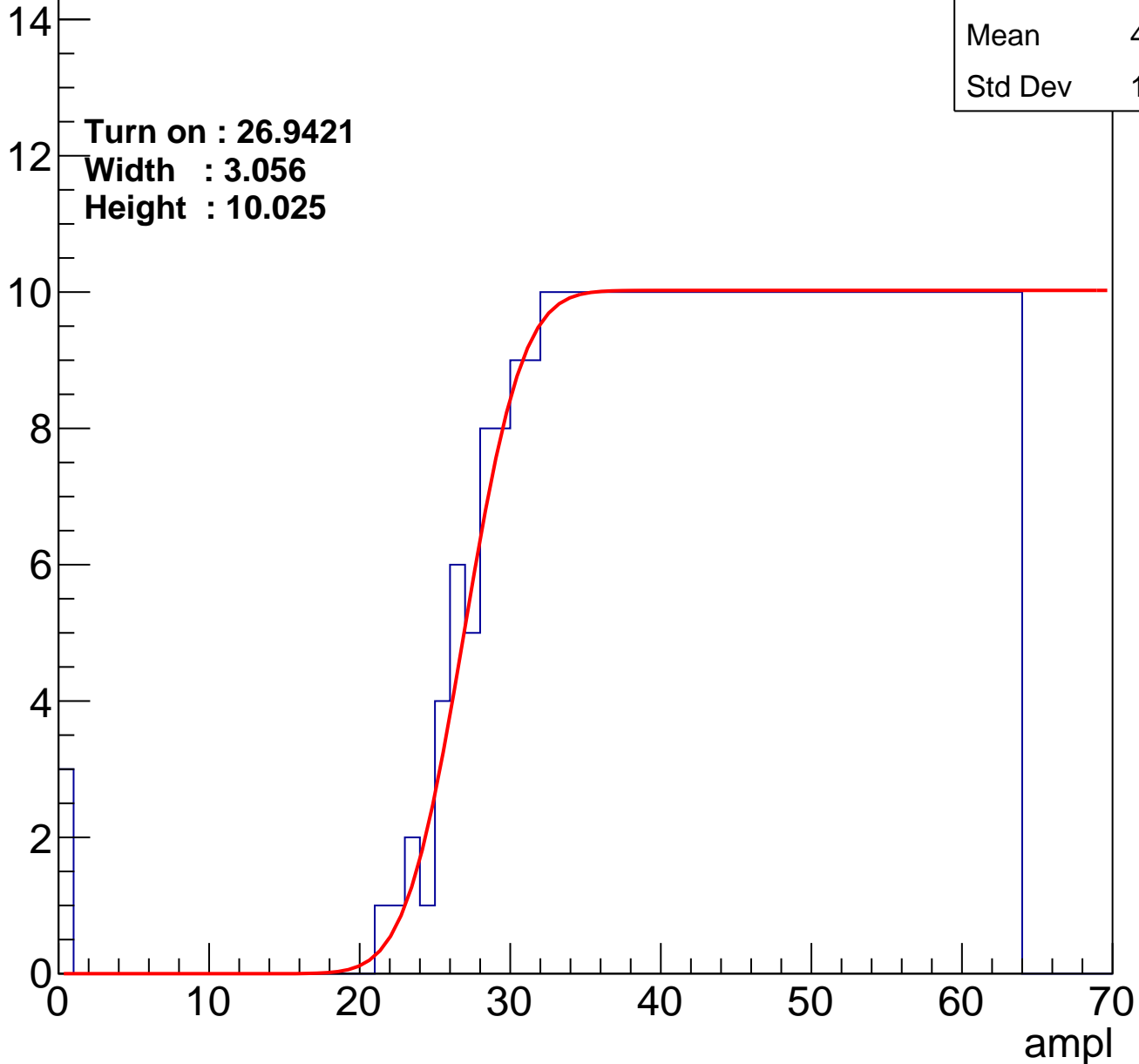
Entries	377
Mean	44.32
Std Dev	11.66

Turn on : 26.9421

Width : 3.056

Height : 10.025

Entry



B0L002S, U15-ch47

calib_packv5_042523_0143.root, FC#8, port C1

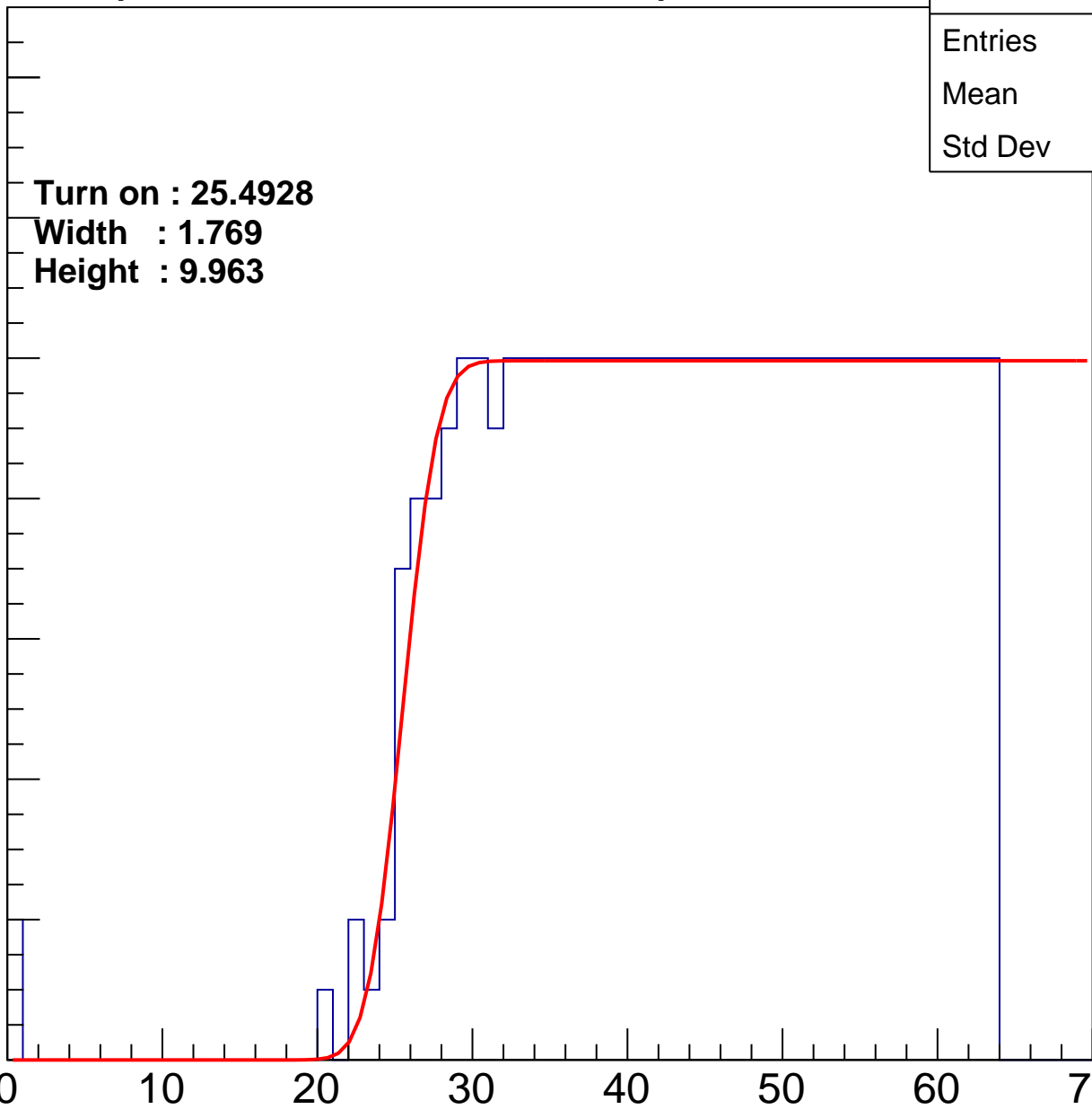
Entry

14
12
10
8
6
4
2
0

Turn on : 25.4928
Width : 1.769
Height : 9.963

Entries	389
Mean	43.84
Std Dev	11.71

ampl



B0L002S, U15-ch48

calib_packv5_042523_0143.root, FC#8, port C1

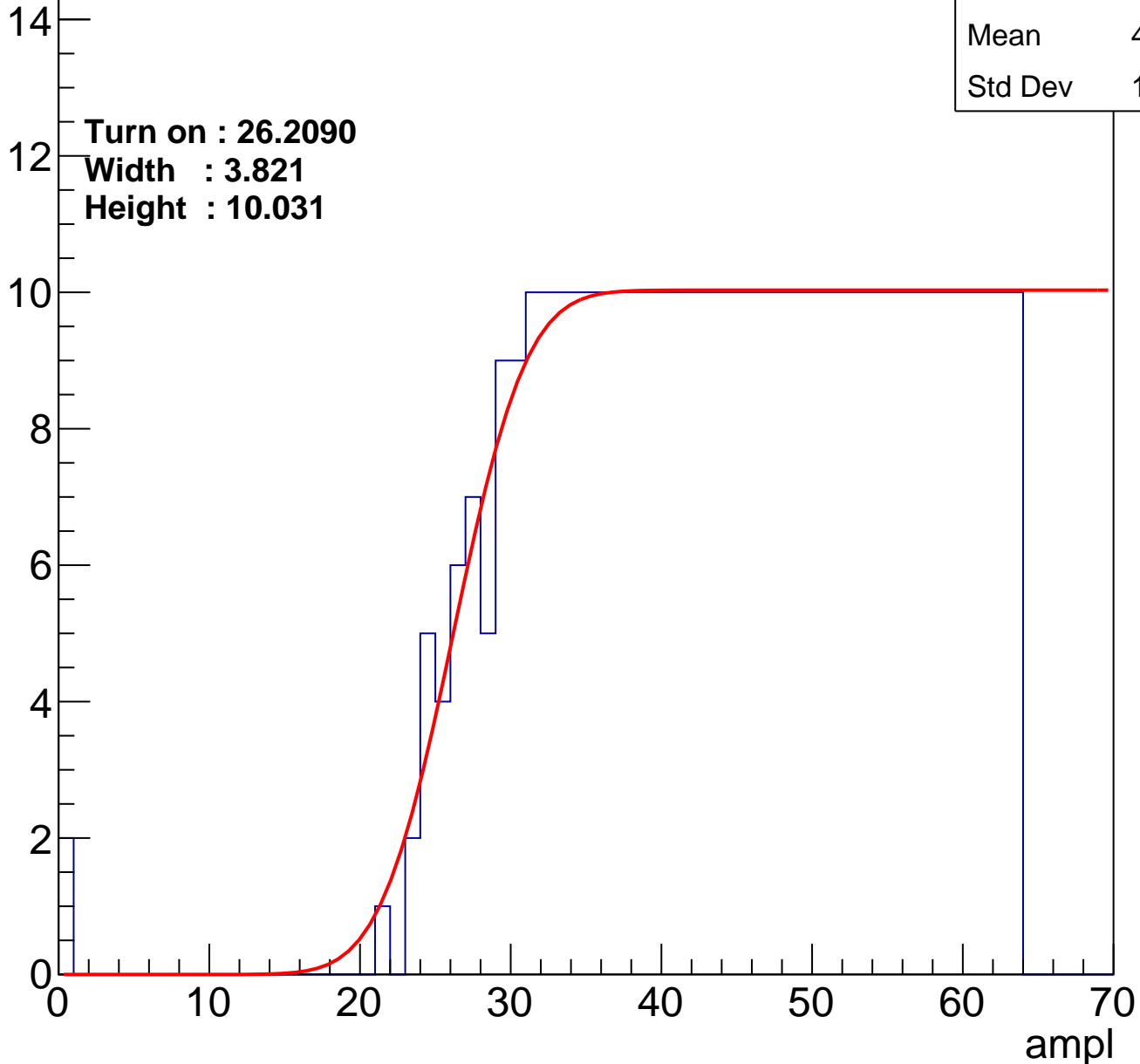
Entries	380
Mean	44.24
Std Dev	11.54

Turn on : 26.2090

Width : 3.821

Height : 10.031

Entry



B0L002S, U15-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.16
Std Dev	11.4

Turn on : 26.0009

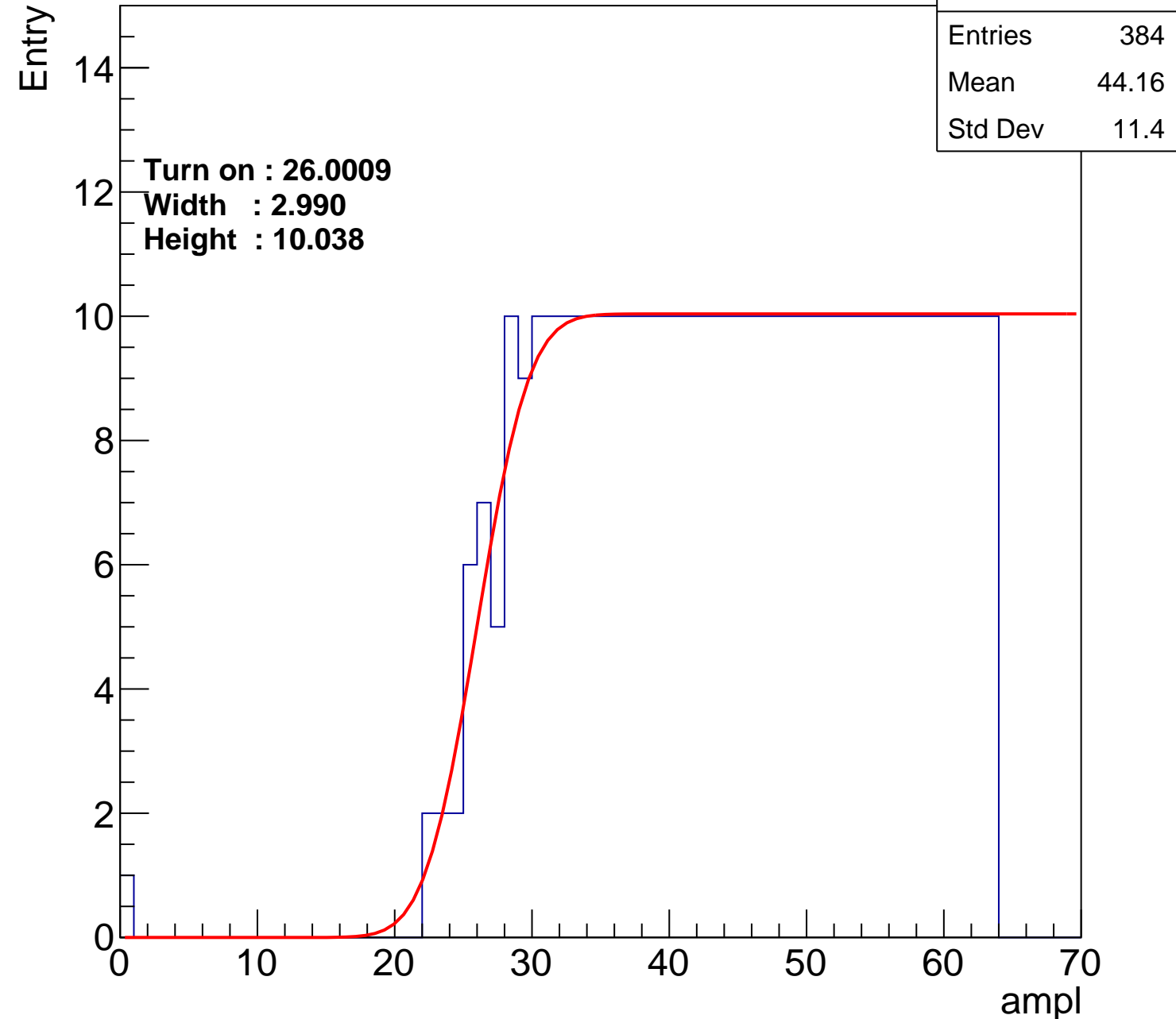
Width : 2.990

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch50

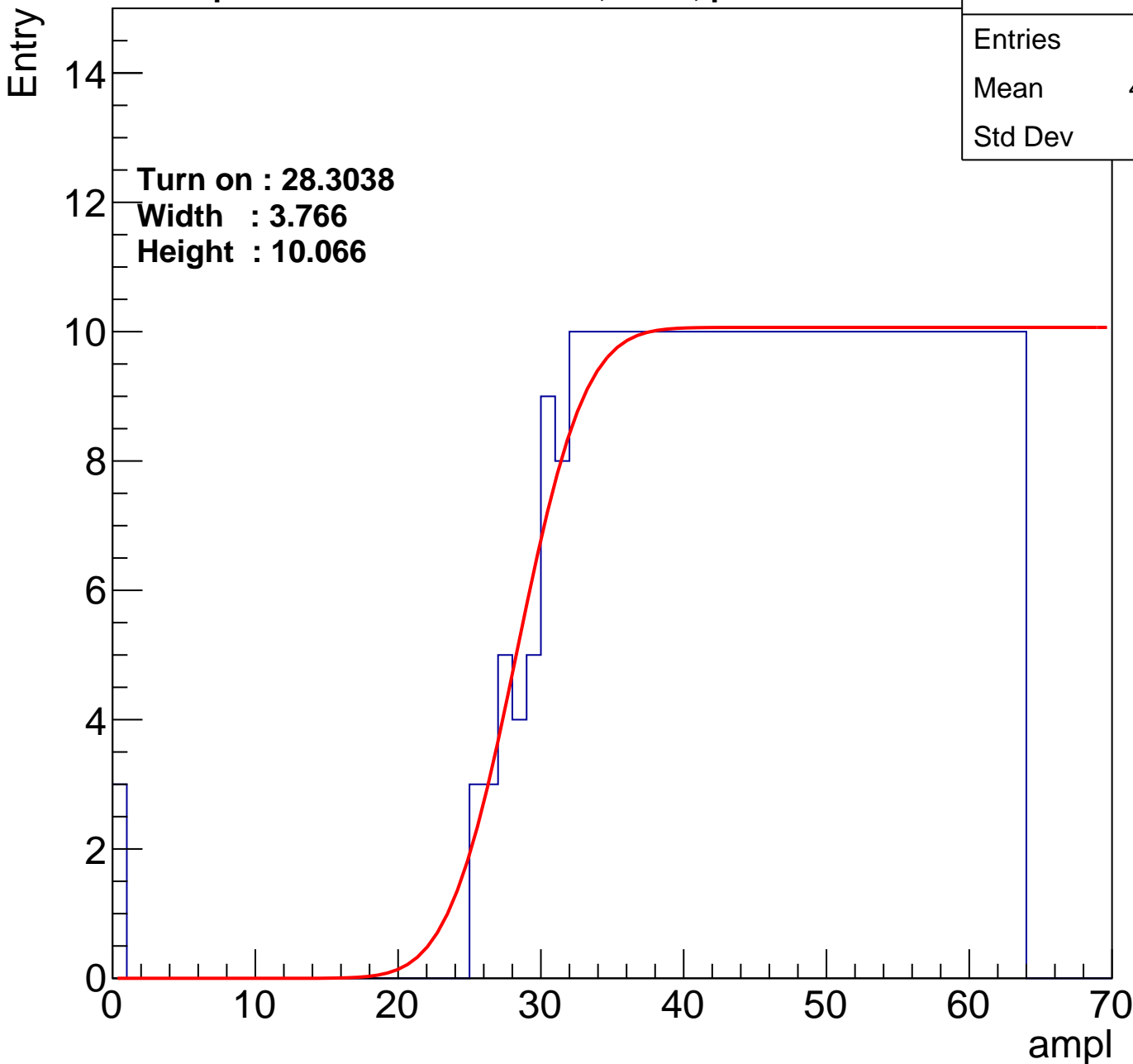
calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.17
Std Dev	11.21

Turn on : 28.3038

Width : 3.766

Height : 10.066



B0L002S, U15-ch51

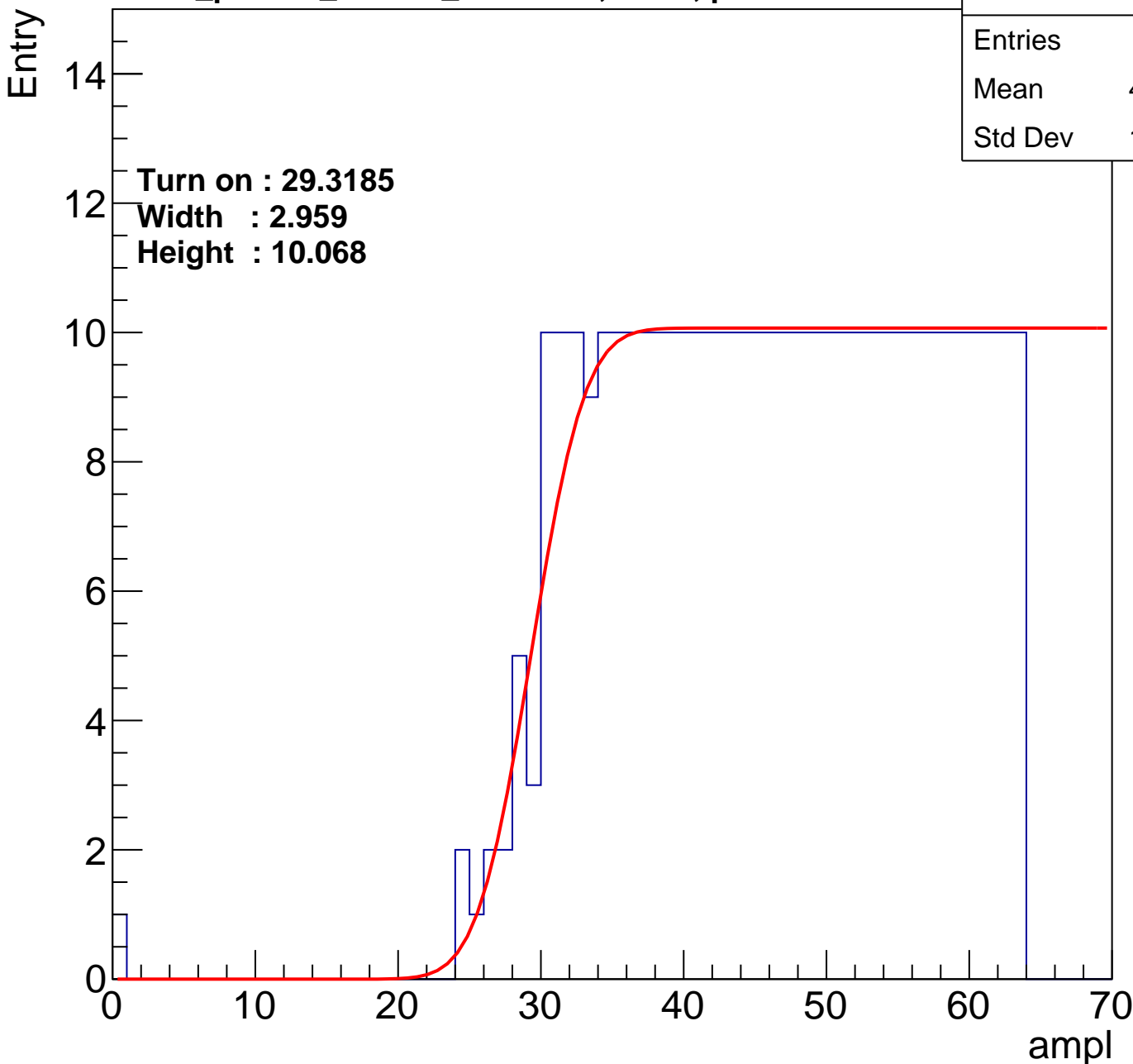
calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.59
Std Dev	10.63

Turn on : 29.3185

Width : 2.959

Height : 10.068



B0L002S, U15-ch52

calib_packv5_042523_0143.root, FC#8, port C1

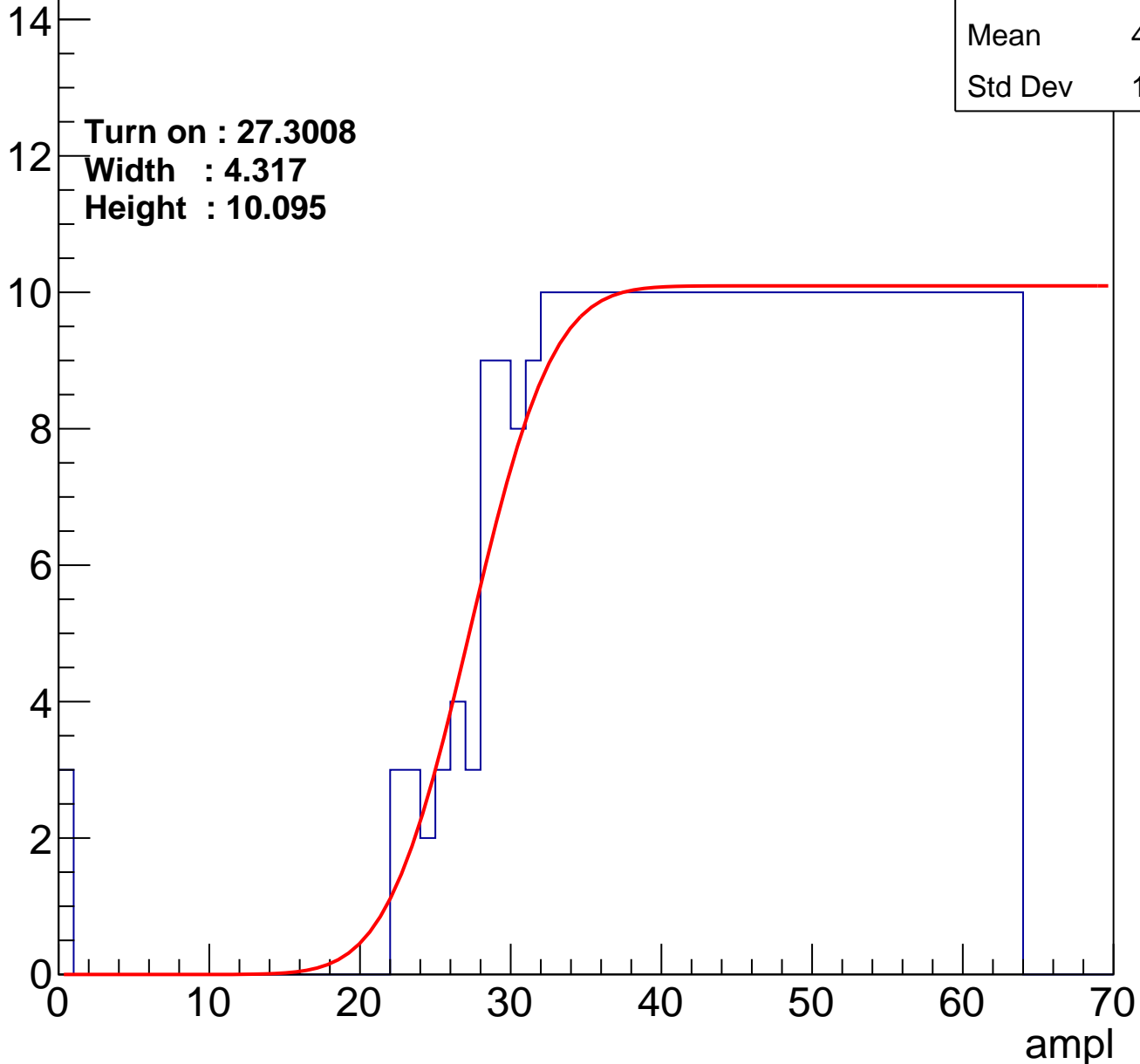
Entries	376
Mean	44.35
Std Dev	11.67

Turn on : 27.3008

Width : 4.317

Height : 10.095

Entry



B0L002S, U15-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	393
Mean	43.52
Std Dev	12.13

Turn on : 25.1342

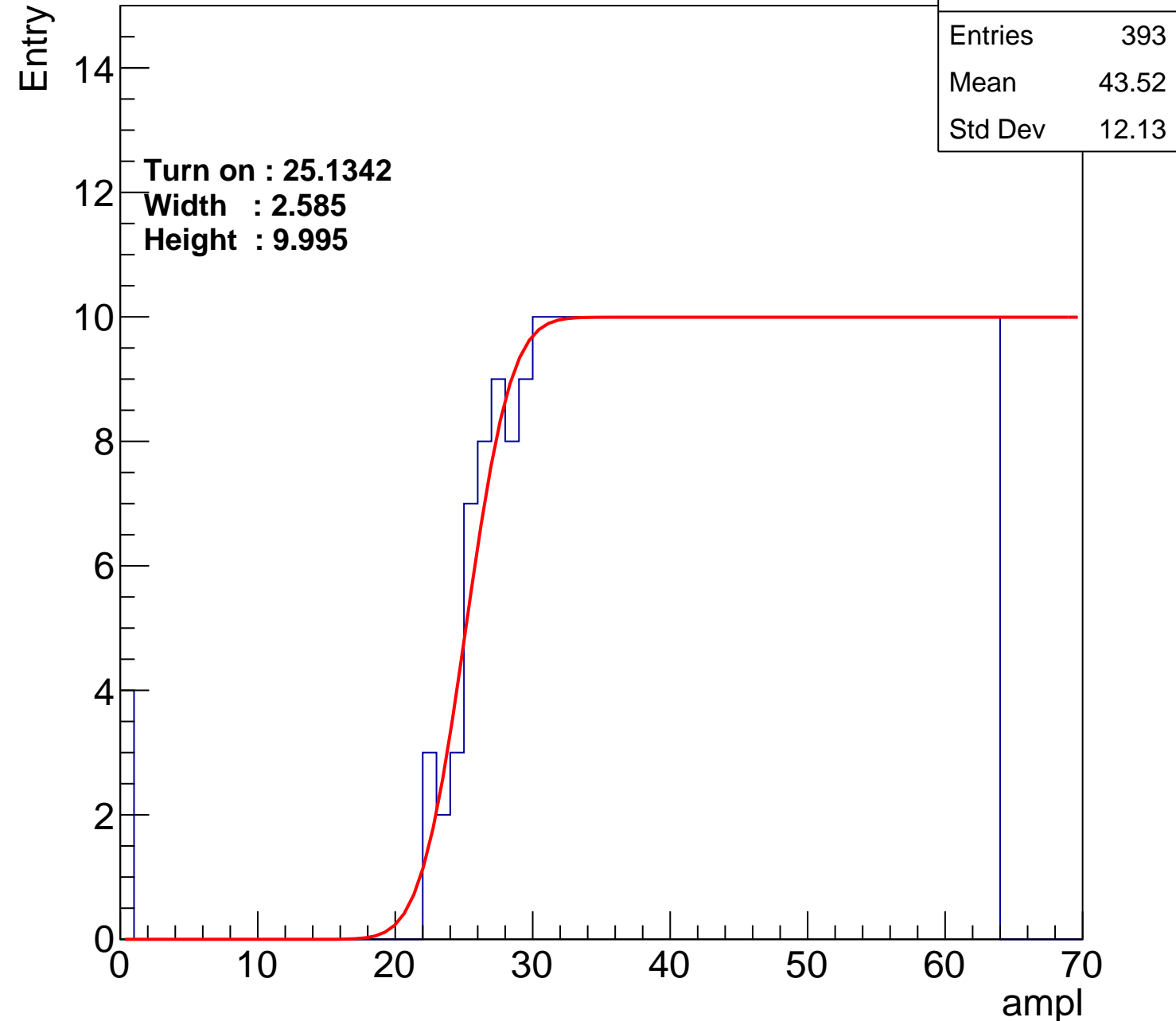
Width : 2.585

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.49
Std Dev	11.05

Turn on : 29.2058

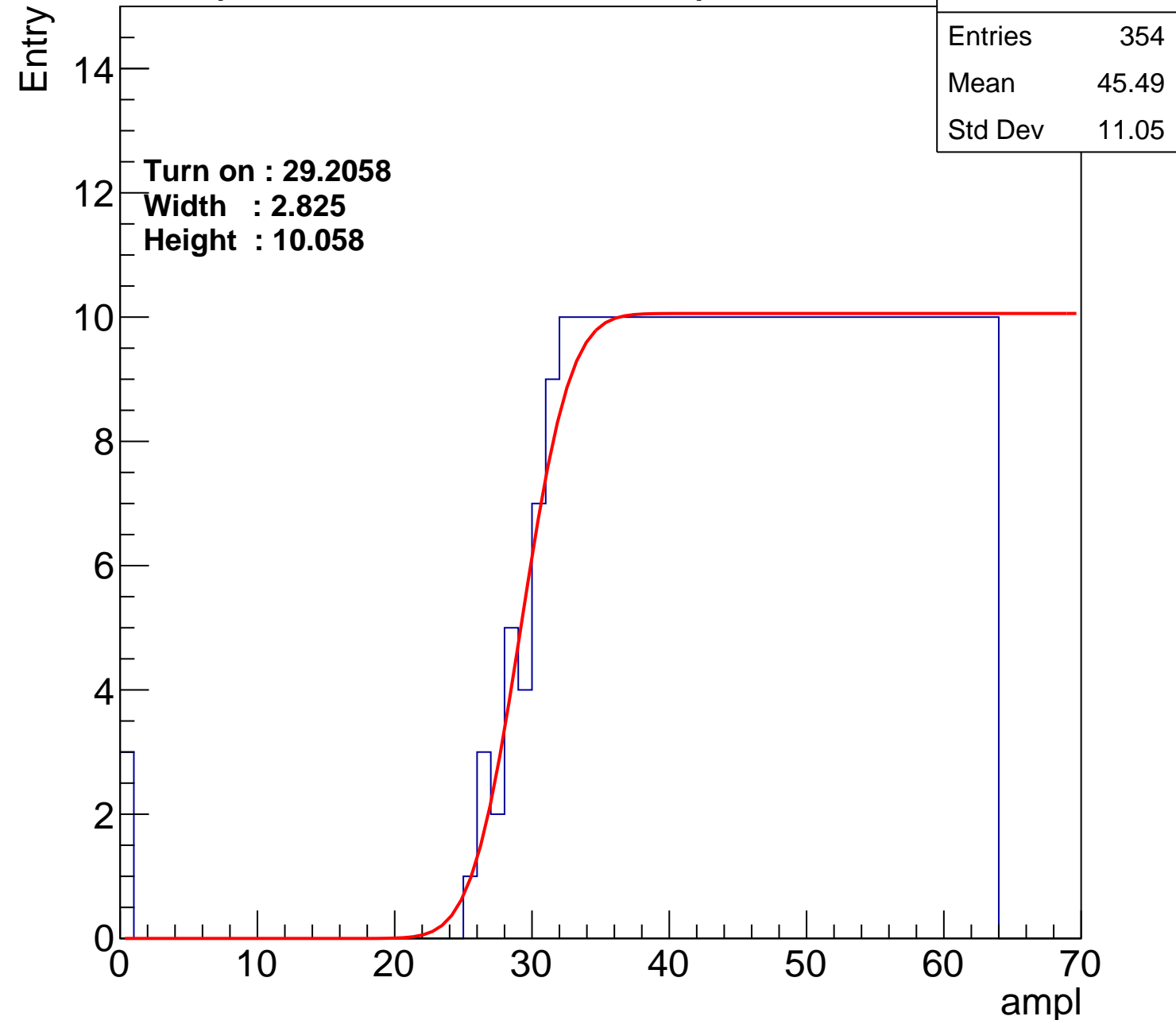
Width : 2.825

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 26.1548

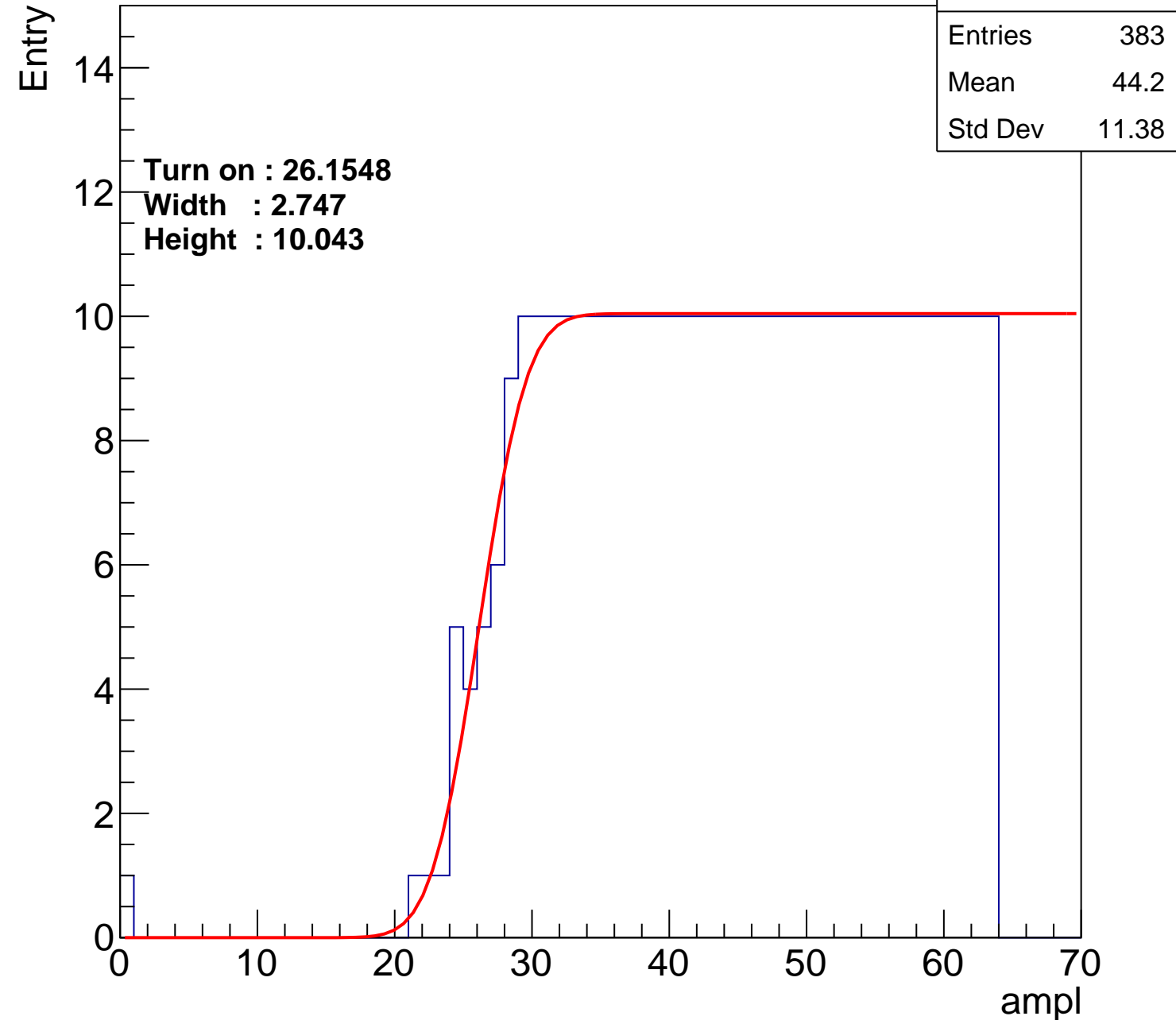
Width : 2.747

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch56

calib_packv5_042523_0143.root, FC#8, port C1

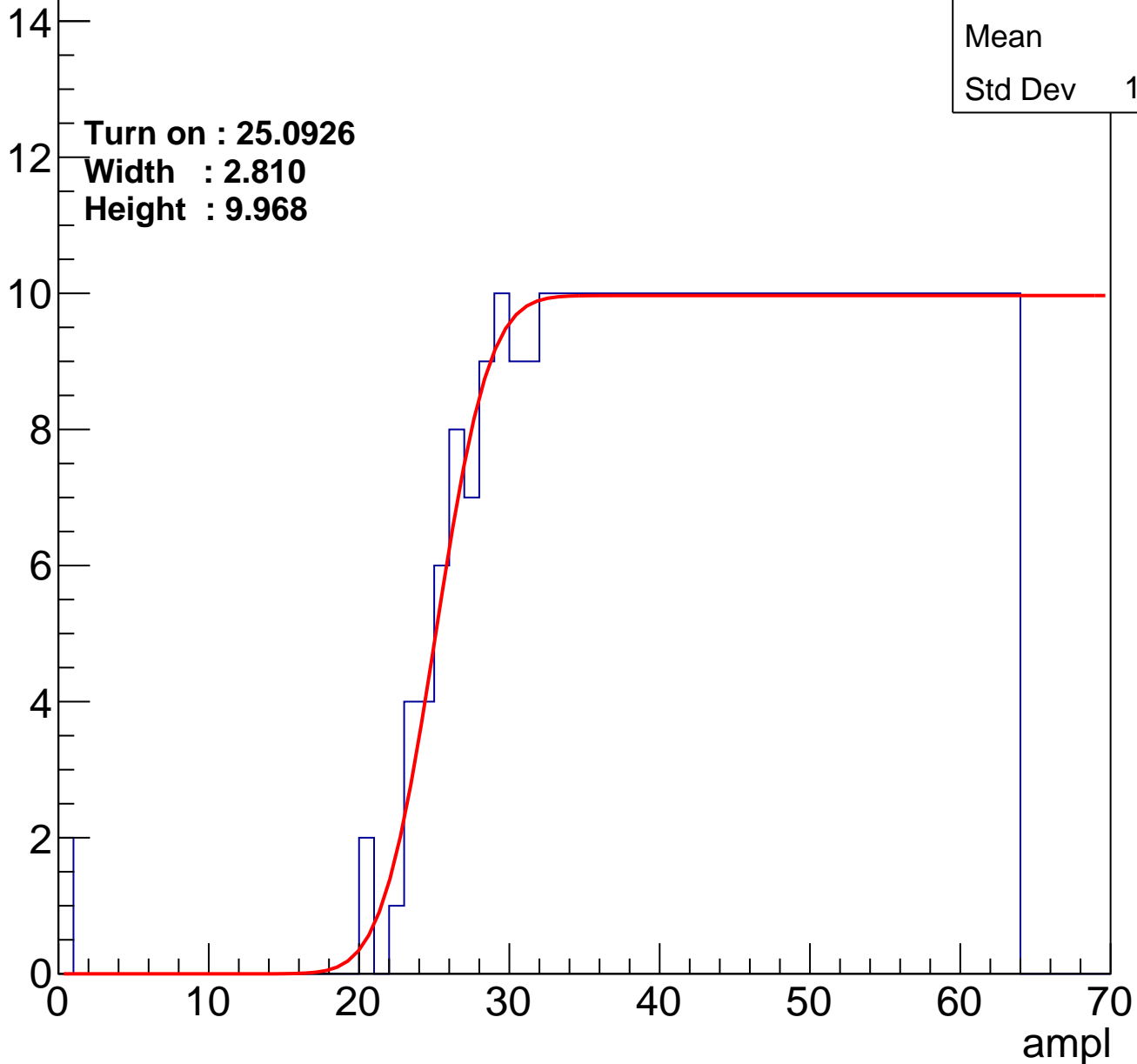
Entries	391
Mean	43.7
Std Dev	11.82

Turn on : 25.0926

Width : 2.810

Height : 9.968

Entry



B0L002S, U15-ch57

calib_packv5_042523_0143.root, FC#8, port C1

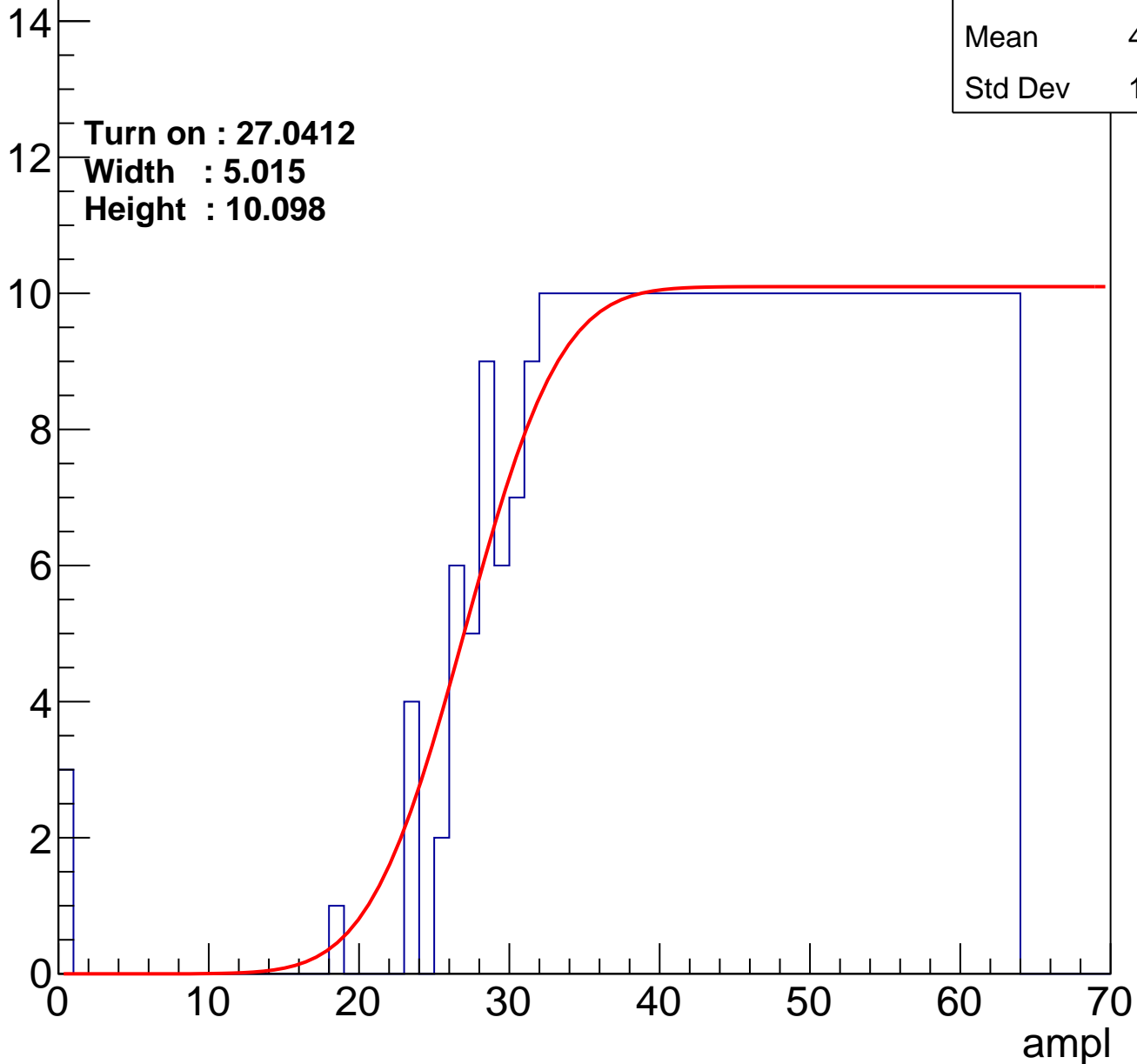
Entries	372
Mean	44.53
Std Dev	11.59

Turn on : 27.0412

Width : 5.015

Height : 10.098

Entry



B0L002S, U15-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.63
Std Dev	11.17

Turn on : 27.1594

Width : 3.414

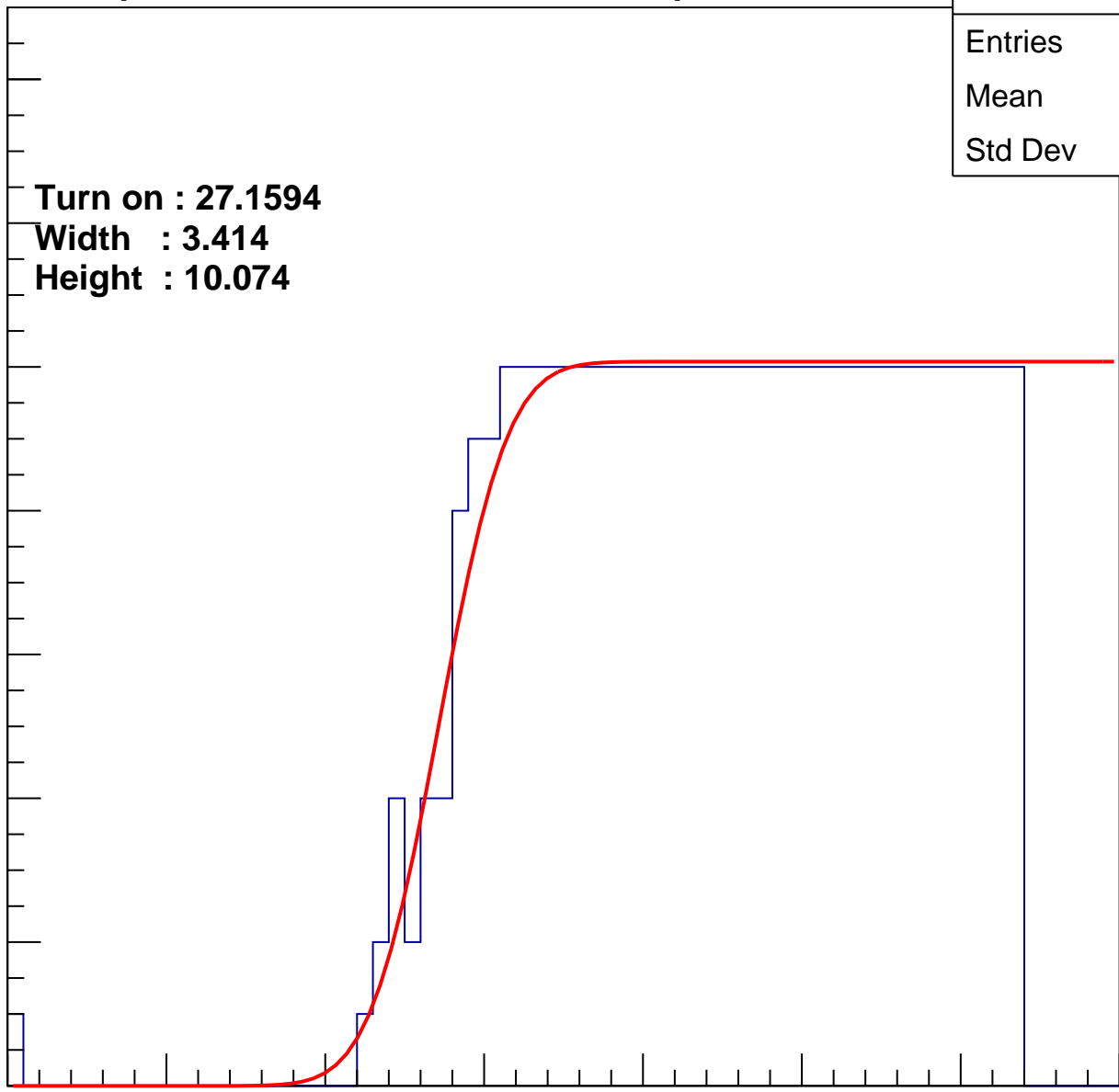
Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U15-ch59

calib_packv5_042523_0143.root, FC#8, port C1

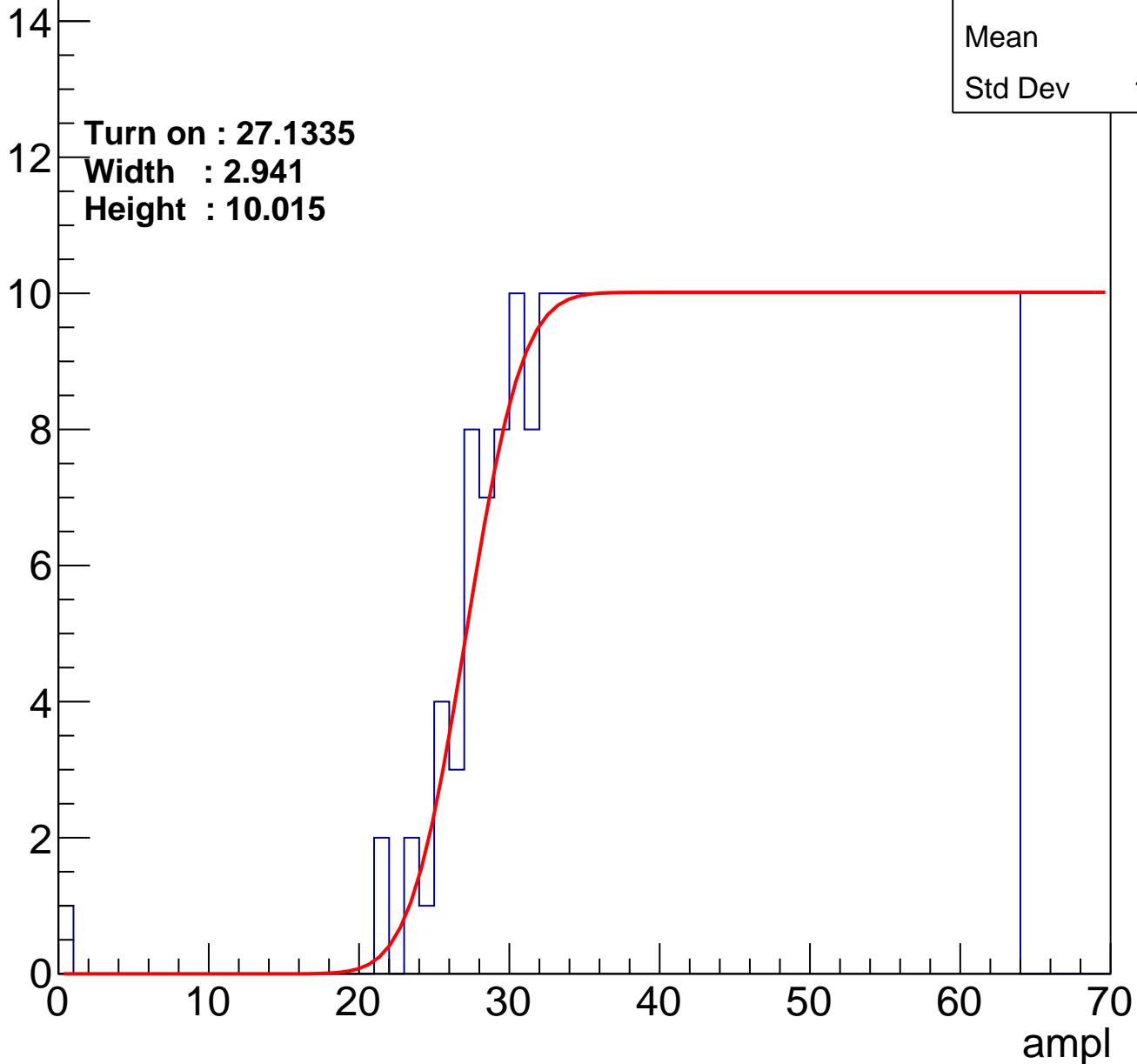
Entries	374
Mean	44.6
Std Dev	11.21

Turn on : 27.1335

Width : 2.941

Height : 10.015

Entry



B0L002S, U15-ch60

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.34
Std Dev	10.76

Turn on : 27.9999

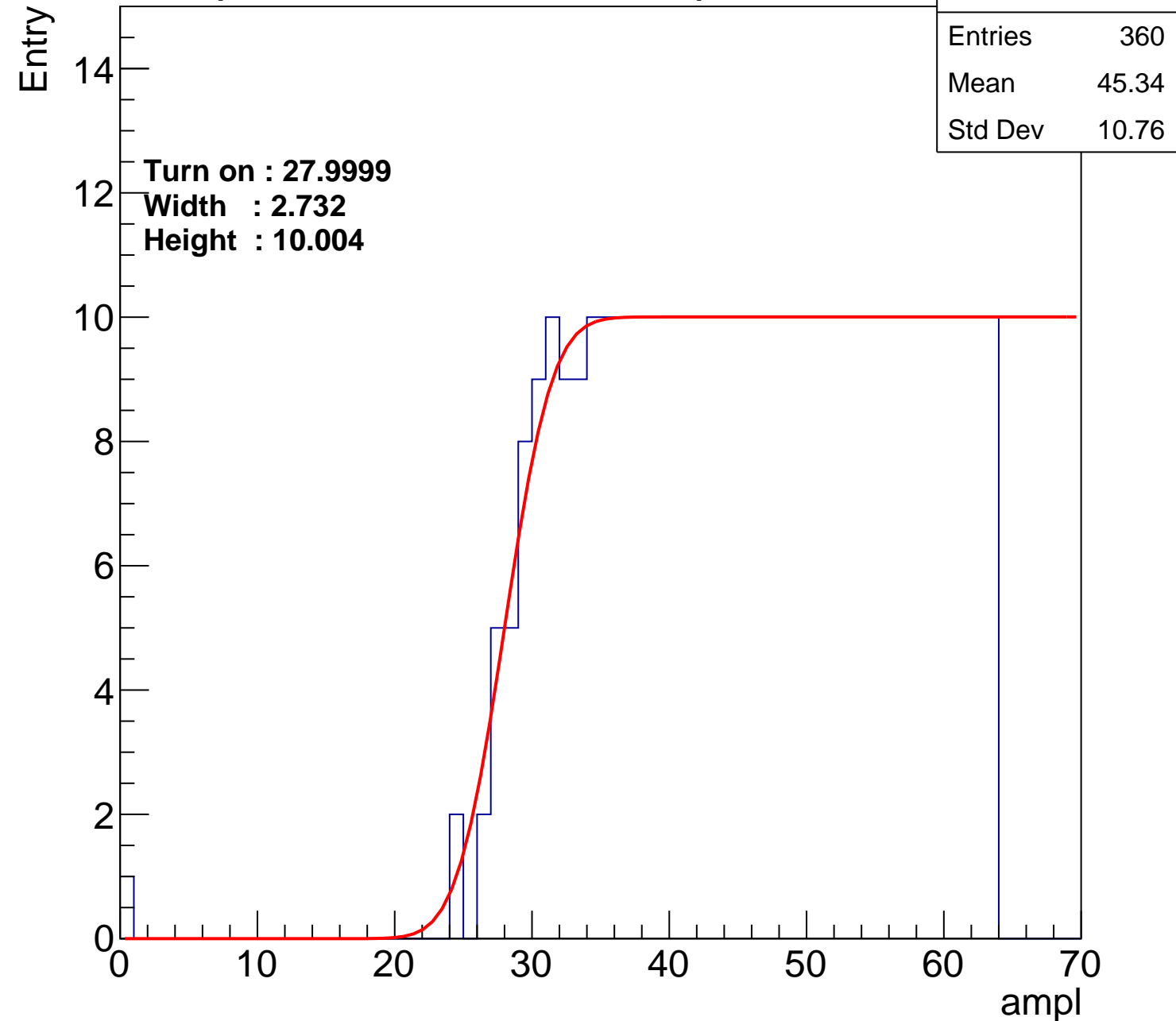
Width : 2.732

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch61

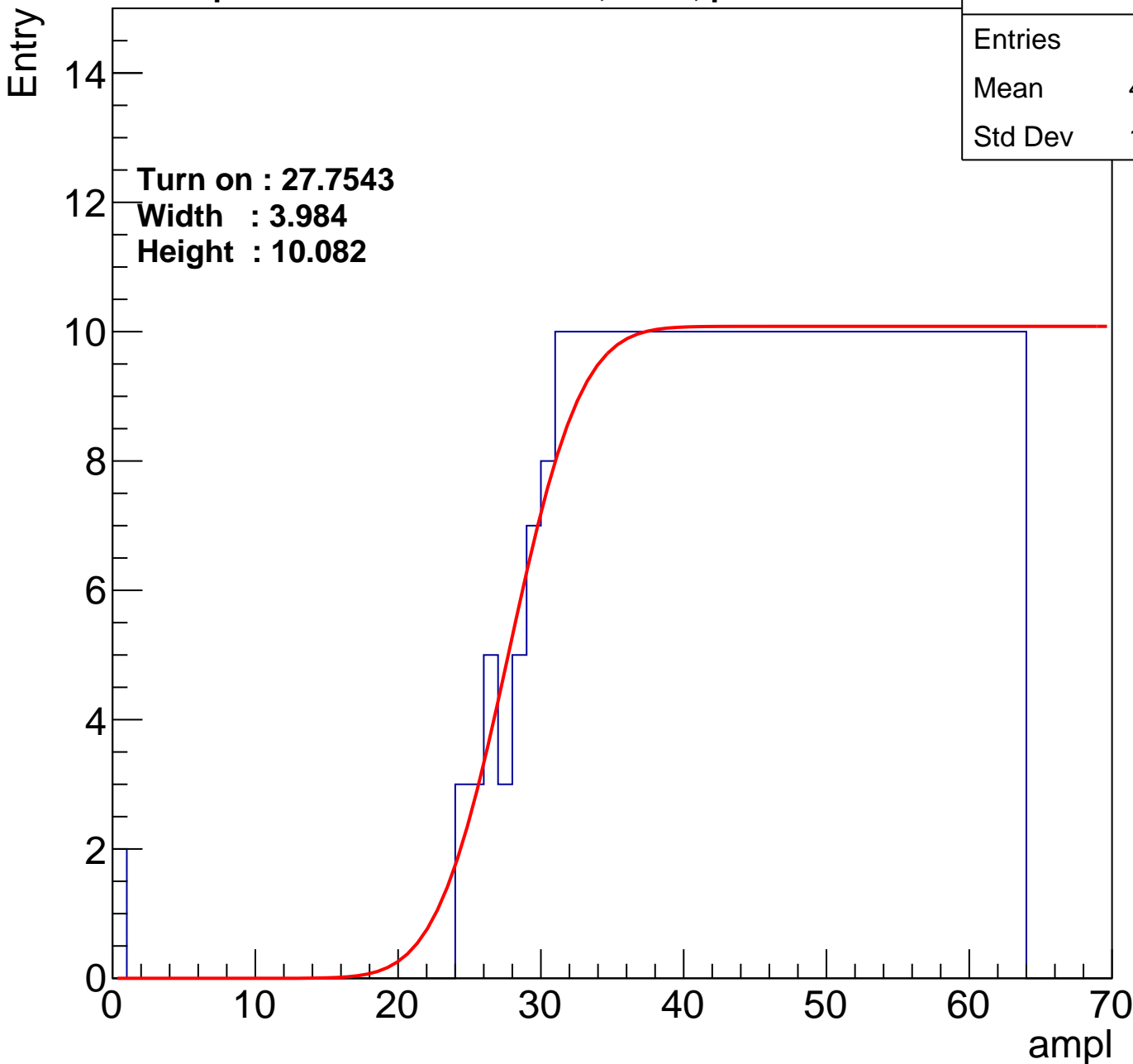
calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.95
Std Dev	11.17

Turn on : 27.7543

Width : 3.984

Height : 10.082



B0L002S, U15-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.61
Std Dev	11.46

Turn on : 27.3516

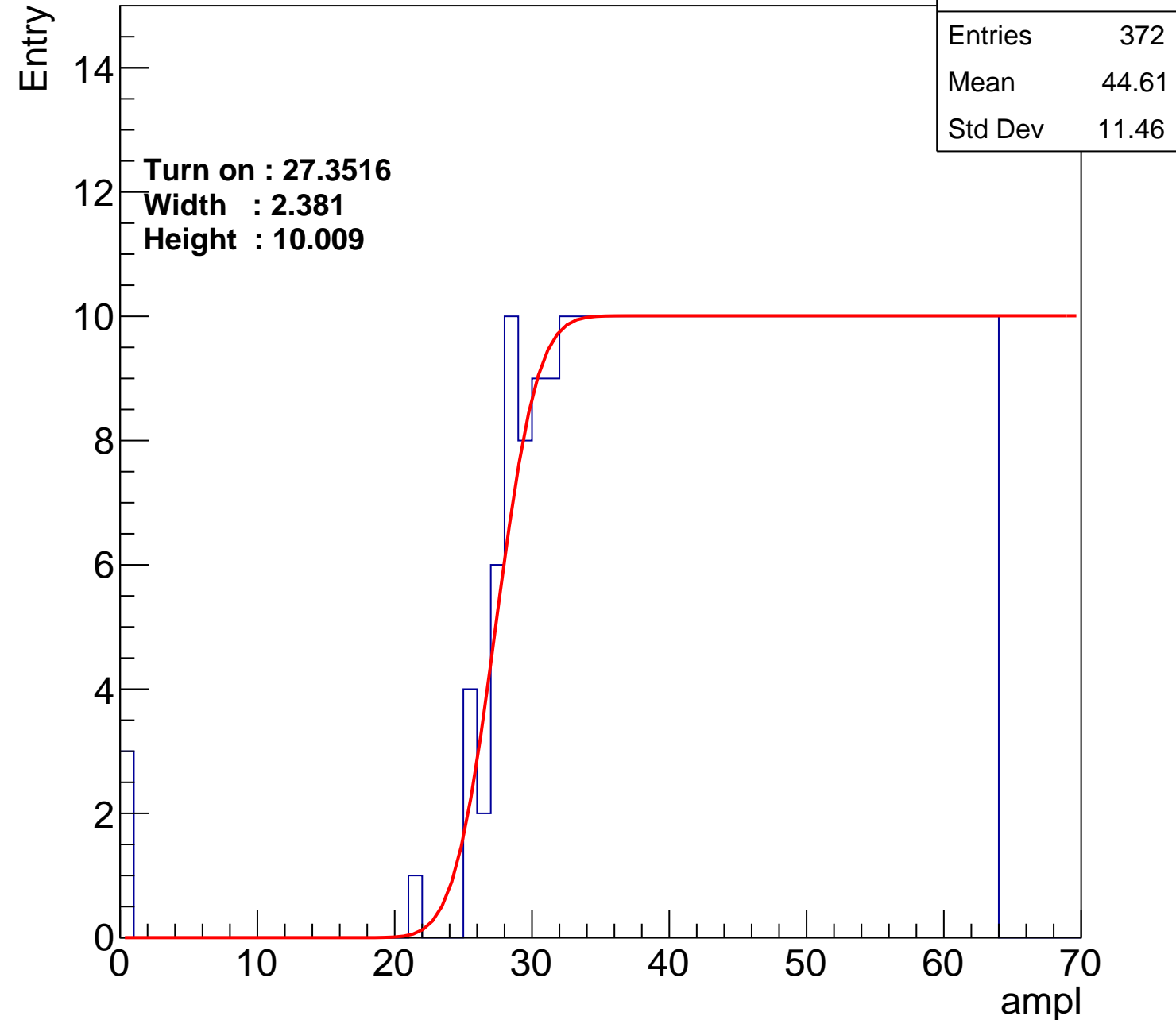
Width : 2.381

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.59
Std Dev	11.55

Turn on : 27.7189

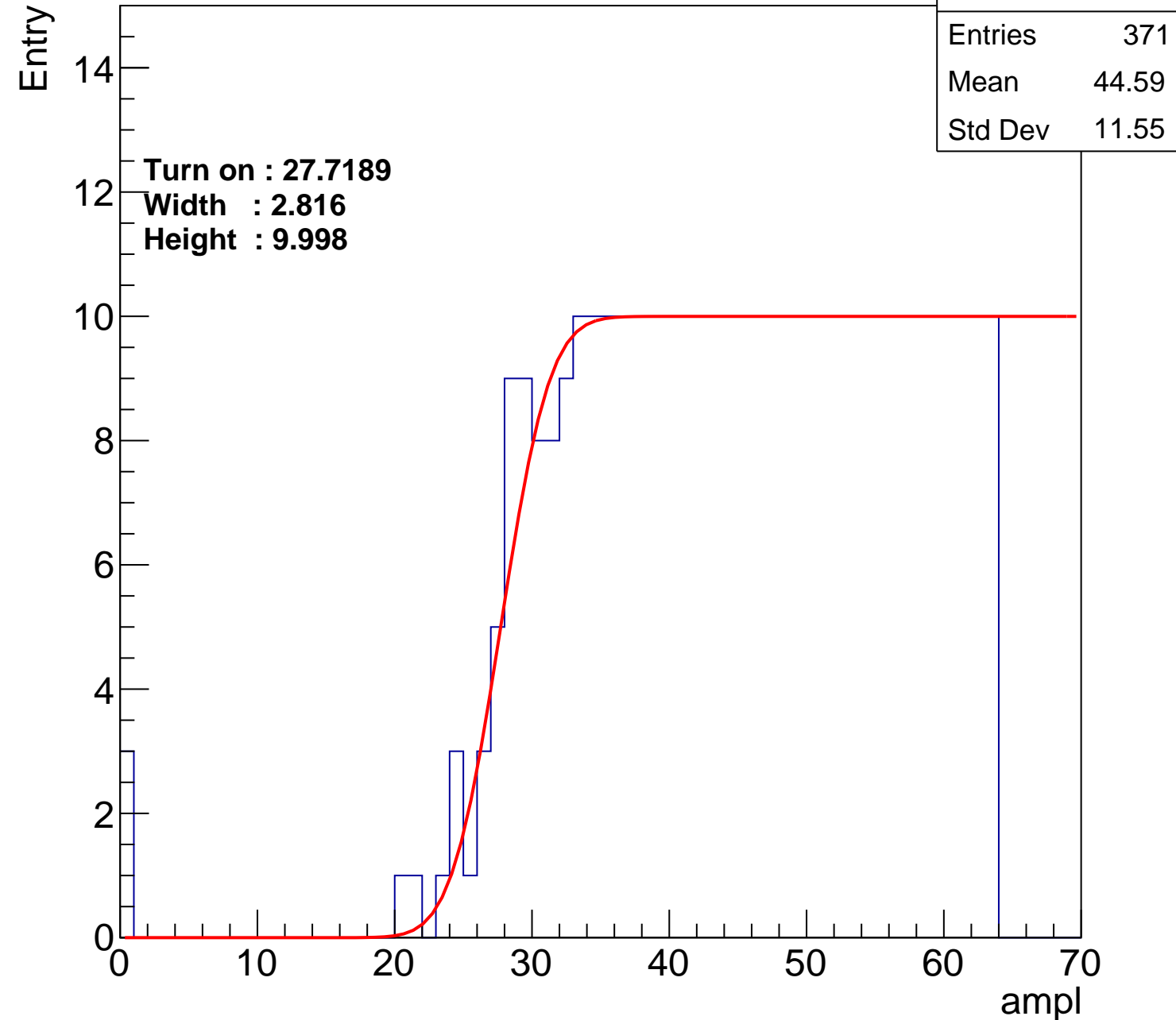
Width : 2.816

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Std Dev	11.42
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Height : 10.045

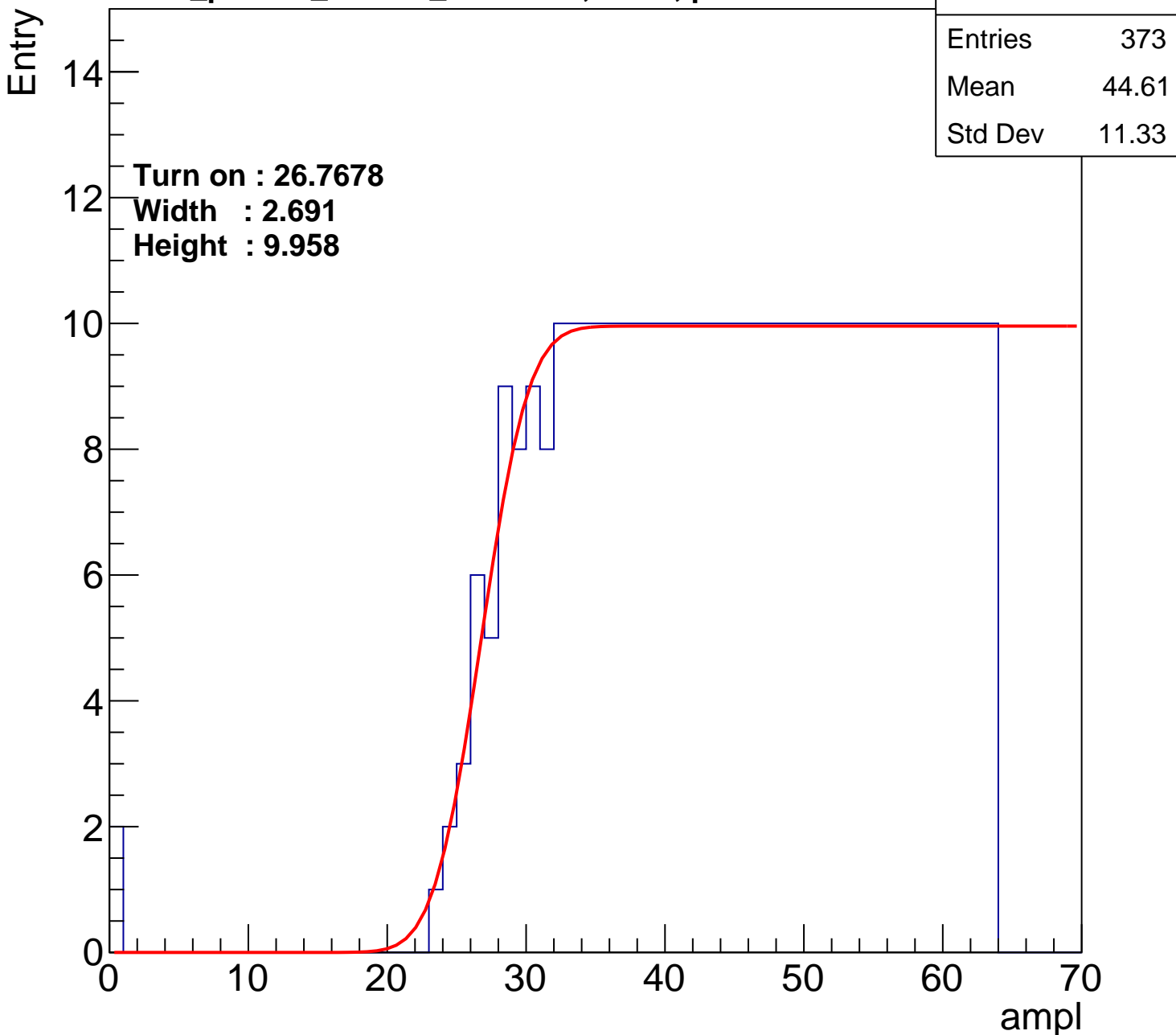


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.61
Std Dev	11.33

Height : 9.958



B0L002S, U15-ch66

calib_packv5_042523_0143.root, FC#8, port C1

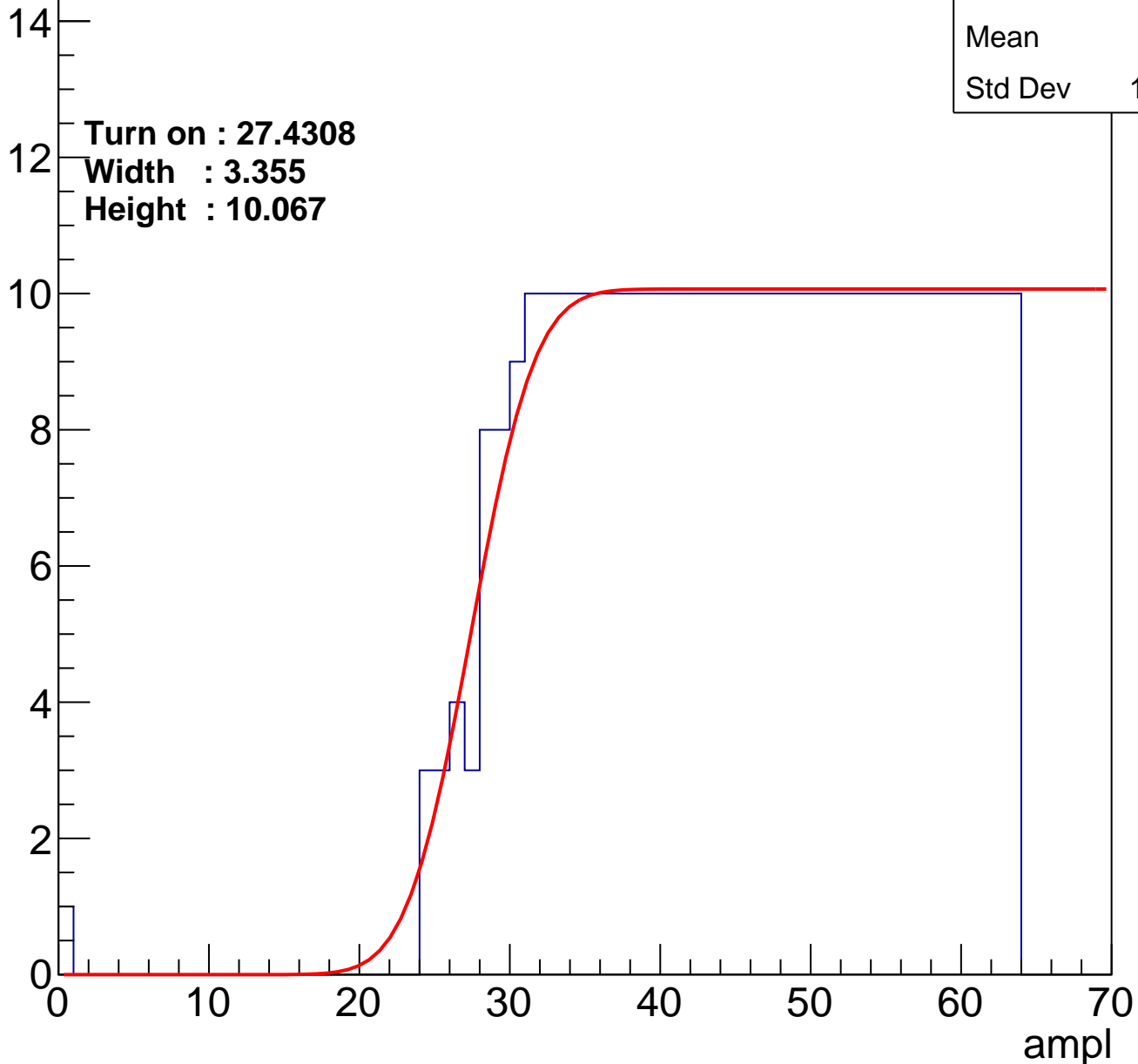
Entries	369
Mean	44.9
Std Dev	10.99

Turn on : 27.4308

Width : 3.355

Height : 10.067

Entry



B0L002S, U15-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.07
Std Dev	11.07

Turn on : 28.0212

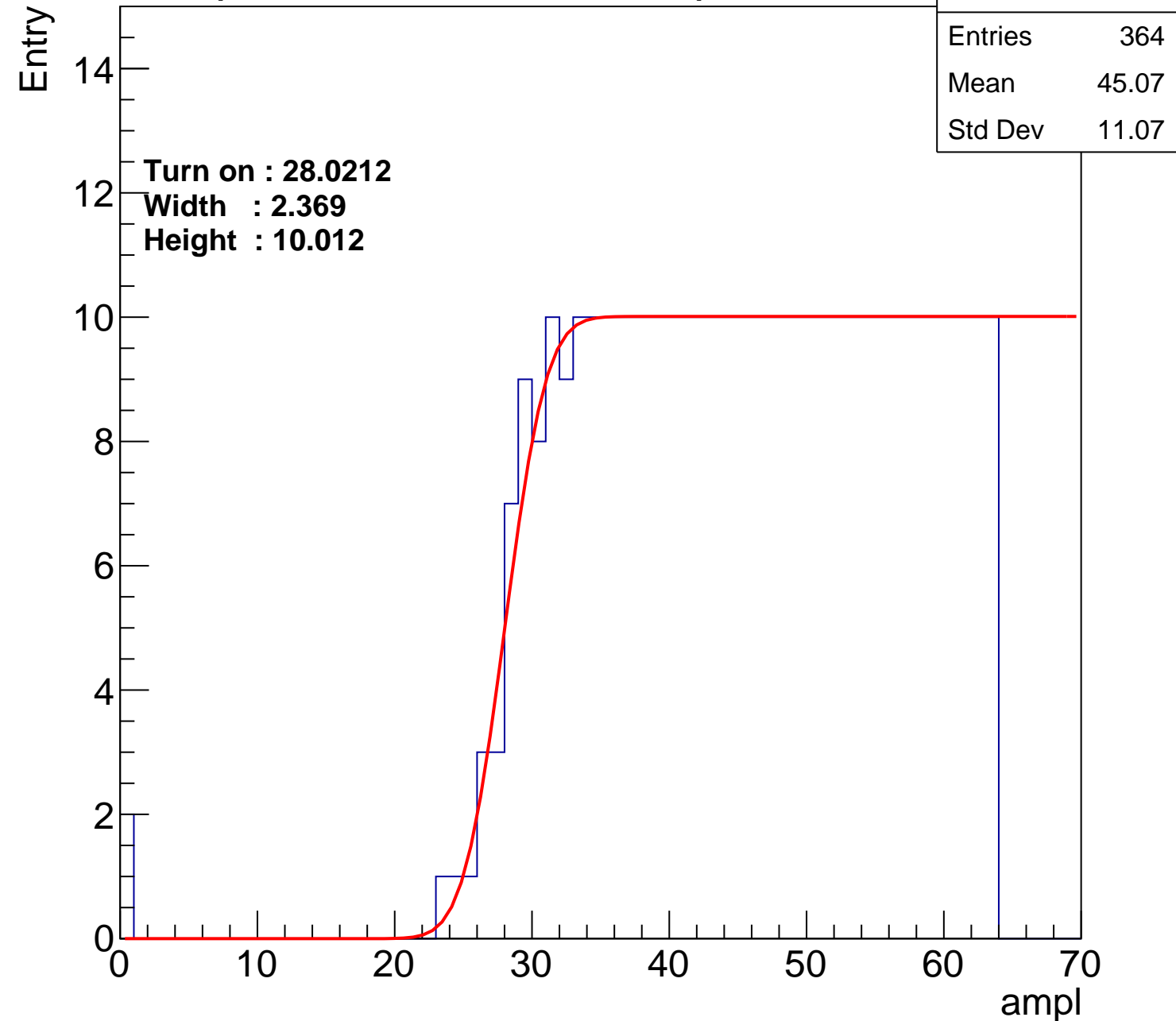
Width : 2.369

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.55
Std Dev	11.88

Turn on : 27.8668

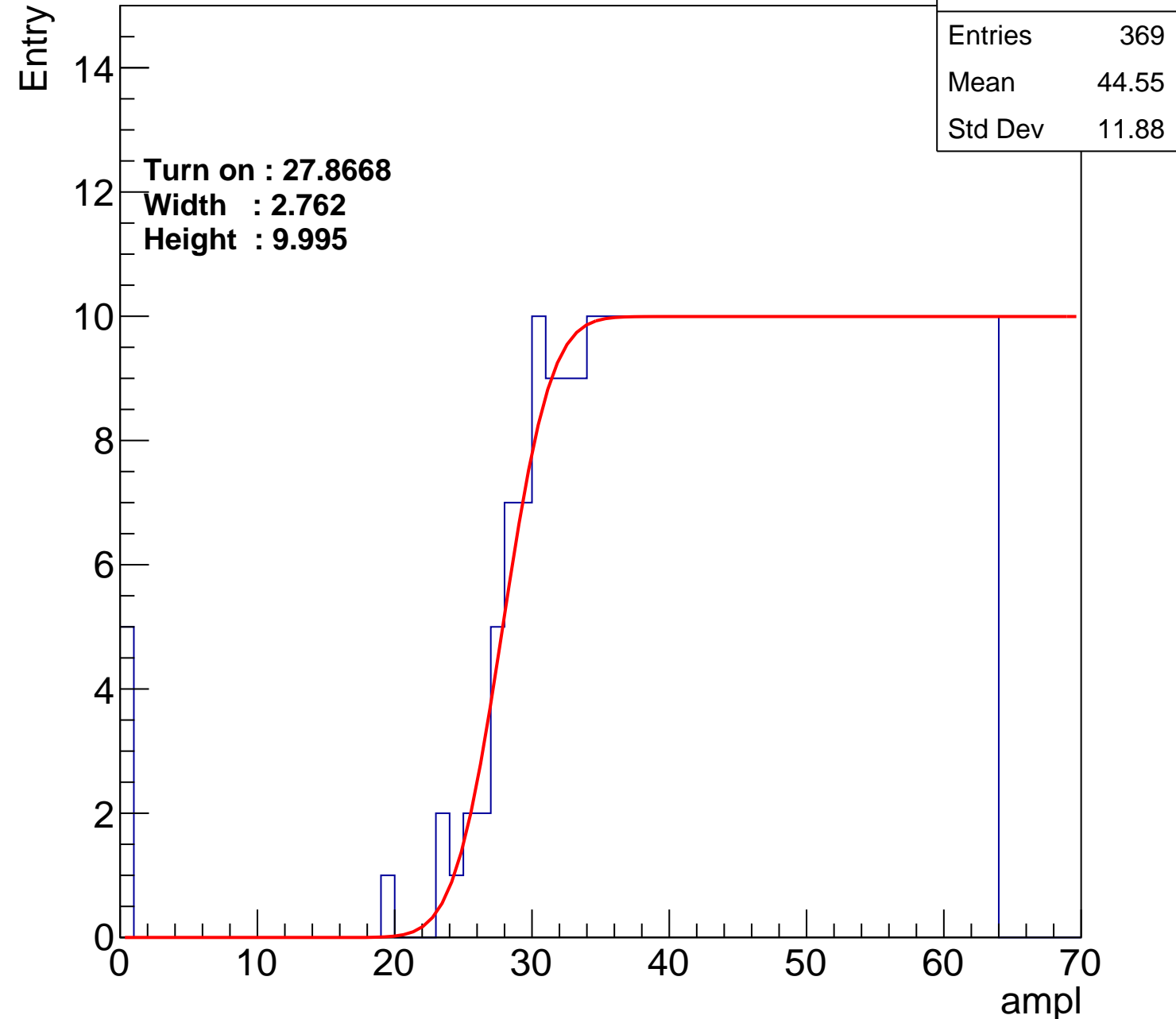
Width : 2.762

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch69

calib_packv5_042523_0143.root, FC#8, port C1

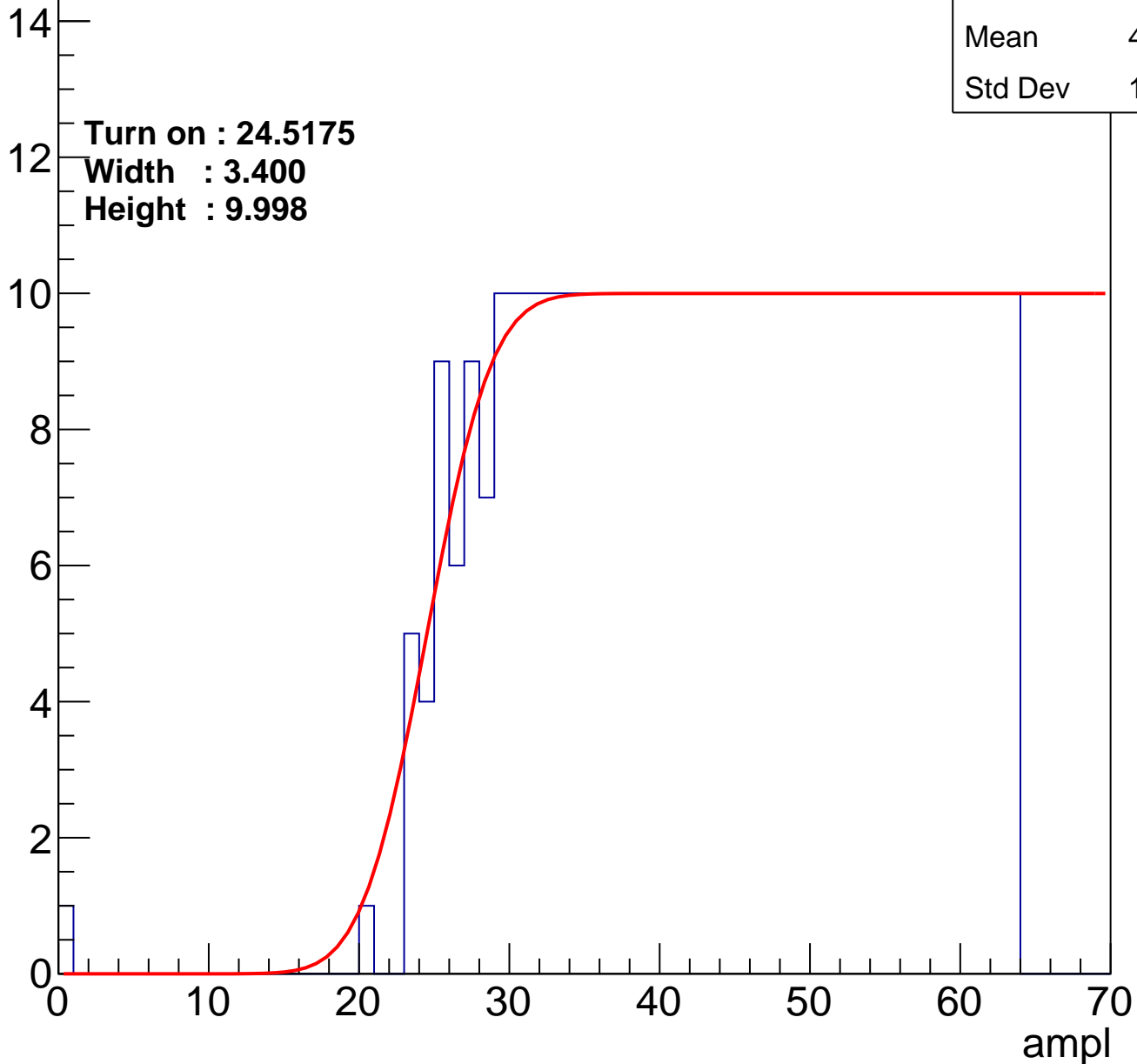
Entries	392
Mean	43.75
Std Dev	11.63

Turn on : 24.5175

Width : 3.400

Height : 9.998

Entry



B0L002S, U15-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.5
Std Dev	11.58

Turn on : 27.3708

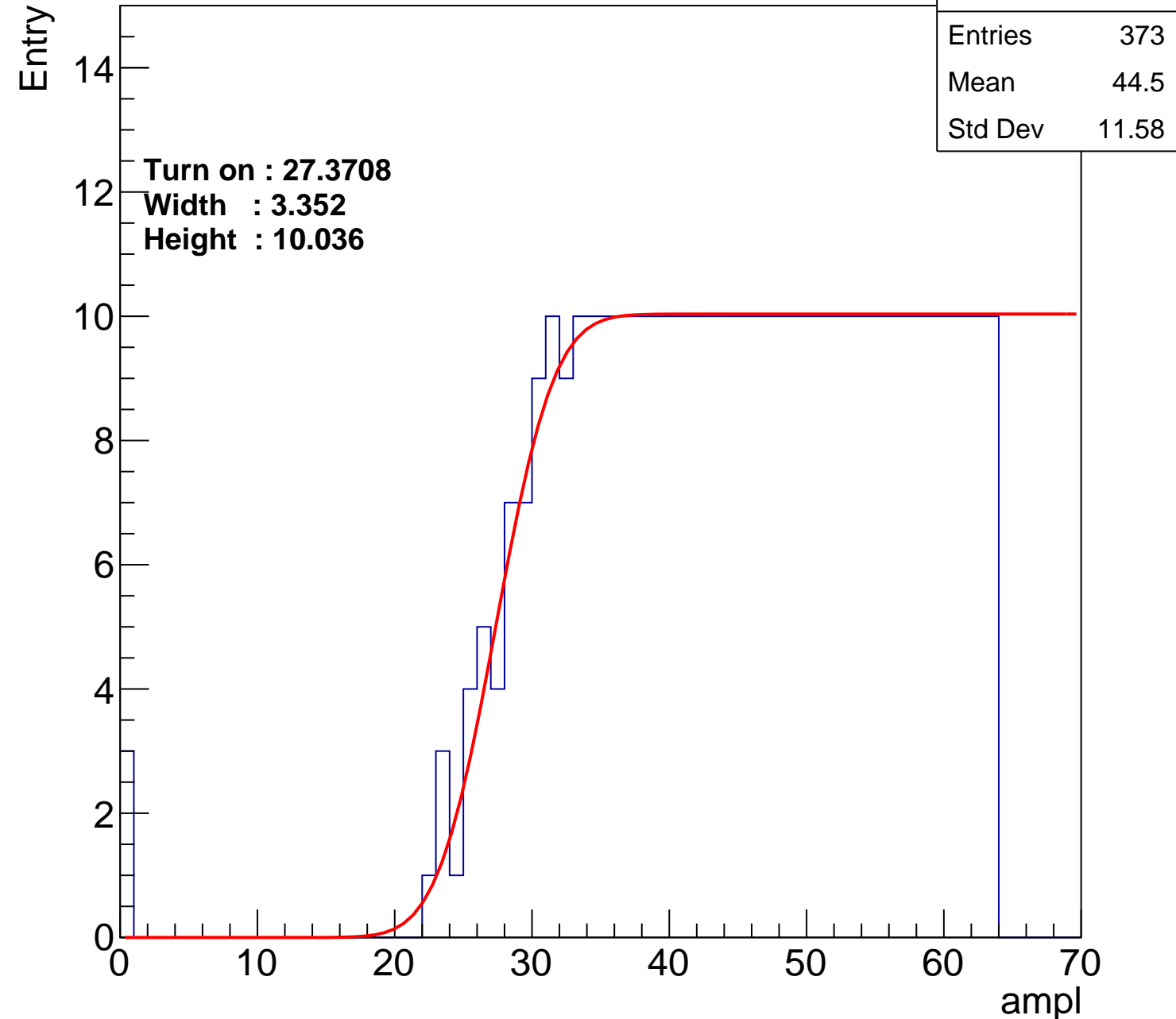
Width : 3.352

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 27.5641

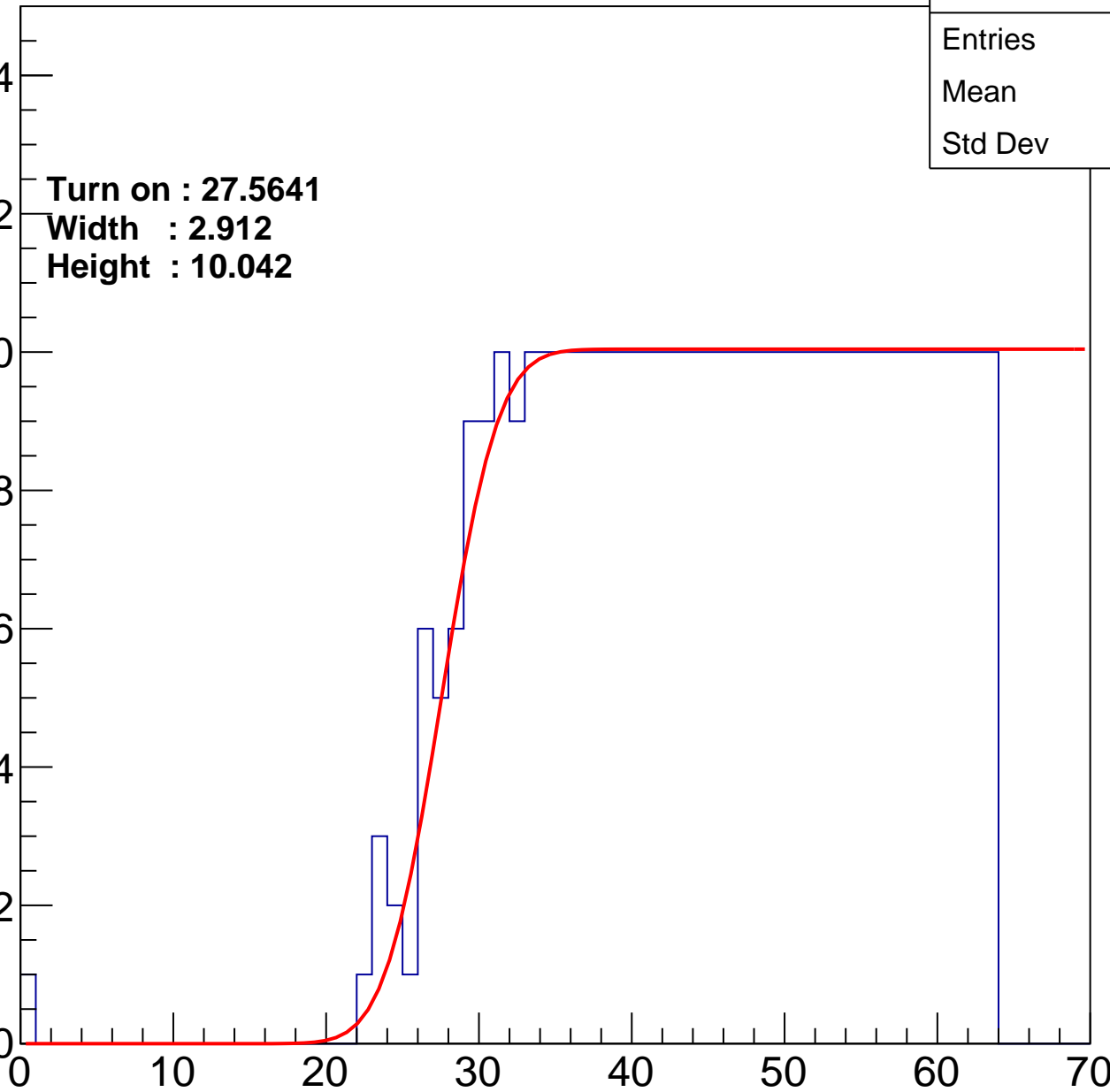
Width : 2.912

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.18
Std Dev	10.86

Turn on : 28.0018

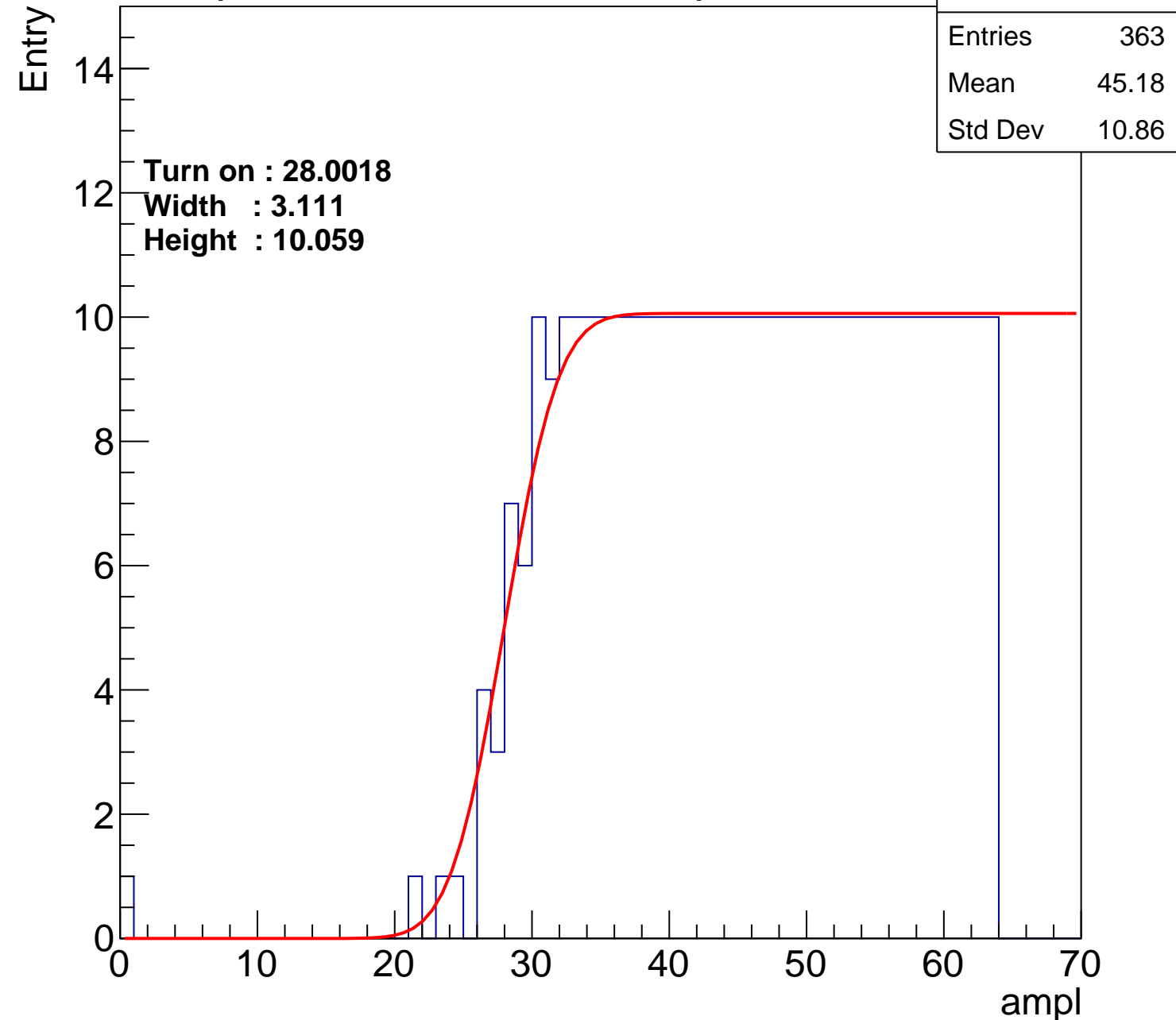
Width : 3.111

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch73

calib_packv5_042523_0143.root, FC#8, port C1

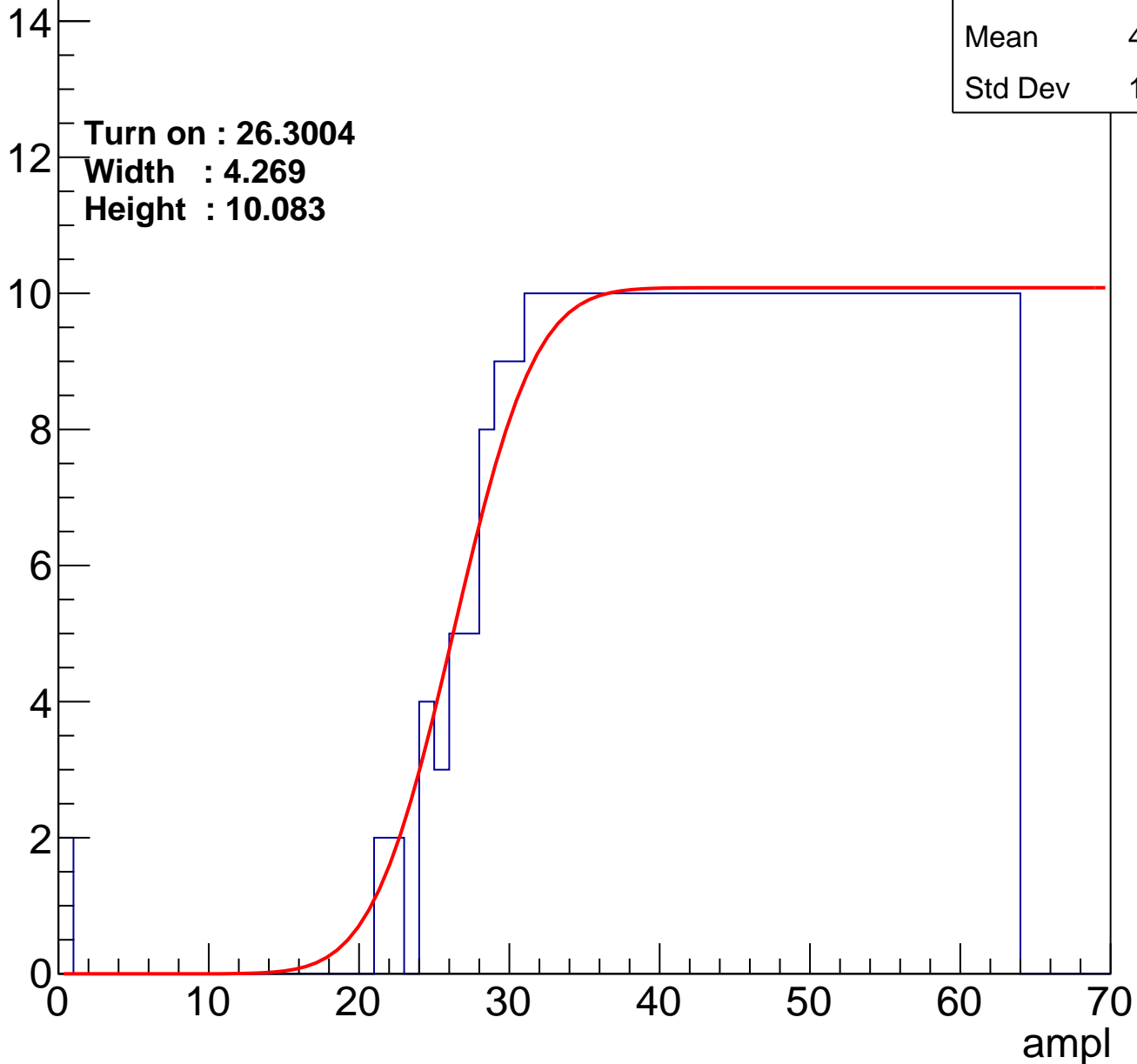
Entries	379
Mean	44.29
Std Dev	11.52

Turn on : 26.3004

Width : 4.269

Height : 10.083

Entry



B0L002S, U15-ch74

calib_packv5_042523_0143.root, FC#8, port C1

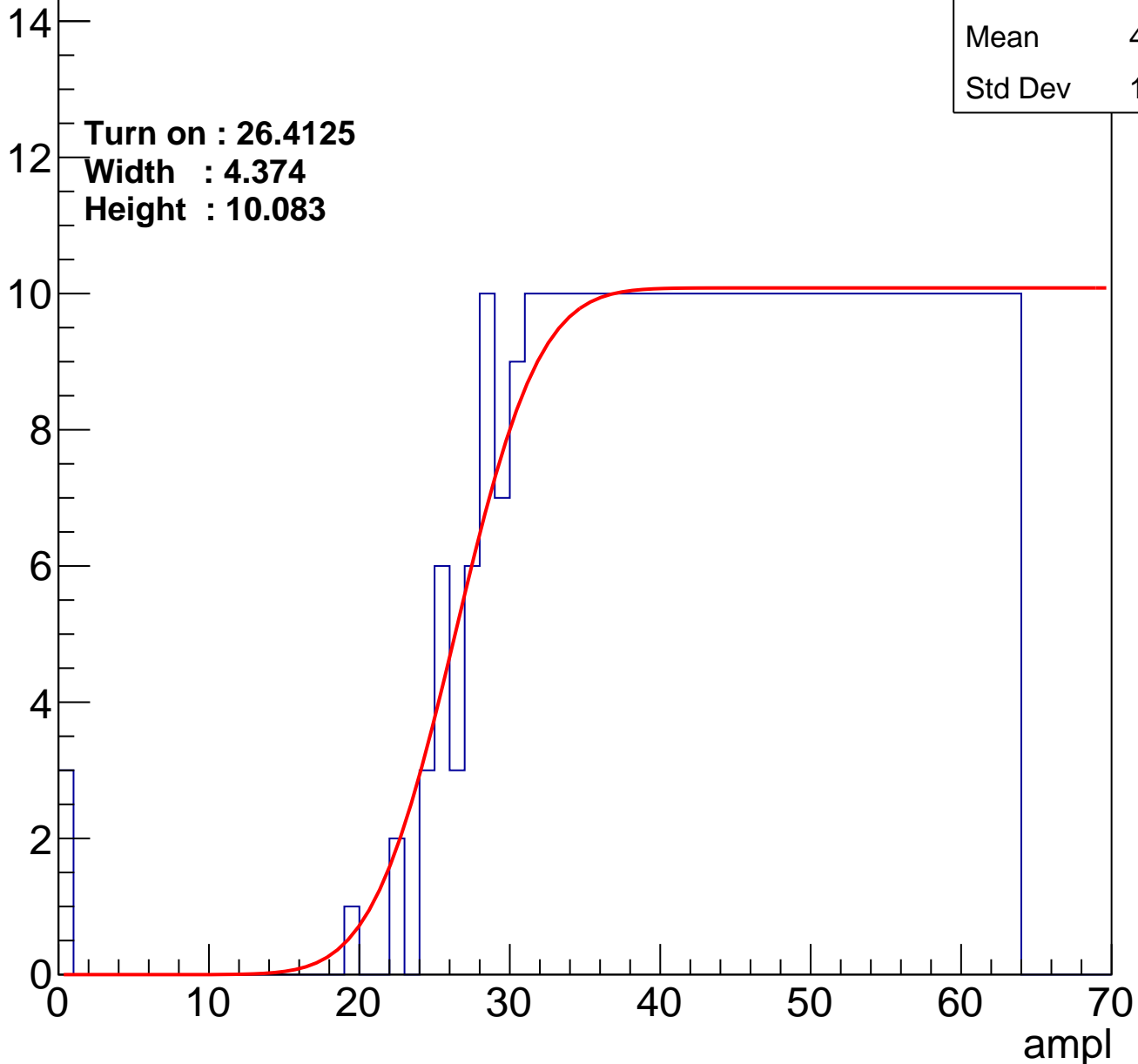
Entries	380
Mean	44.18
Std Dev	11.73

Turn on : 26.4125

Width : 4.374

Height : 10.083

Entry



B0L002S, U15-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.43
Std Dev	11.6

Turn on : 27.1808

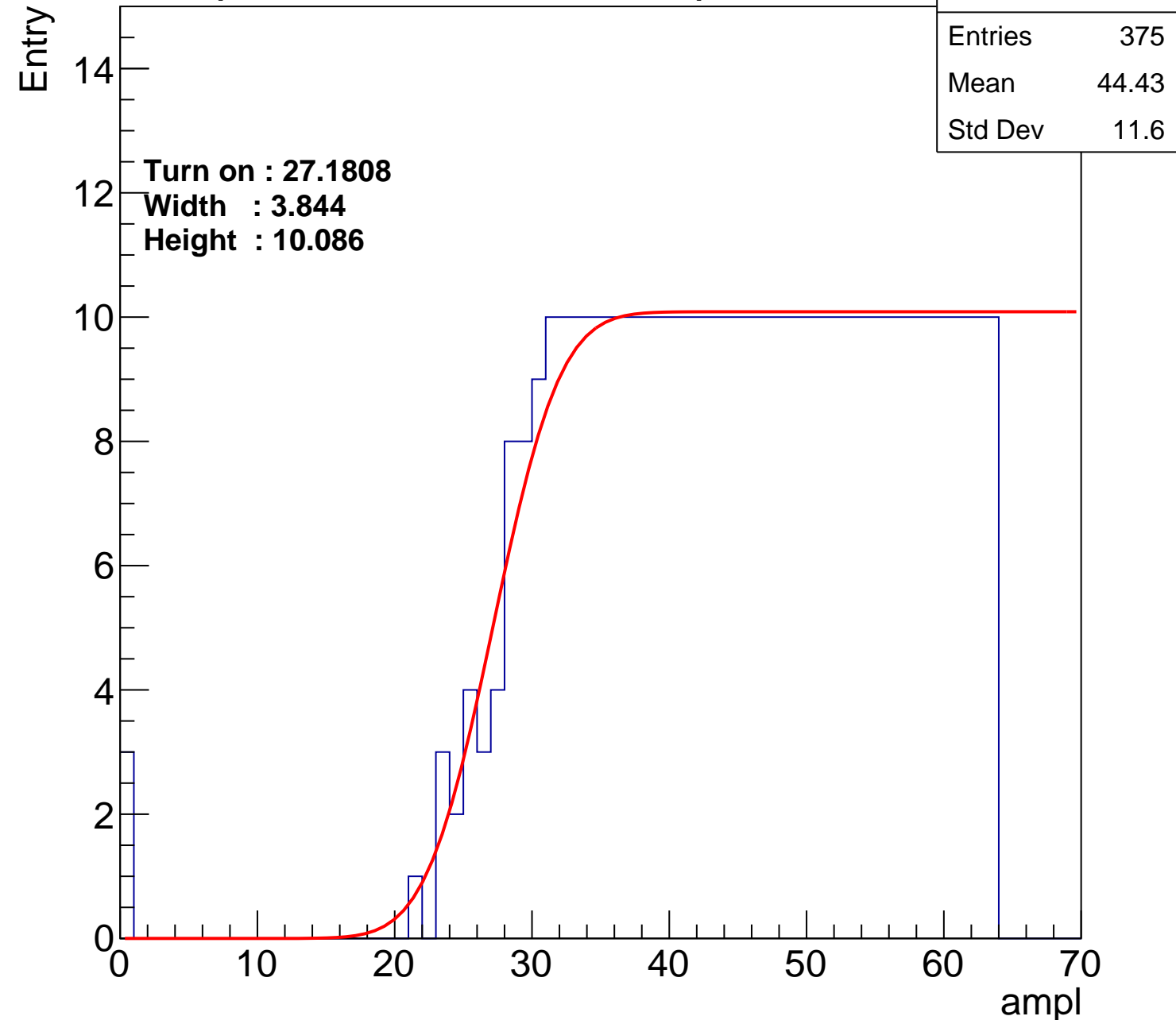
Width : 3.844

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch76

calib_packv5_042523_0143.root, FC#8, port C1

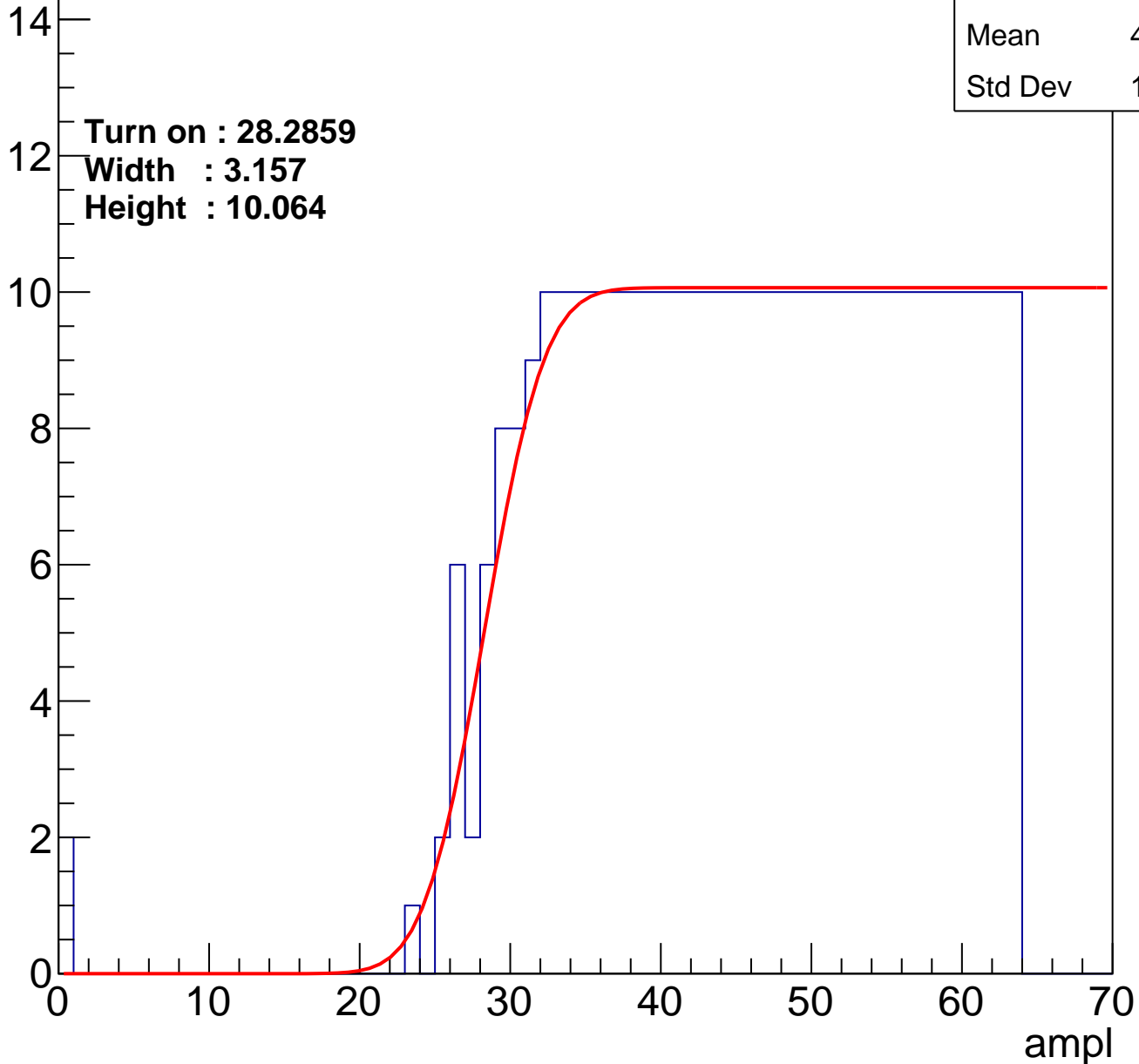
Entries	364
Mean	45.06
Std Dev	11.09

Turn on : 28.2859

Width : 3.157

Height : 10.064

Entry



B0L002S, U15-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.06
Std Dev	11.19

Turn on : 28.3937

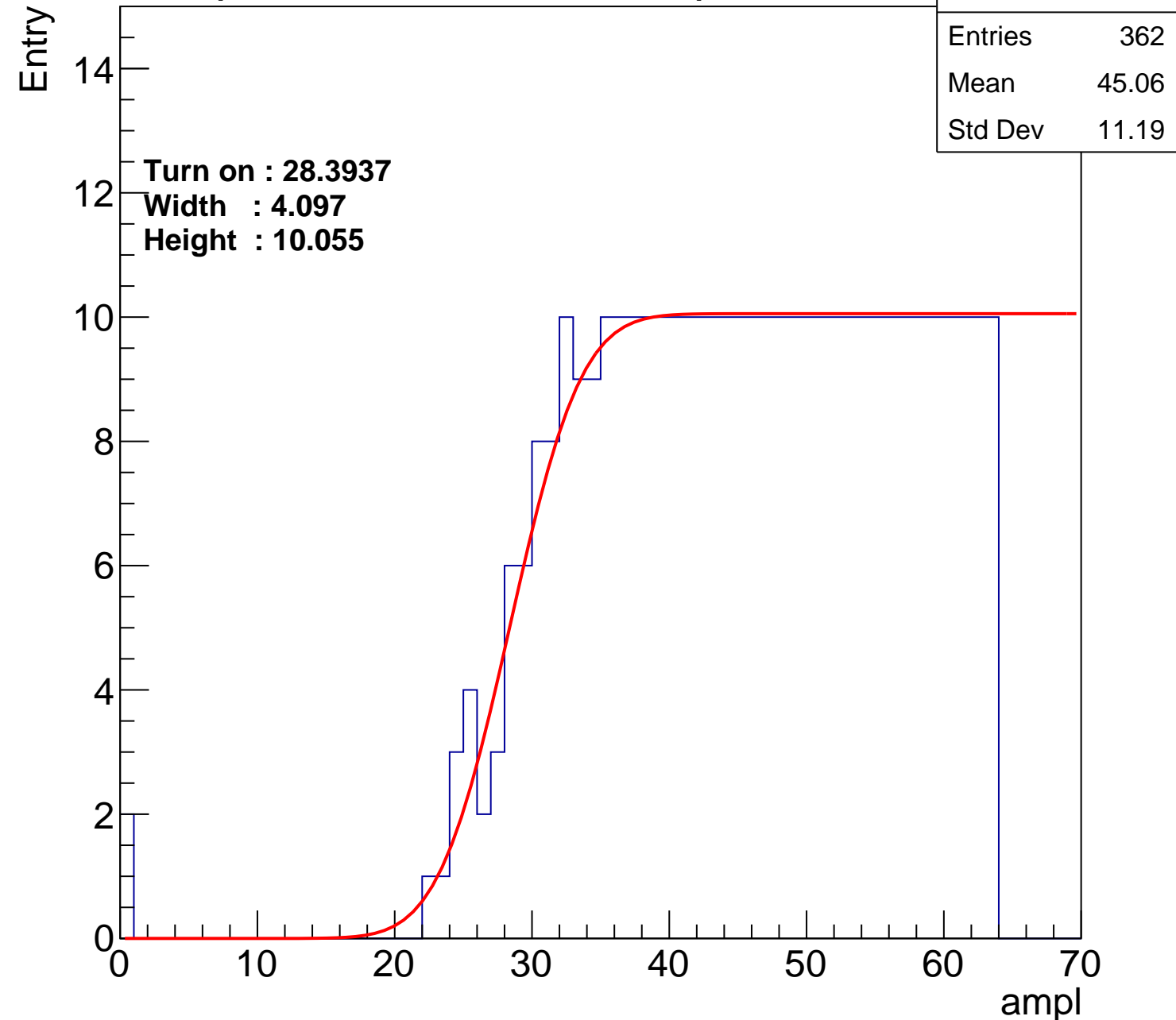
Width : 4.097

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch78

calib_packv5_042523_0143.root, FC#8, port C1

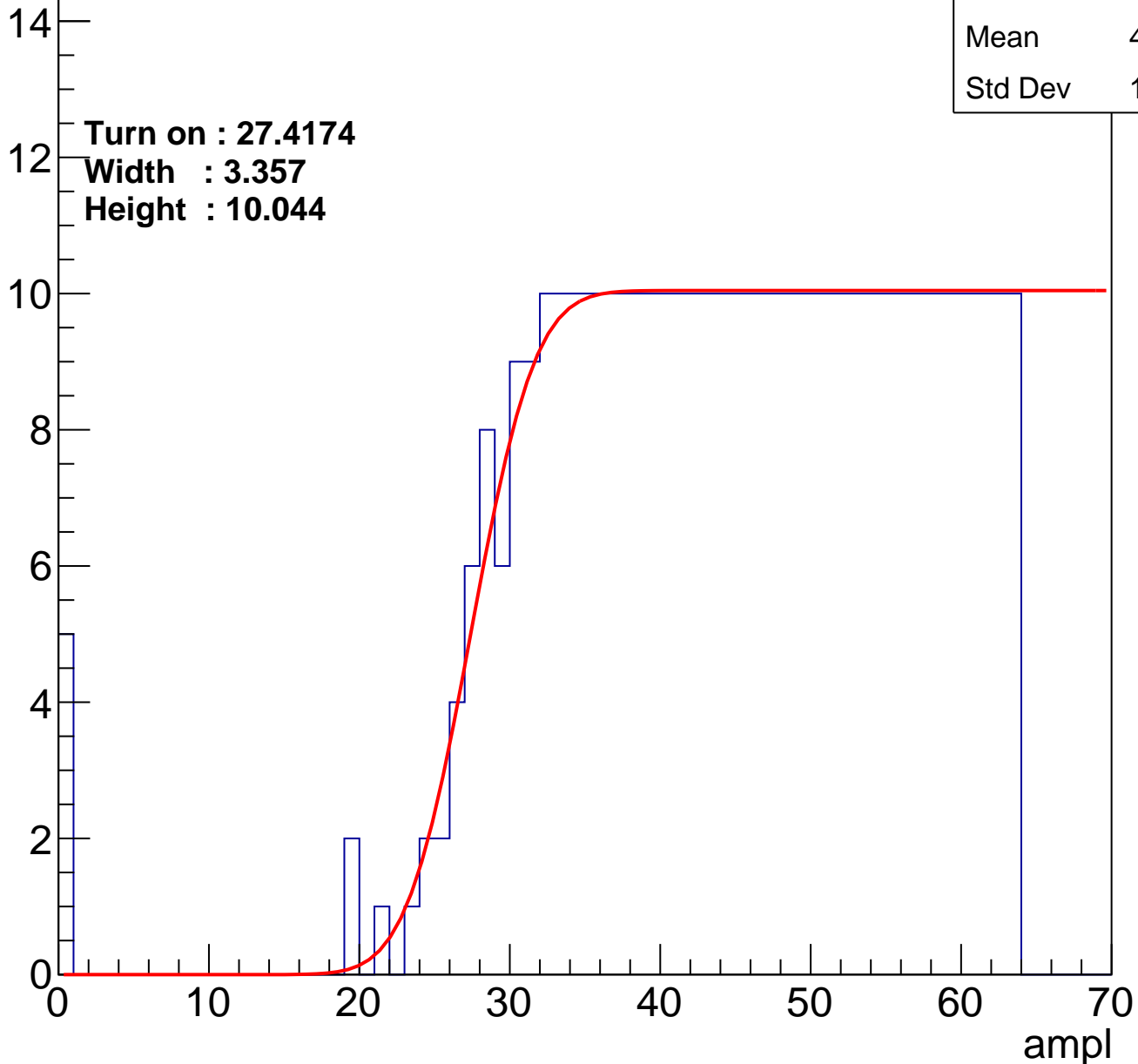
Entries	375
Mean	44.25
Std Dev	12.03

Turn on : 27.4174

Width : 3.357

Height : 10.044

Entry



B0L002S, U15-ch79

calib_packv5_042523_0143.root, FC#8, port C1

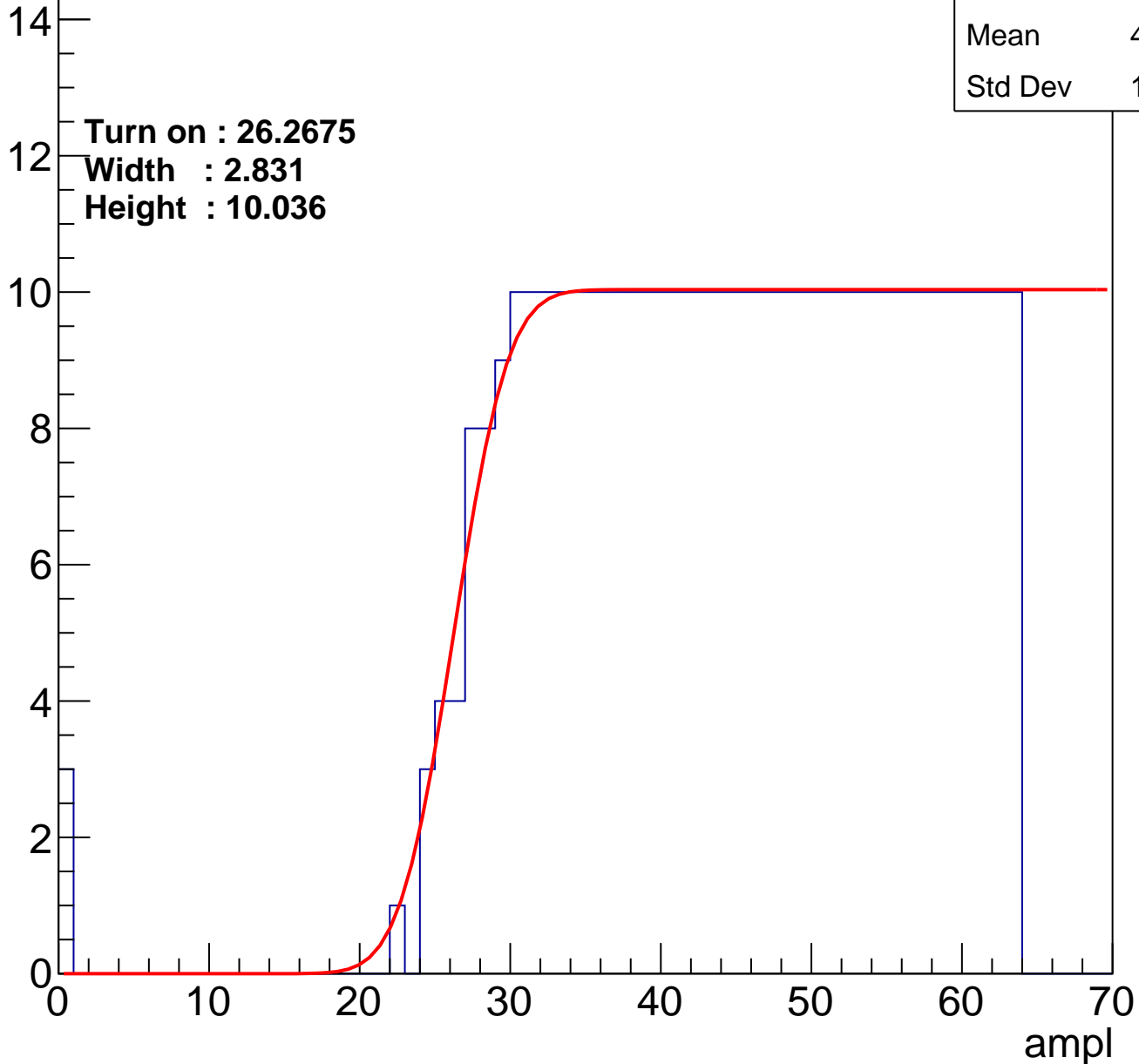
Entries	380
Mean	44.23
Std Dev	11.64

Turn on : 26.2675

Width : 2.831

Height : 10.036

Entry



B0L002S, U15-ch80

calib_packv5_042523_0143.root, FC#8, port C1

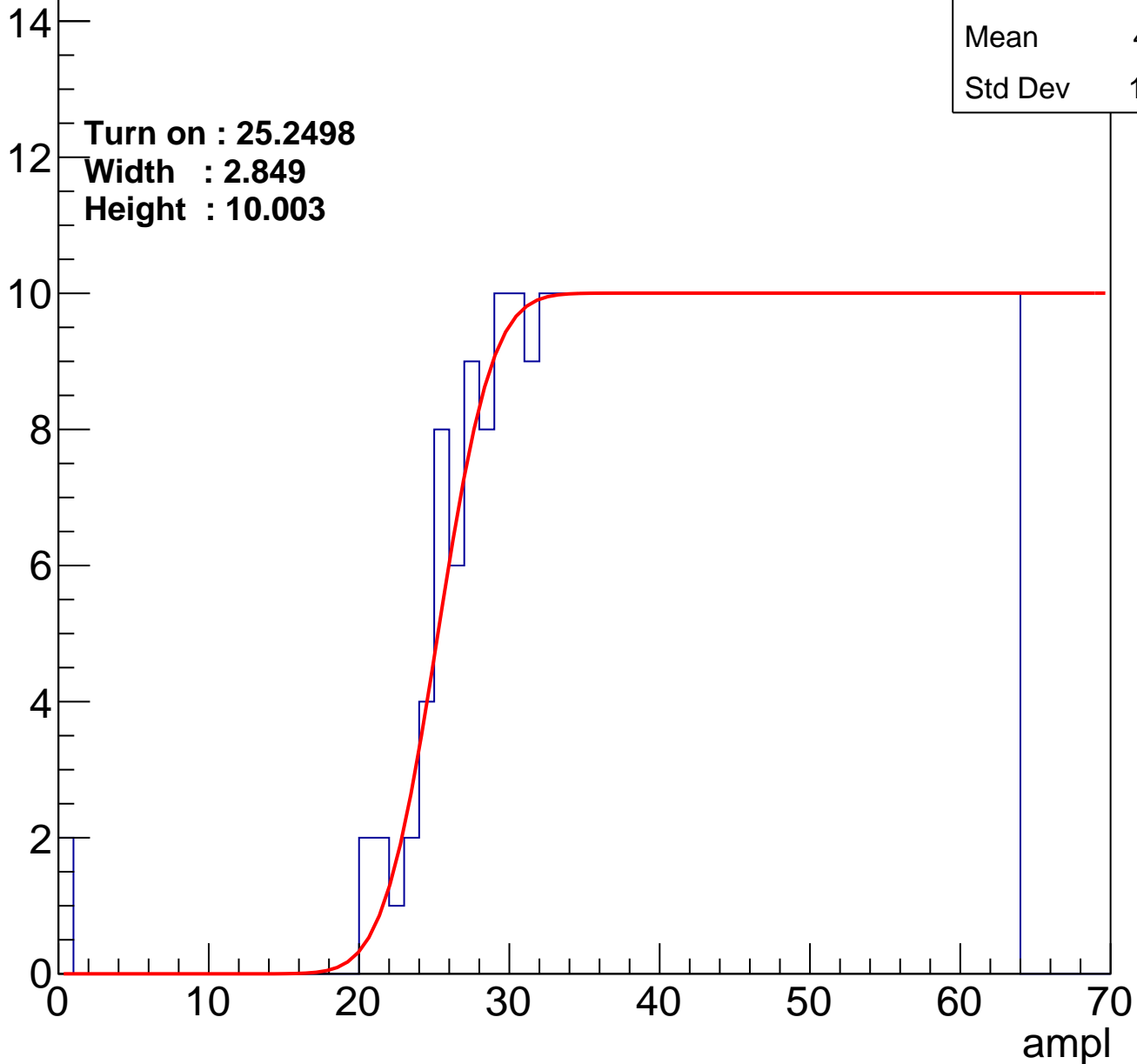
Entries	393
Mean	43.61
Std Dev	11.87

Turn on : 25.2498

Width : 2.849

Height : 10.003

Entry



B0L002S, U15-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.09
Std Dev	11.63

Turn on : 26.1145

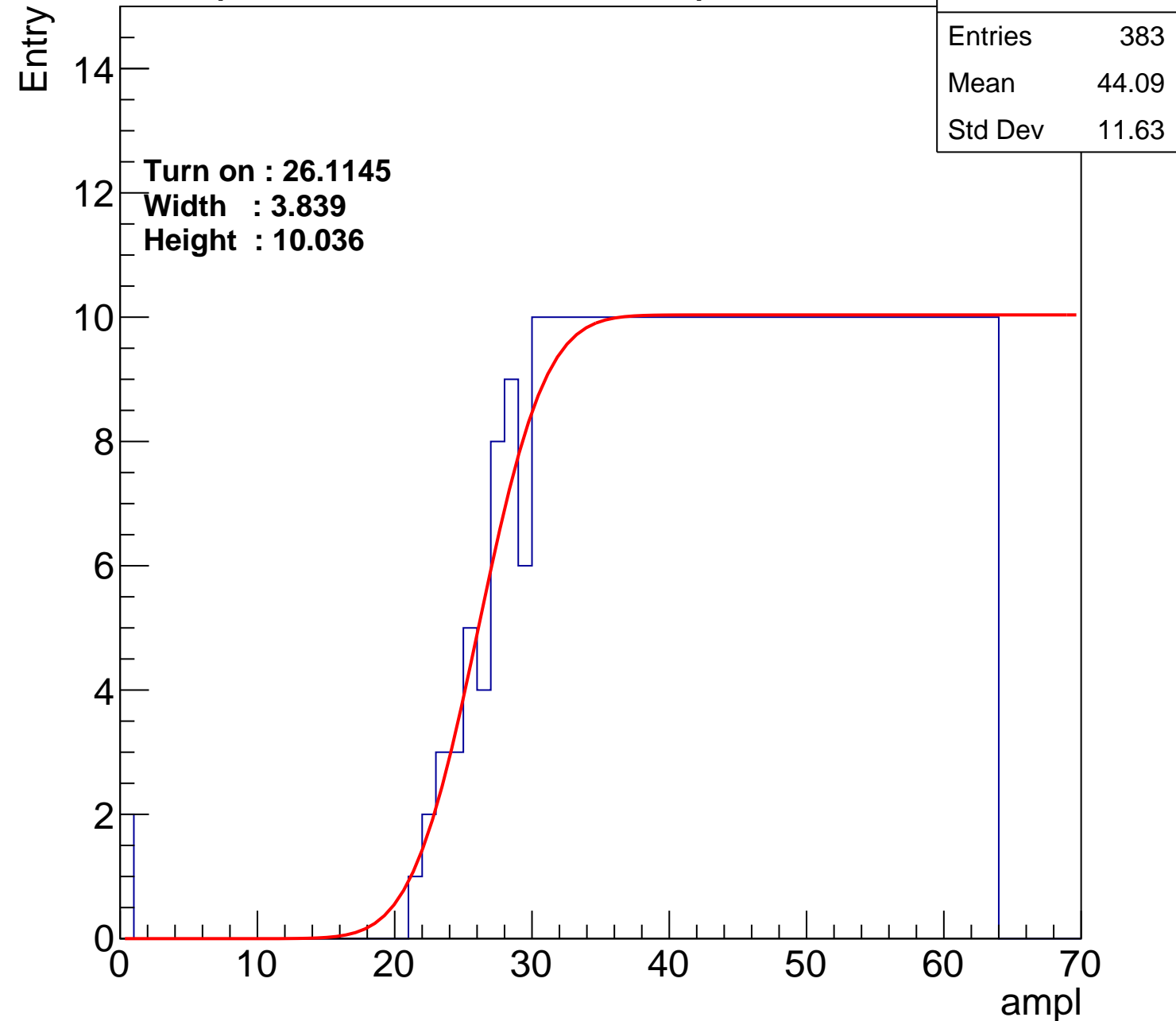
Width : 3.839

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.11
Std Dev	11.61

Turn on : 29.3527

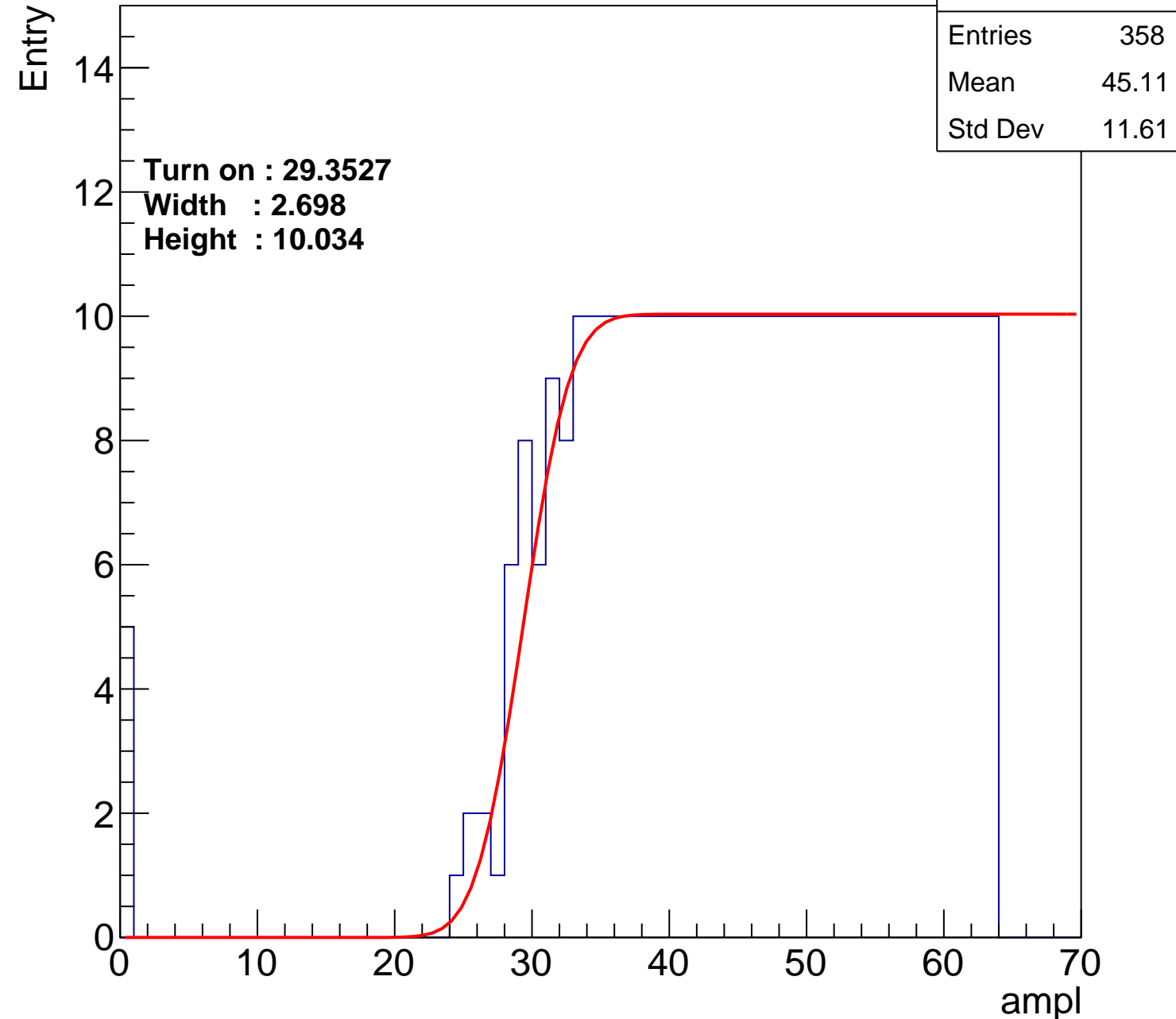
Width : 2.698

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 28.0429

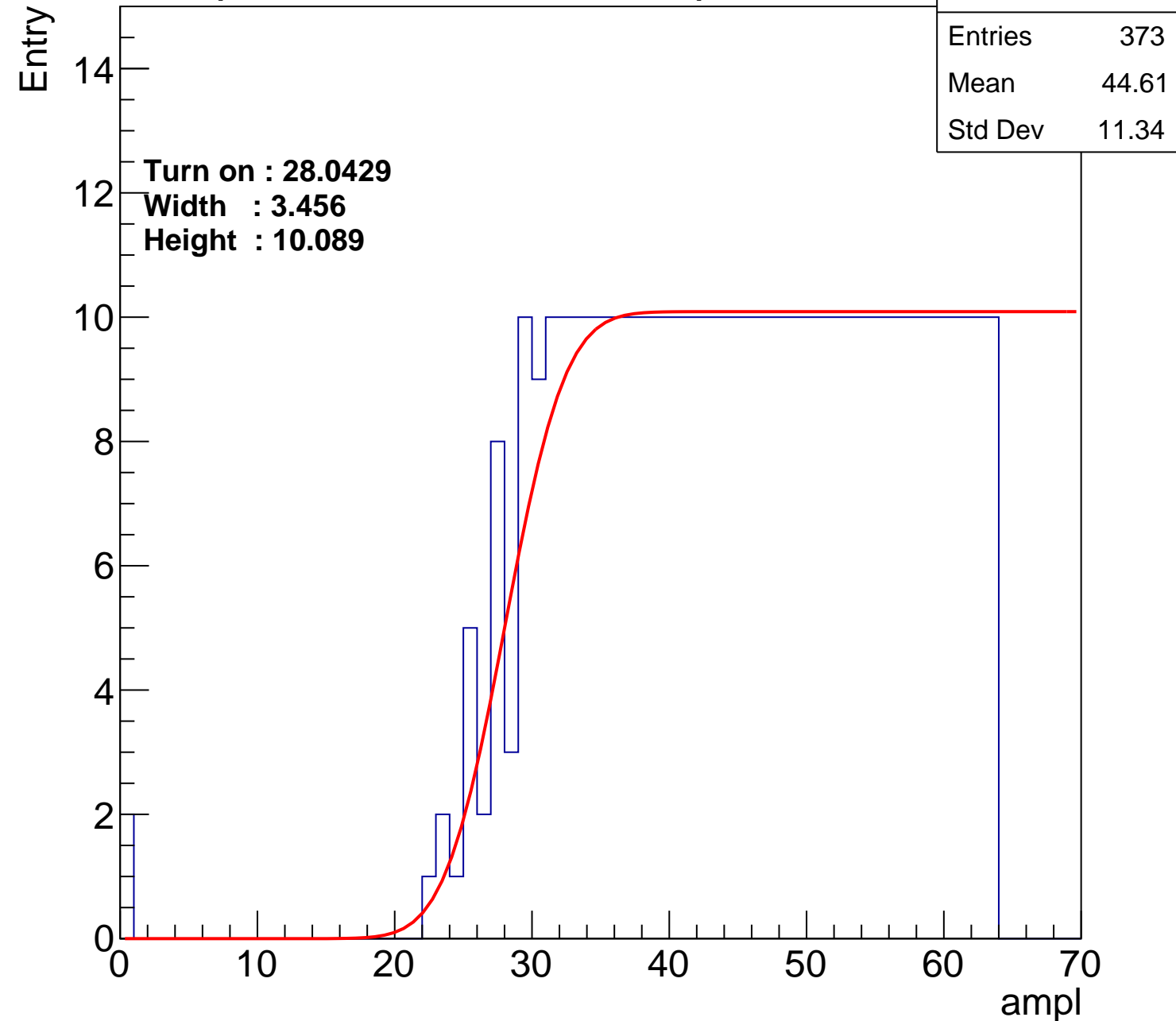
Width : 3.456

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	398
Mean	43.4
Std Dev	11.94

Turn on : 24.4068

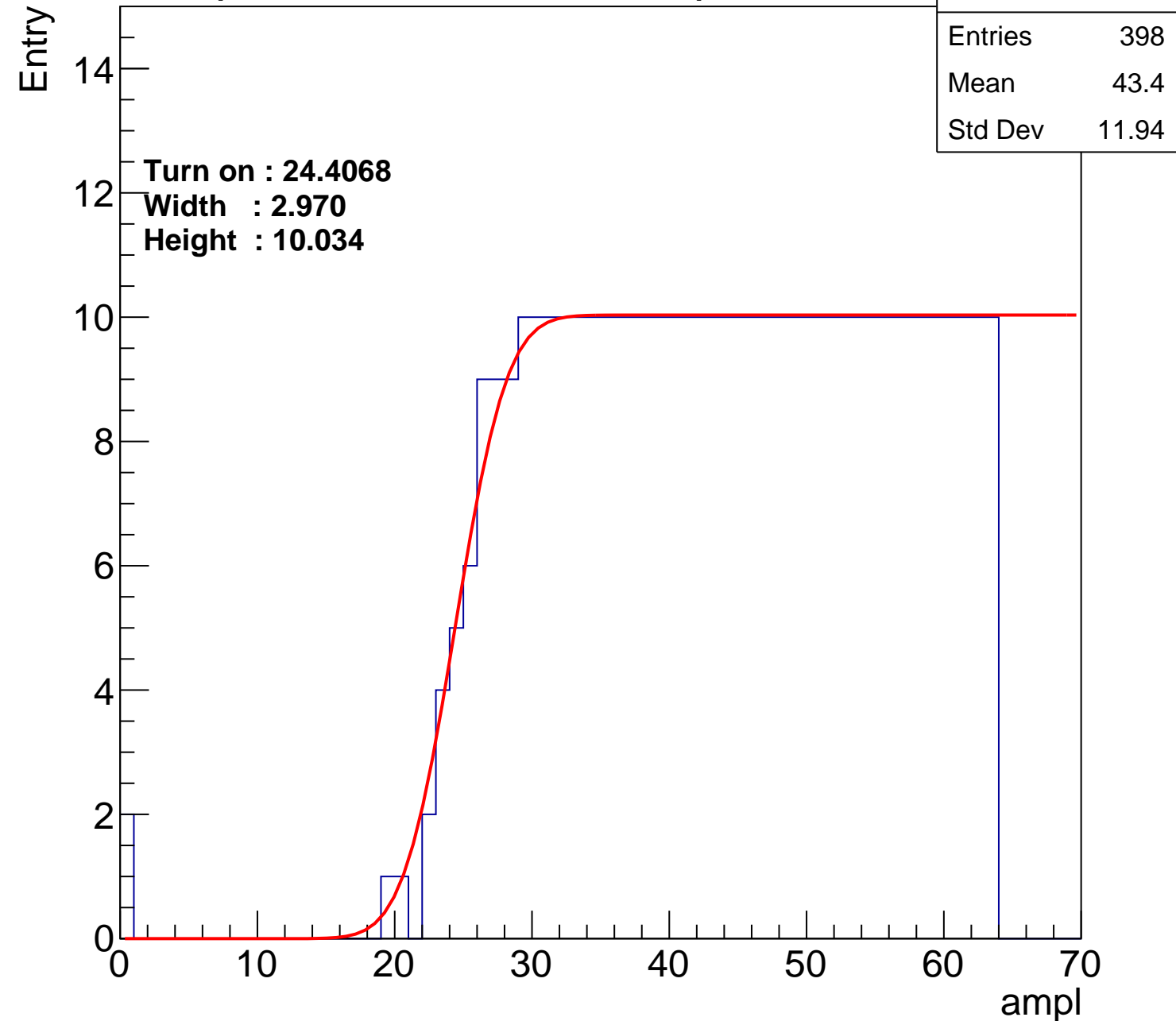
Width : 2.970

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 25.3359
Width : 3.066
Height : 9.966



B0L002S, U15-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.6059

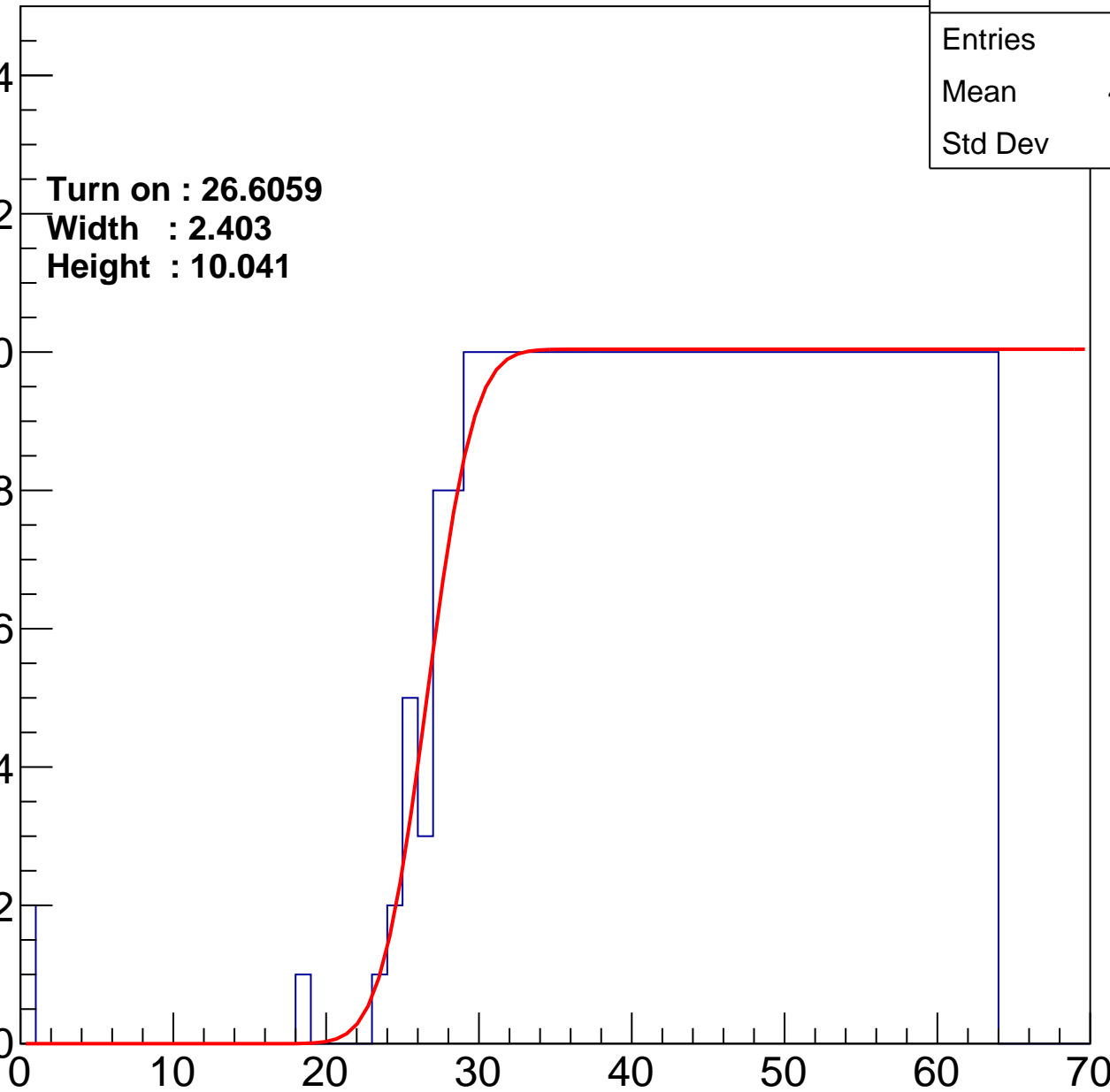
Width : 2.403

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.47
Std Dev	11.9

Turn on : 27.4846

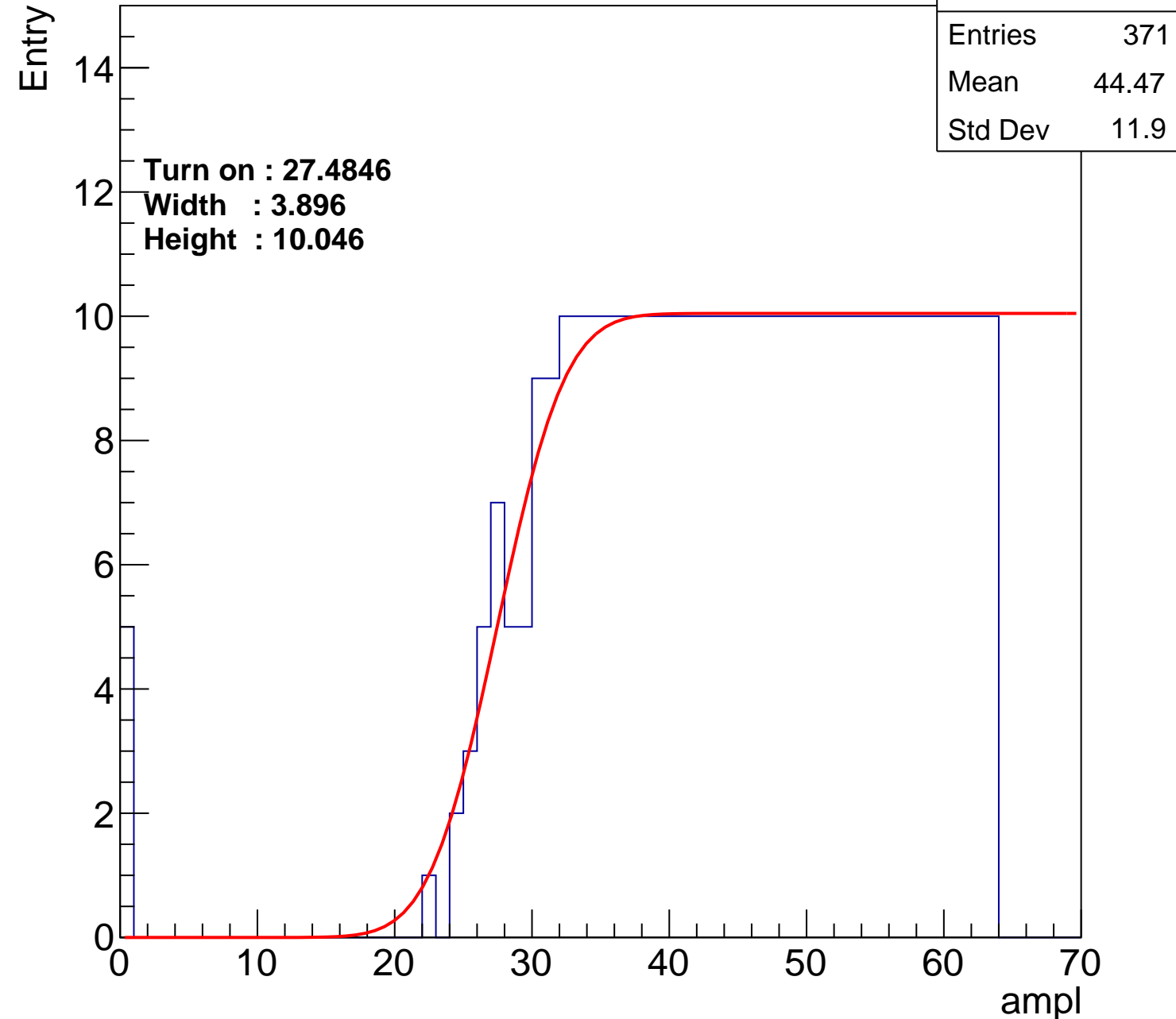
Width : 3.896

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch88

calib_packv5_042523_0143.root, FC#8, port C1

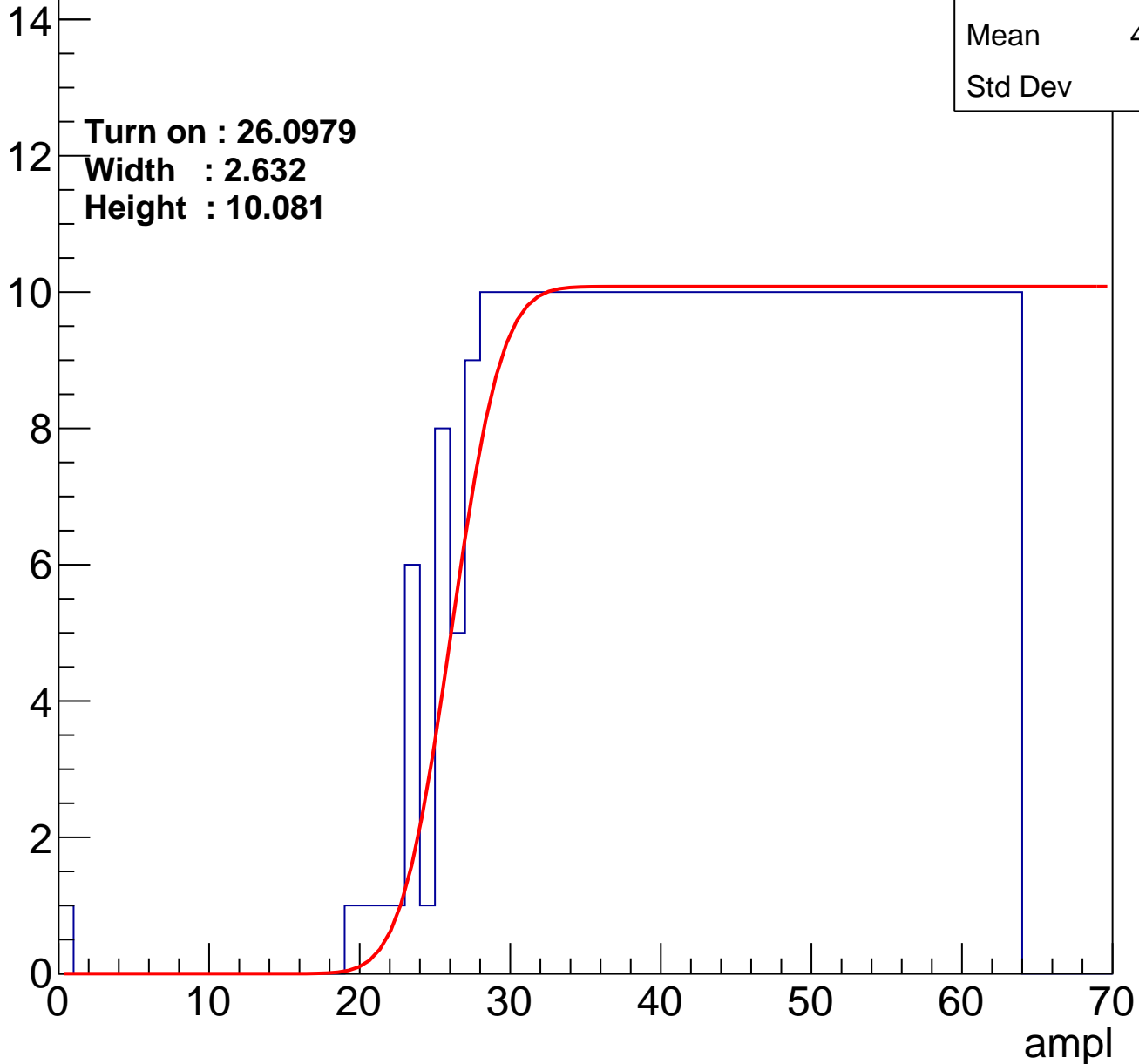
Entries	394
Mean	43.65
Std Dev	11.7

Turn on : 26.0979

Width : 2.632

Height : 10.081

Entry



B0L002S, U15-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.77
Std Dev	12.06

Turn on : 26.0561

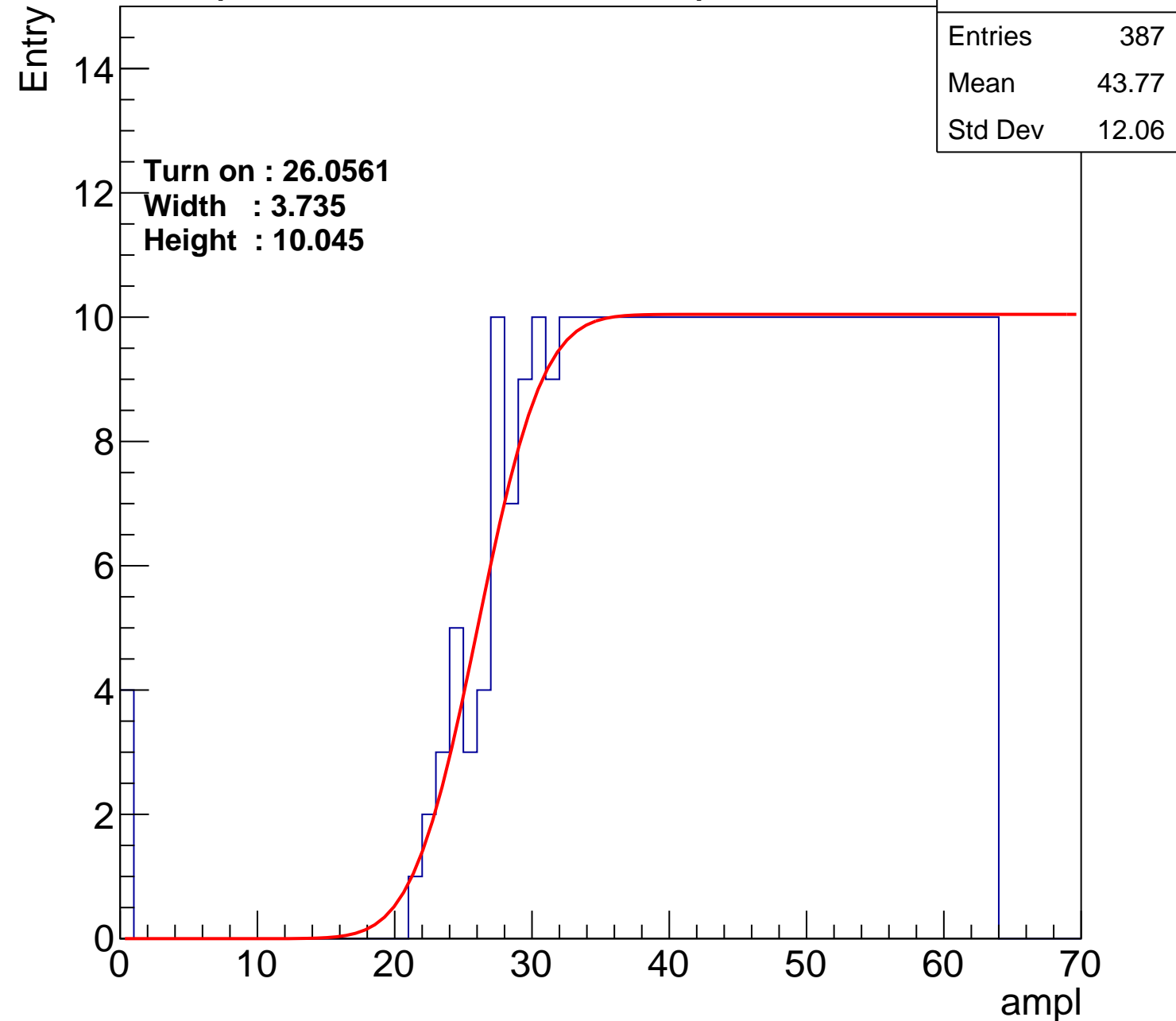
Width : 3.735

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch90

calib_packv5_042523_0143.root, FC#8, port C1

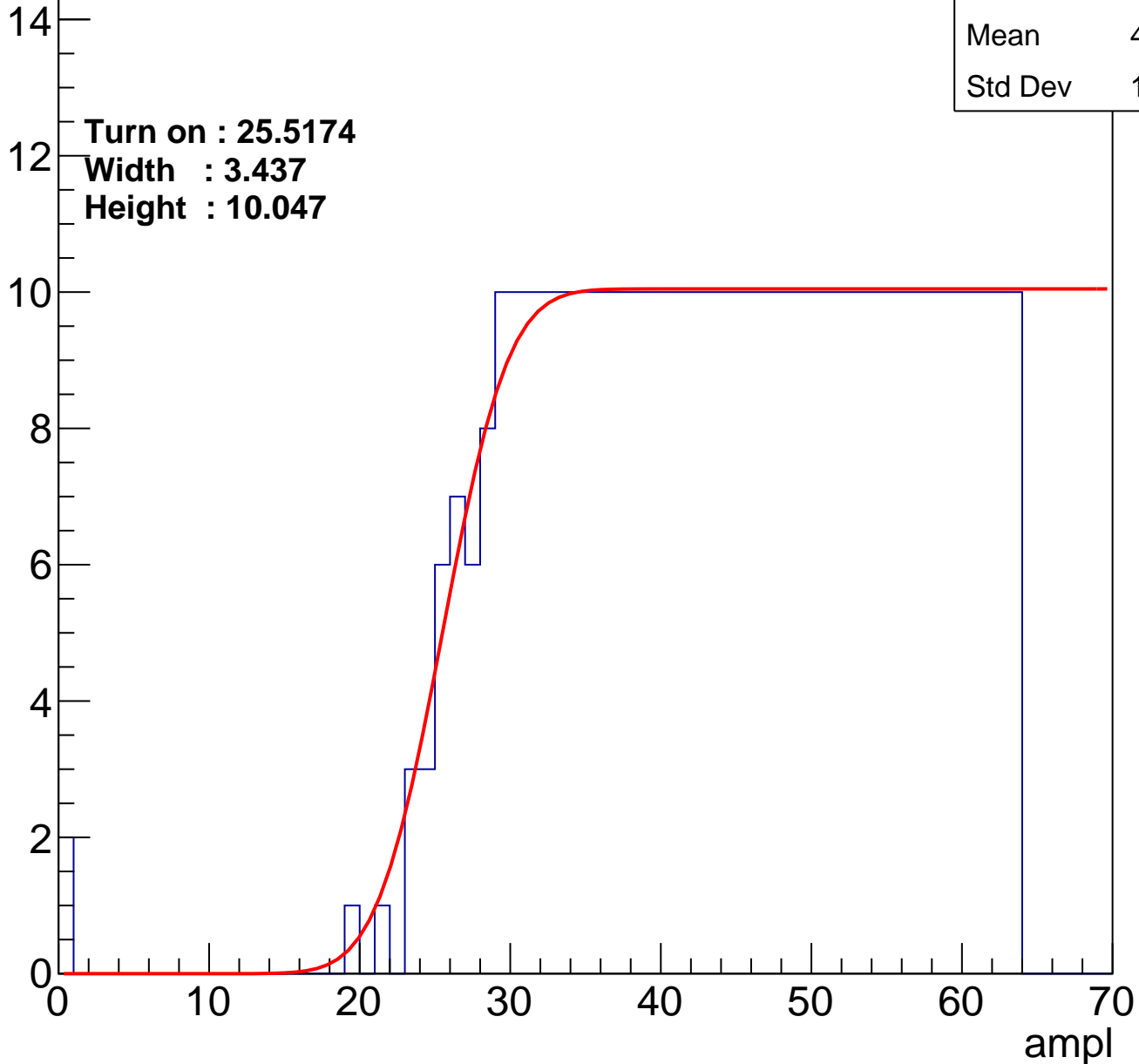
Entries	387
Mean	43.93
Std Dev	11.69

Turn on : 25.5174

Width : 3.437

Height : 10.047

Entry



B0L002S, U15-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.4747

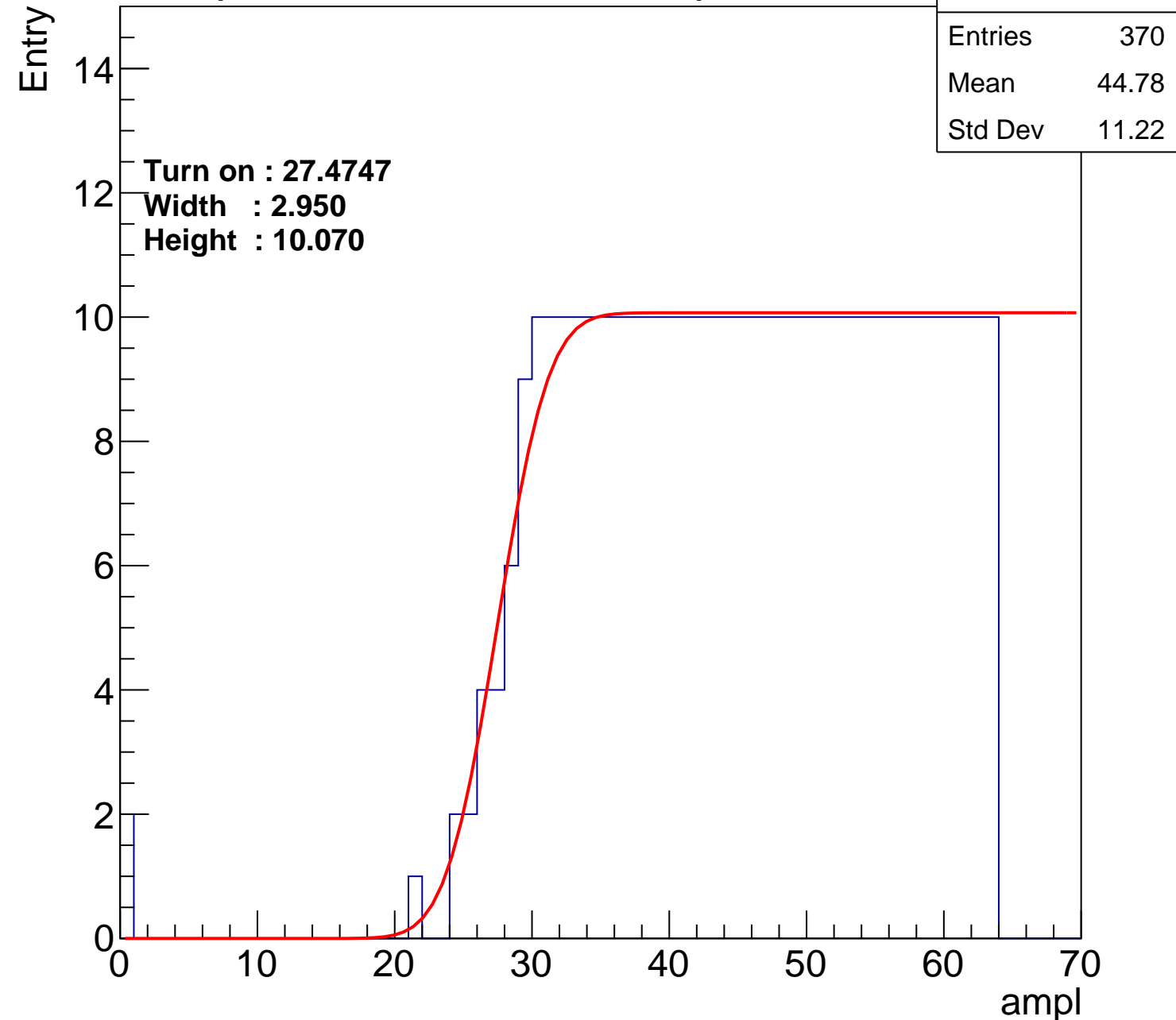
Width : 2.950

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch92

calib_packv5_042523_0143.root, FC#8, port C1

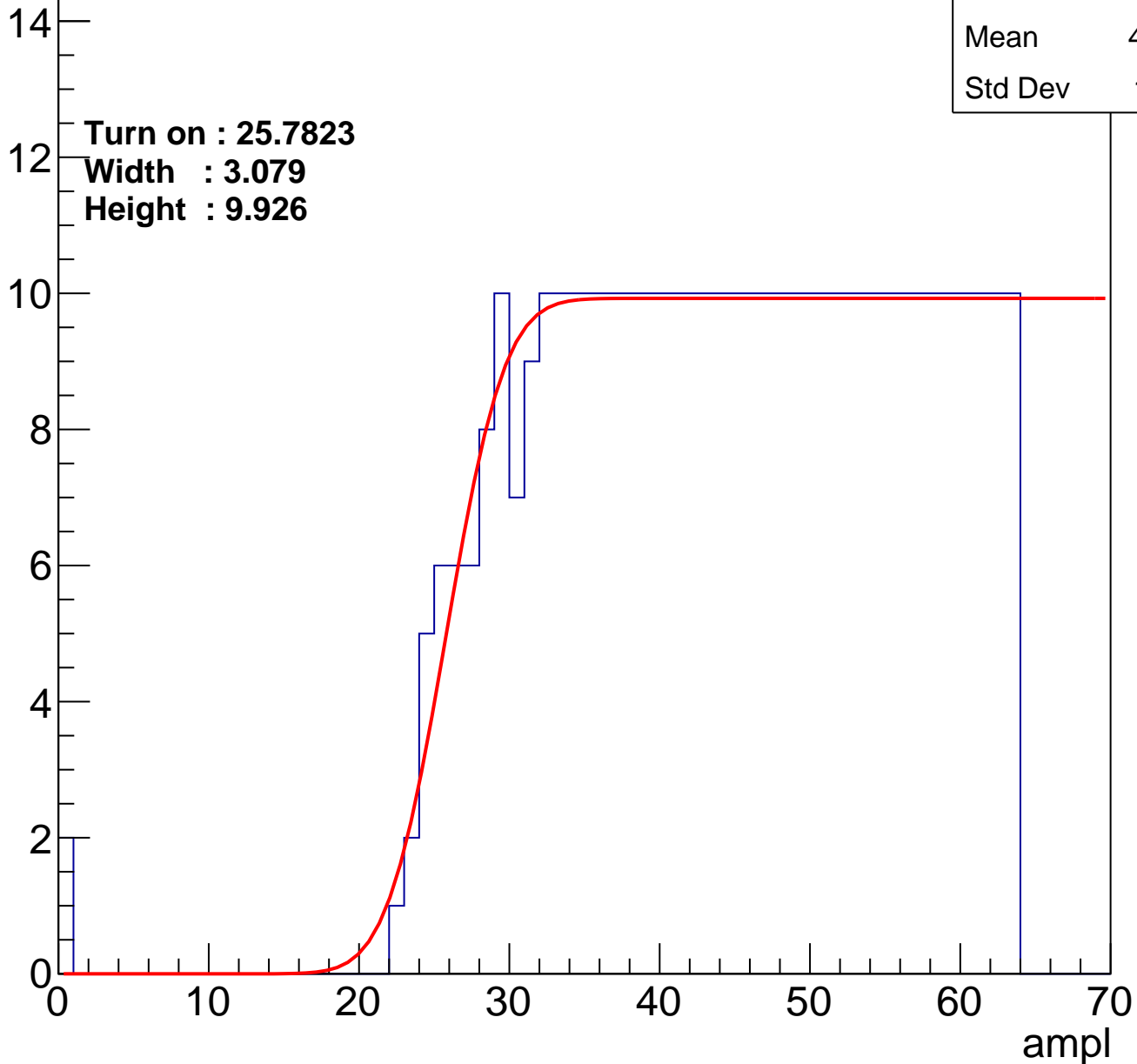
Entries	382
Mean	44.13
Std Dev	11.61

Turn on : 25.7823

Width : 3.079

Height : 9.926

Entry



B0L002S, U15-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	393
Mean	43.72
Std Dev	11.63

Turn on : 24.8464

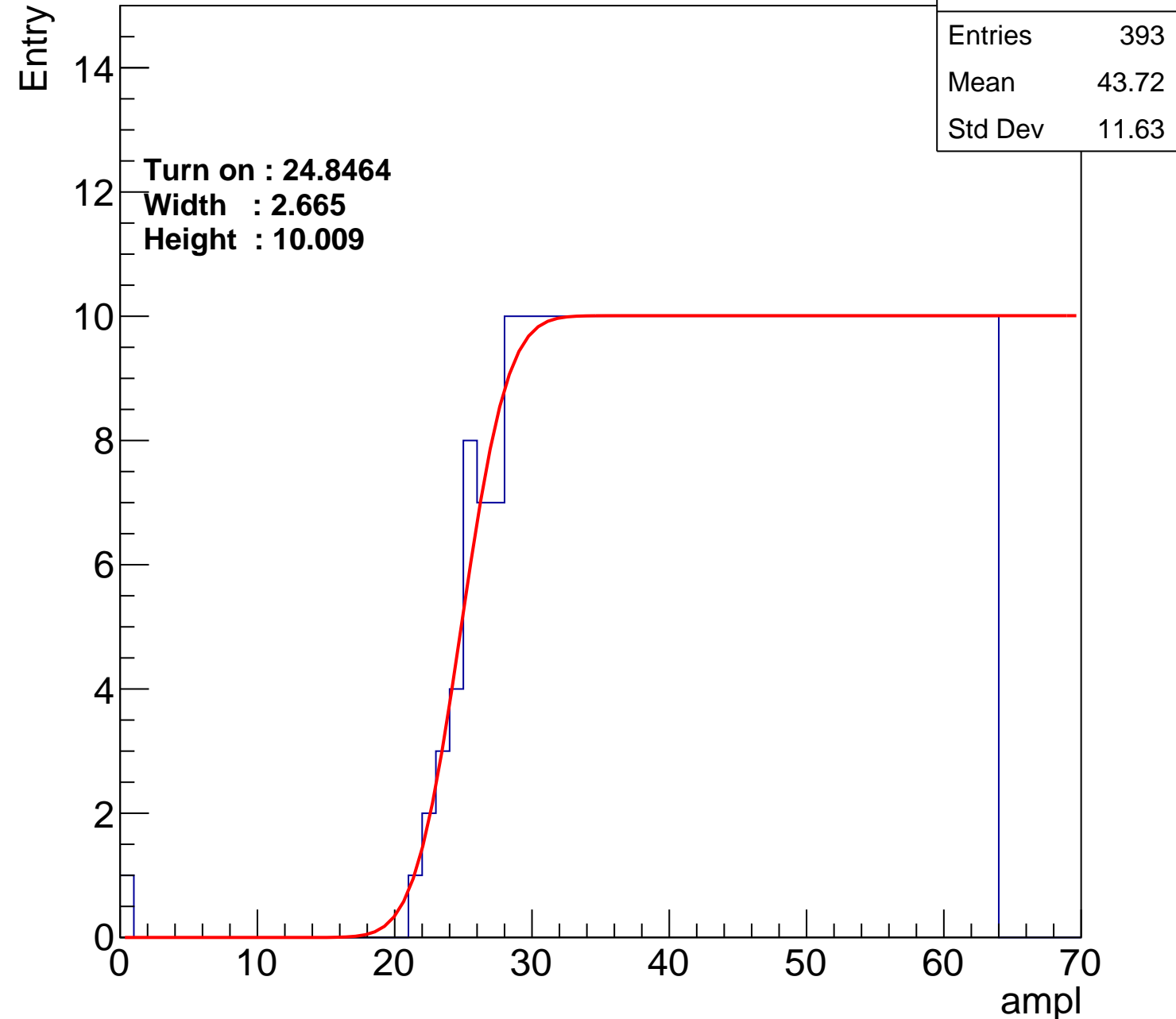
Width : 2.665

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch94

calib_packv5_042523_0143.root, FC#8, port C1

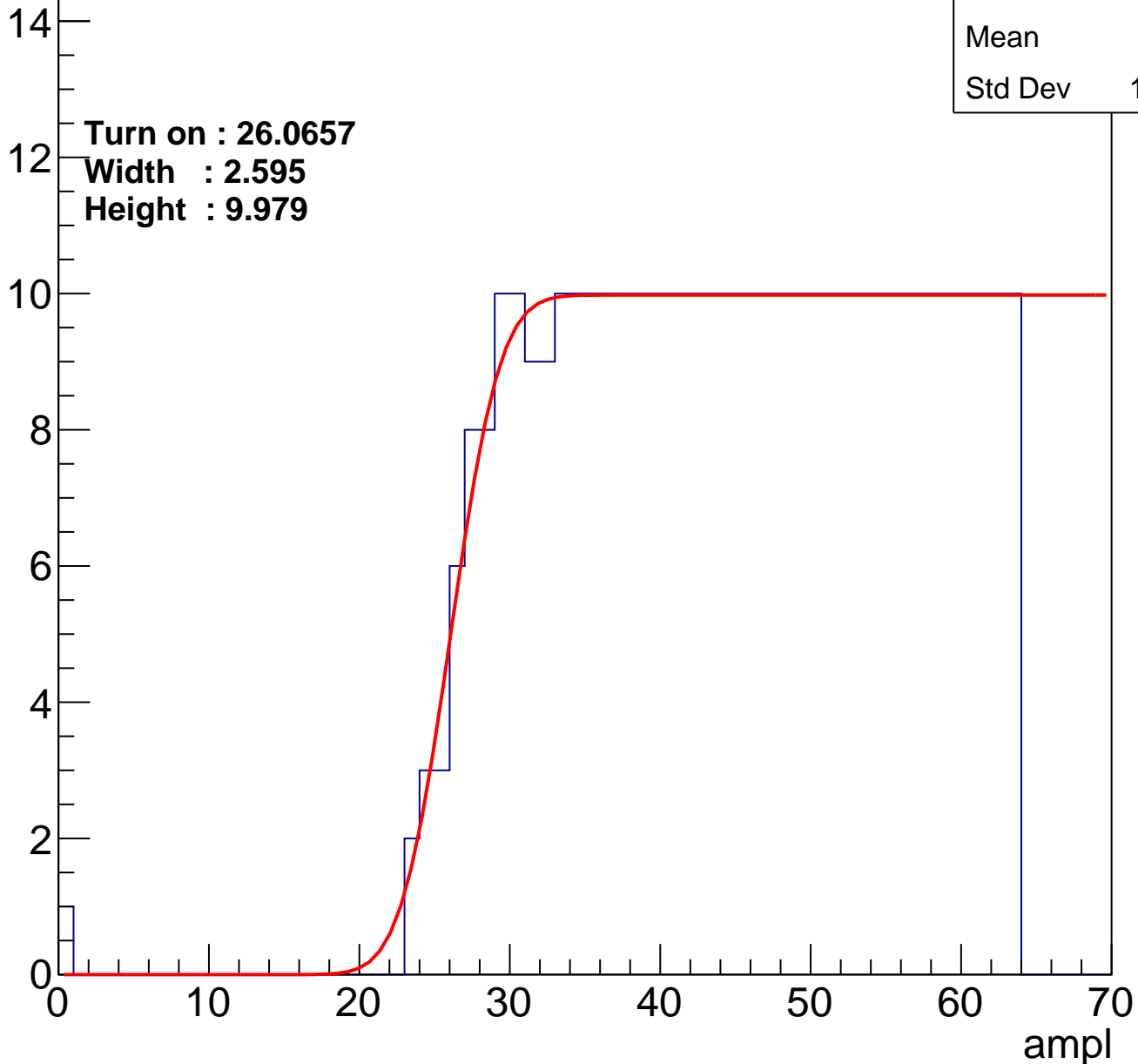
Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.0657

Width : 2.595

Height : 9.979

Entry



B0L002S, U15-ch95

calib_packv5_042523_0143.root, FC#8, port C1

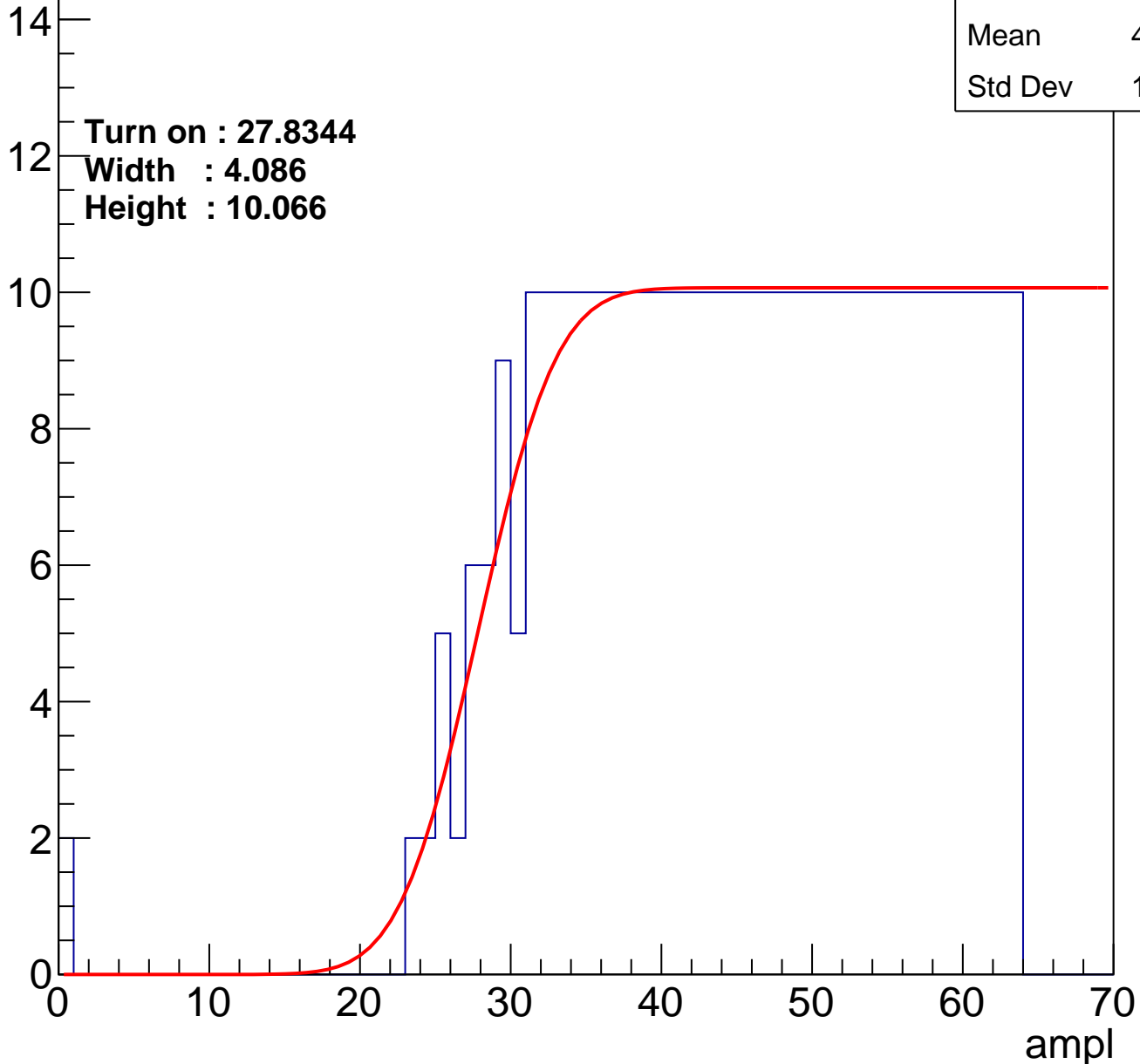
Entries	369
Mean	44.78
Std Dev	11.28

Turn on : 27.8344

Width : 4.086

Height : 10.066

Entry



calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.4
Std Dev	11.4

Mean	44.4
------	------

Std Dev	11.4
---------	------

Width : 2.899

Height : 10.060



B0L002S, U15-ch97

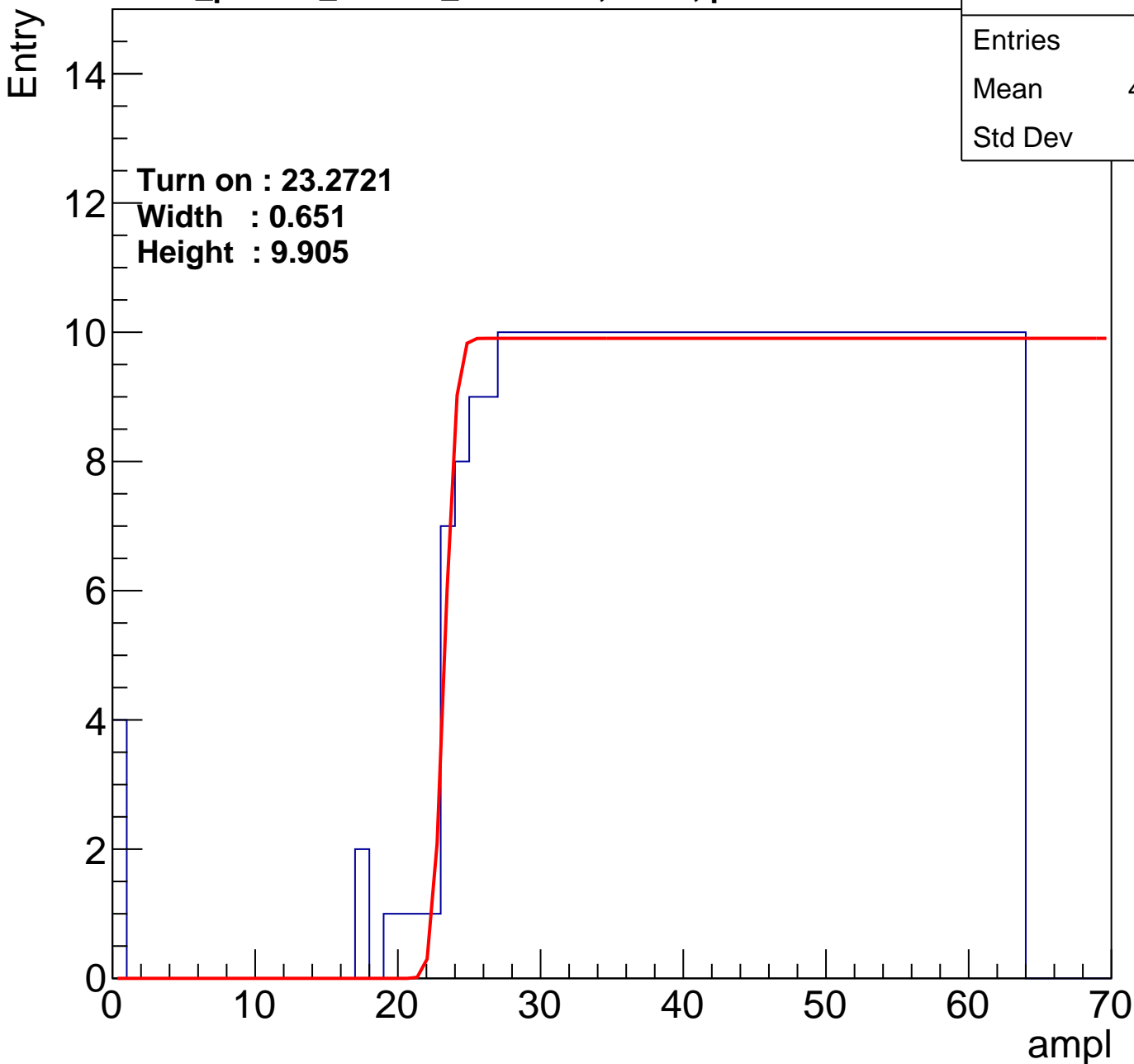
calib_packv5_042523_0143.root, FC#8, port C1

Entries	413
Mean	42.56
Std Dev	12.6

Turn on : 23.2721

Width : 0.651

Height : 9.905



B0L002S, U15-ch98

calib_packv5_042523_0143.root, FC#8, port C1

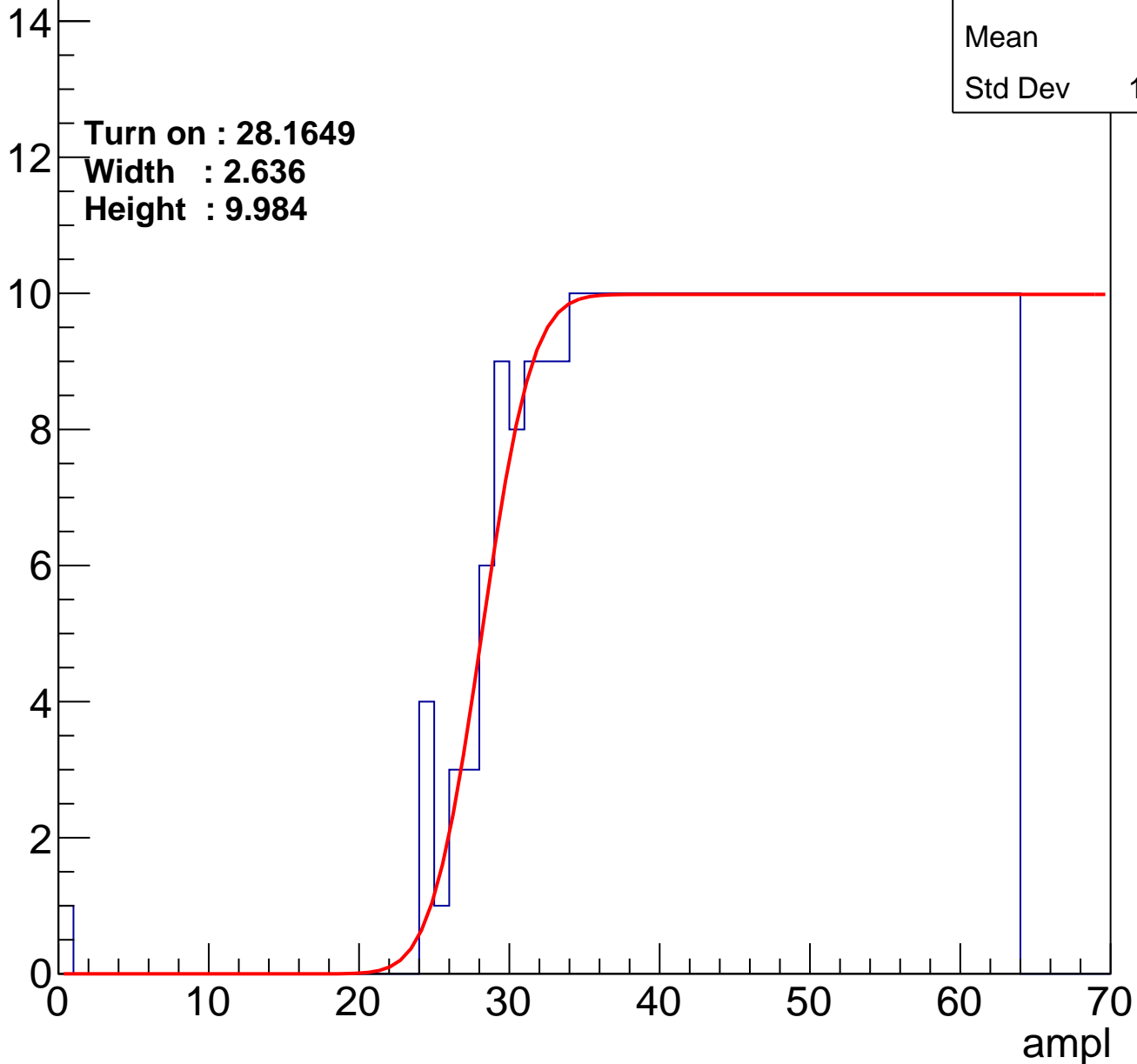
Entries	362
Mean	45.2
Std Dev	10.87

Turn on : 28.1649

Width : 2.636

Height : 9.984

Entry



B0L002S, U15-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.31
Std Dev	11.2

Turn on : 28.6893

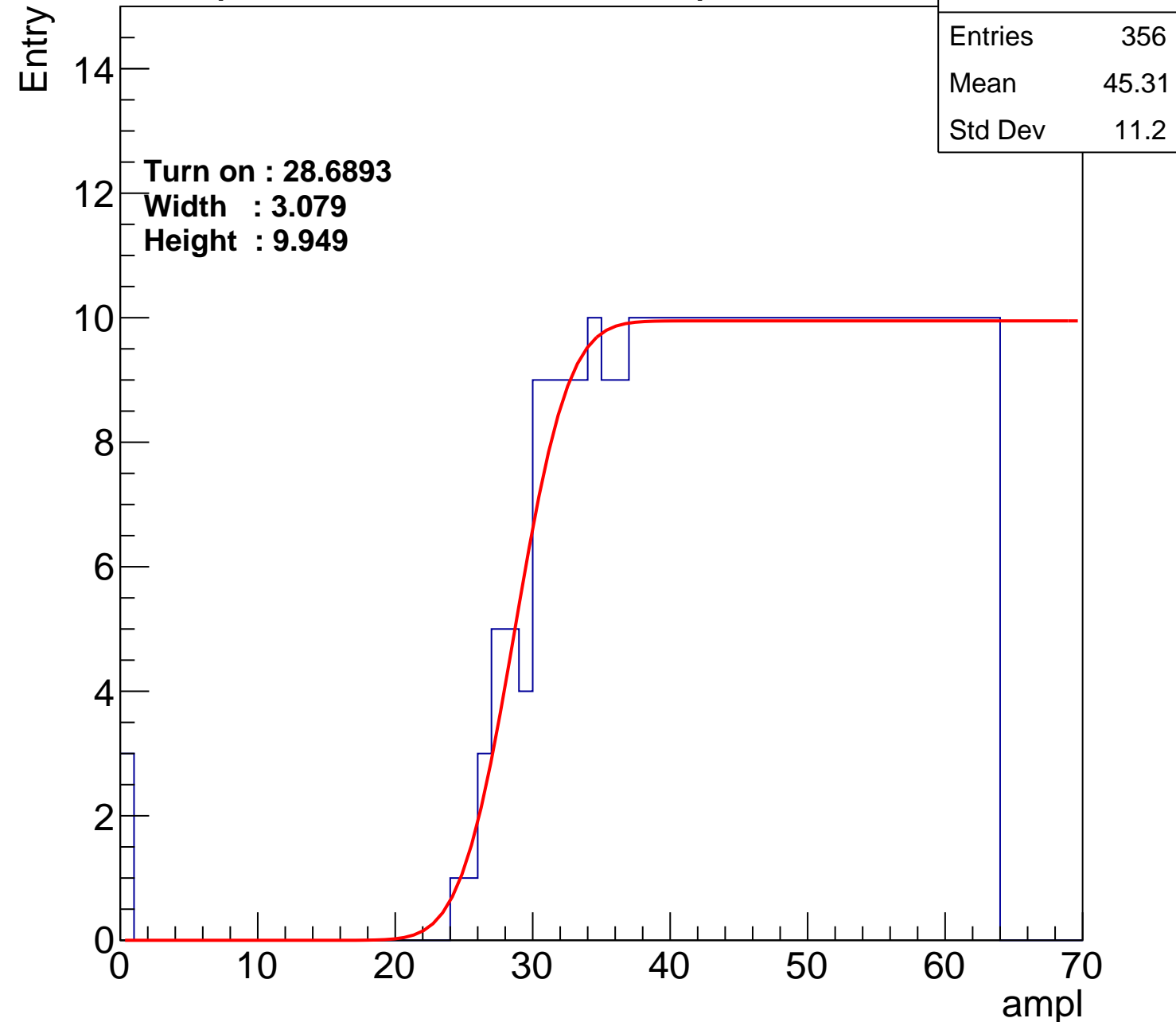
Width : 3.079

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.37
Std Dev	10.96

Turn on : 28.4440

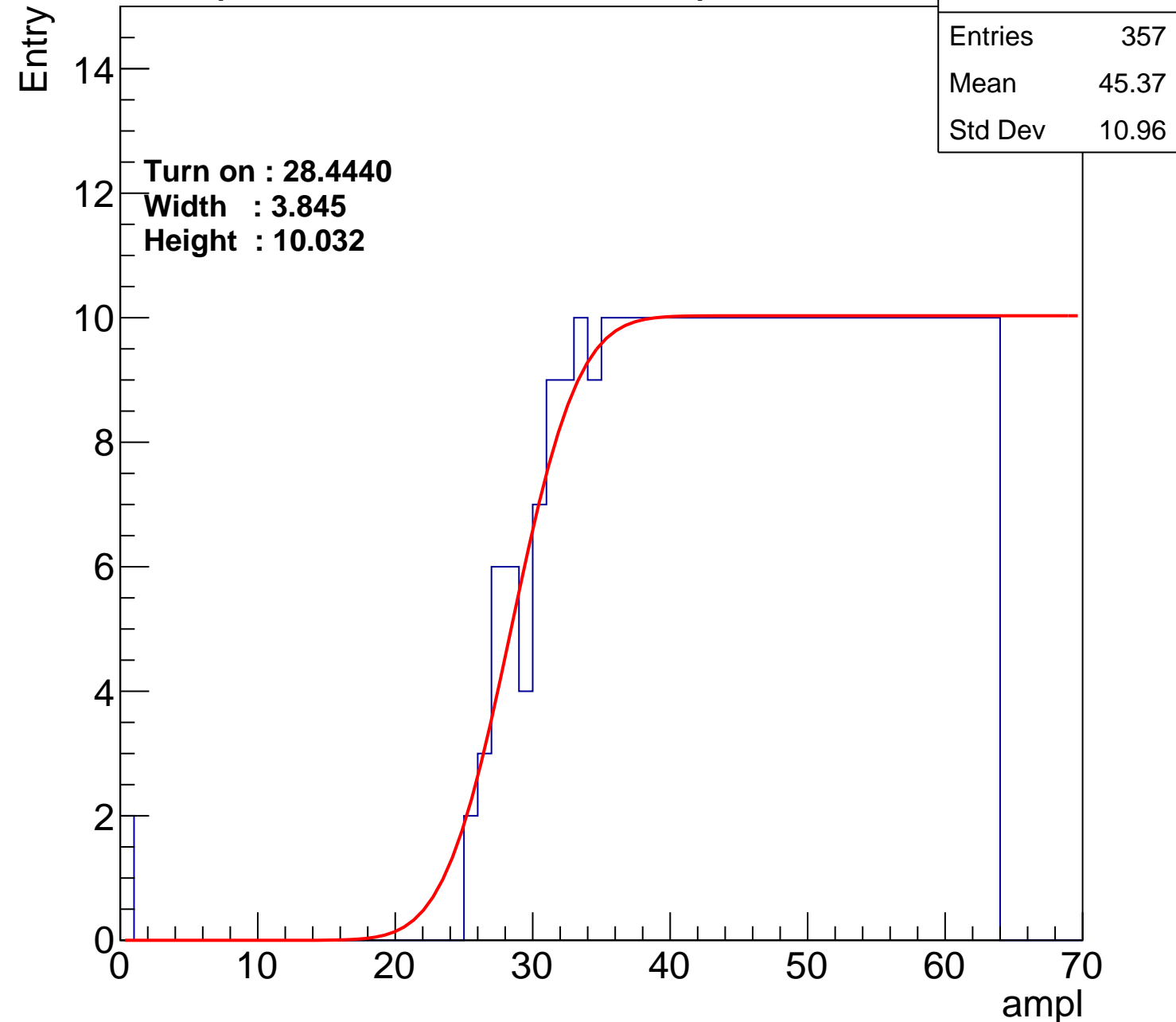
Width : 3.845

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.82
Std Dev	11.41

Turn on : 28.1049

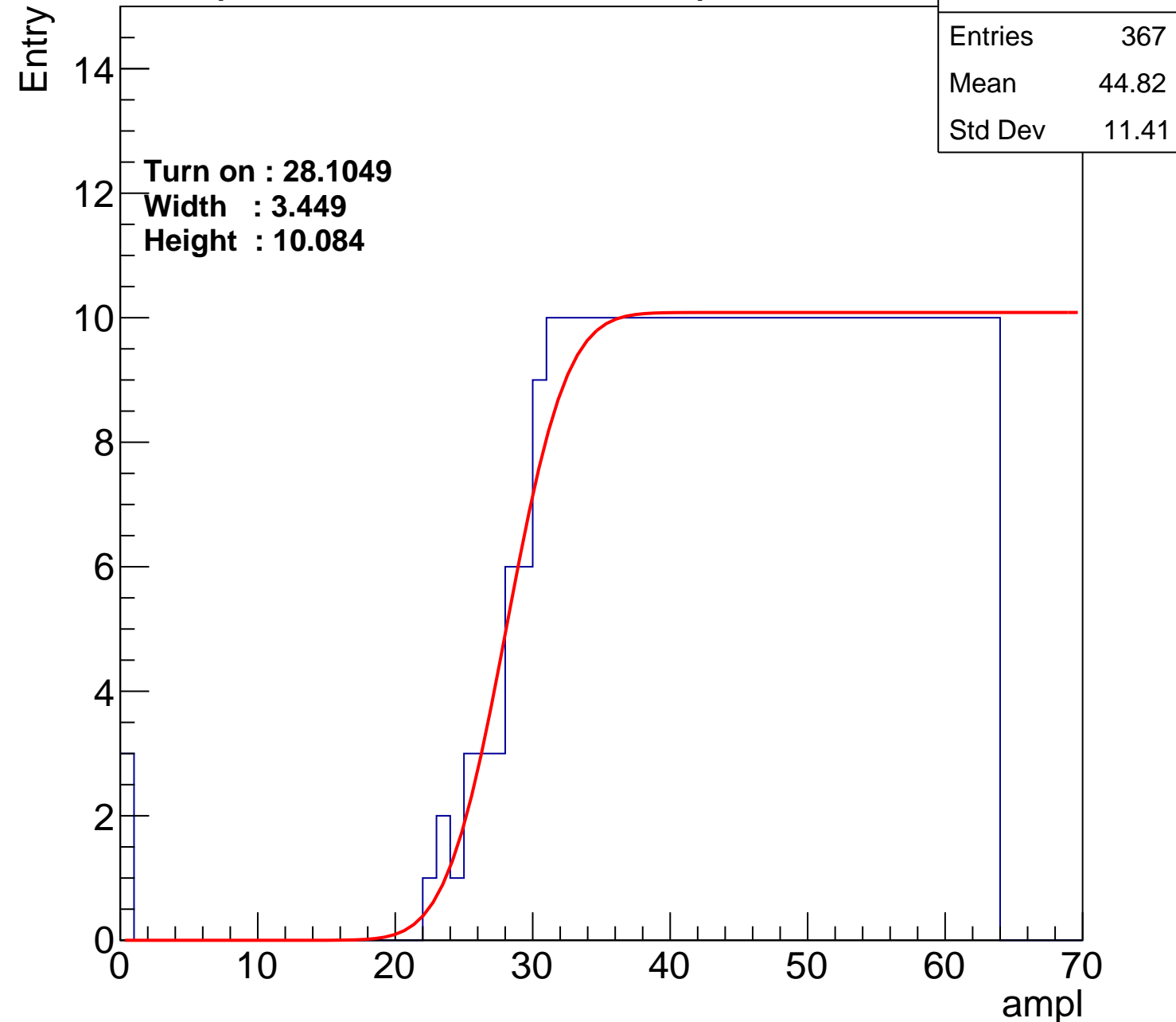
Width : 3.449

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch102

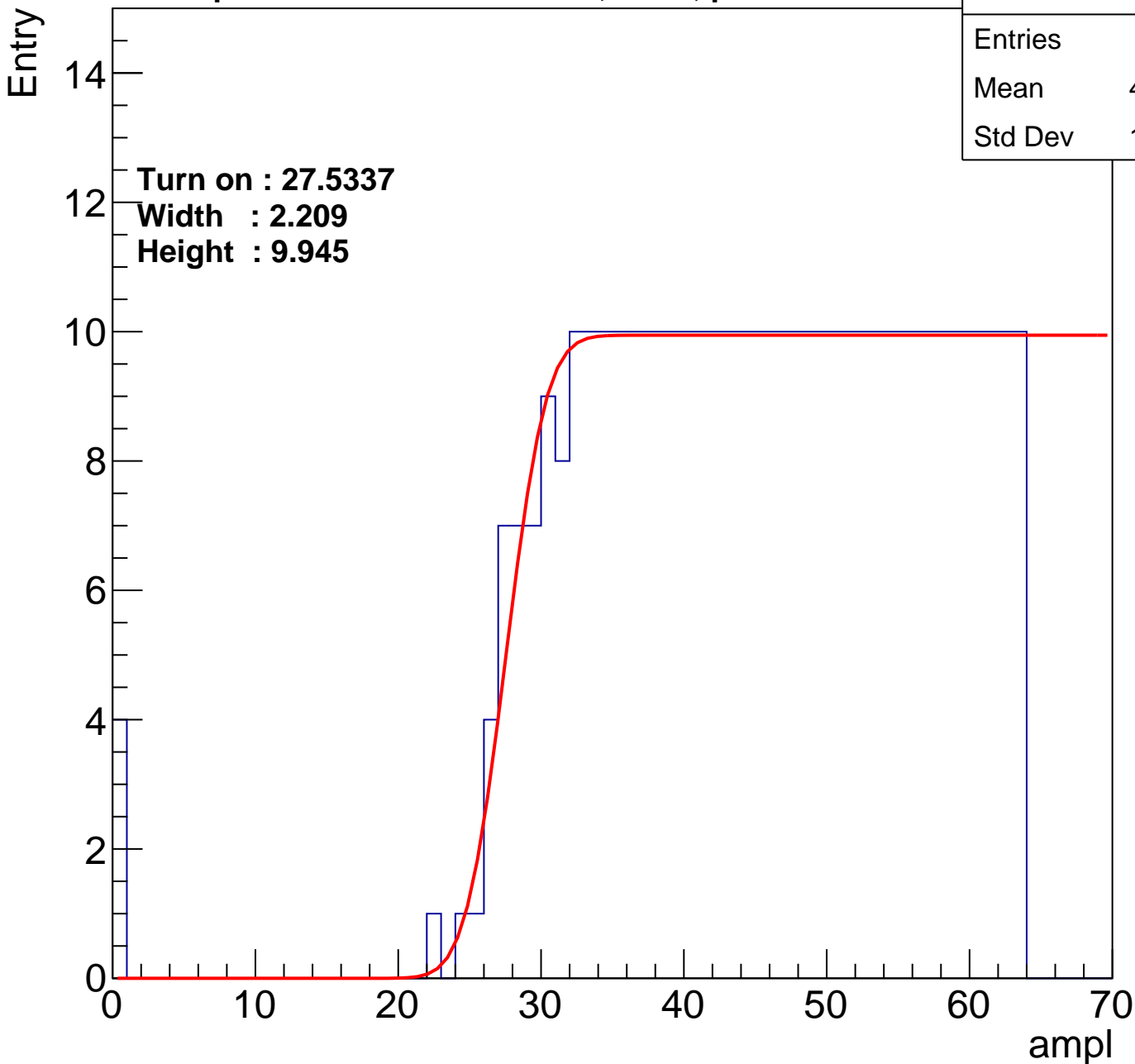
calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.66
Std Dev	11.62

Turn on : 27.5337

Width : 2.209

Height : 9.945



B0L002S, U15-ch103

calib_packv5_042523_0143.root, FC#8, port C1

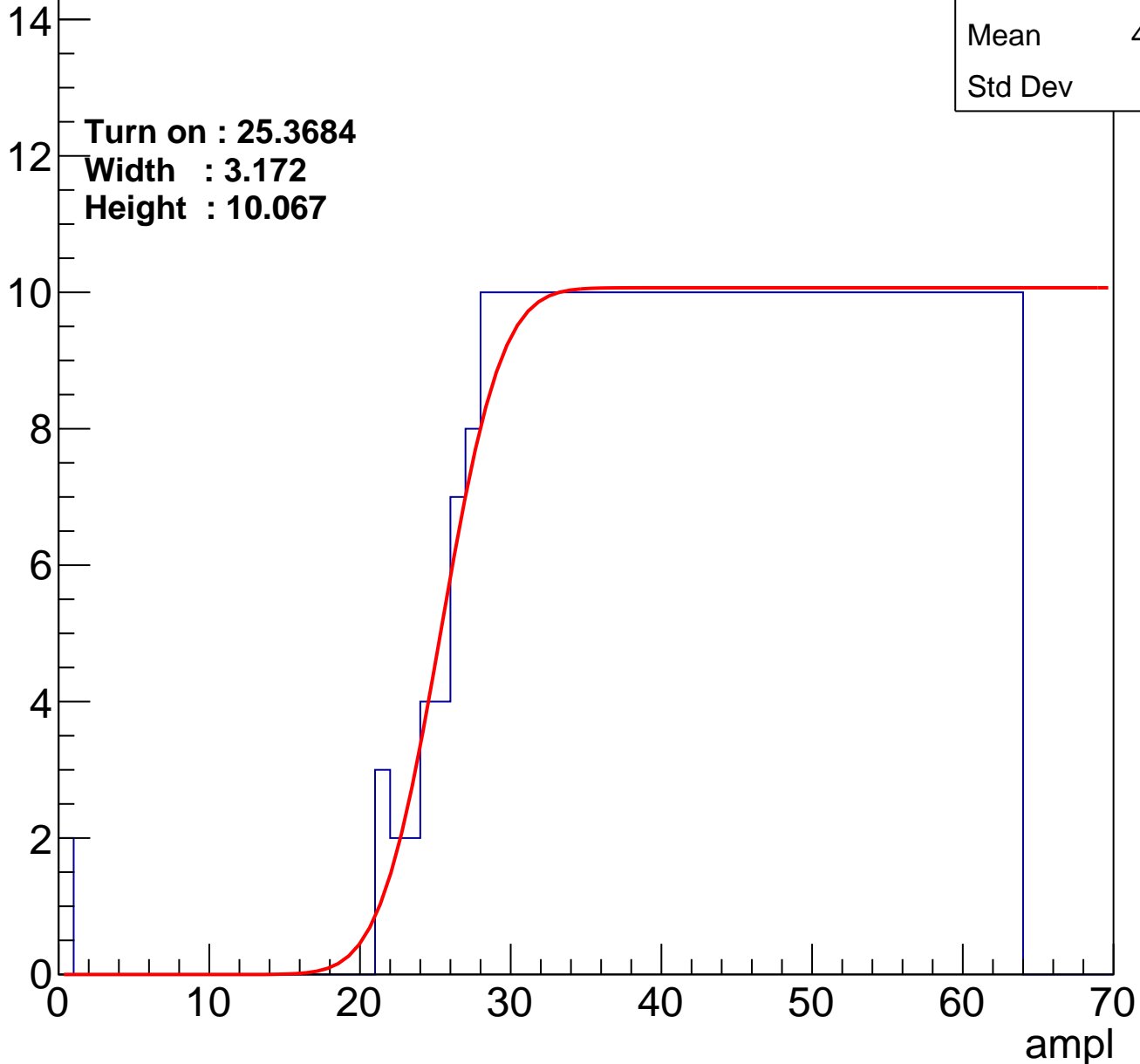
Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.3684

Width : 3.172

Height : 10.067

Entry



B0L002S, U15-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.66
Std Dev	11.54

Turn on : 27.4502

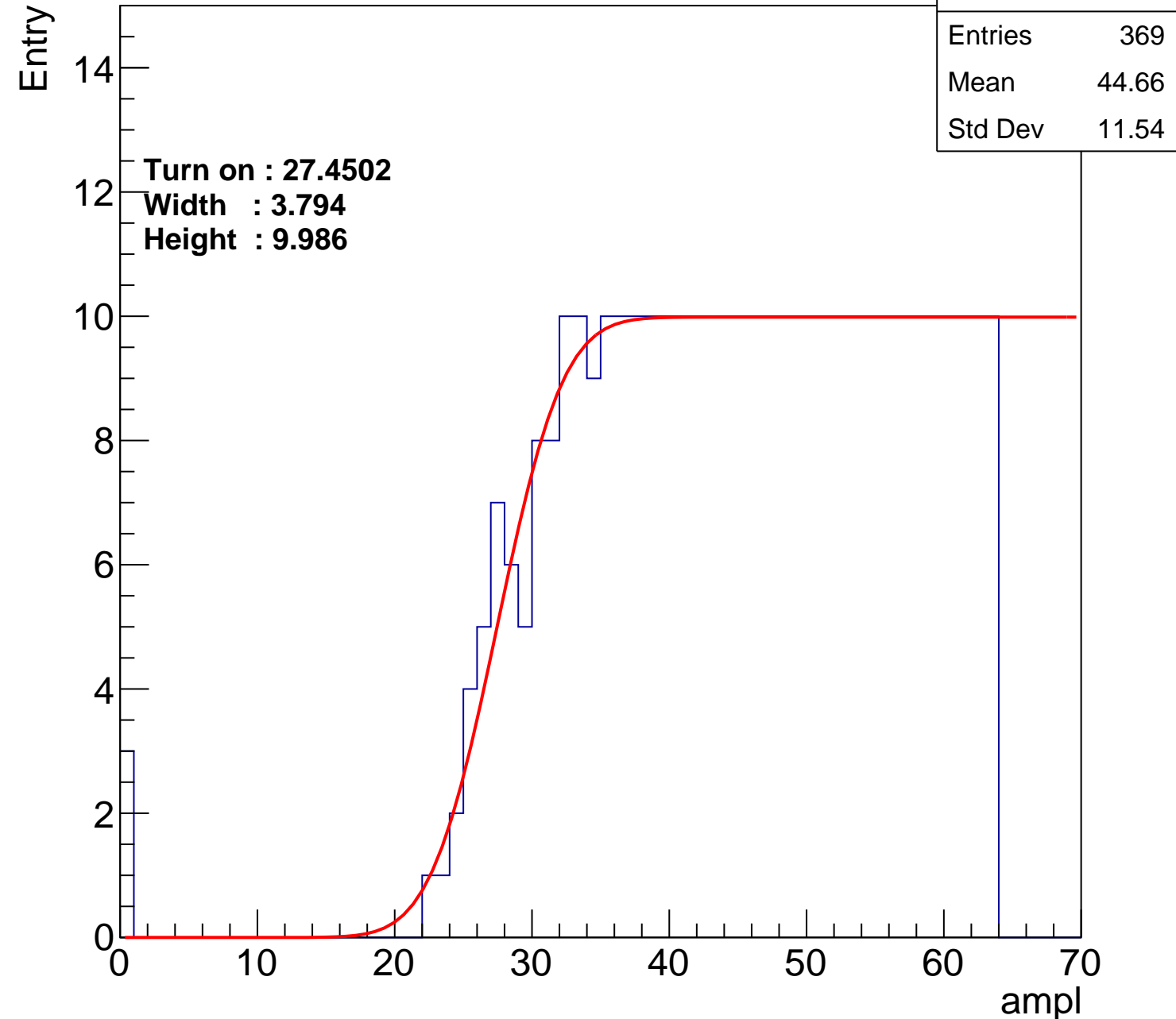
Width : 3.794

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.33
Std Dev	11.46

Turn on : 26.4742

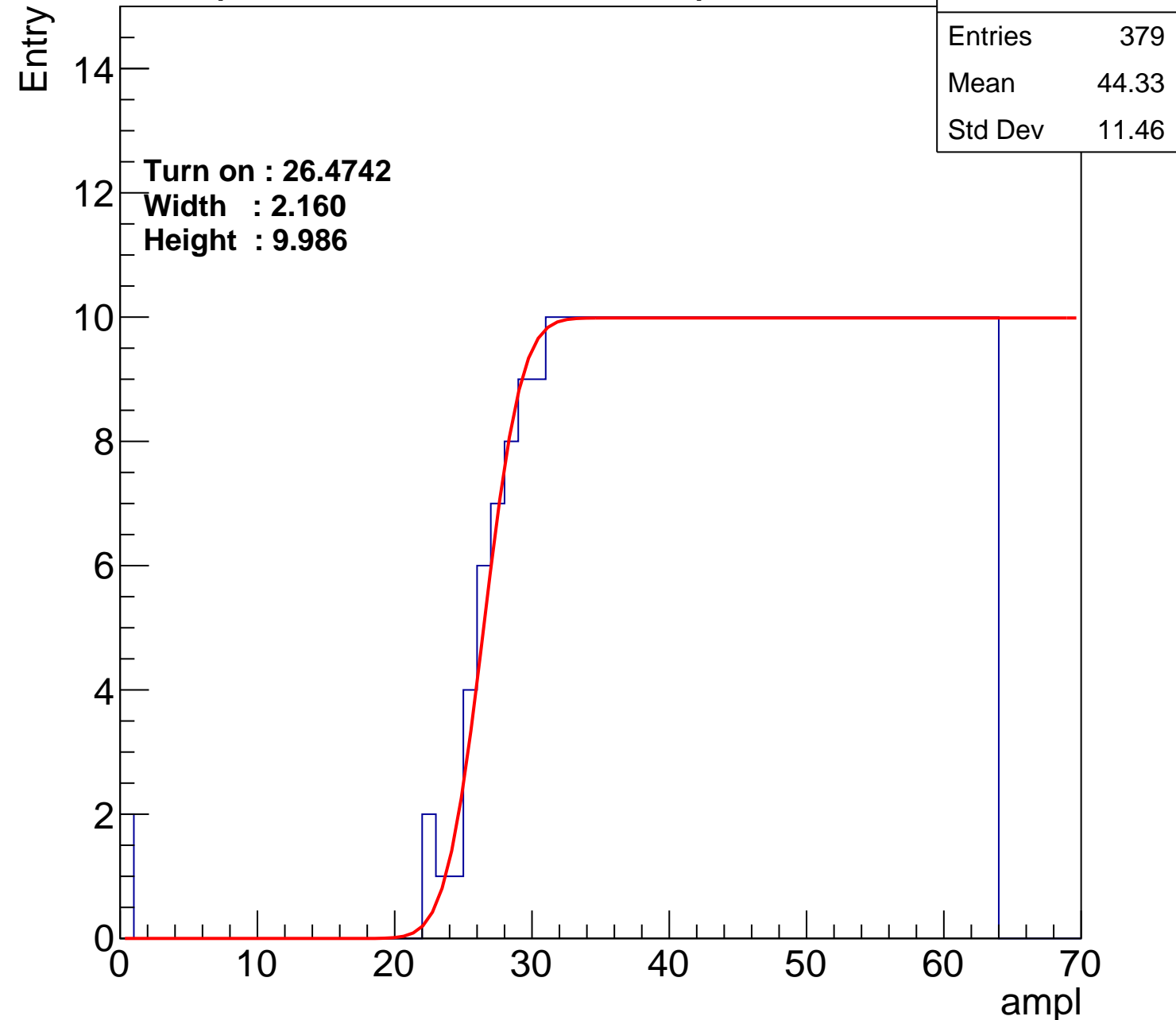
Width : 2.160

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 26.7267

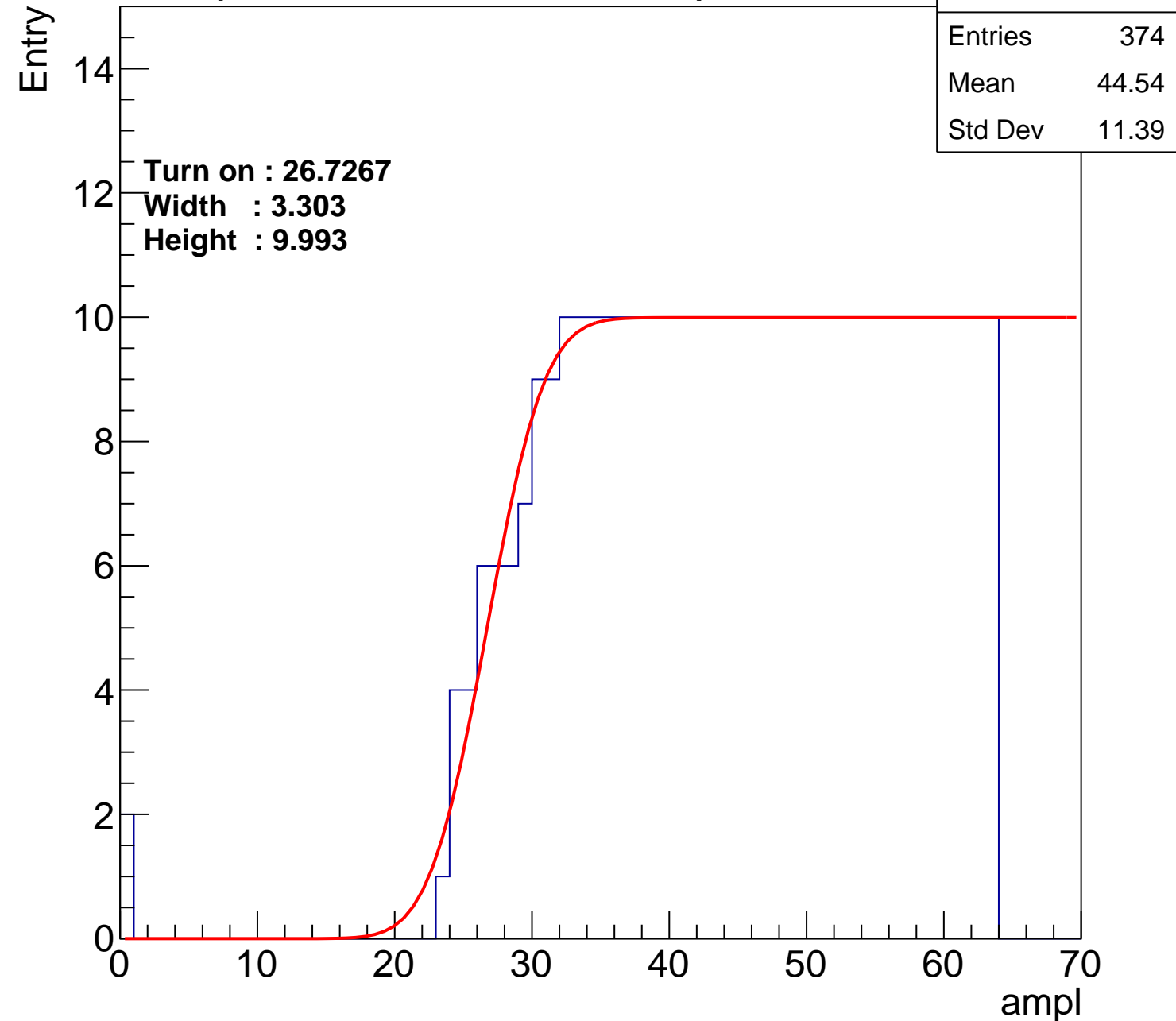
Width : 3.303

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.79
Std Dev	10.57

Turn on : 29.7076

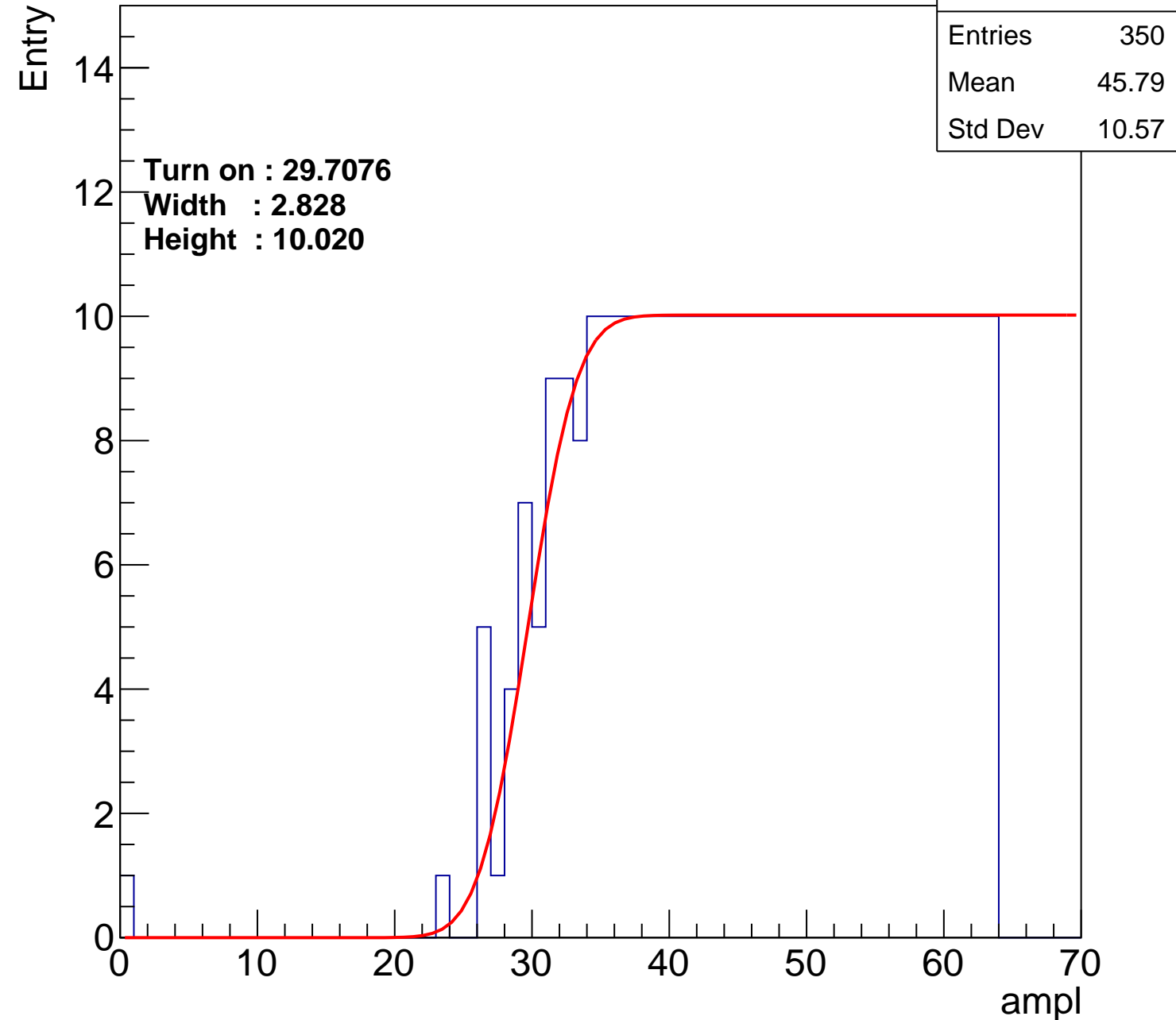
Width : 2.828

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.11
Std Dev	11.72

Turn on : 26.5717

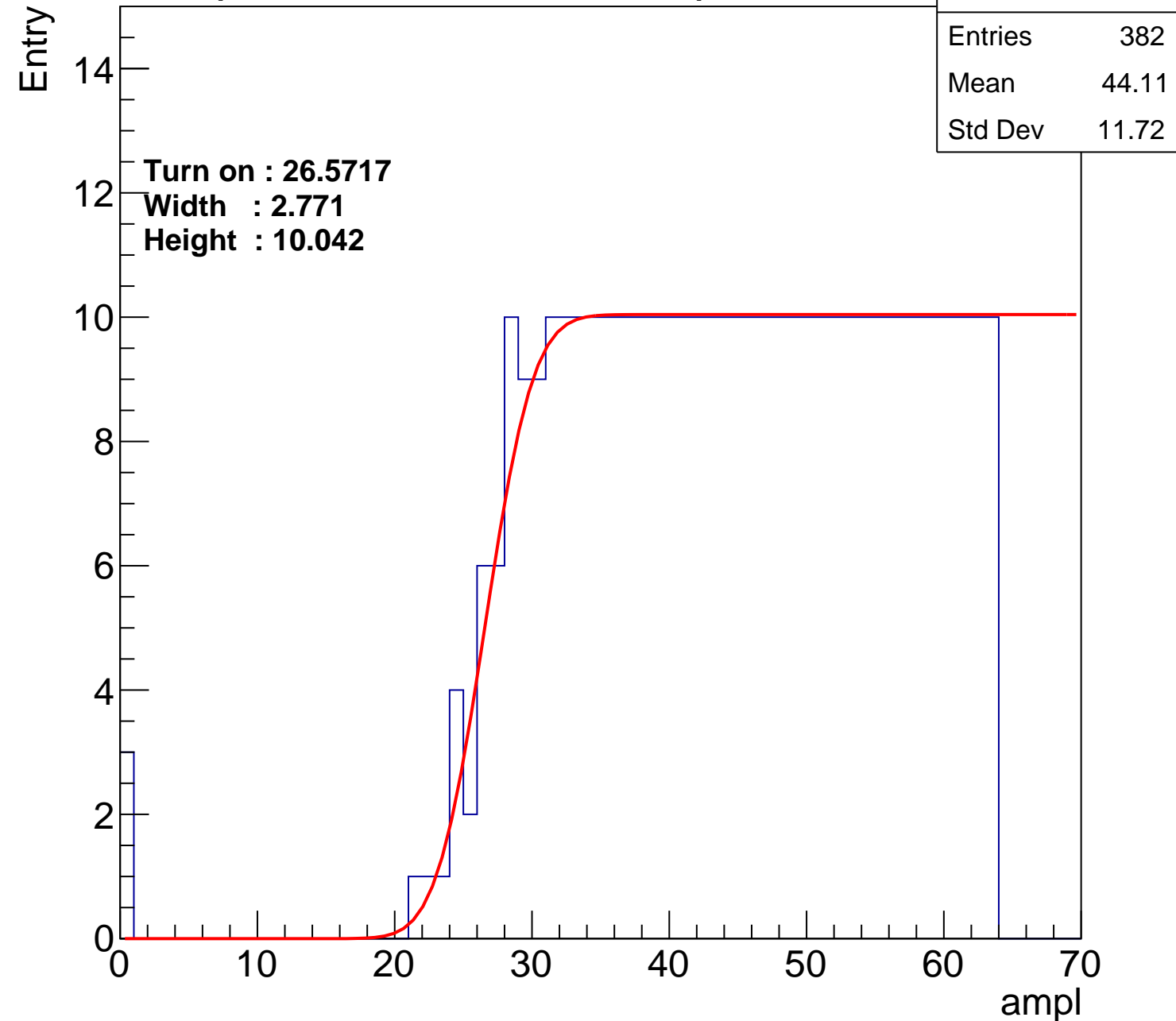
Width : 2.771

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch109

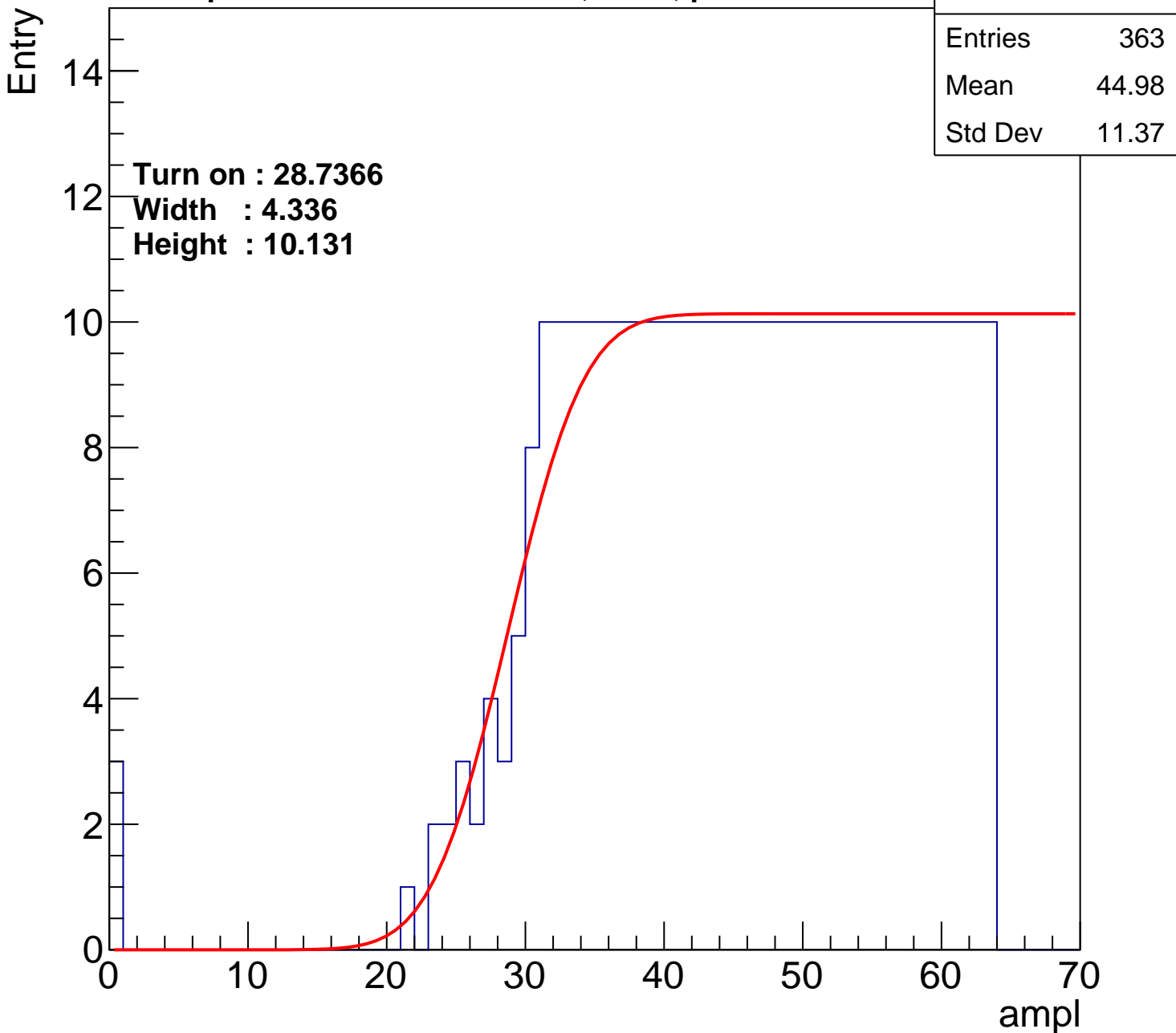
calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	44.98
Std Dev	11.37

Turn on : 28.7366

Width : 4.336

Height : 10.131



B0L002S, U15-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	393
Mean	43.53
Std Dev	12.06

Turn on : 25.5200

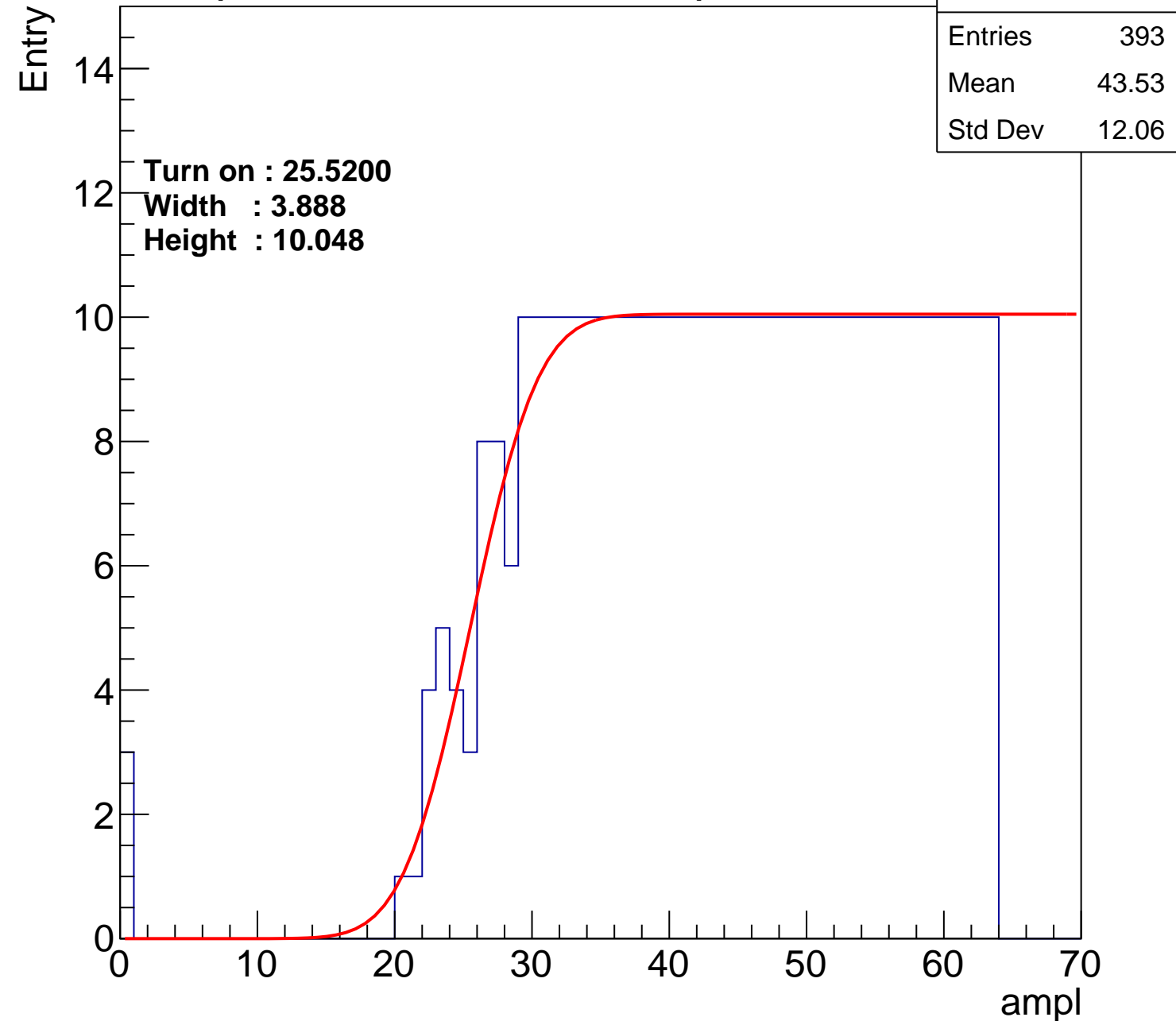
Width : 3.888

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 27.2597

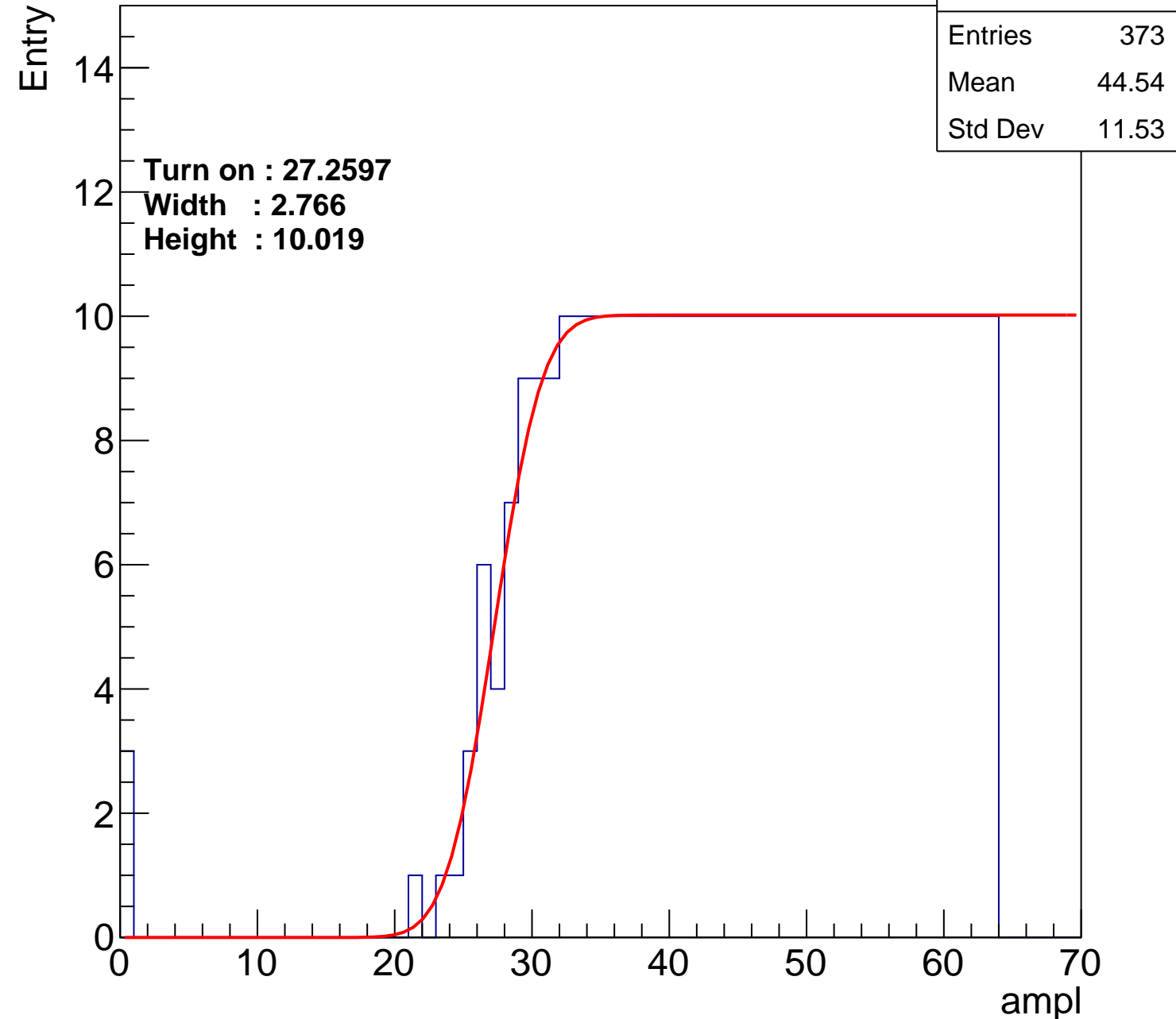
Width : 2.766

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.05
Std Dev	10.91

Turn on : 27.9398

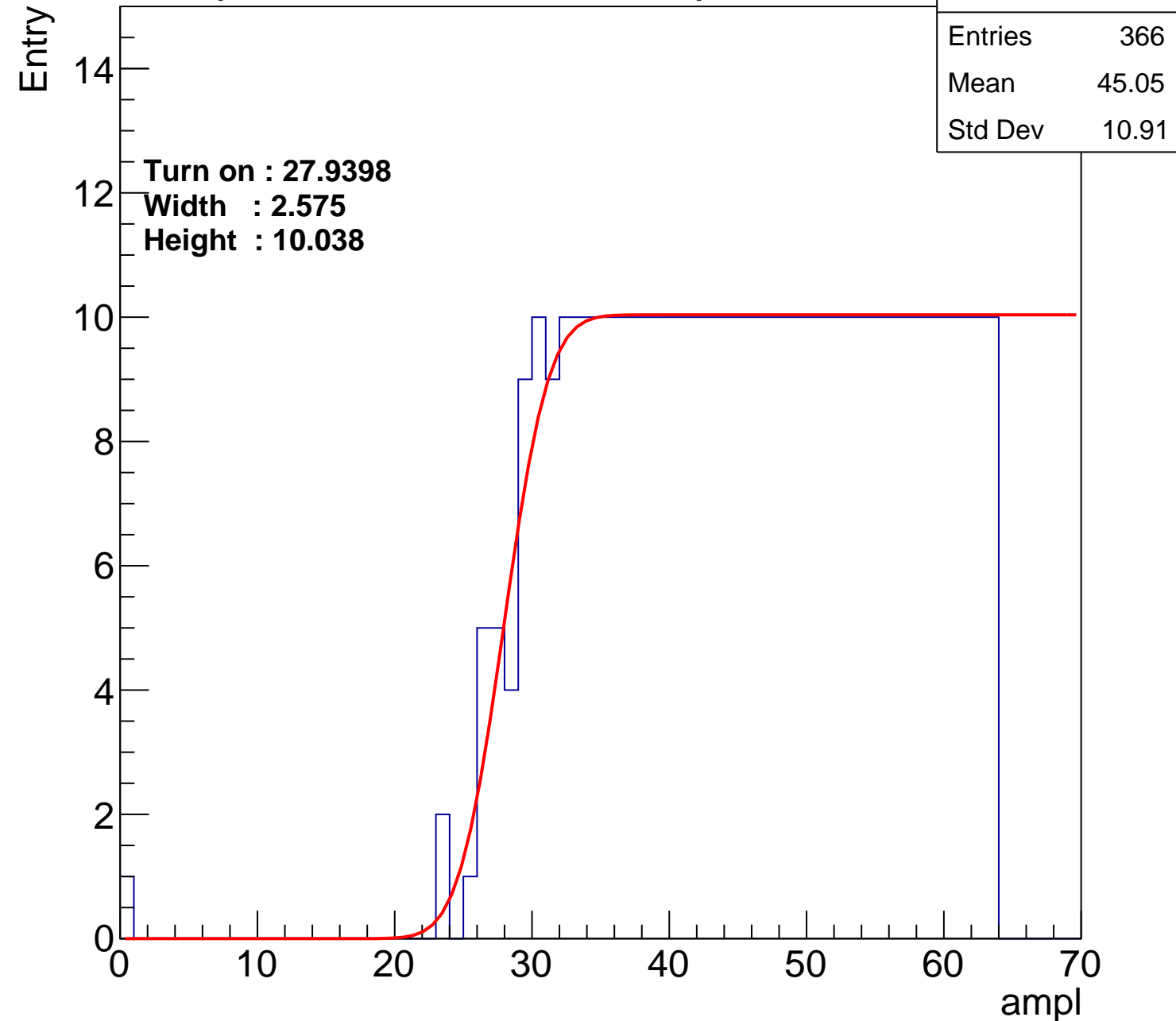
Width : 2.575

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch113

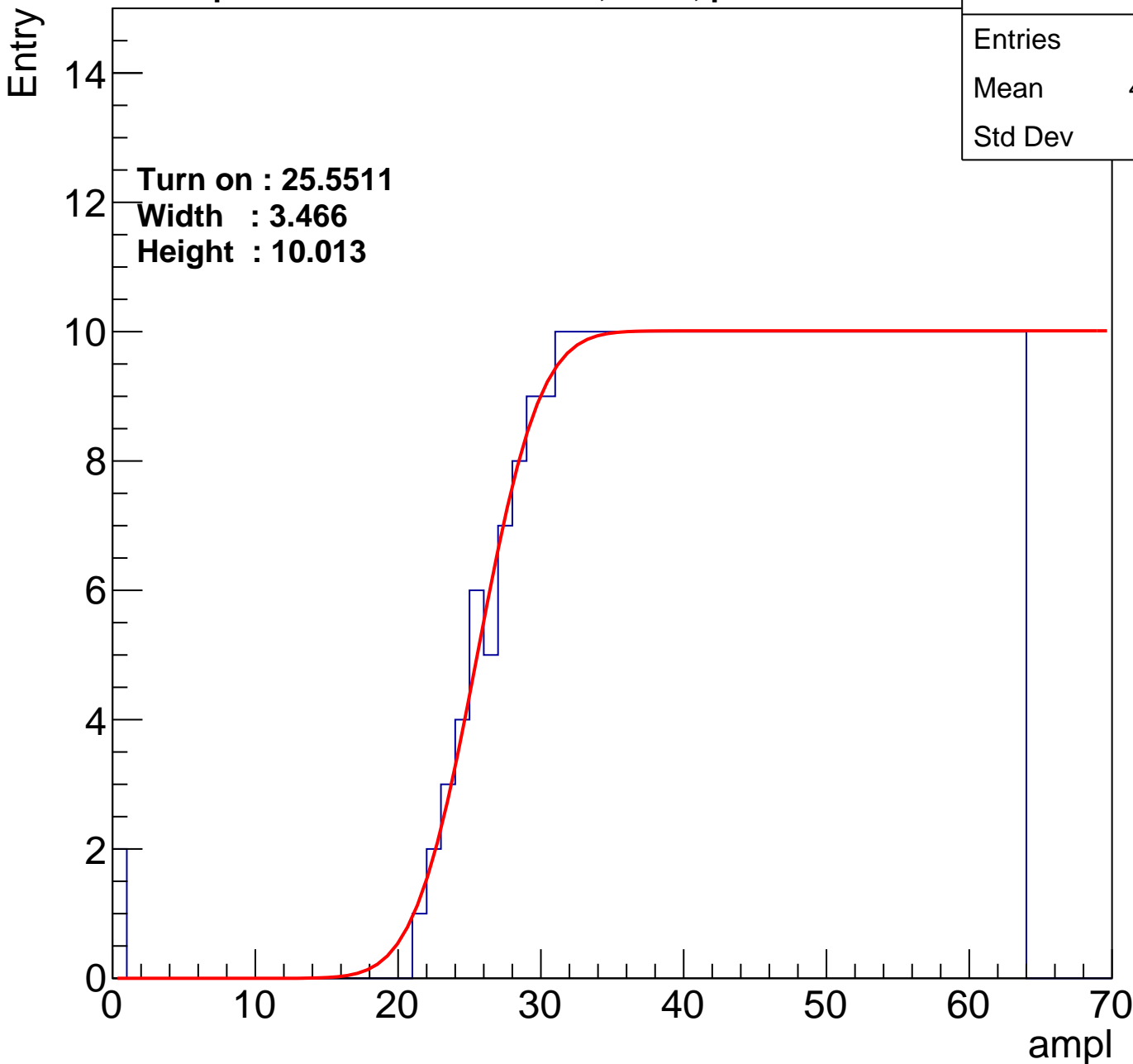
calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	43.95
Std Dev	11.7

Turn on : 25.5511

Width : 3.466

Height : 10.013



B0L002S, U15-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.79
Std Dev	11.25

Turn on : 27.3478

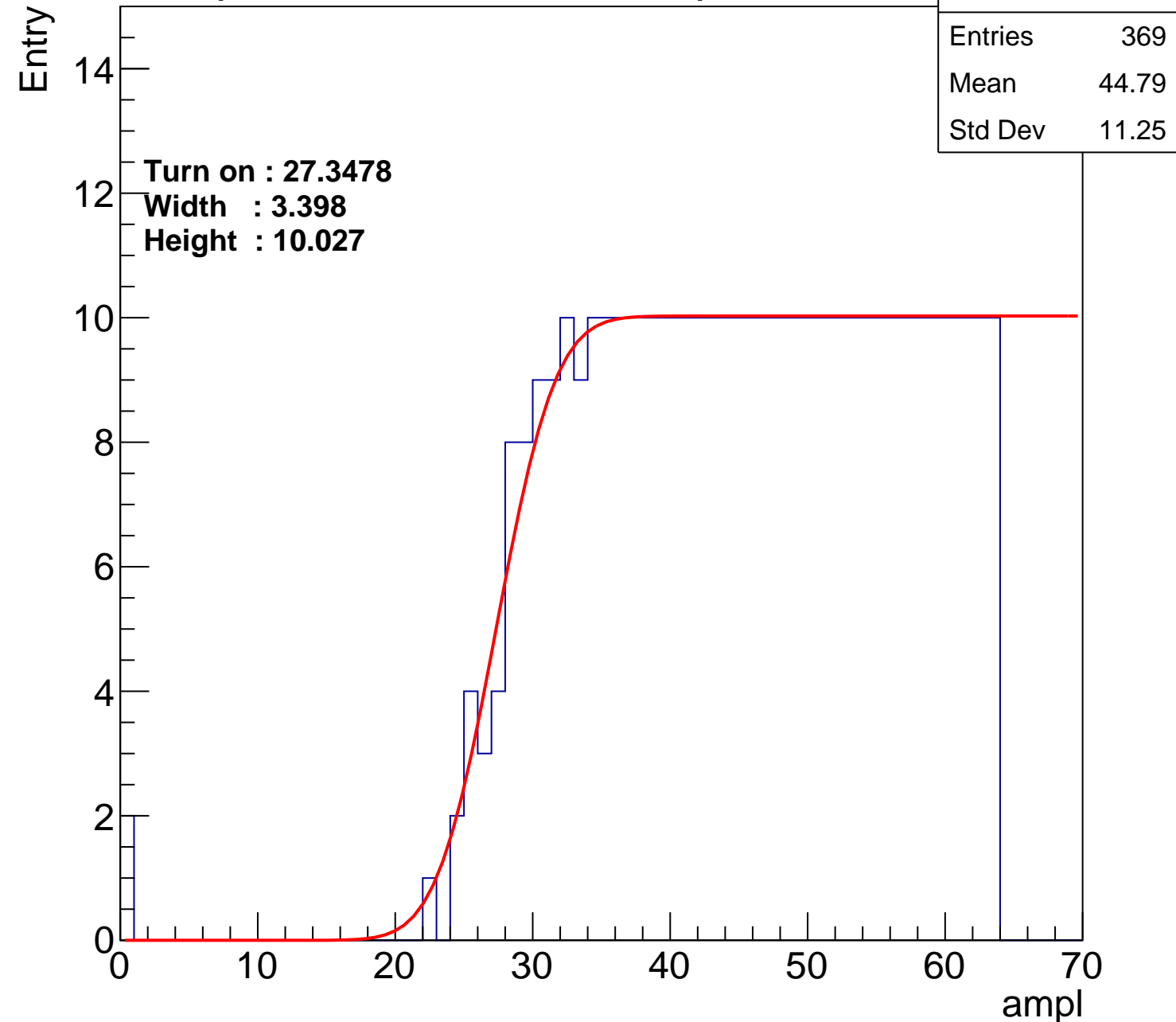
Width : 3.398

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.57
Std Dev	11.45

Turn on : 26.9287

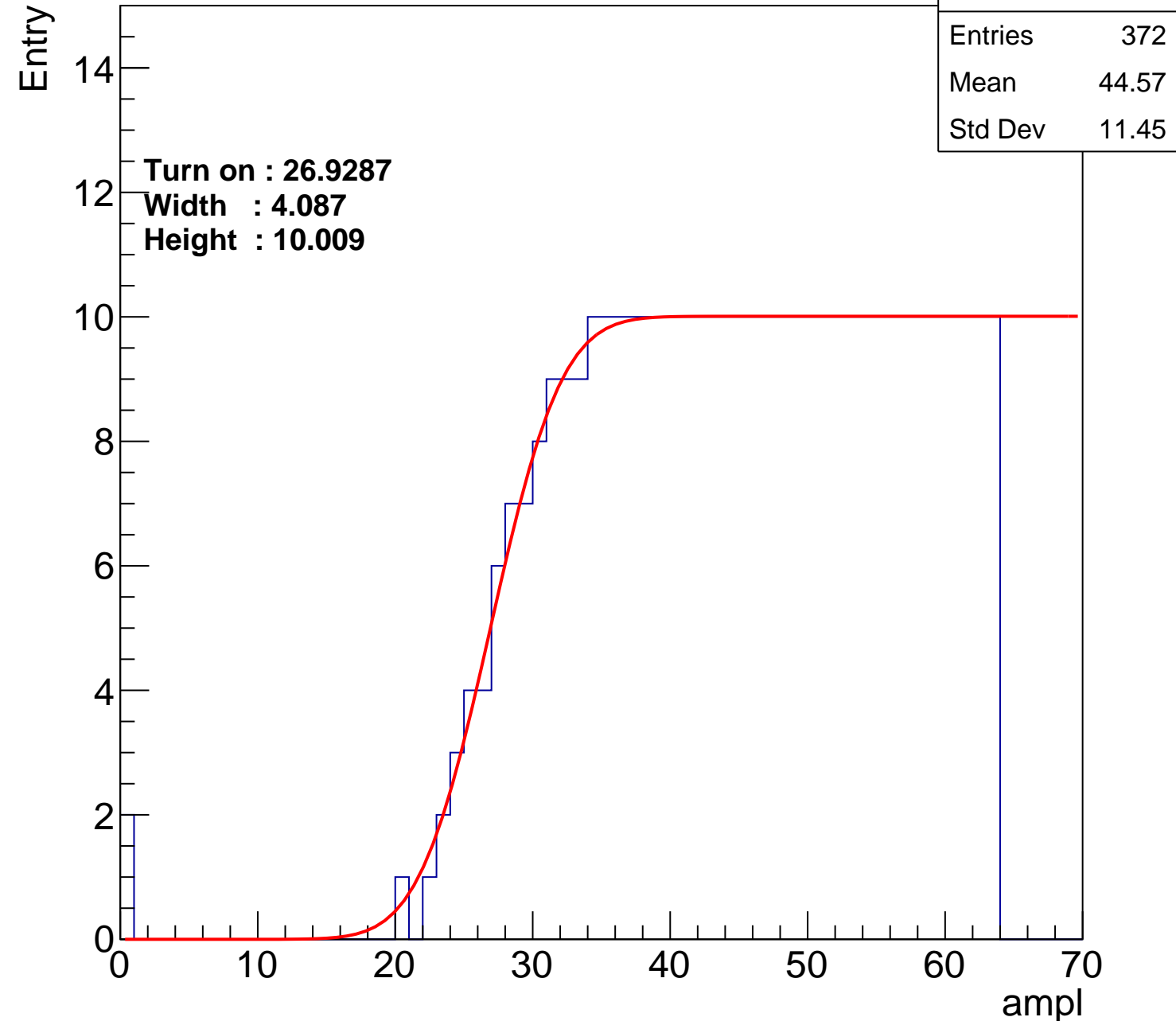
Width : 4.087

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.26
Std Dev	11.19

Turn on : 28.8313

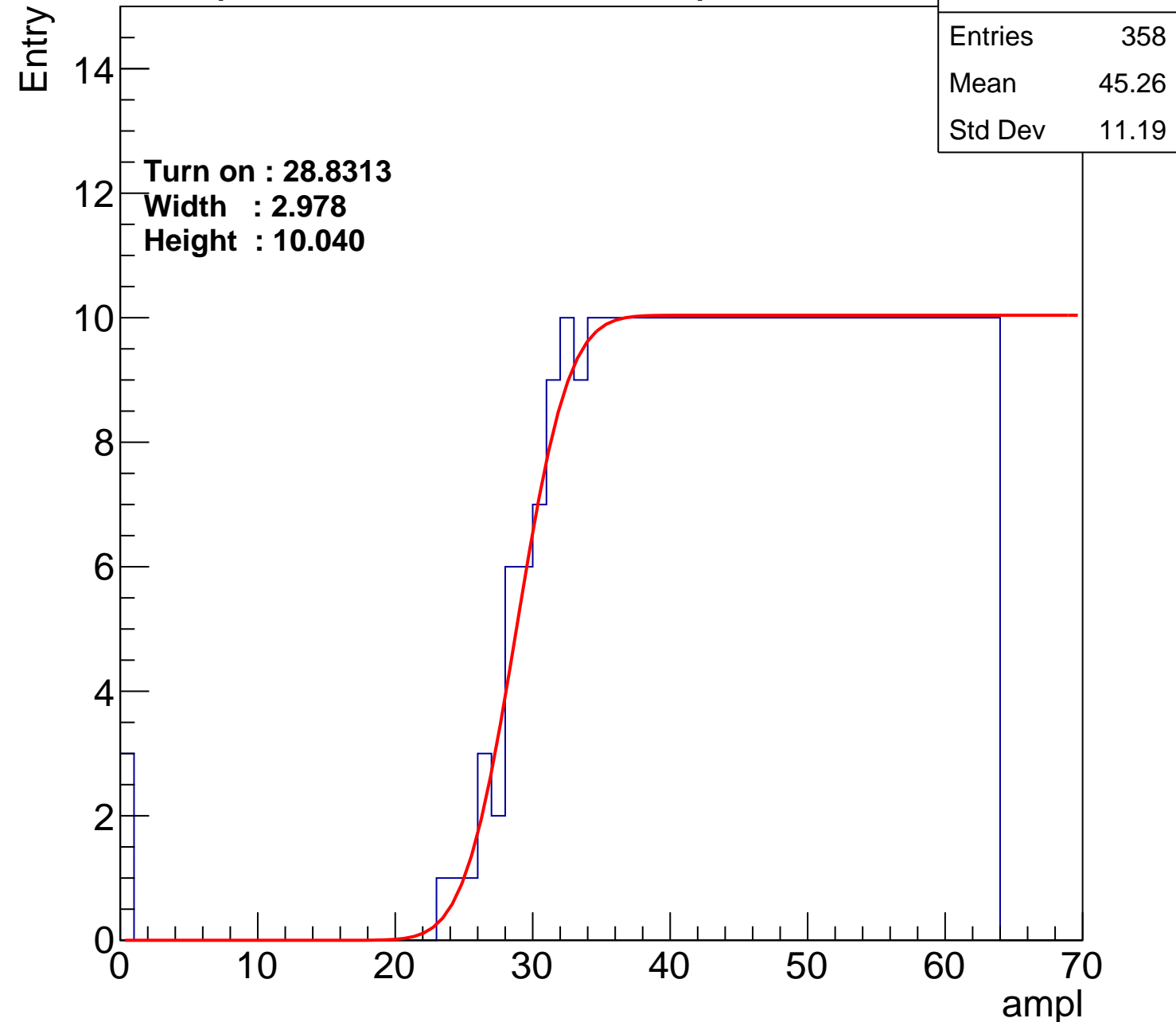
Width : 2.978

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.72
Std Dev	10.75

Turn on : 29.0450

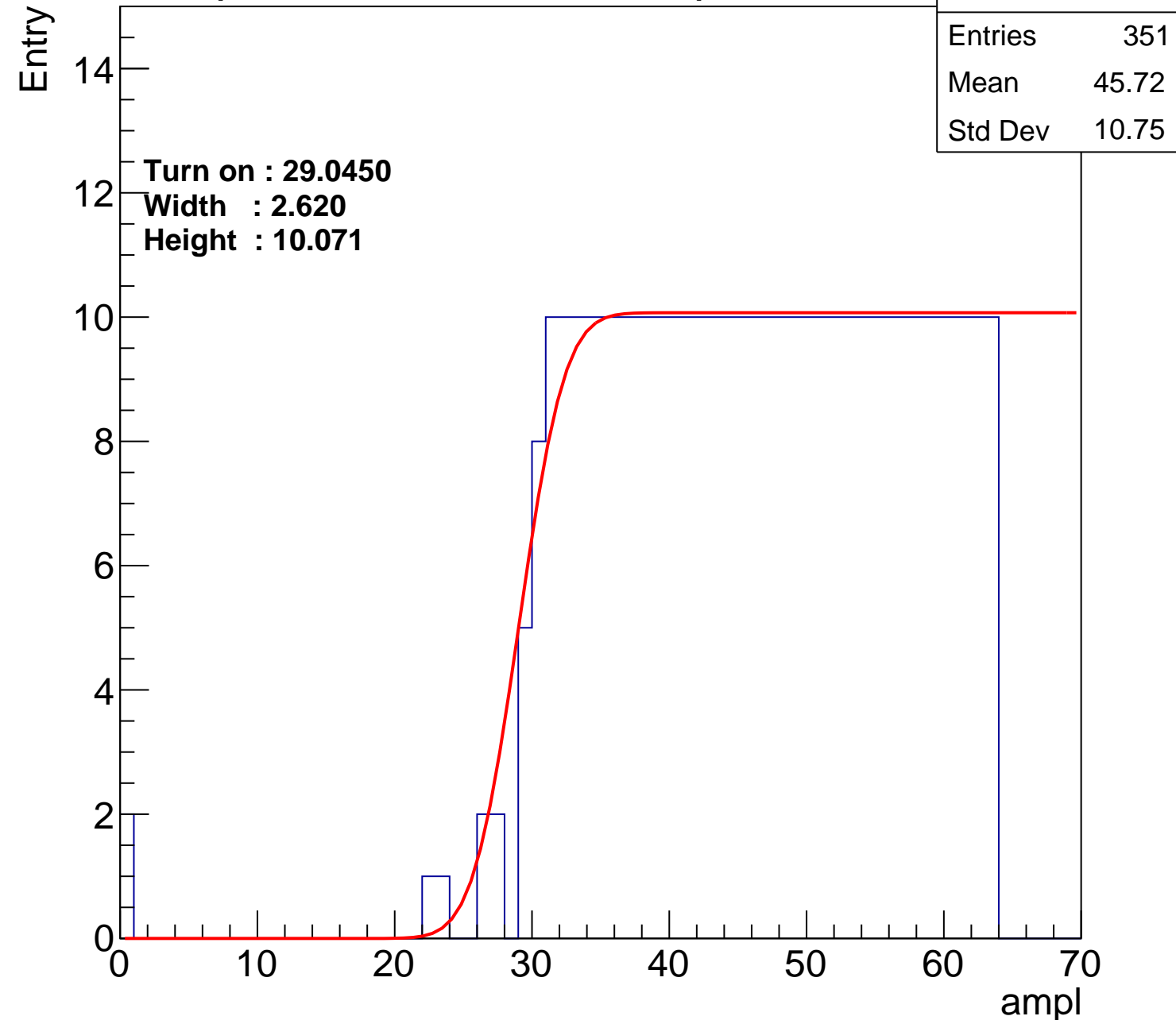
Width : 2.620

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.23
Std Dev	11.99

Turn on : 27.0115

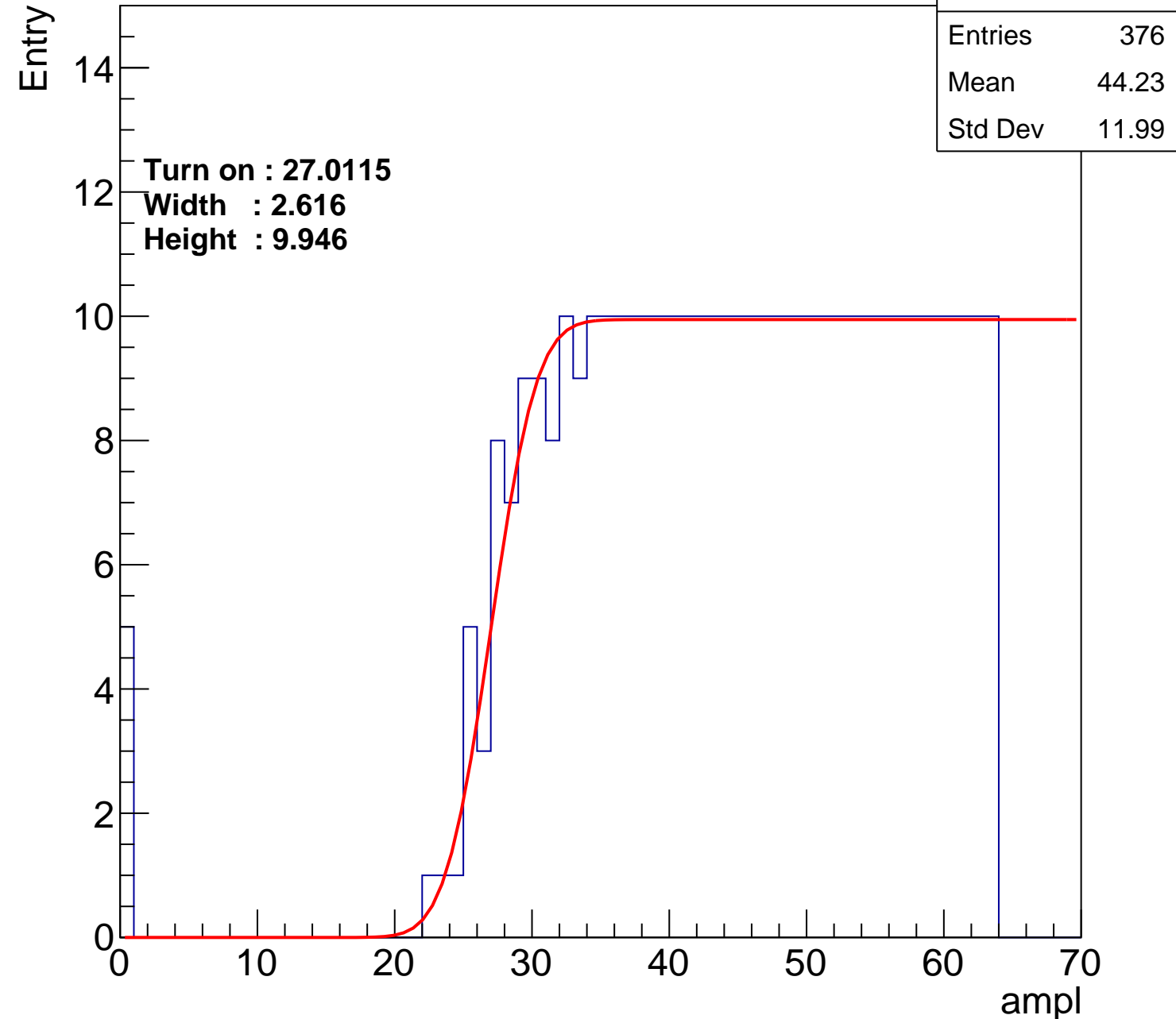
Width : 2.616

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.48
Std Dev	11.57

Turn on : 27.2221

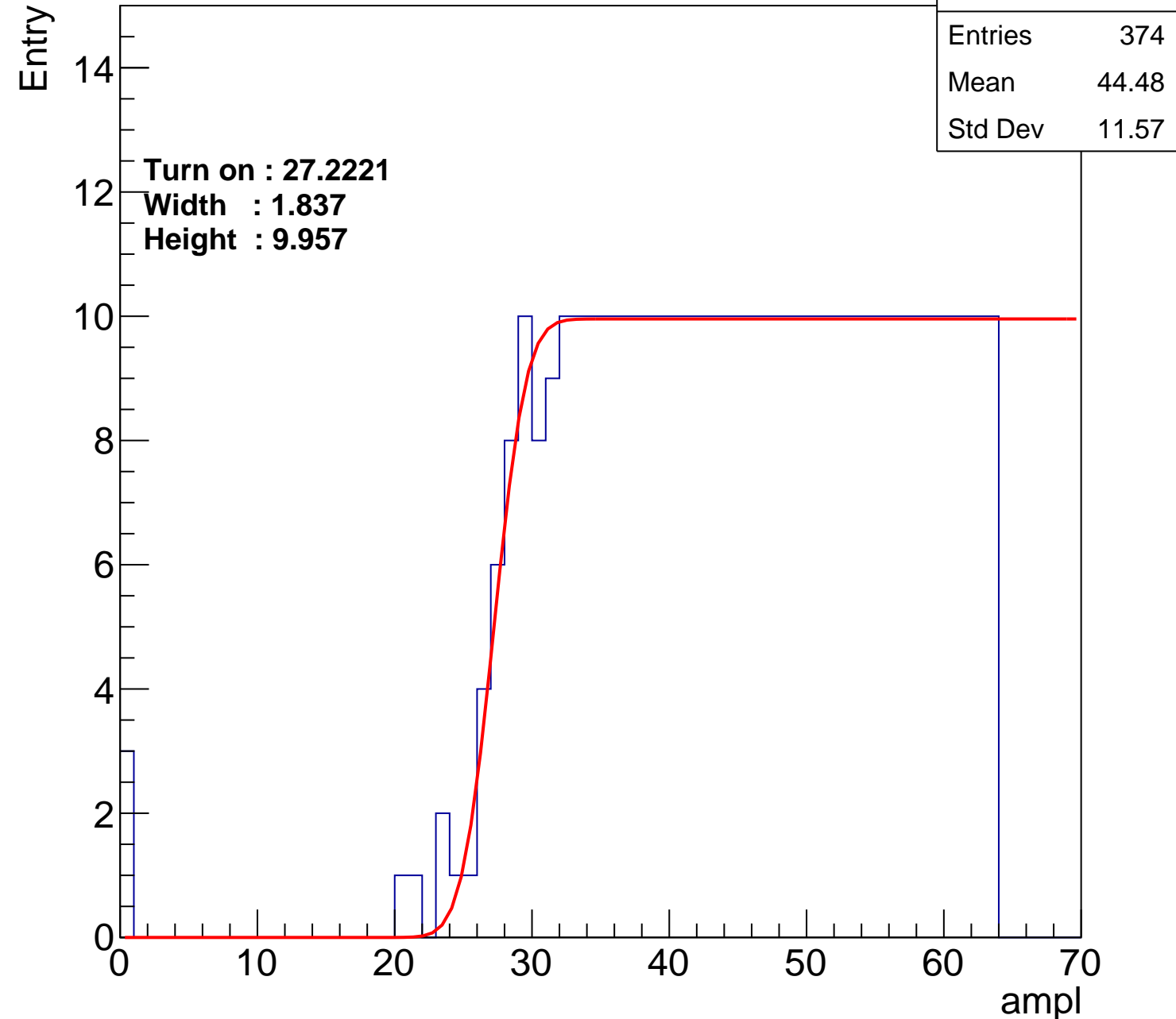
Width : 1.837

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	388
Mean	43.85
Std Dev	11.75

Turn on : 25.4427

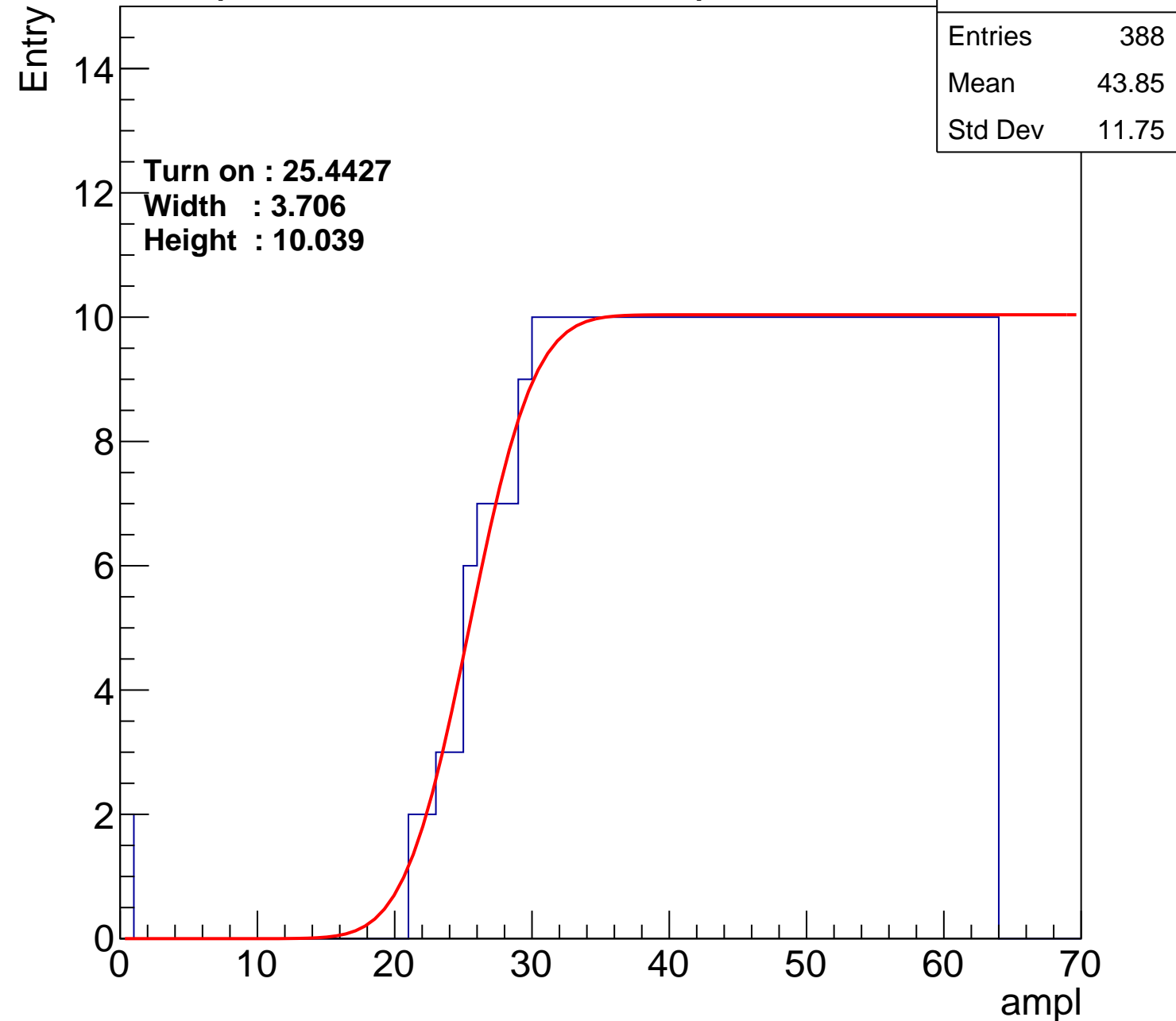
Width : 3.706

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	43.93
Std Dev	11.91

Turn on : 26.8392

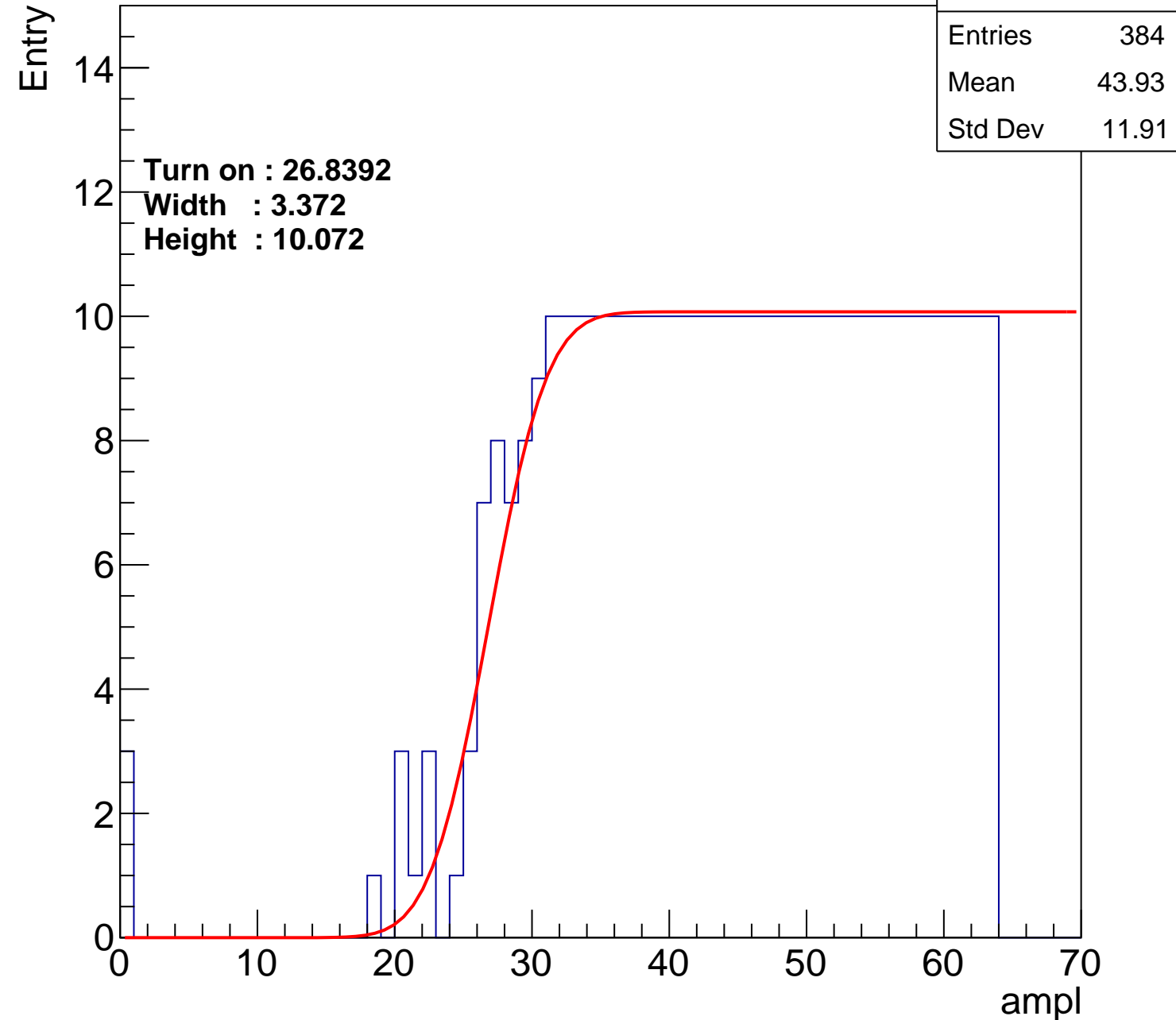
Width : 3.372

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.64
Std Dev	11.14

Turn on : 26.6668

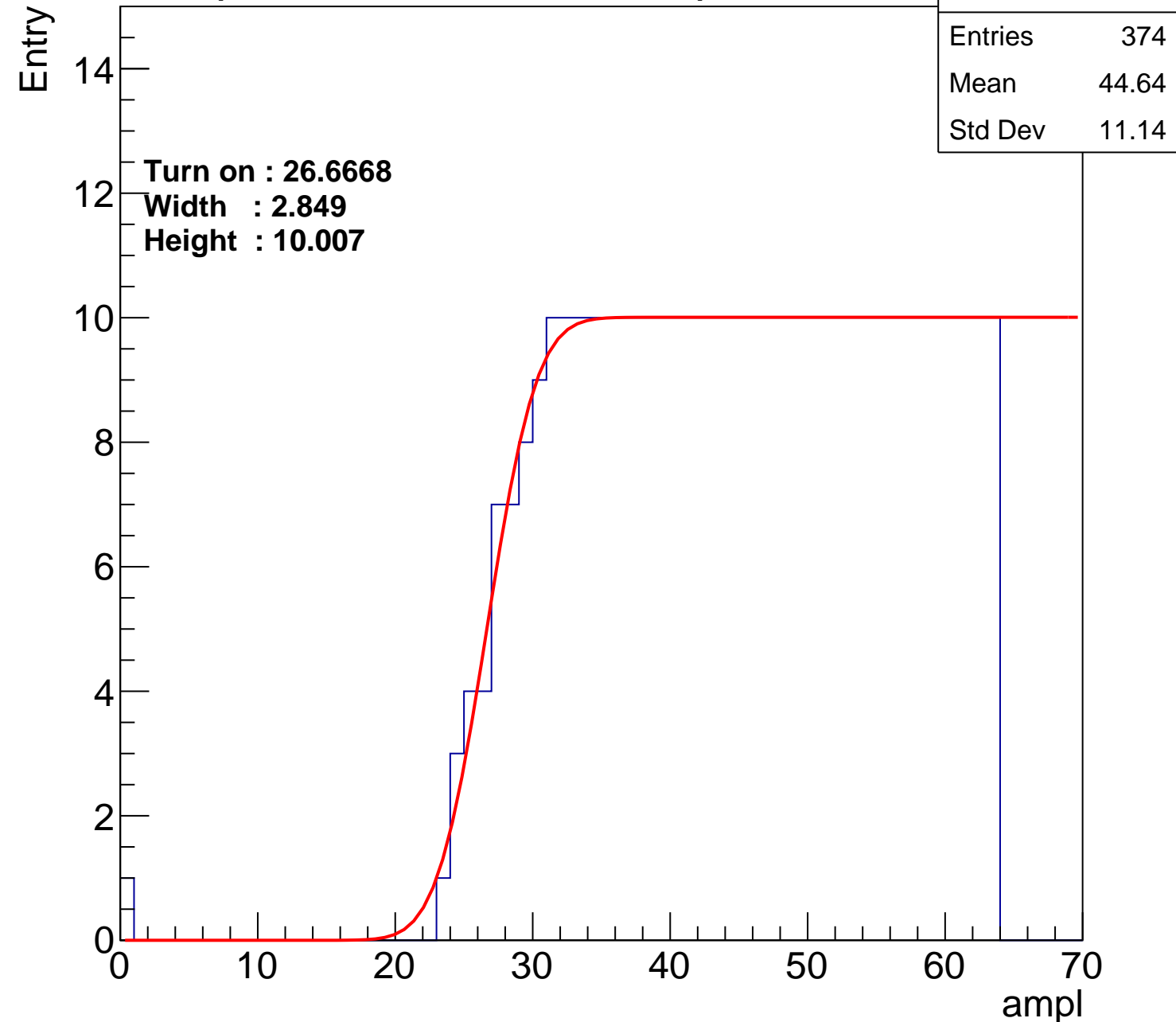
Width : 2.849

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	400
Mean	43.17
Std Dev	12.32

Turn on : 24.2938

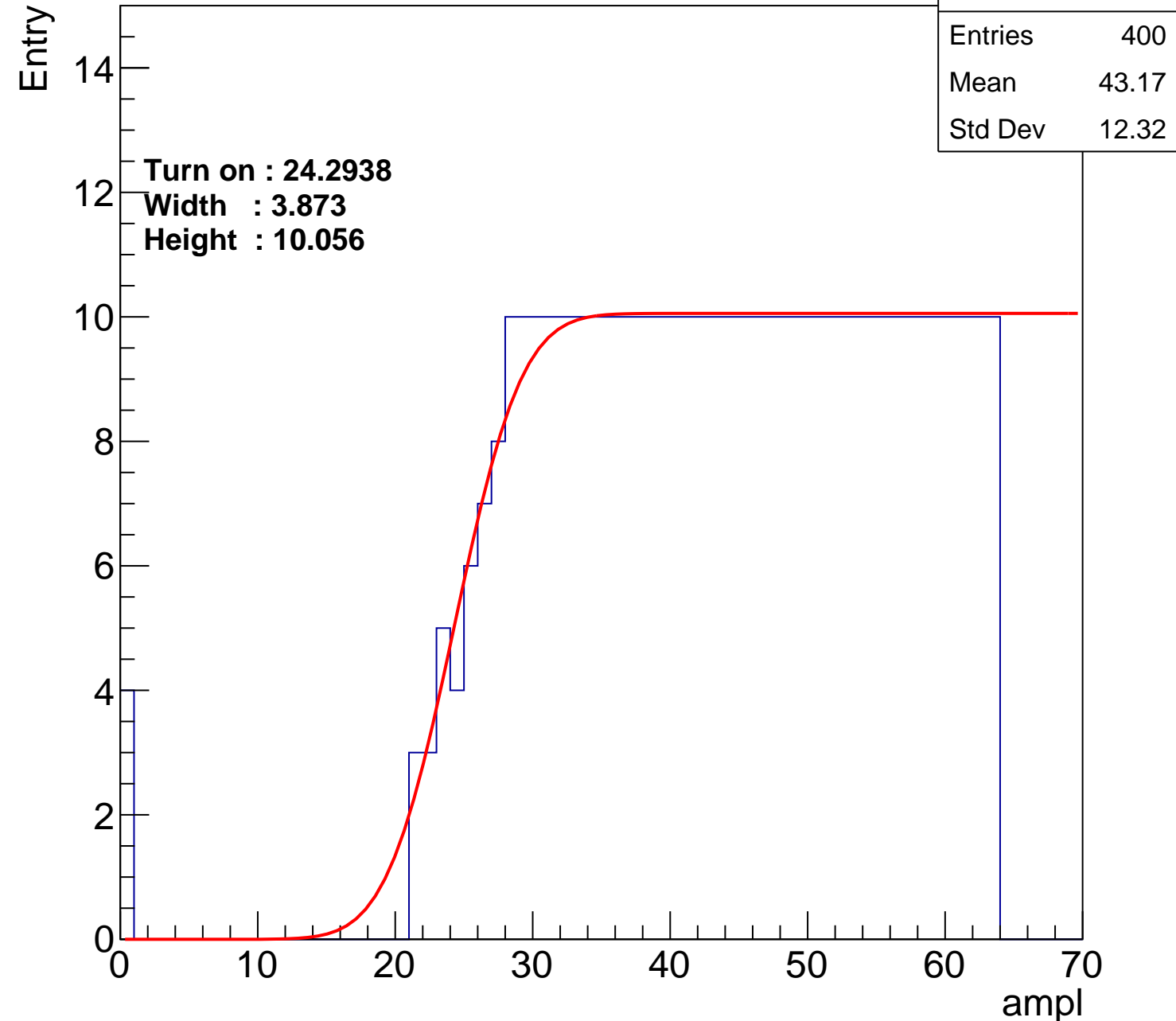
Width : 3.873

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.63
Std Dev	11.48

Turn on : 27.2948

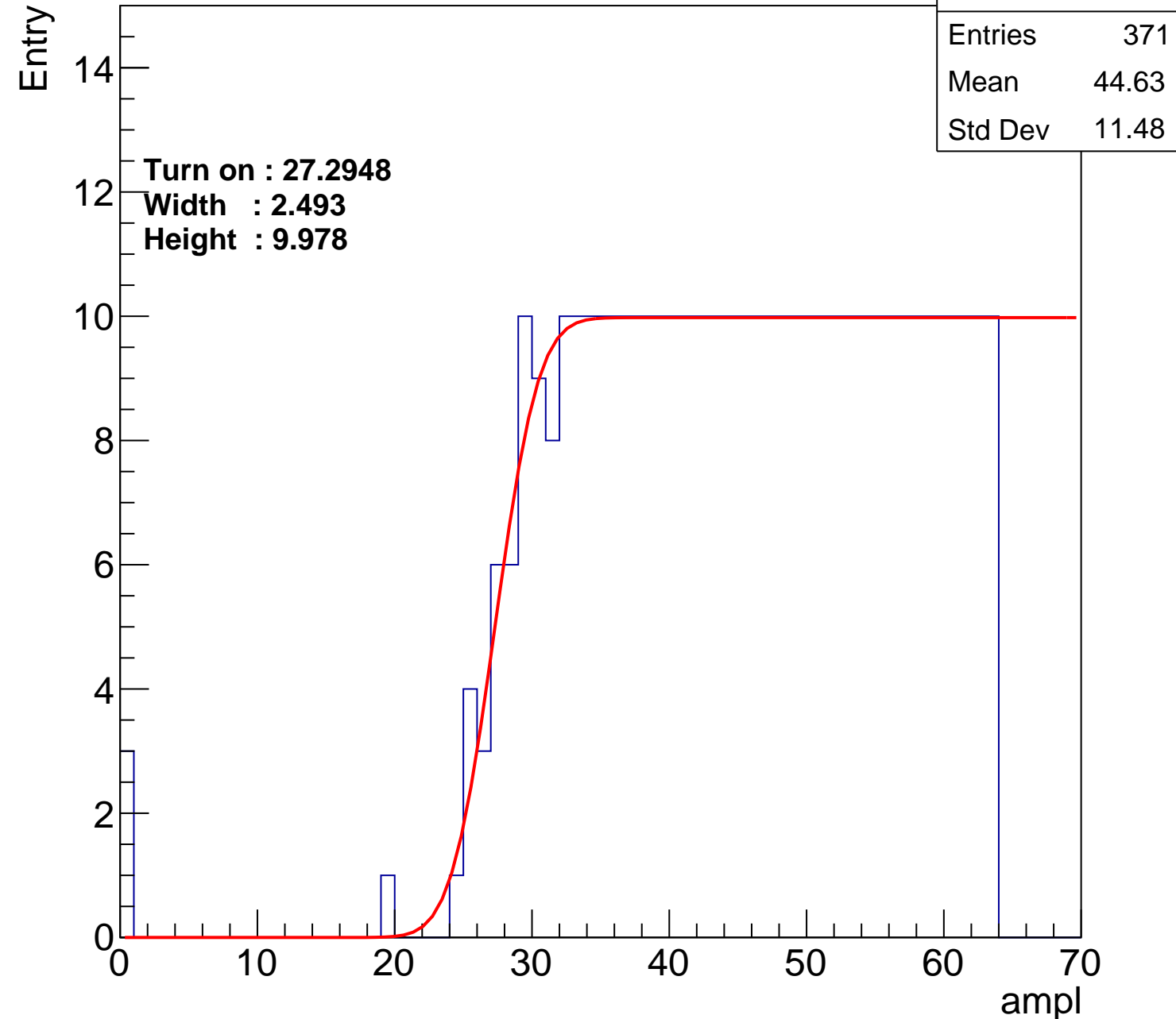
Width : 2.493

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.65
Std Dev	11.38

Turn on : 27.5934

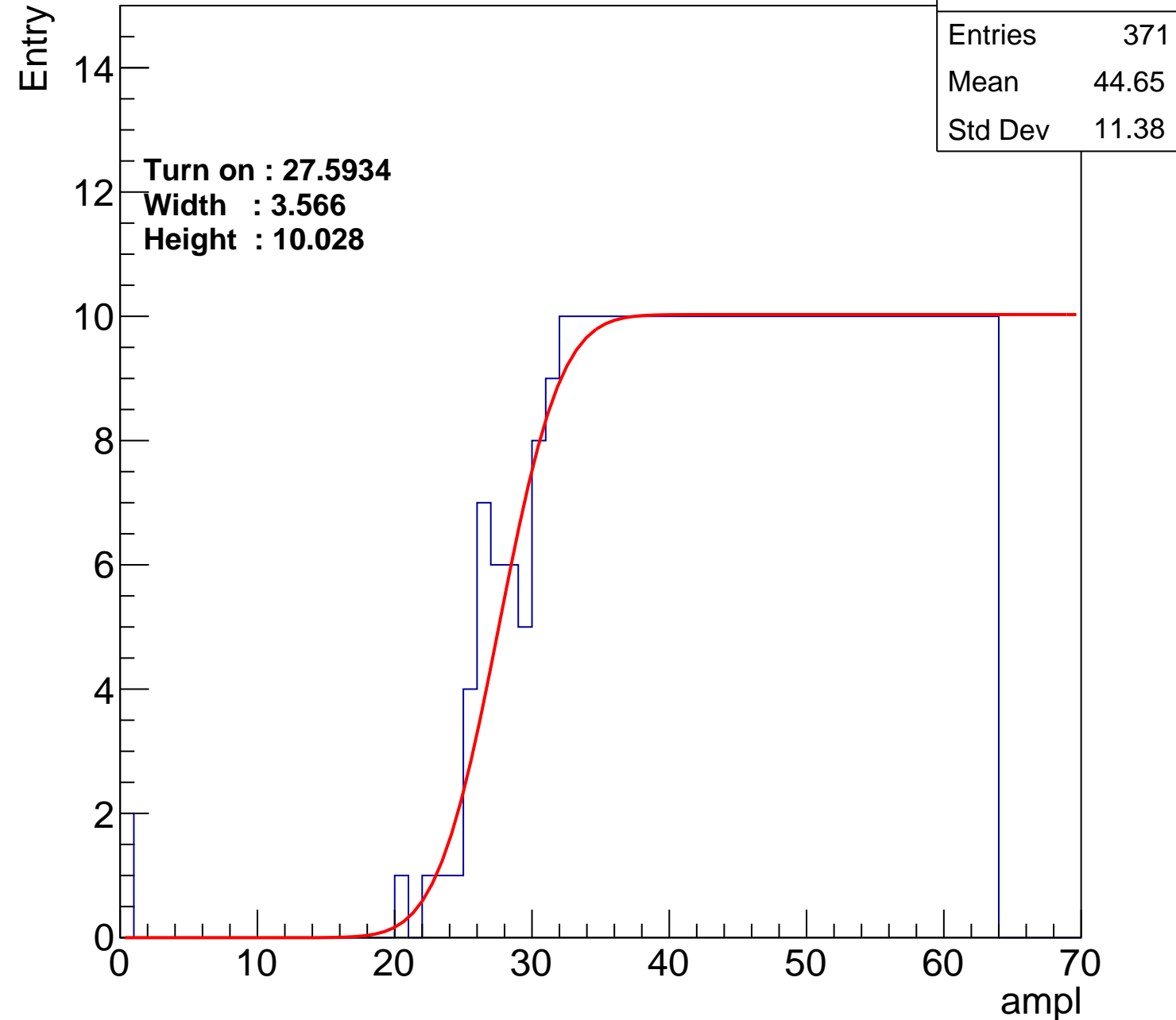
Width : 3.566

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.27
Std Dev	11.99

Turn on : 27.2027

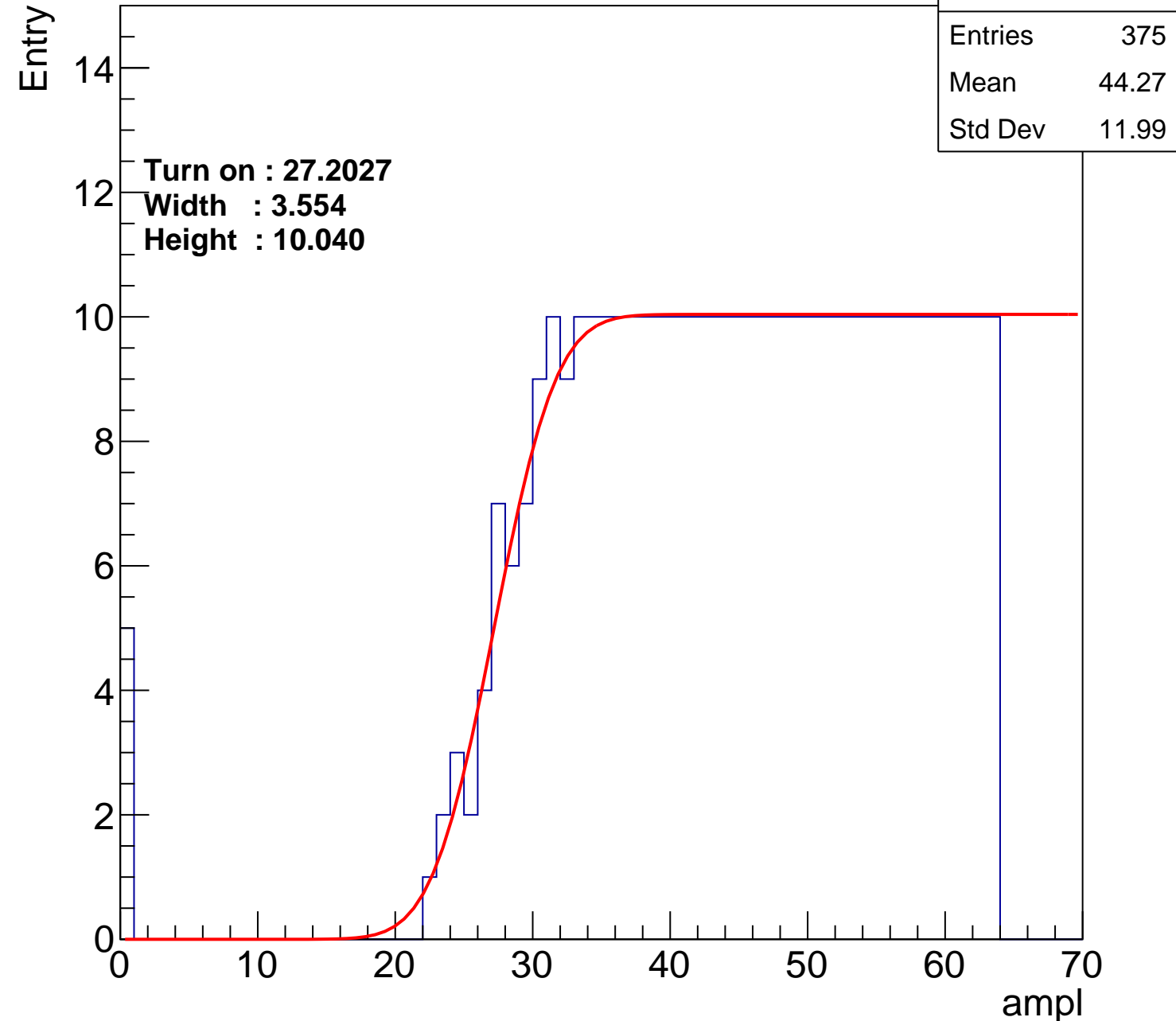
Width : 3.554

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	46.03
Std Dev	10.4

Turn on : 30.0297

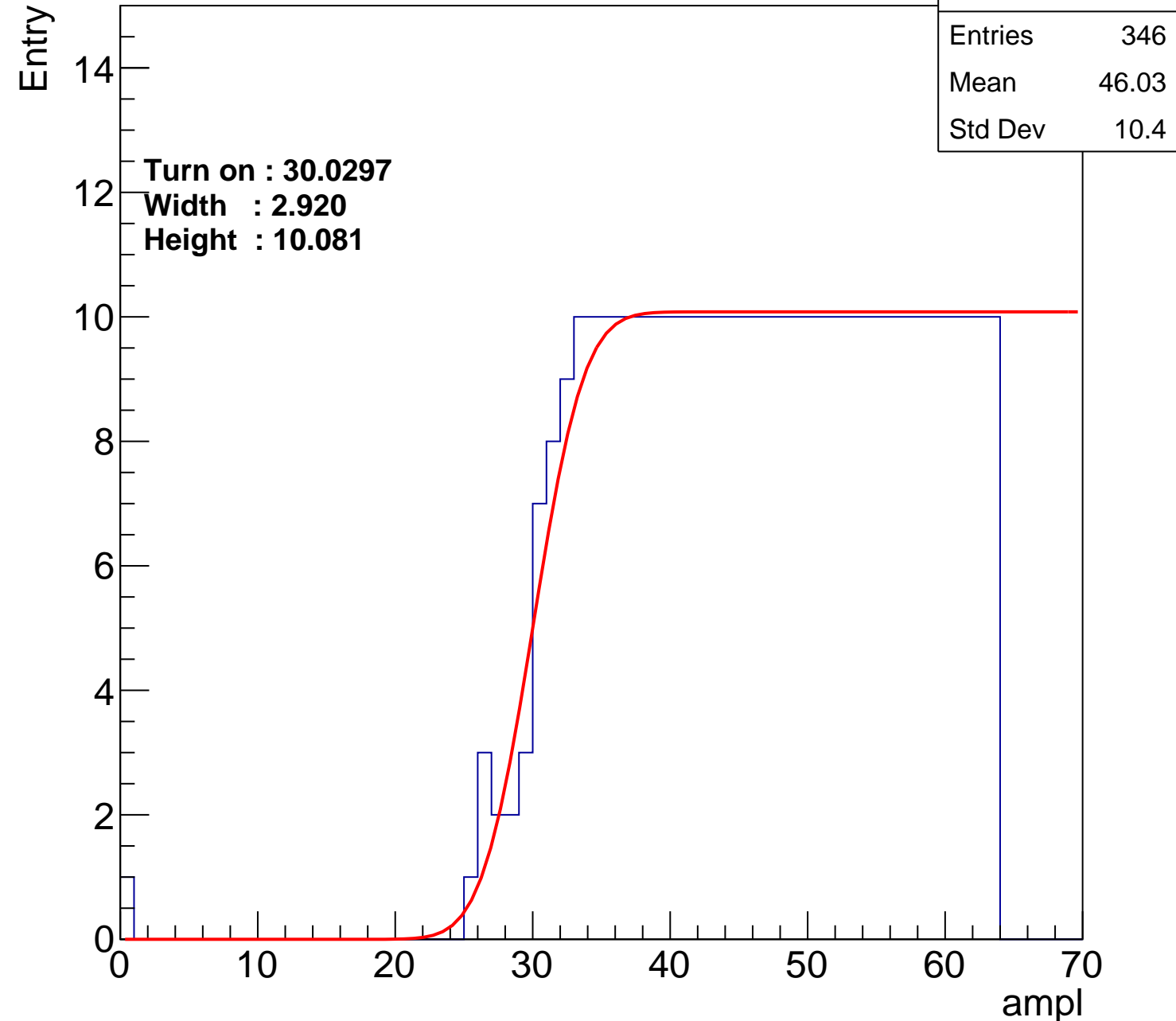
Width : 2.920

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U15-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	46.03
Std Dev	10.4

Turn on : 30.0297

Width : 2.920

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl

