

B0L002S, U16-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 26.1631

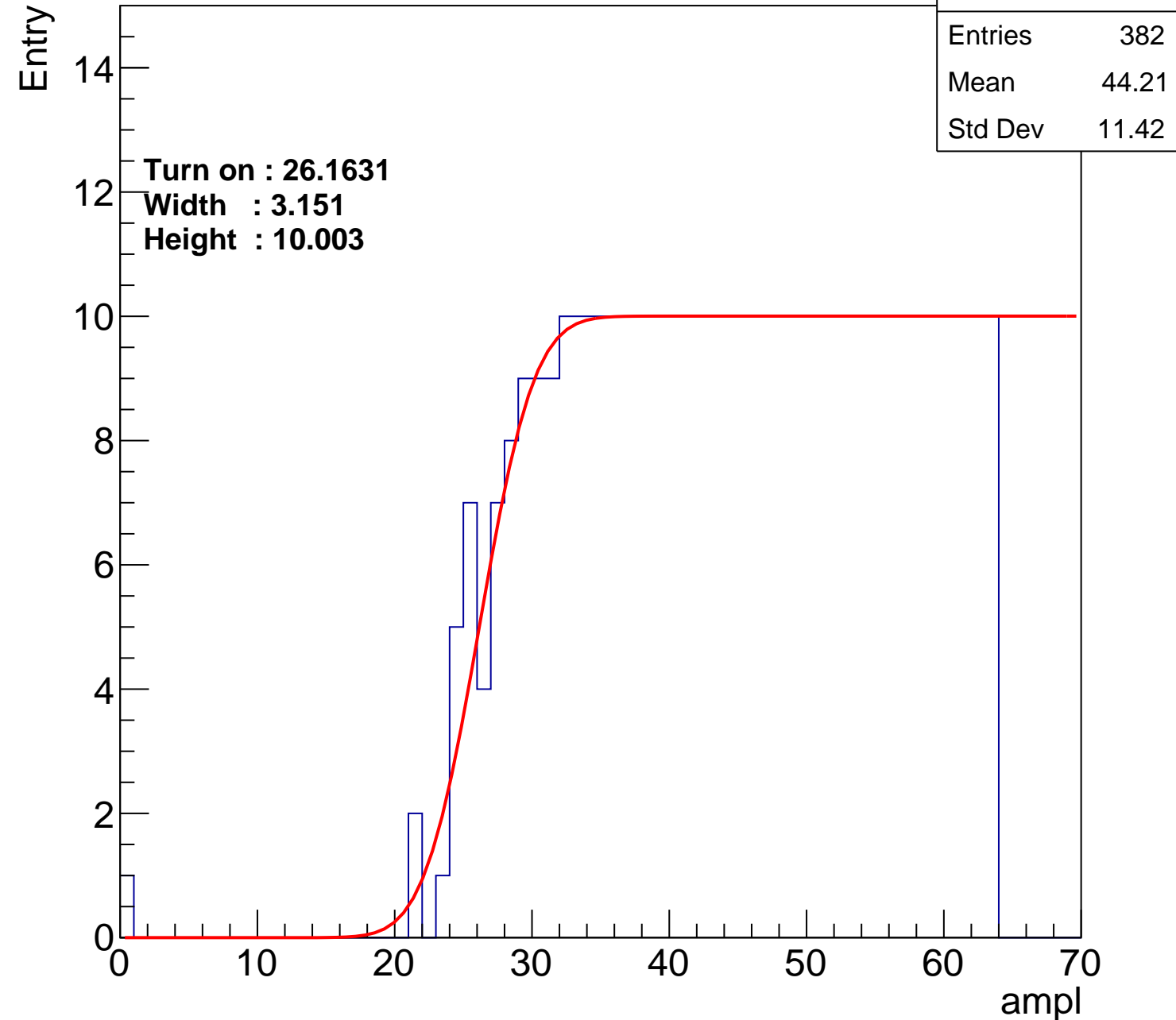
Width : 3.151

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	44.04
Std Dev	11.61

Turn on : 25.7071

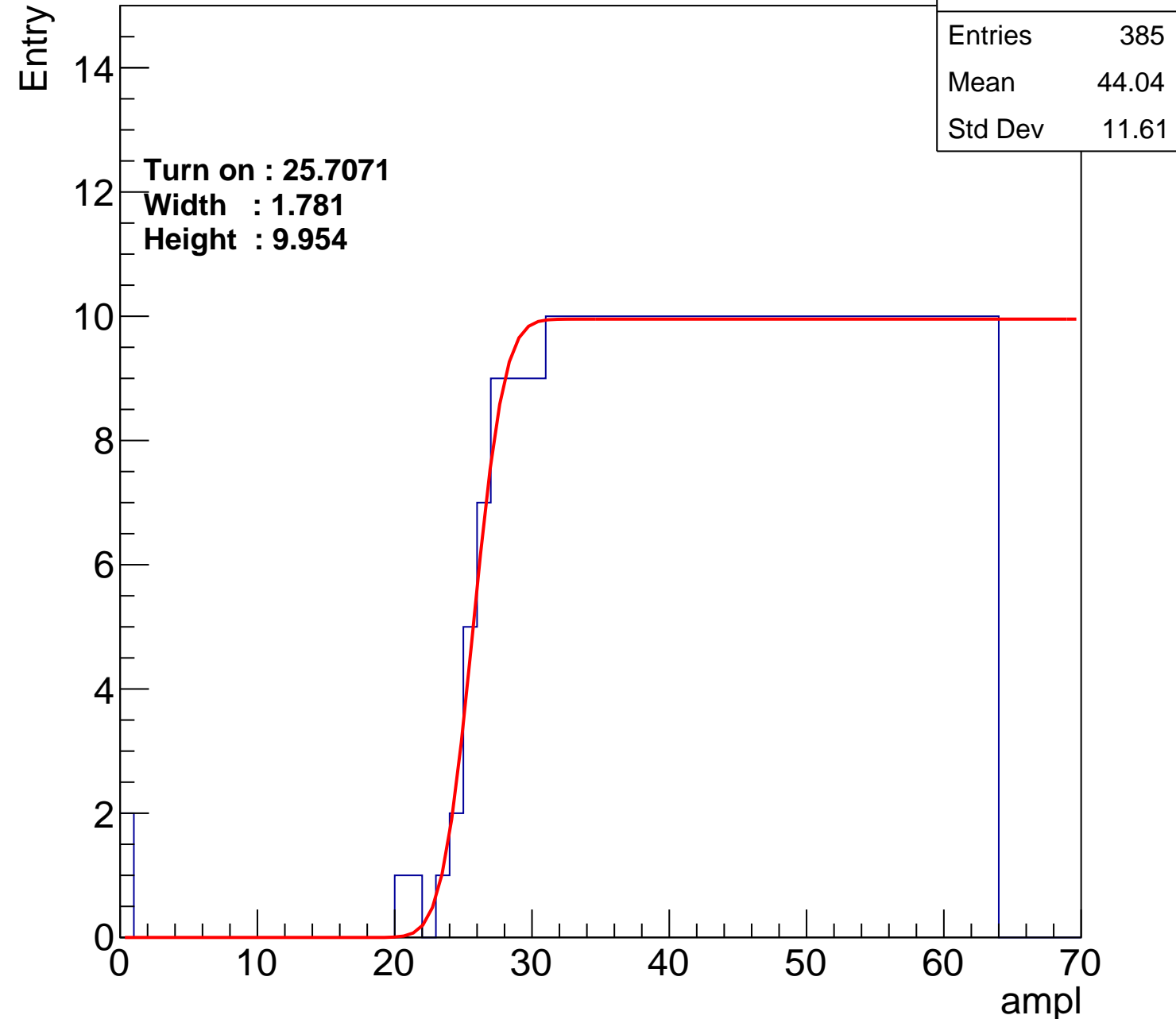
Width : 1.781

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.64
Std Dev	11.21

Turn on : 26.8622

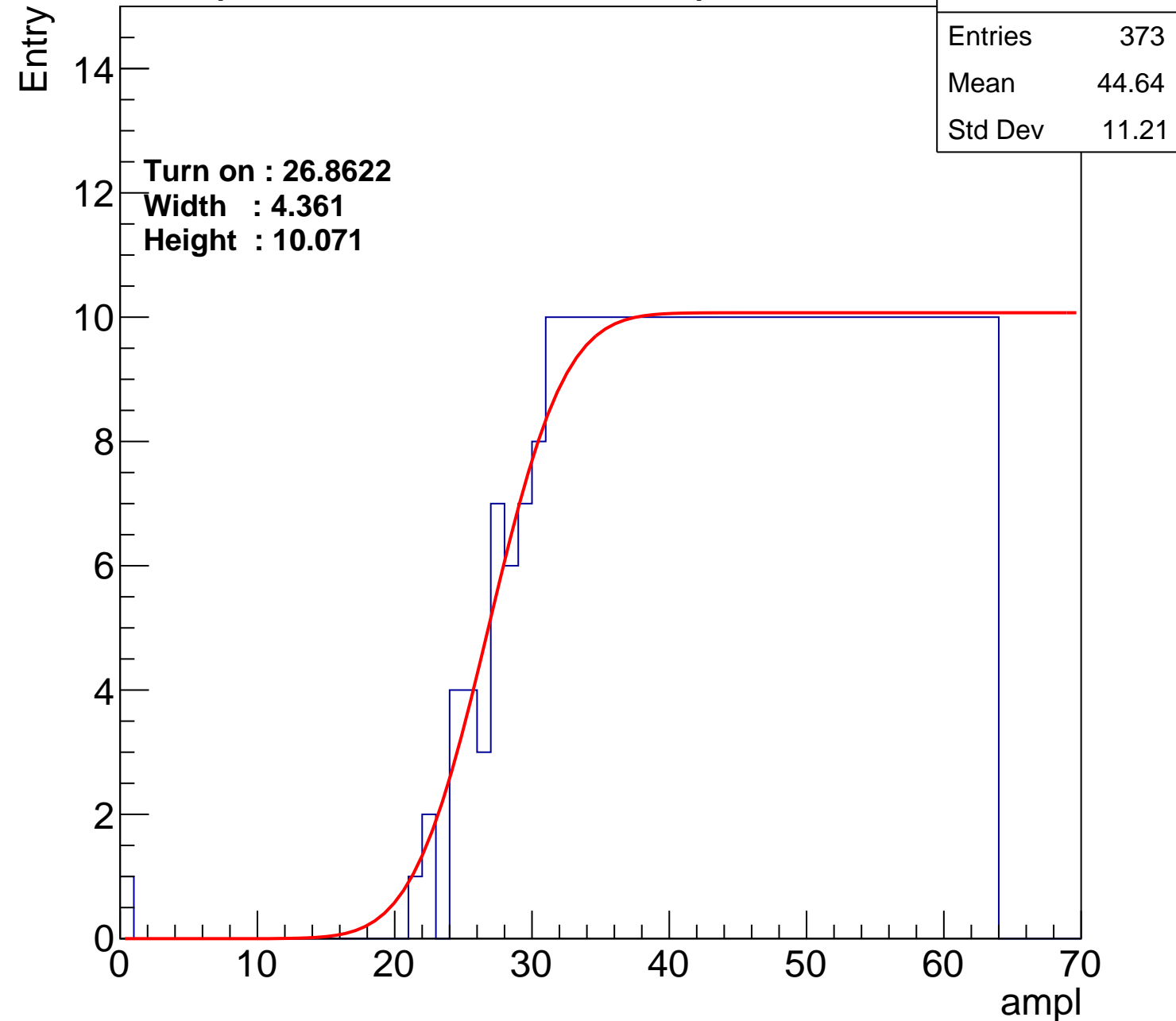
Width : 4.361

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.02
Std Dev	11.89

Turn on : 26.5244

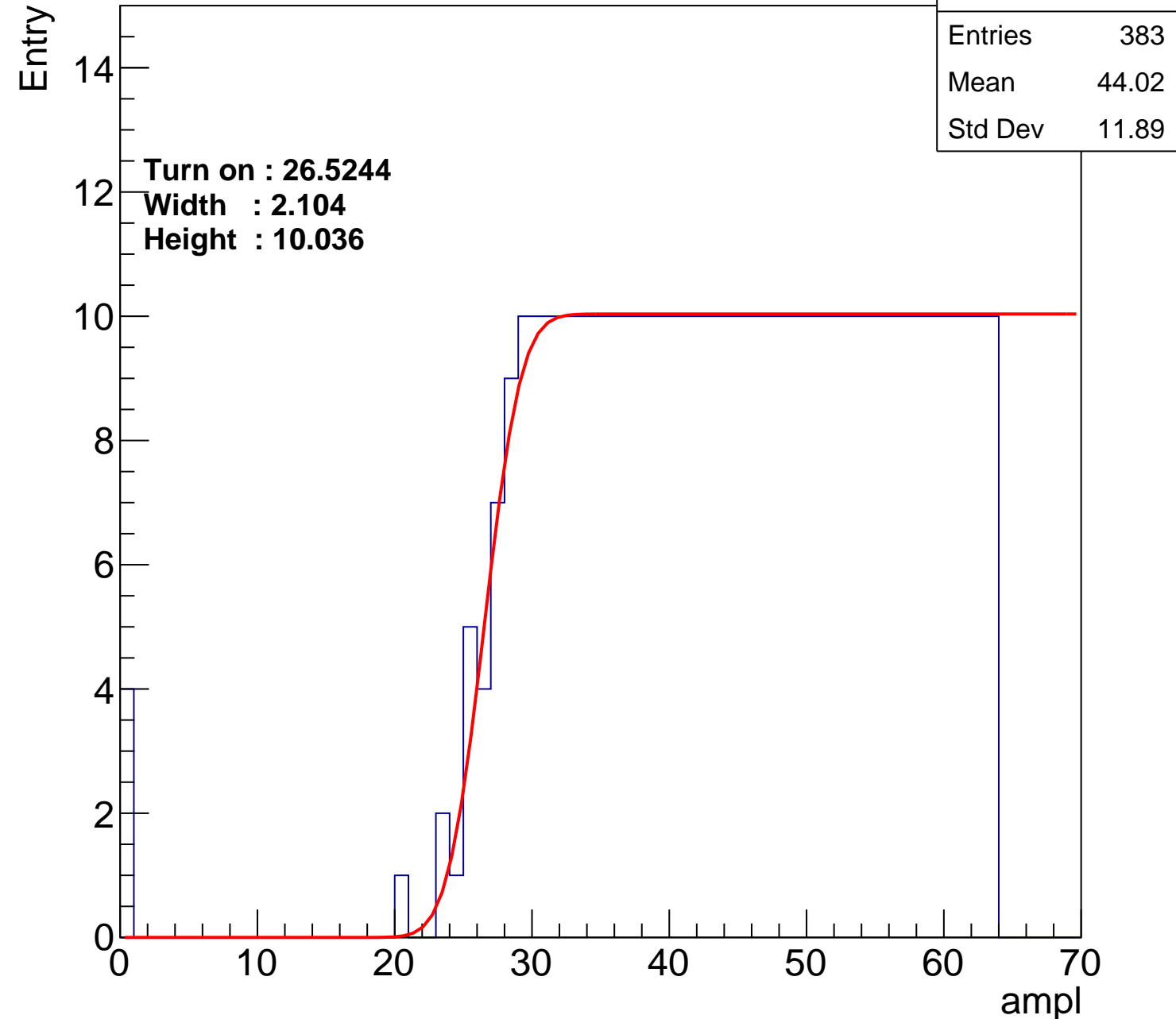
Width : 2.104

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch4

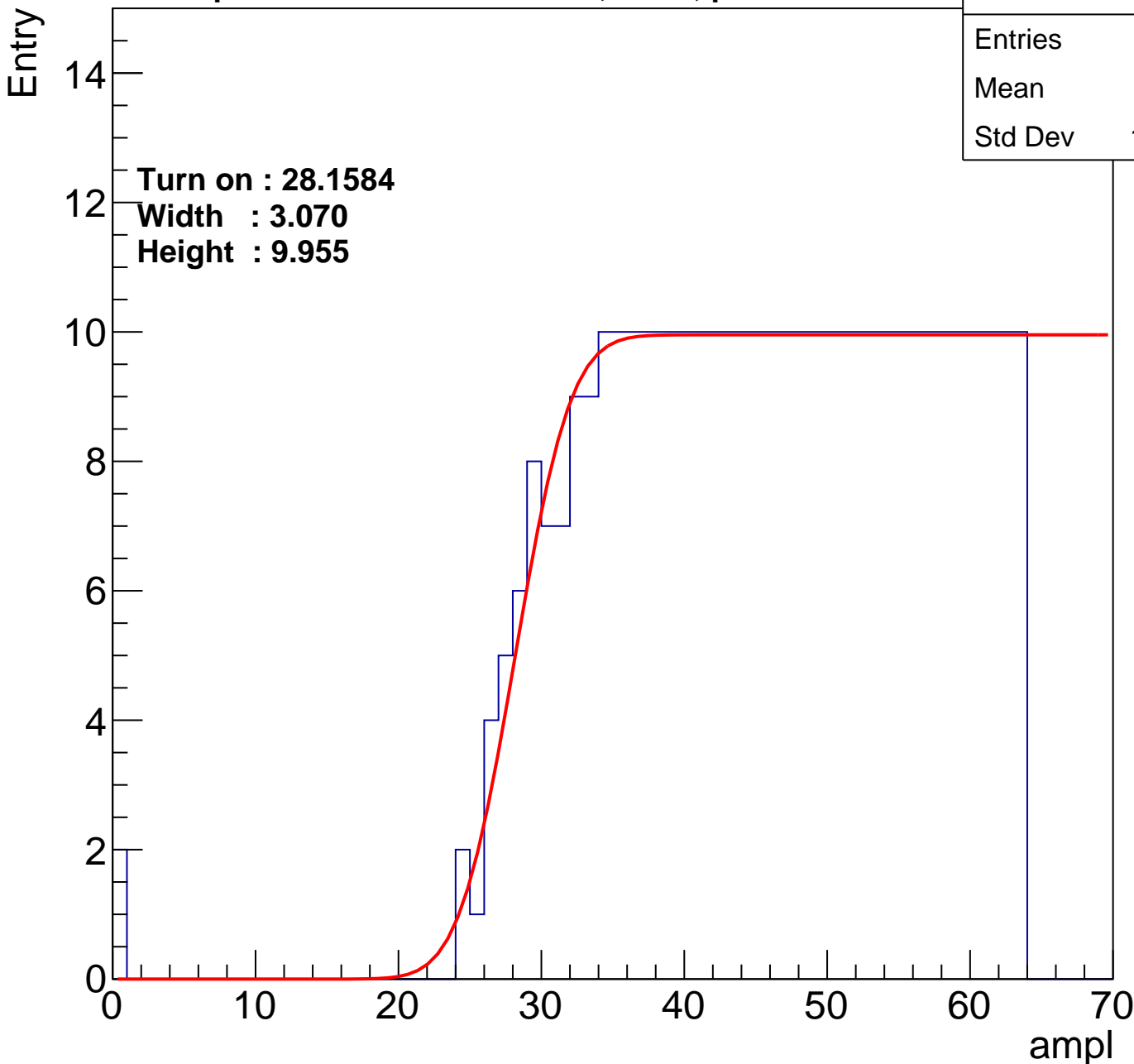
calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.21
Std Dev	11.07

Turn on : 28.1584

Width : 3.070

Height : 9.955



B0L002S, U16-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.24
Std Dev	11.06

Turn on : 28.3691

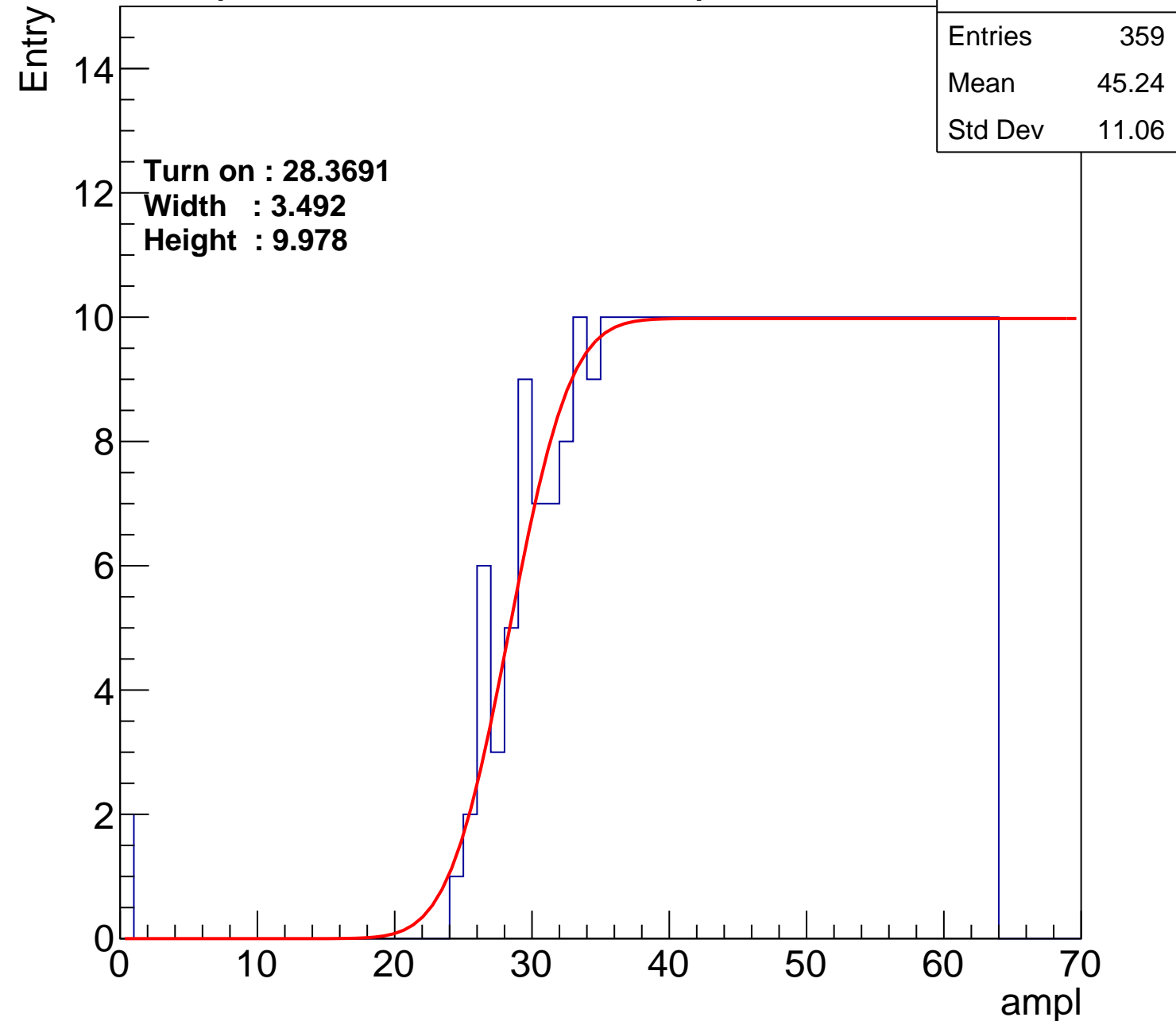
Width : 3.492

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch6

calib_packv5_042523_0143.root, FC#8, port C1

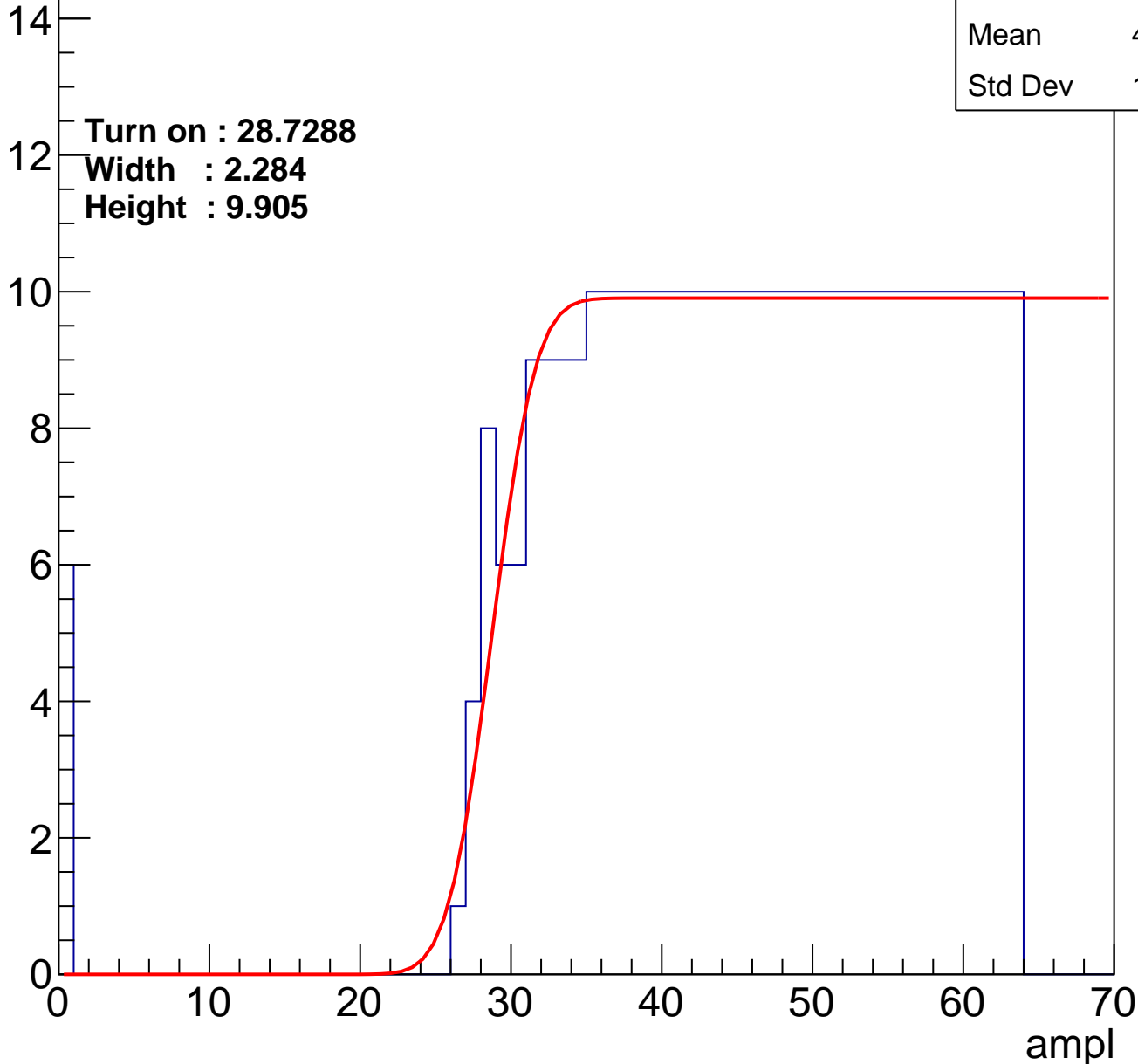
Entry

Entries	357
Mean	45.08
Std Dev	11.79

Turn on : 28.7288

Width : 2.284

Height : 9.905



B0L002S, U16-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.83
Std Dev	11.05

Turn on : 27.0372

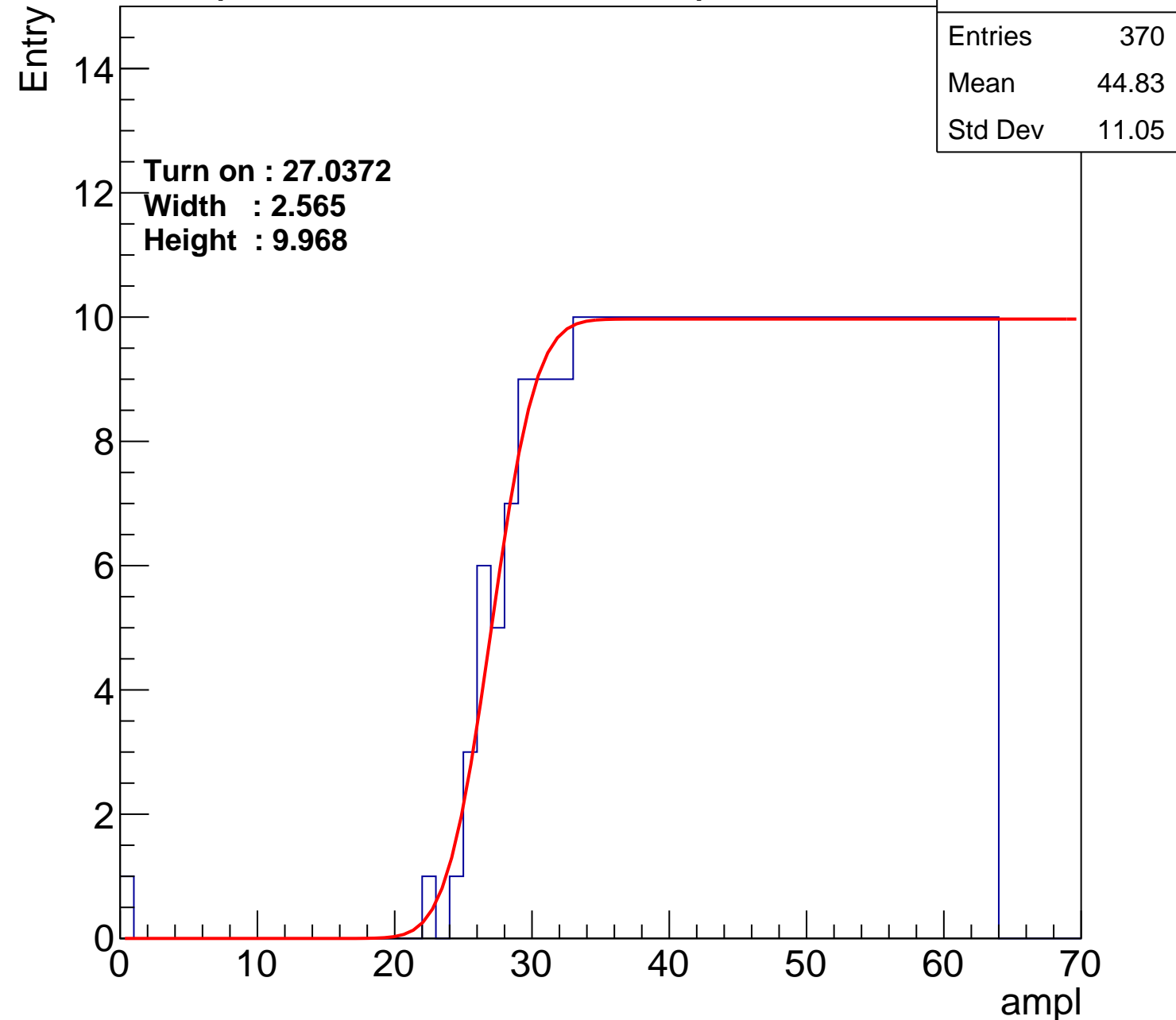
Width : 2.565

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.99
Std Dev	11.21

Turn on : 28.1095

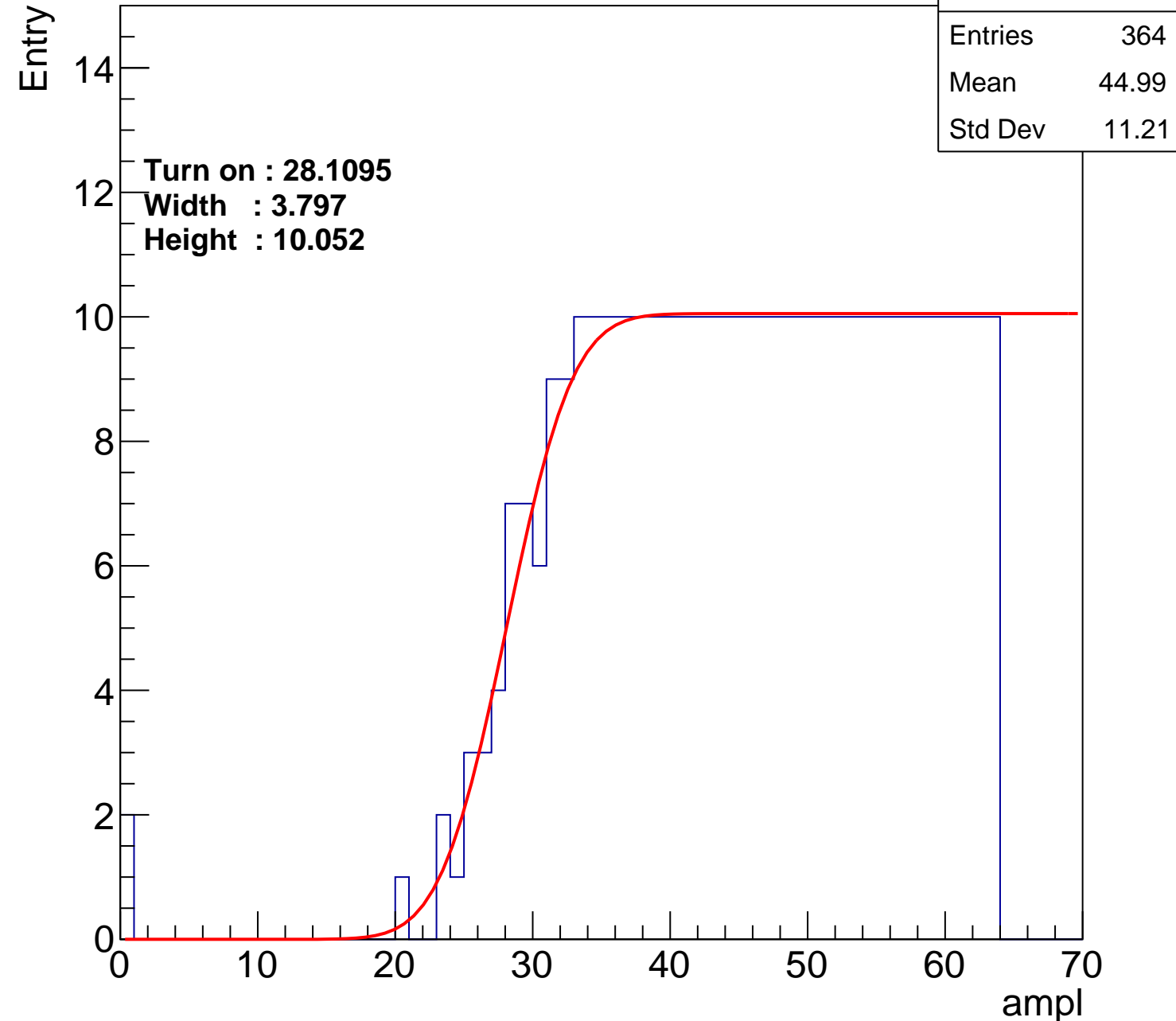
Width : 3.797

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch9

calib_packv5_042523_0143.root, FC#8, port C1

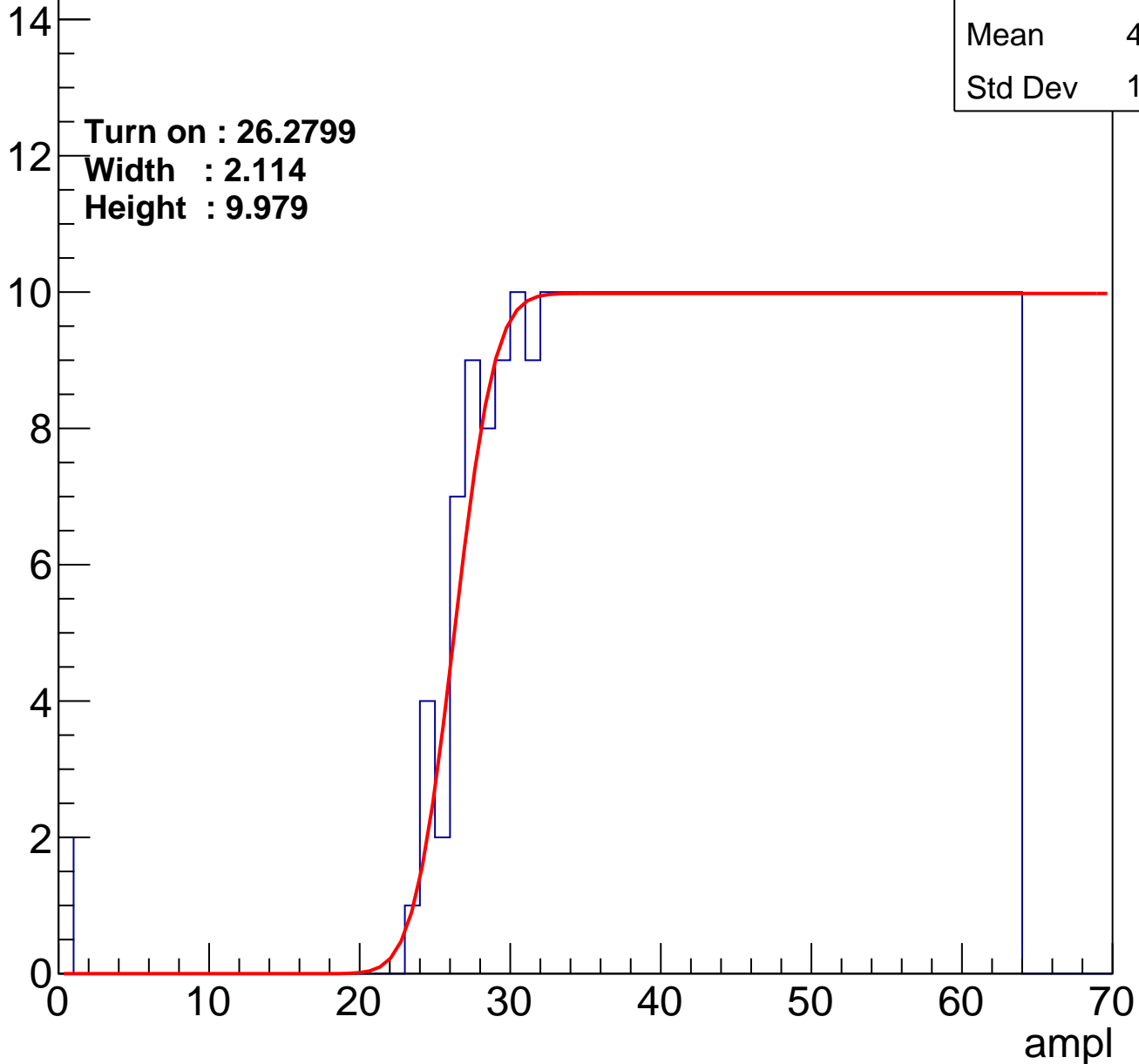
Entry

Entries	381
Mean	44.25
Std Dev	11.48

Turn on : 26.2799

Width : 2.114

Height : 9.979



B0L002S, U16-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	392
Mean	43.72
Std Dev	11.68

Turn on : 25.3808

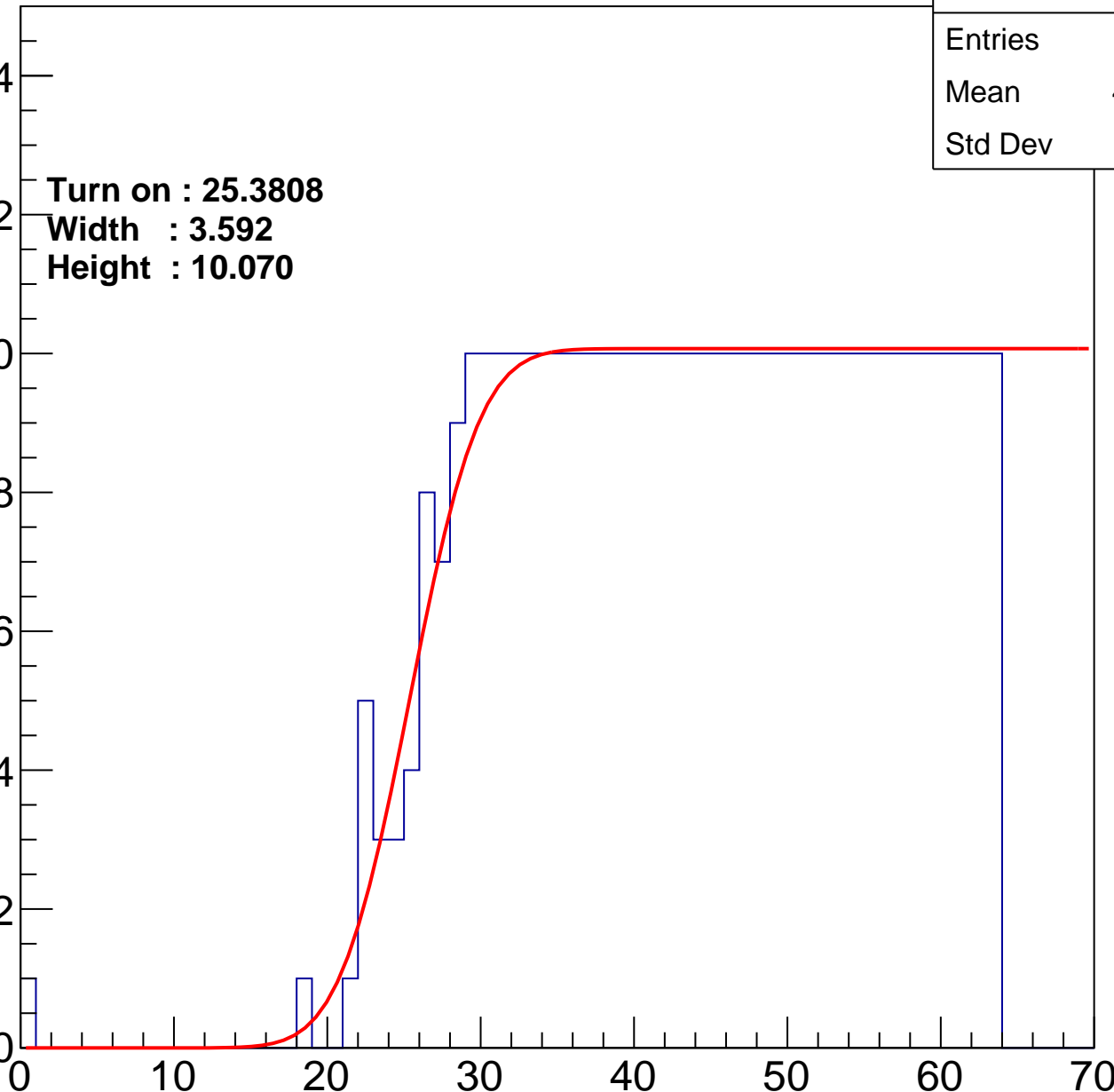
Width : 3.592

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 27.3002

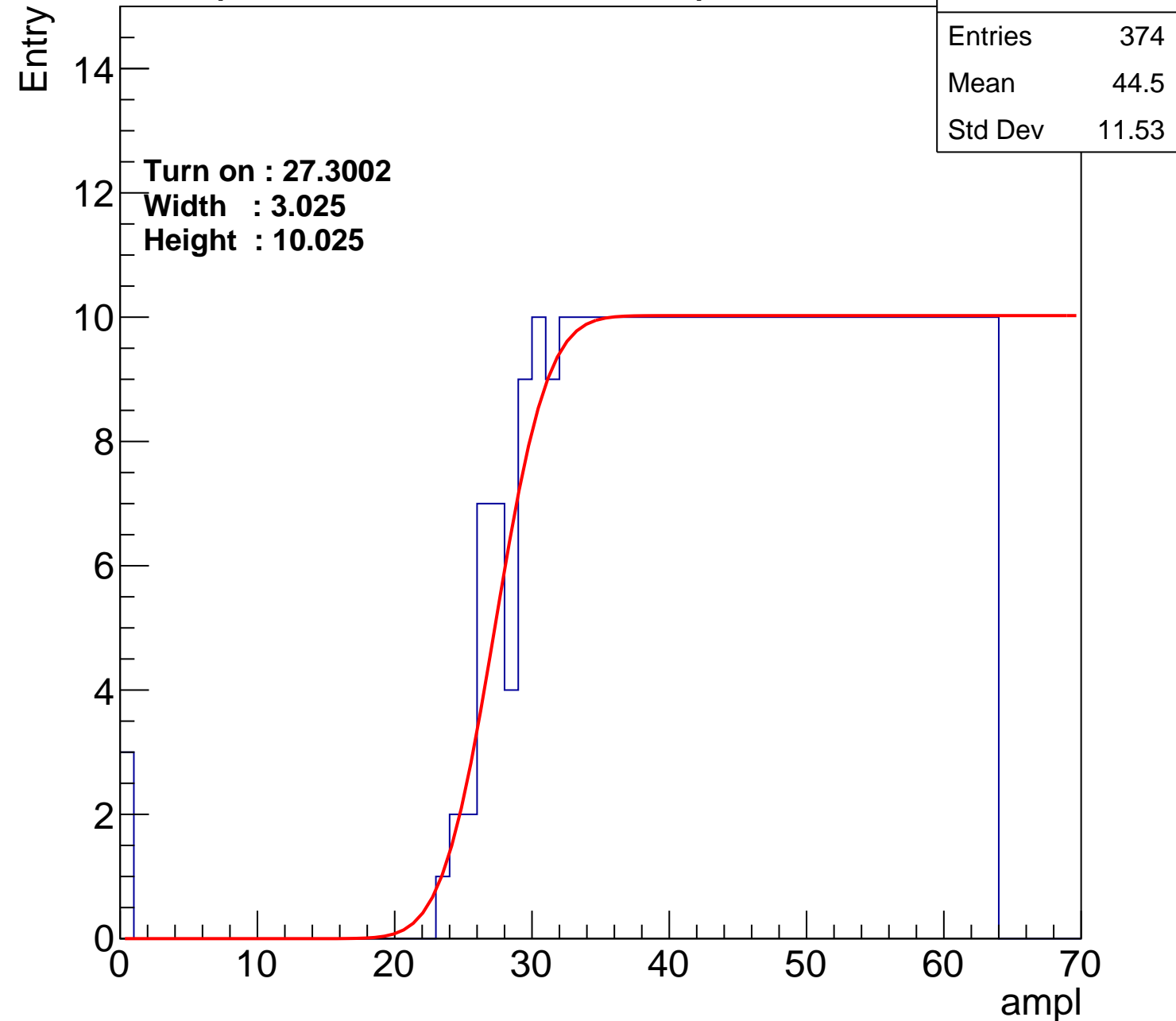
Width : 3.025

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch12

calib_packv5_042523_0143.root, FC#8, port C1

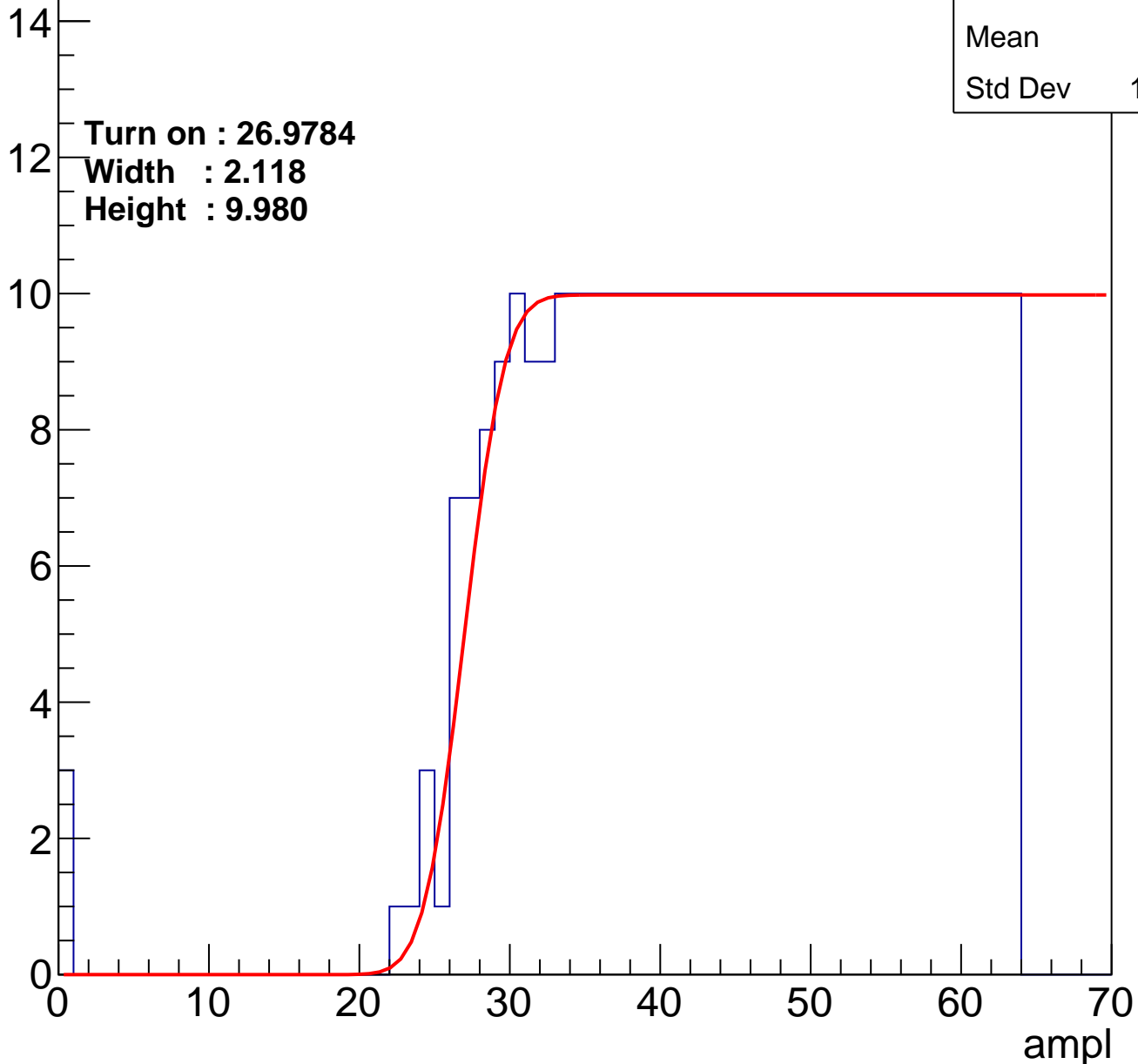
Entries	378
Mean	44.3
Std Dev	11.63

Turn on : 26.9784

Width : 2.118

Height : 9.980

Entry



B0L002S, U16-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.8144

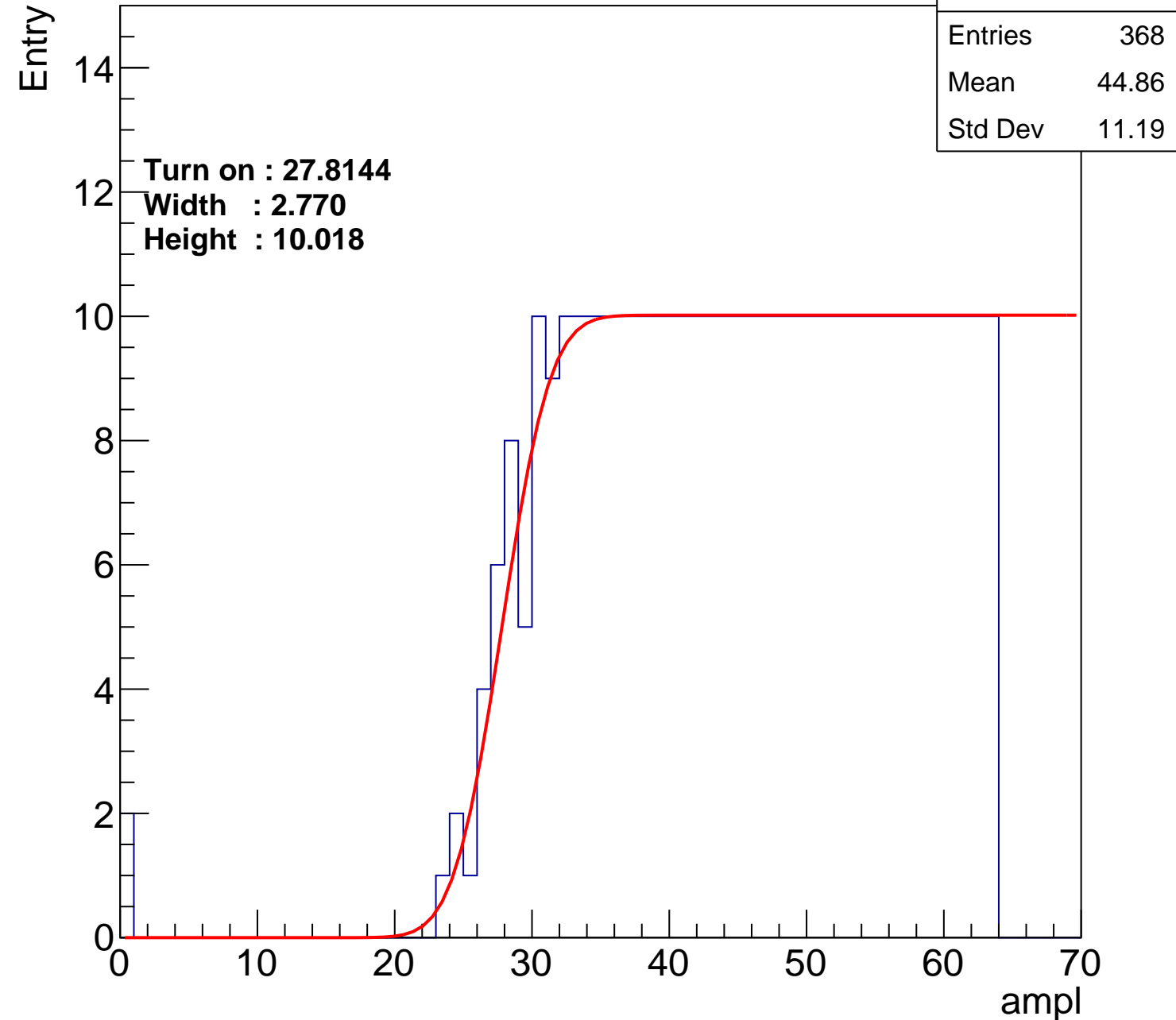
Width : 2.770

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch14

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.26
Std Dev	11.96

Turn on : 27.1499

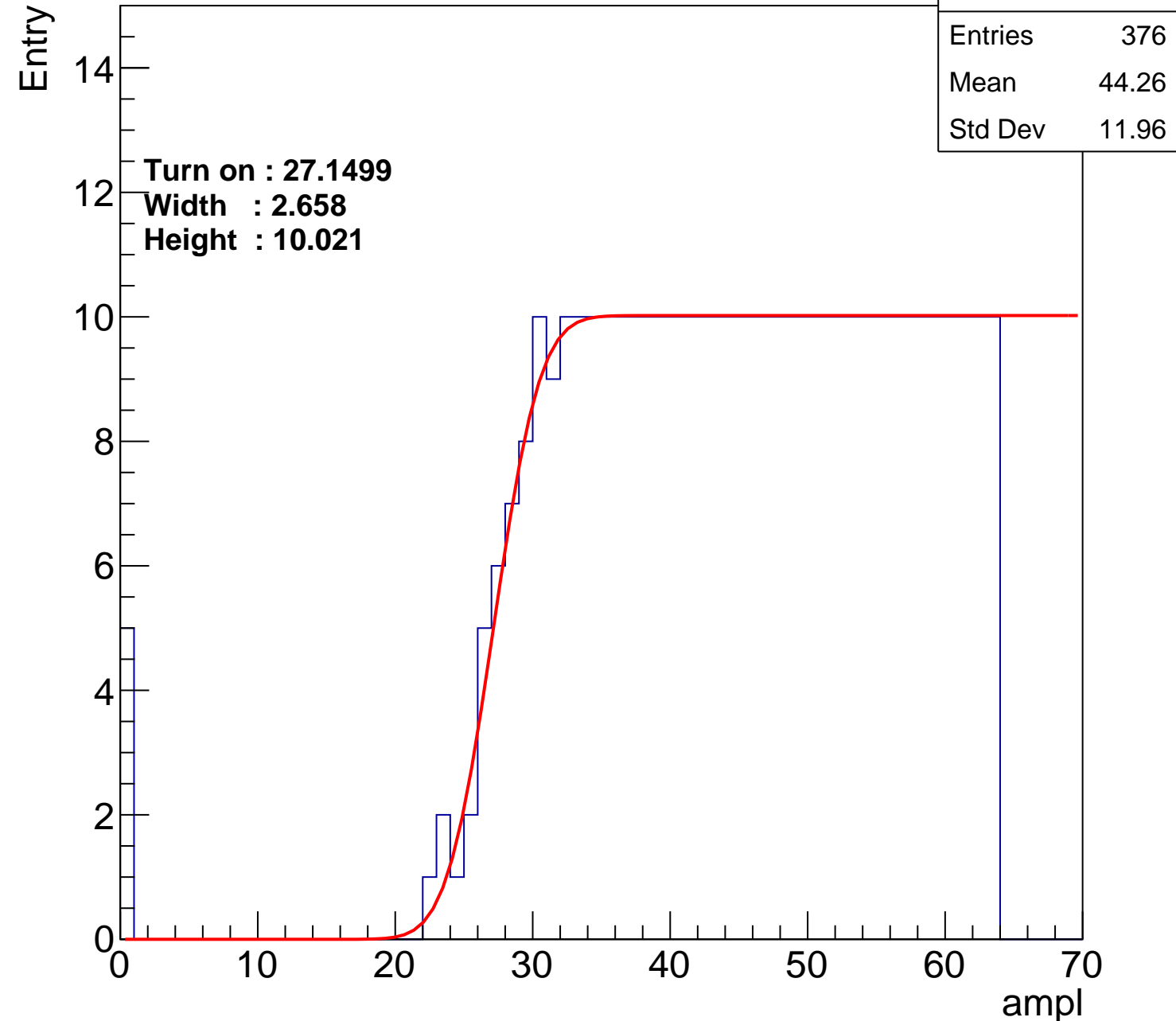
Width : 2.658

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch15

calib_packv5_042523_0143.root, FC#8, port C1

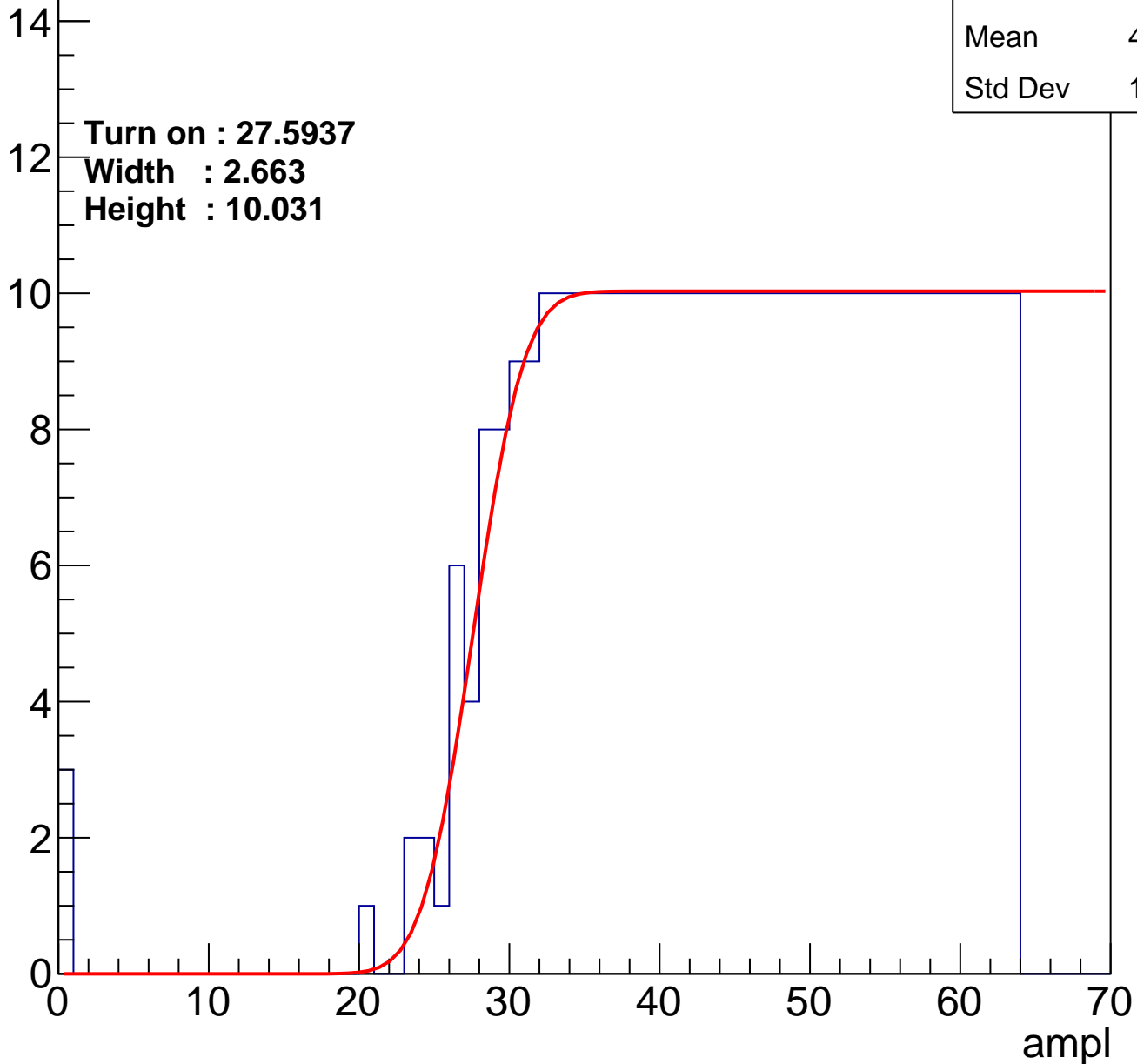
Entries	373
Mean	44.53
Std Dev	11.55

Turn on : 27.5937

Width : 2.663

Height : 10.031

Entry



B0L002S, U16-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 26.8463

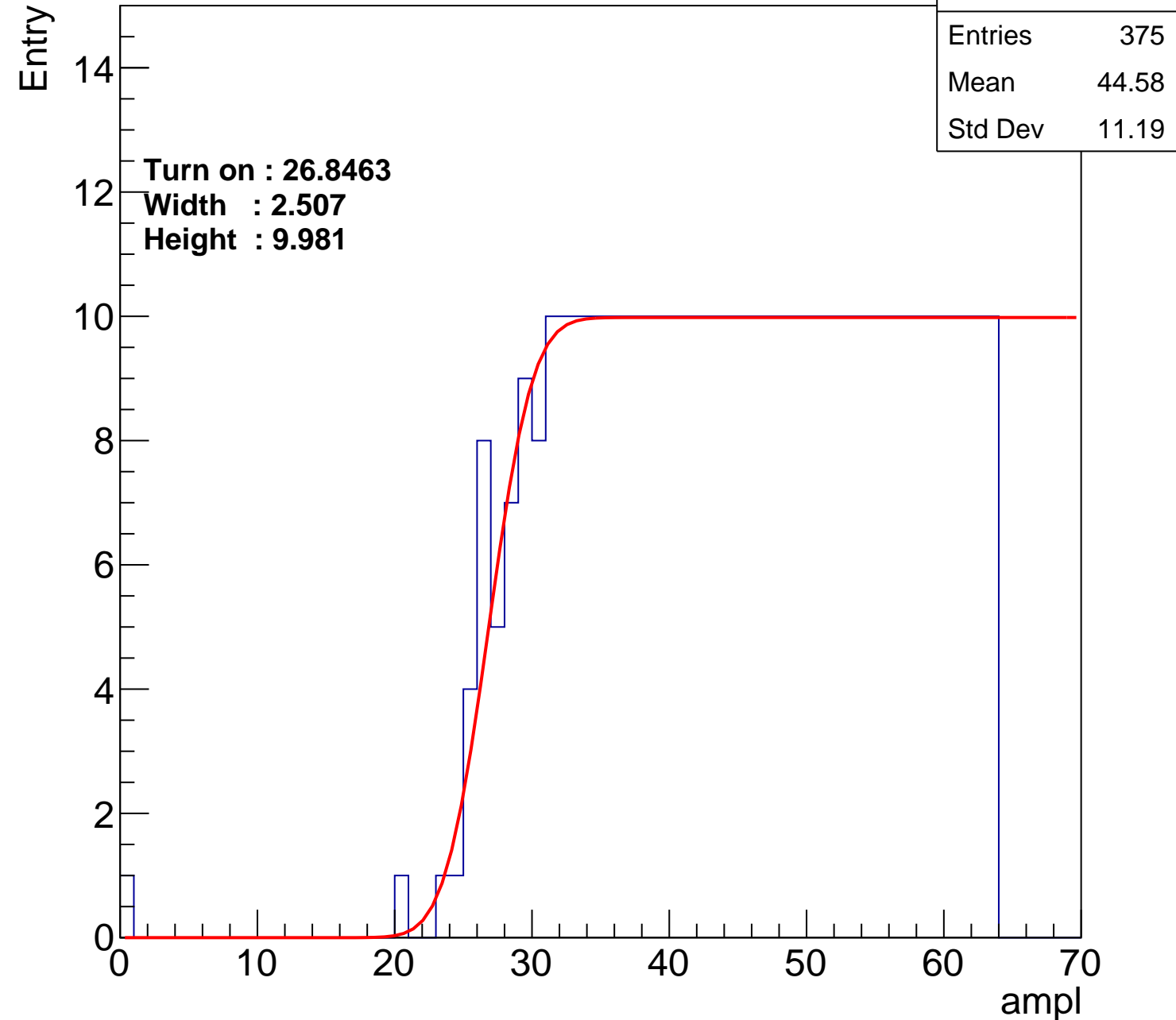
Width : 2.507

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.45
Std Dev	11.37

Turn on : 26.9334

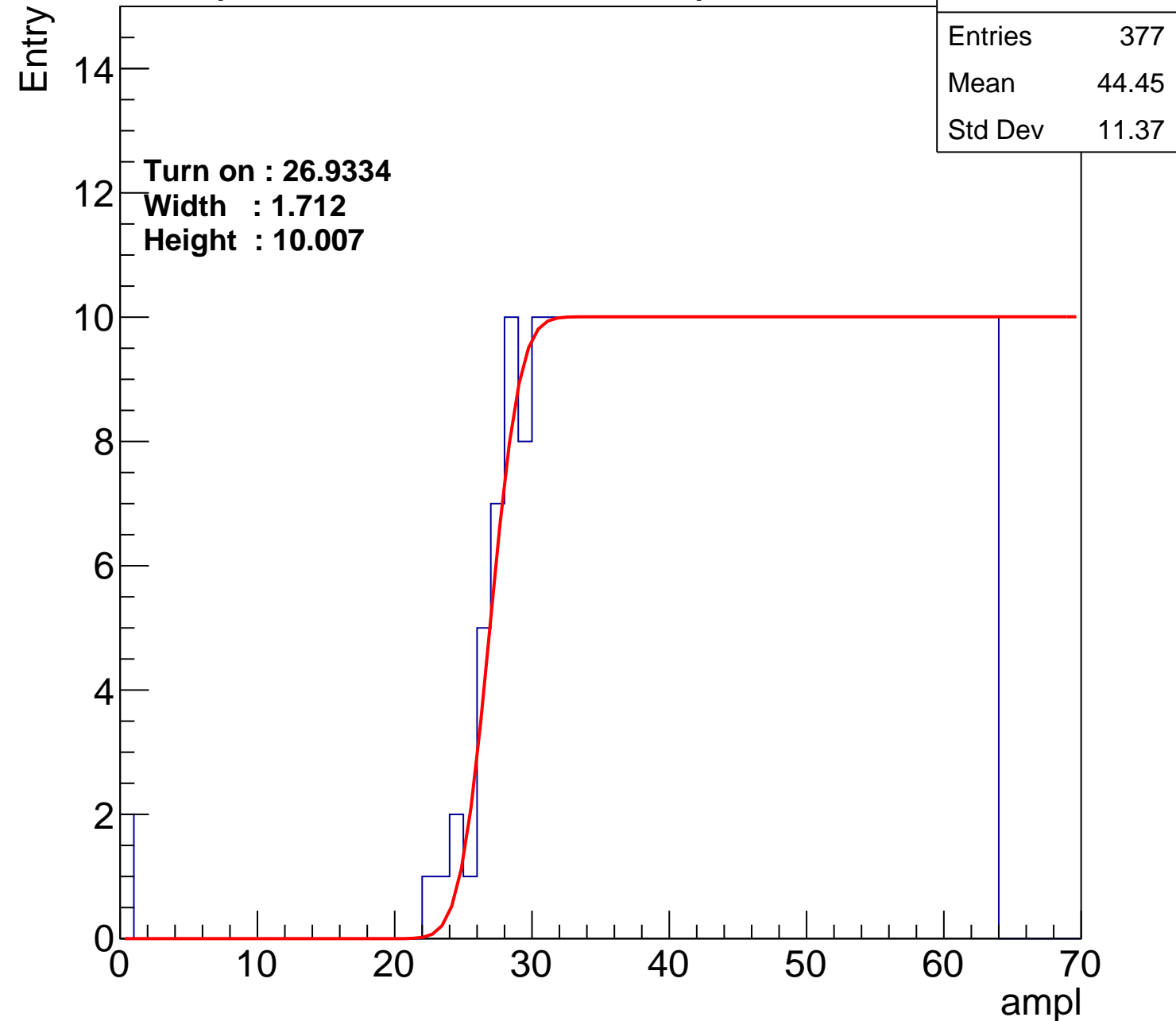
Width : 1.712

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 26.3368

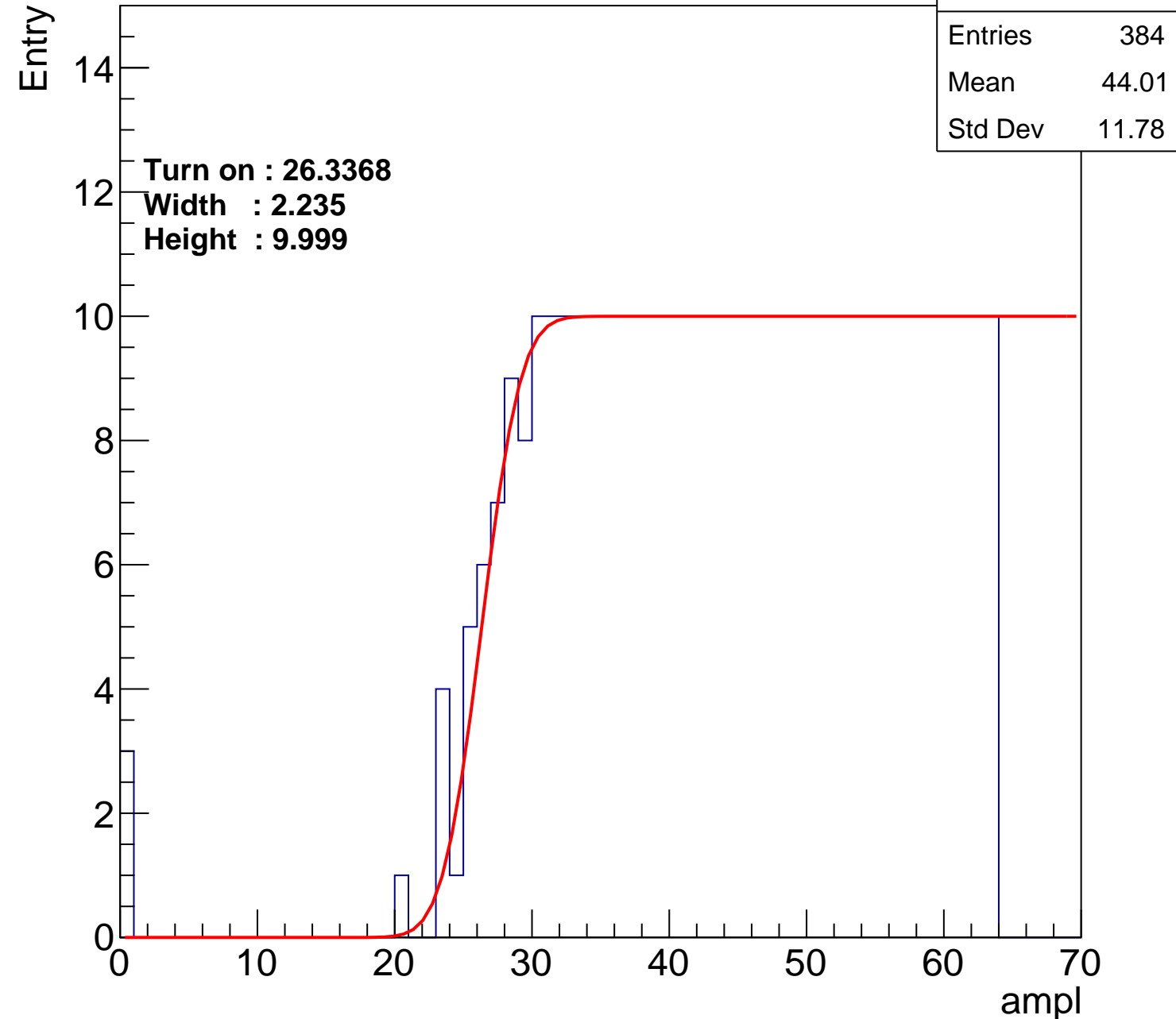
Width : 2.235

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.71
Std Dev	10.74

Turn on : 29.0679

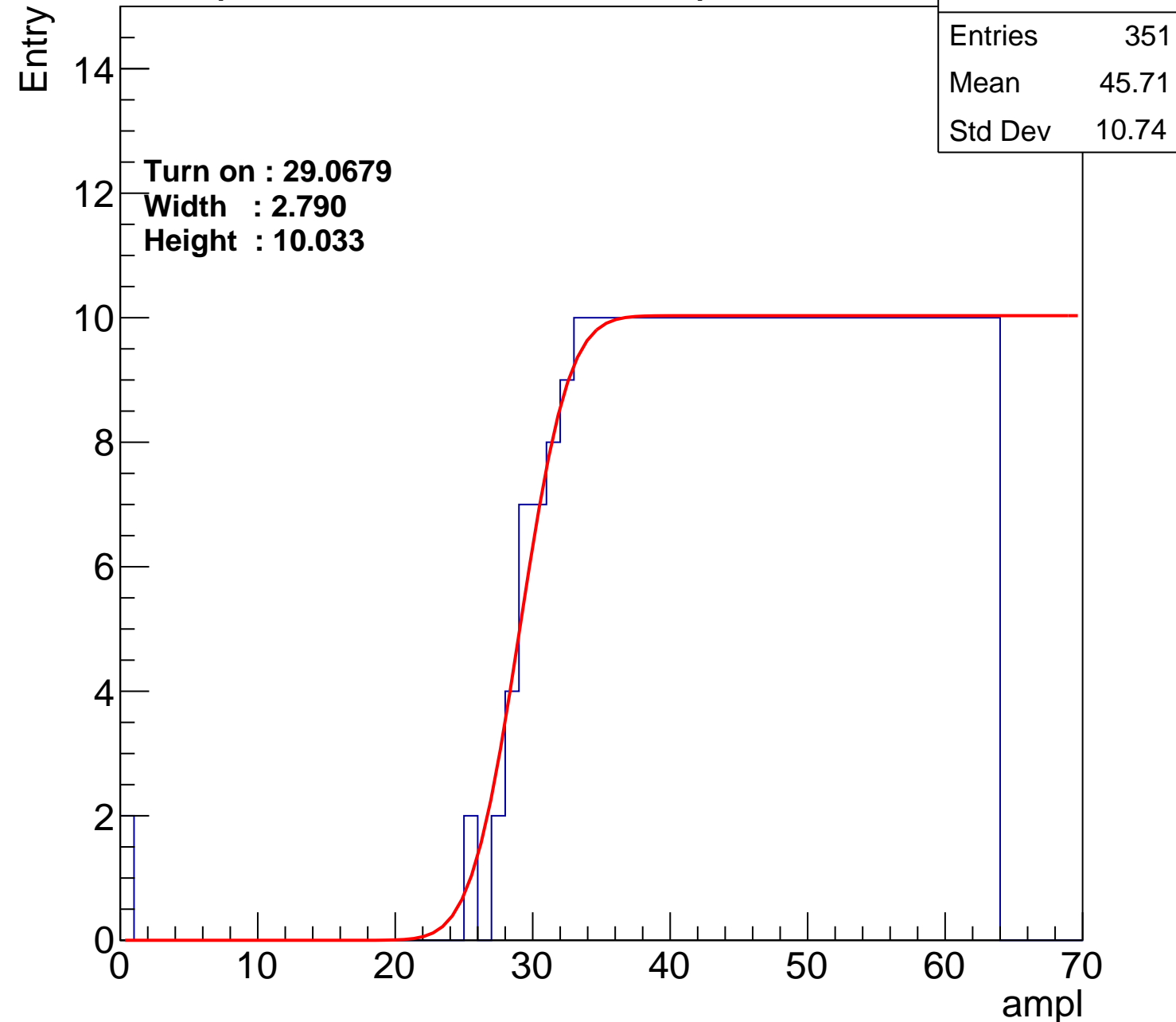
Width : 2.790

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.81
Std Dev	11.65

Turn on : 25.3437

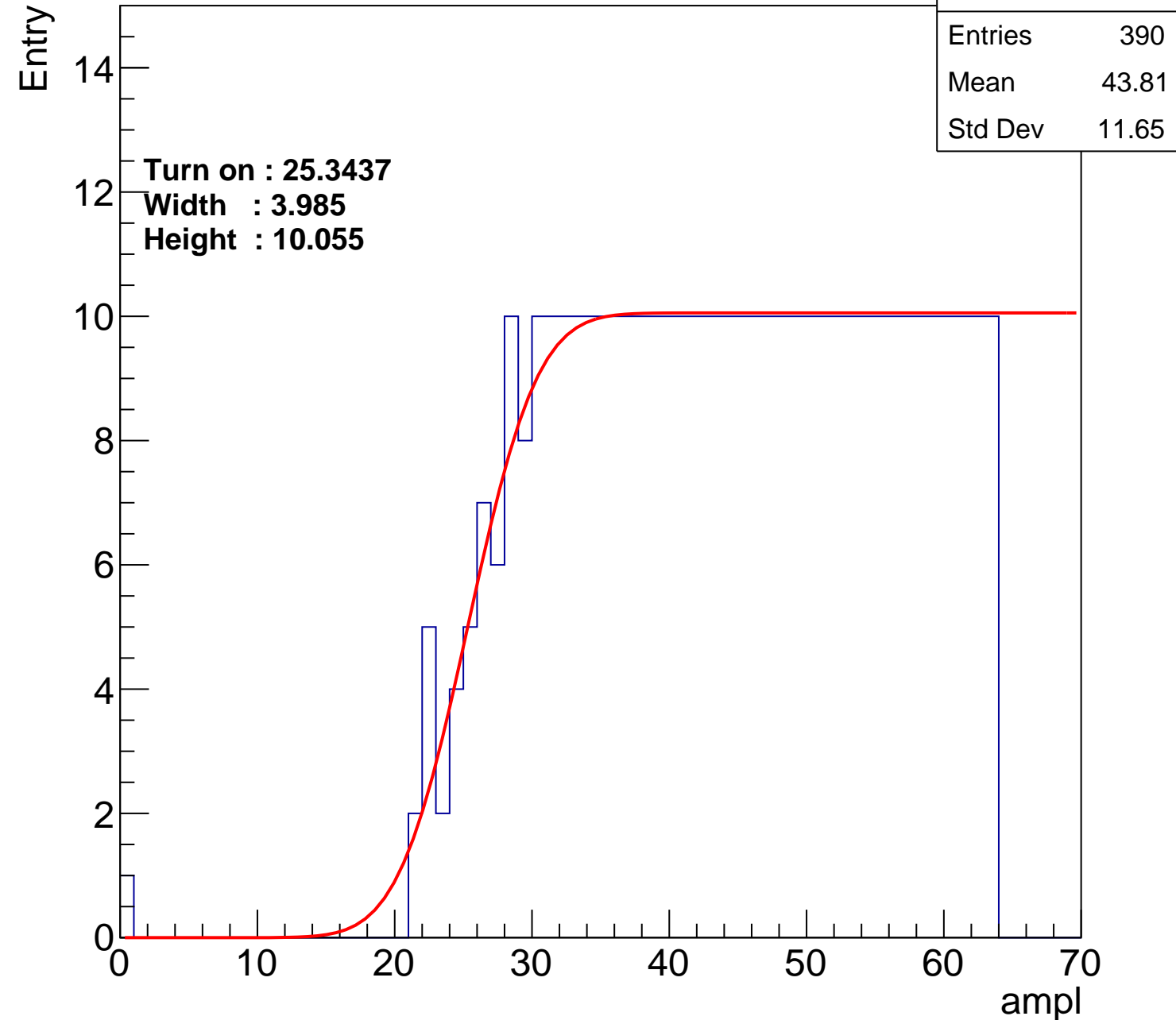
Width : 3.985

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.25
Std Dev	11.79

Turn on : 26.7547

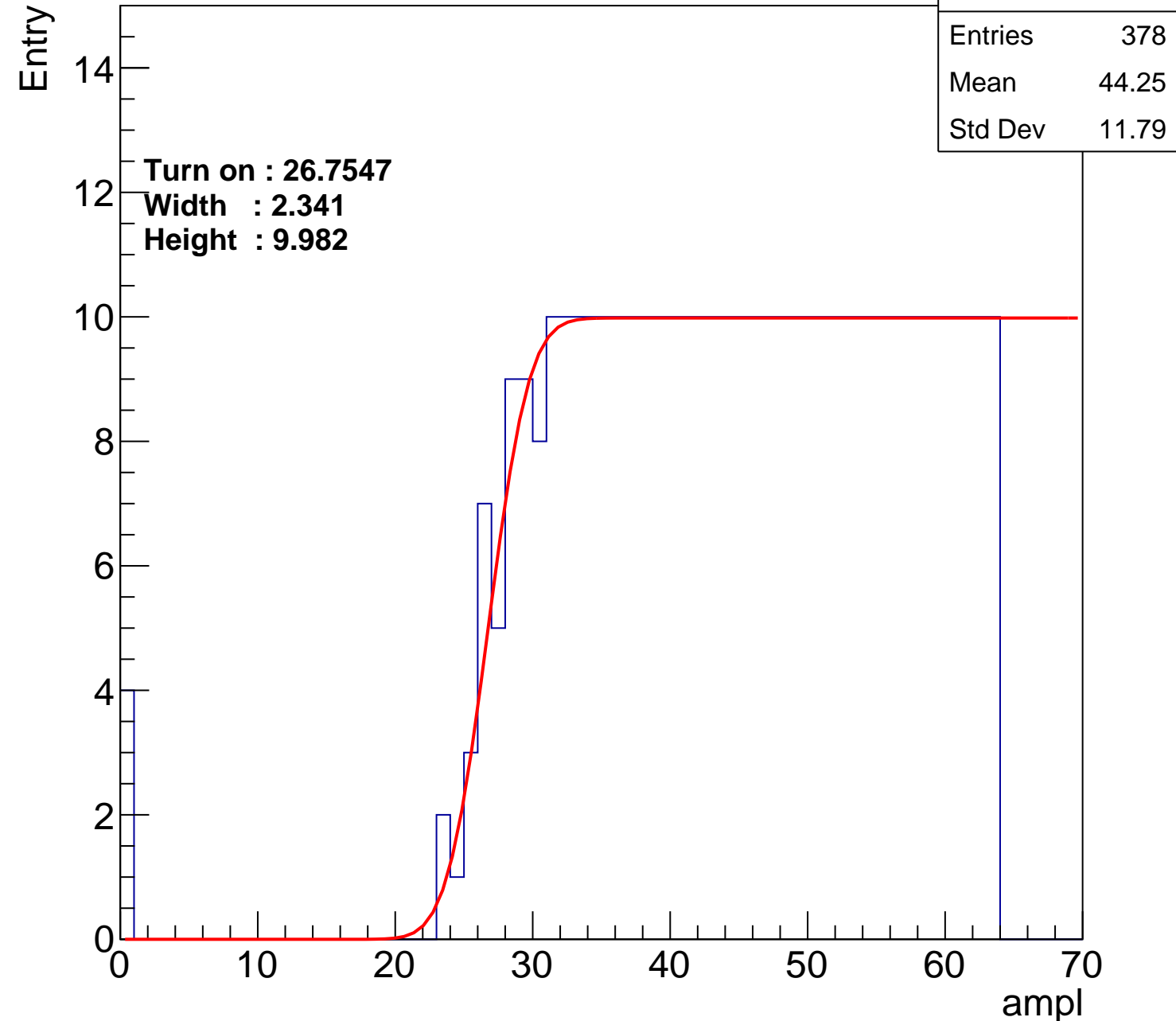
Width : 2.341

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.27
Std Dev	10.97

Turn on : 28.0915

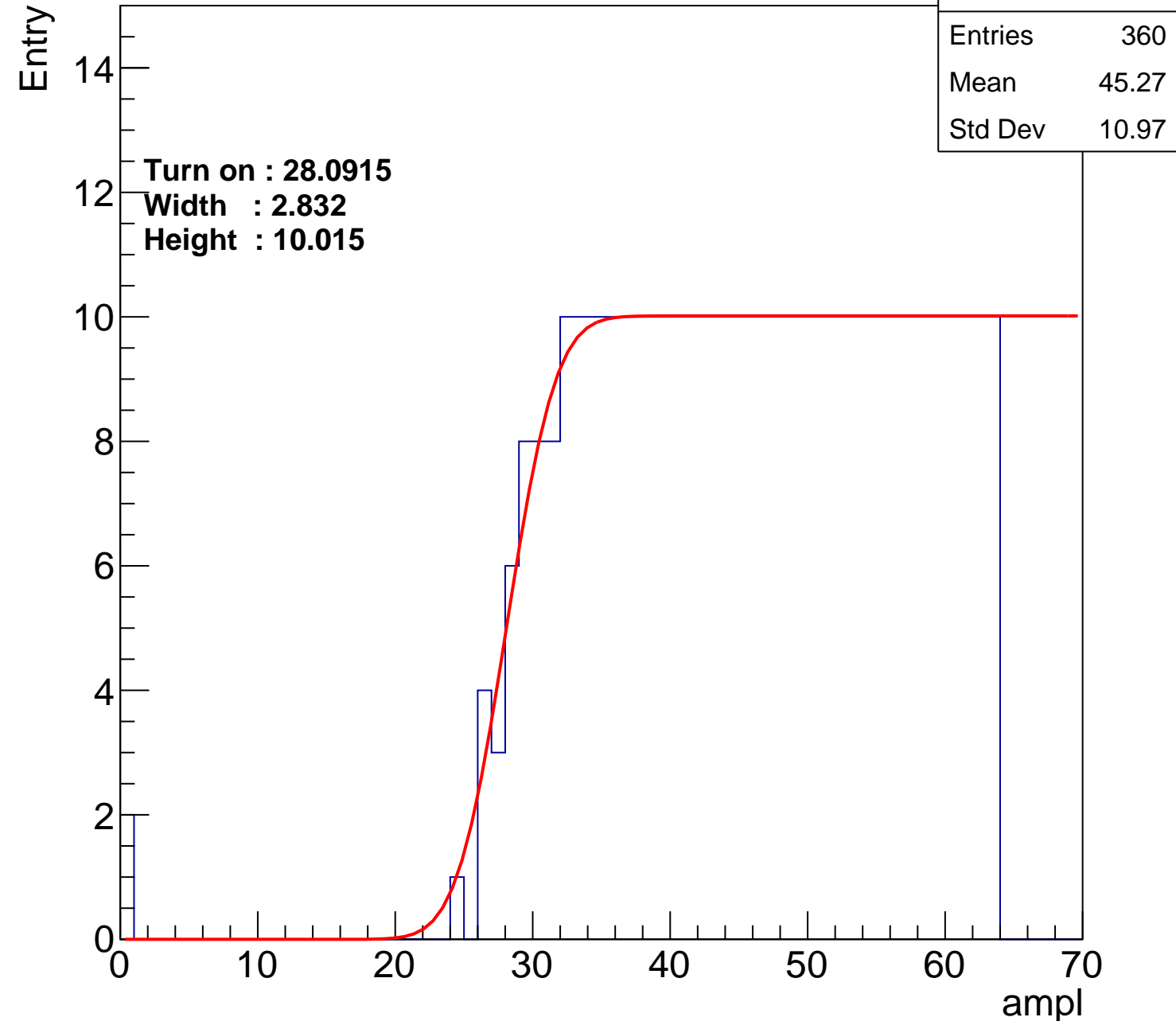
Width : 2.832

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.01
Std Dev	11.26

Turn on : 28.2604

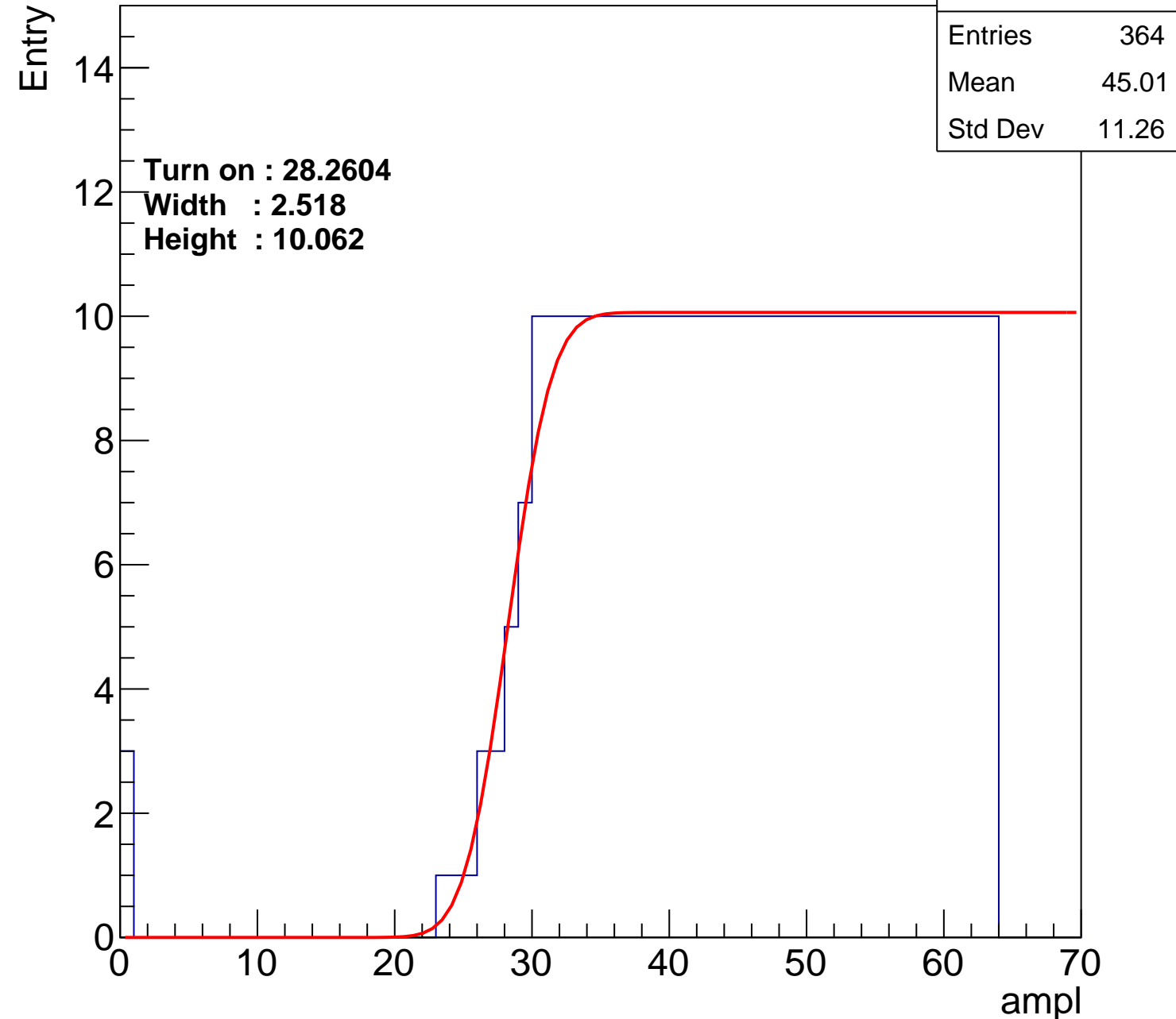
Width : 2.518

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.64
Std Dev	12.12

Turn on : 24.6445

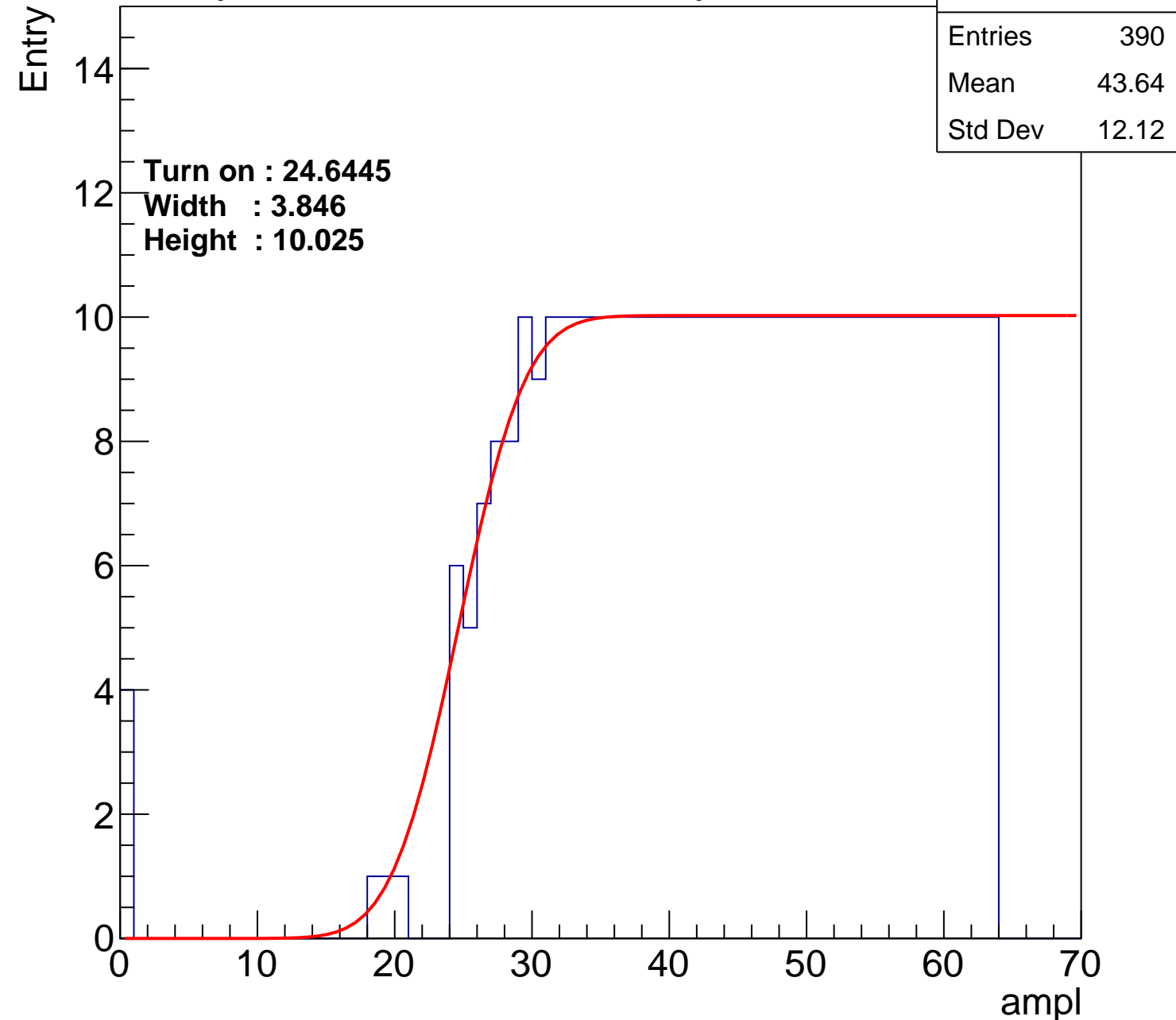
Width : 3.846

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.3
Std Dev	11.77

Turn on : 26.8882

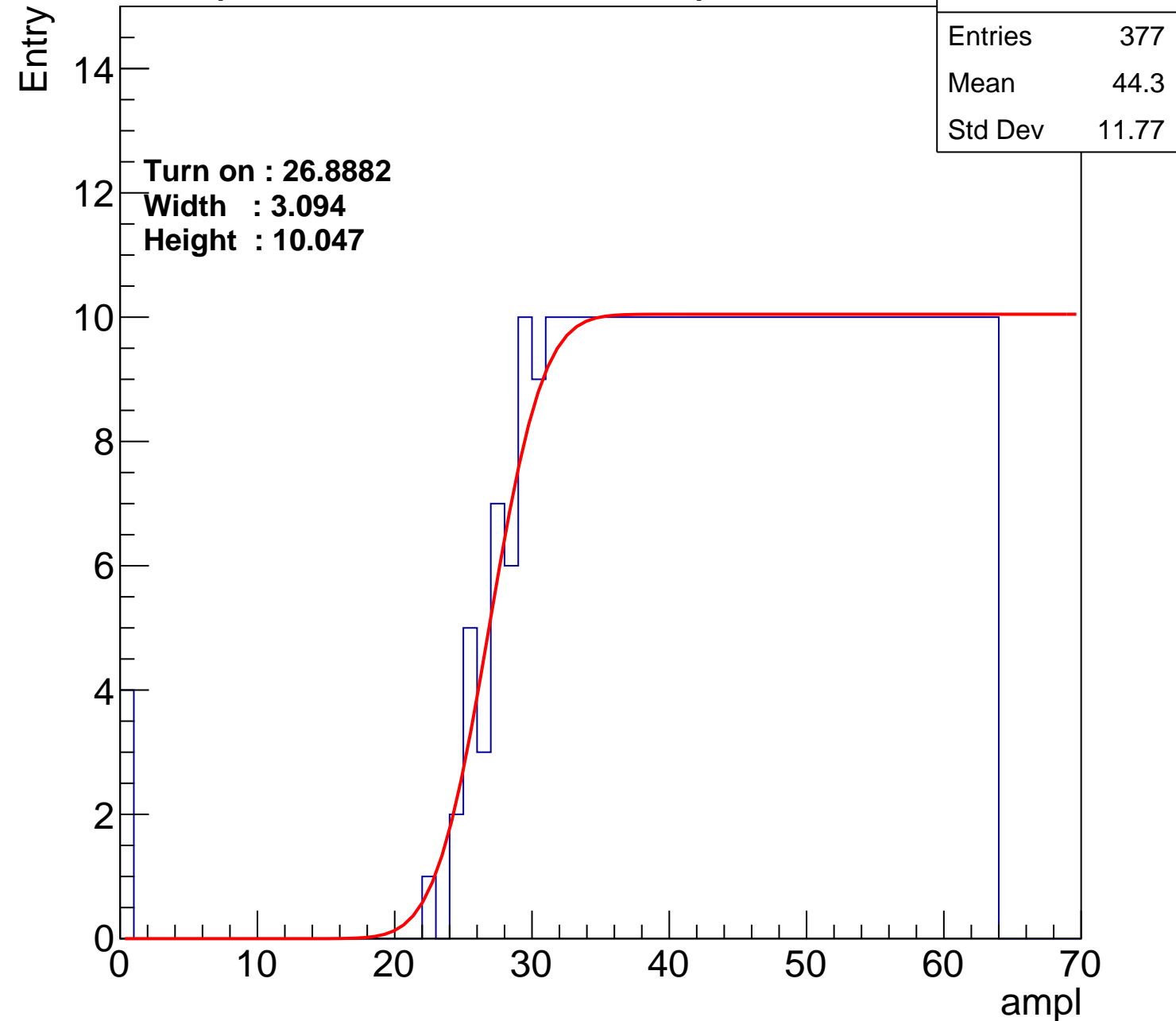
Width : 3.094

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch26

calib_packv5_042523_0143.root, FC#8, port C1

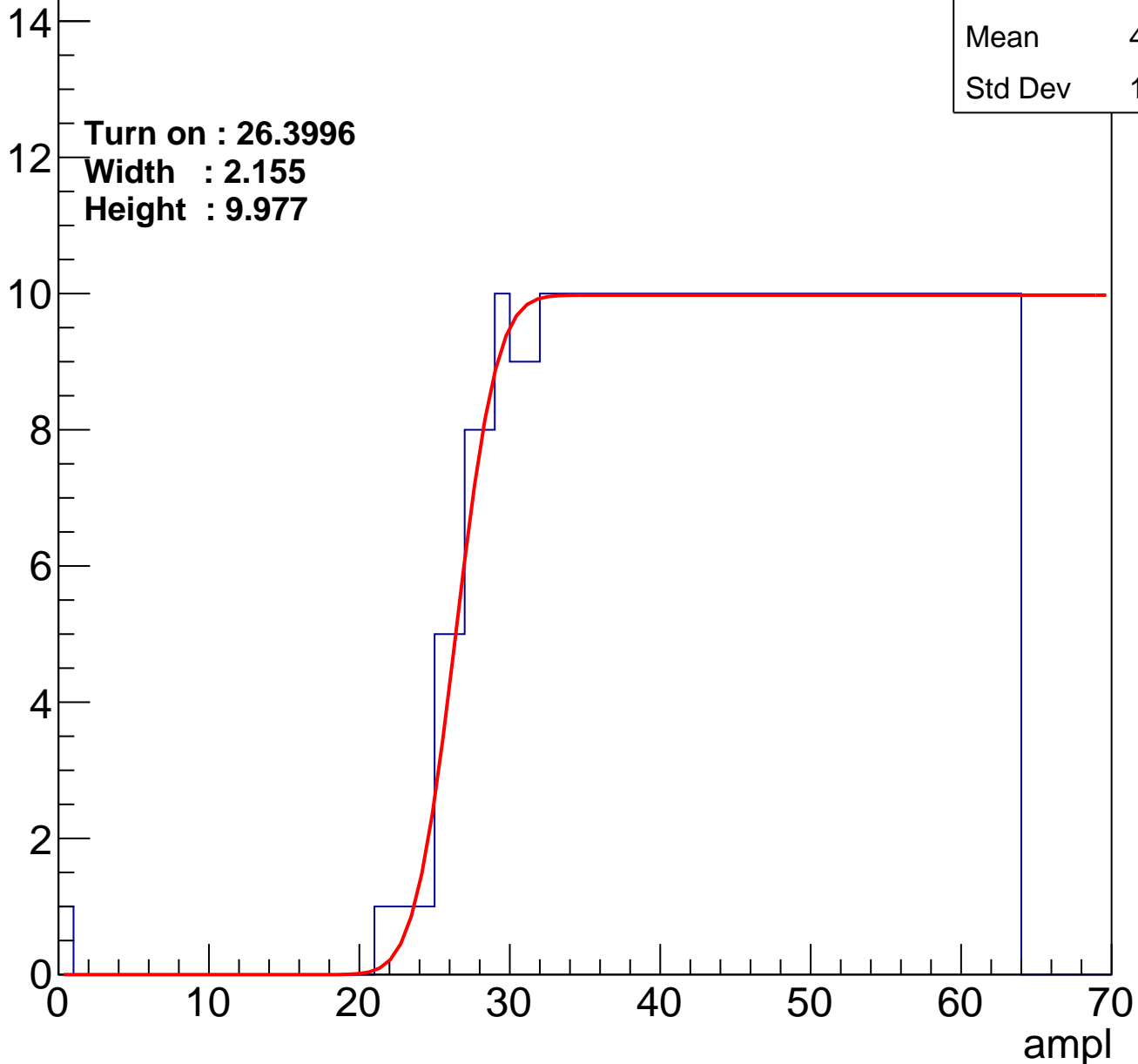
Entries	379
Mean	44.39
Std Dev	11.28

Turn on : 26.3996

Width : 2.155

Height : 9.977

Entry



B0L002S, U16-ch27

calib_packv5_042523_0143.root, FC#8, port C1

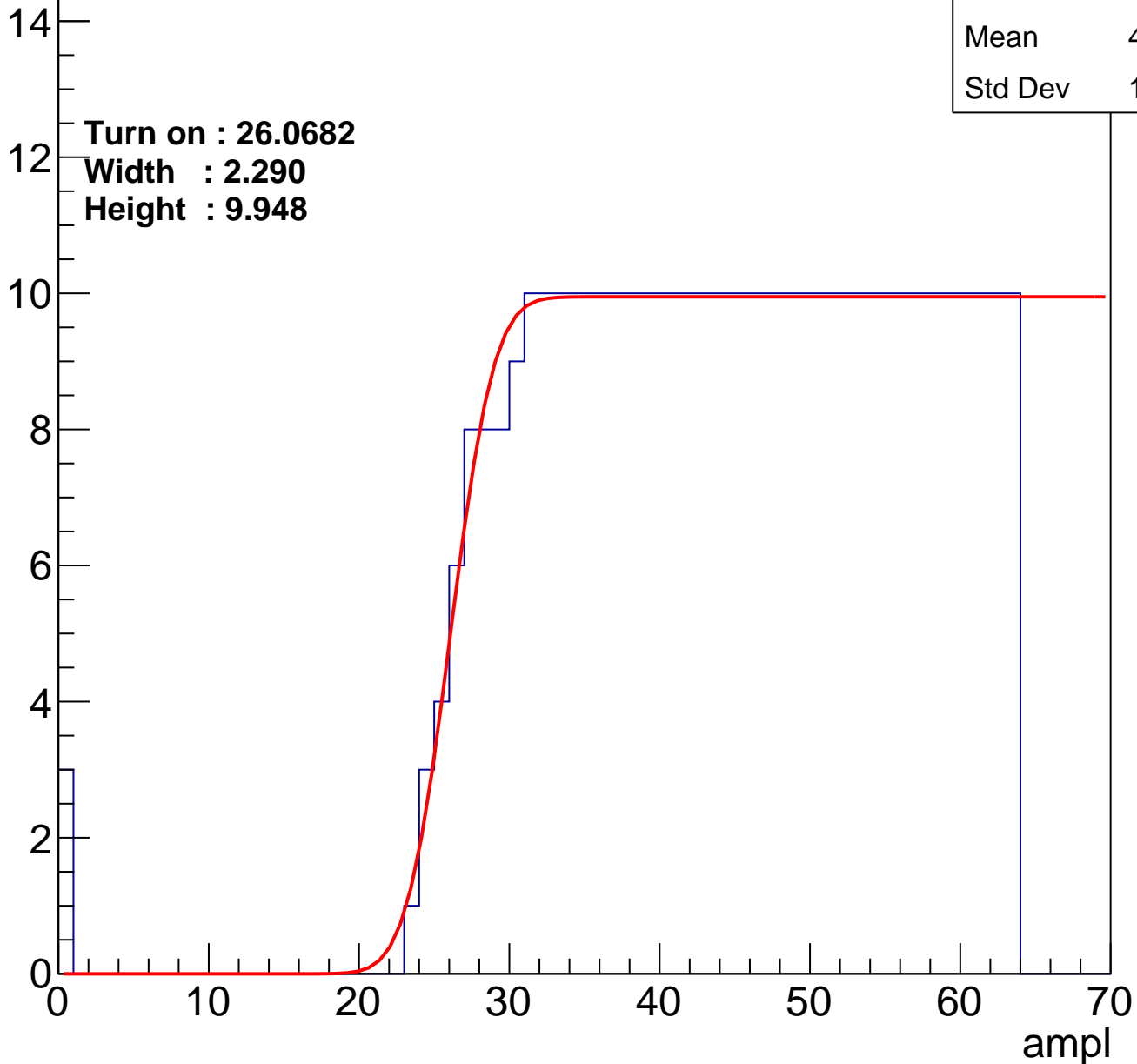
Entries	380
Mean	44.22
Std Dev	11.66

Turn on : 26.0682

Width : 2.290

Height : 9.948

Entry



B0L002S, U16-ch28

calib_packv5_042523_0143.root, FC#8, port C1

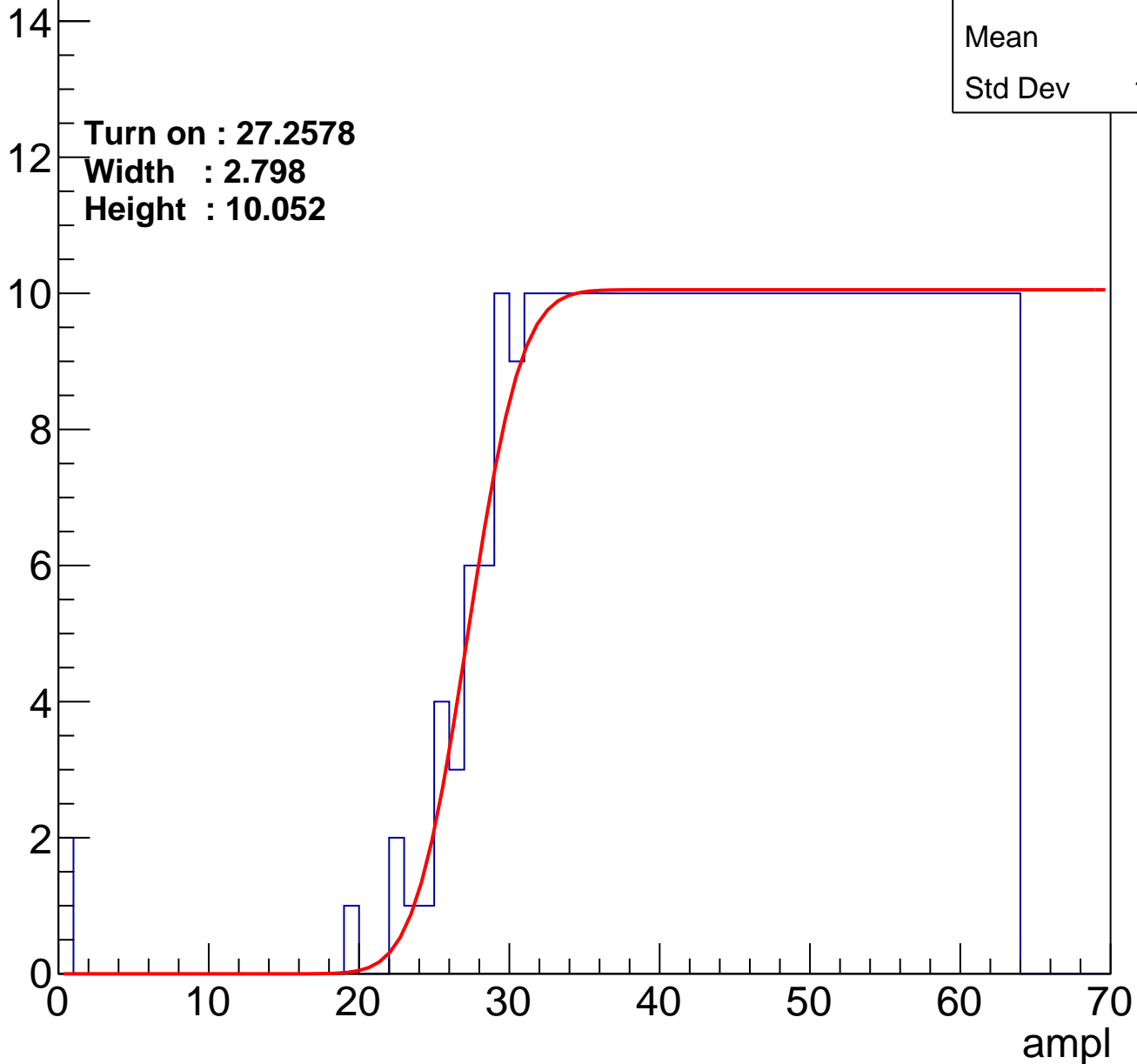
Entries	375
Mean	44.5
Std Dev	11.41

Turn on : 27.2578

Width : 2.798

Height : 10.052

Entry



B0L002S, U16-ch29

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.68
Std Dev	11.47

Turn on : 27.4719

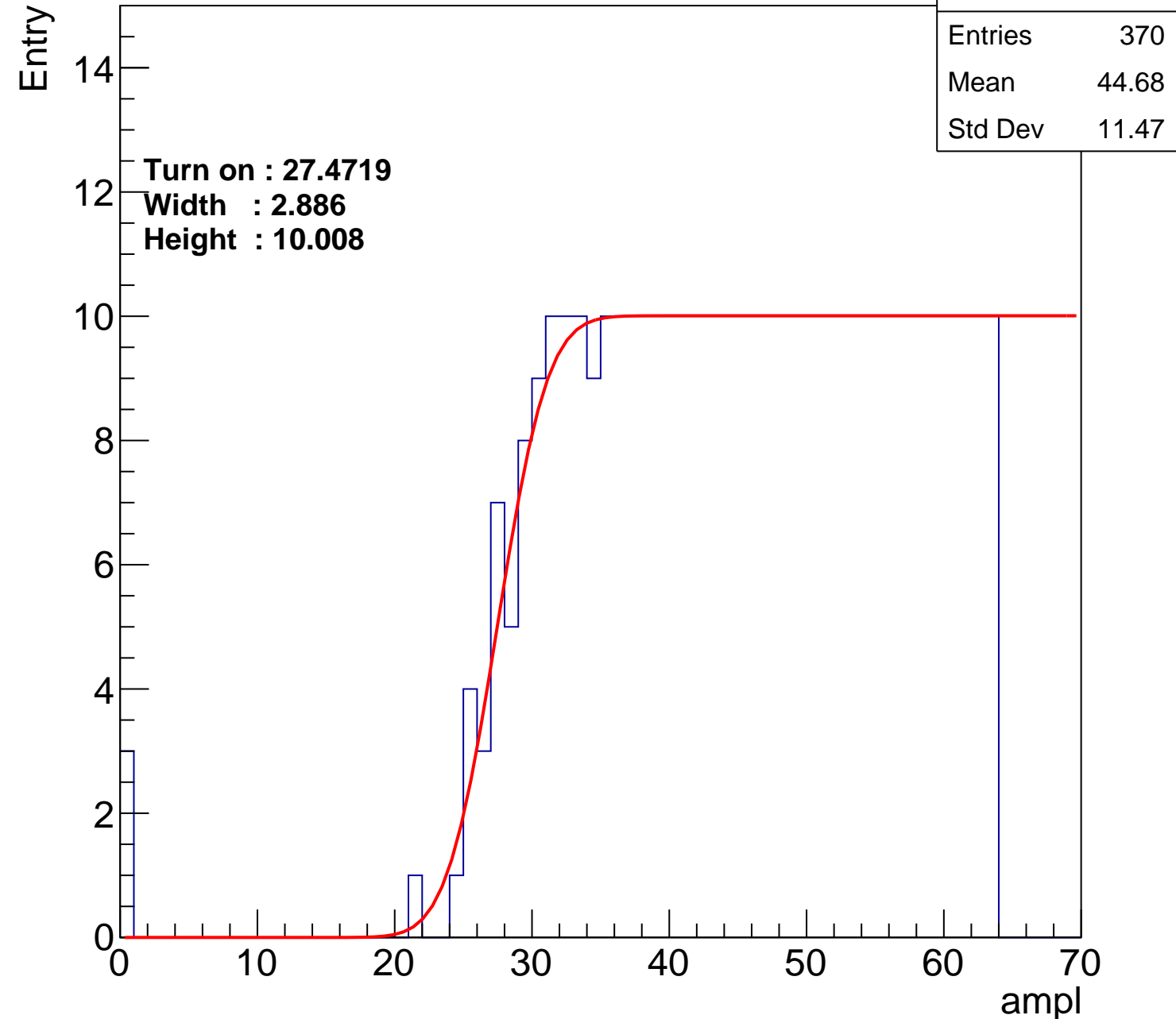
Width : 2.886

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.14
Std Dev	11.6

Turn on : 26.1724

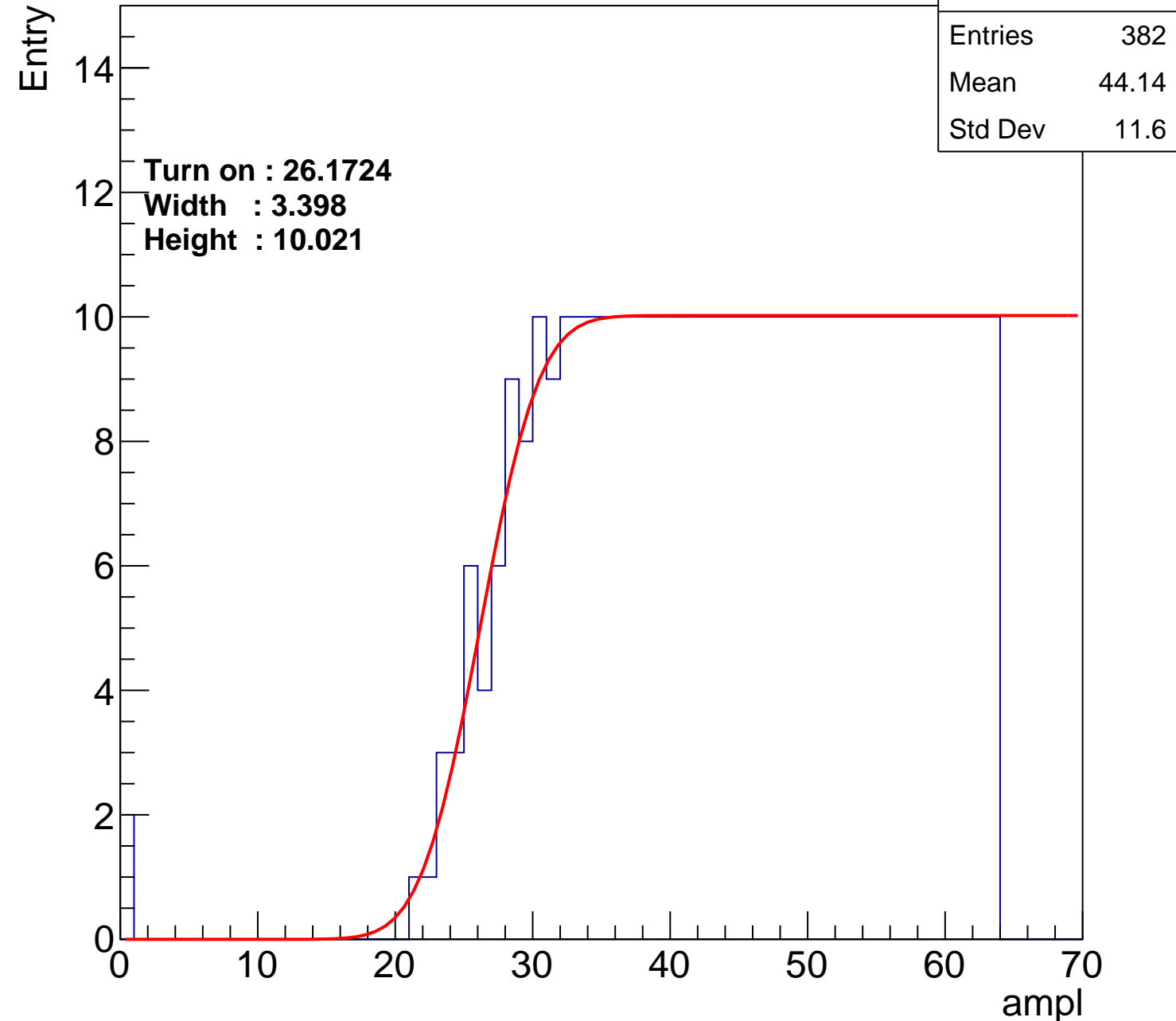
Width : 3.398

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 25.2952

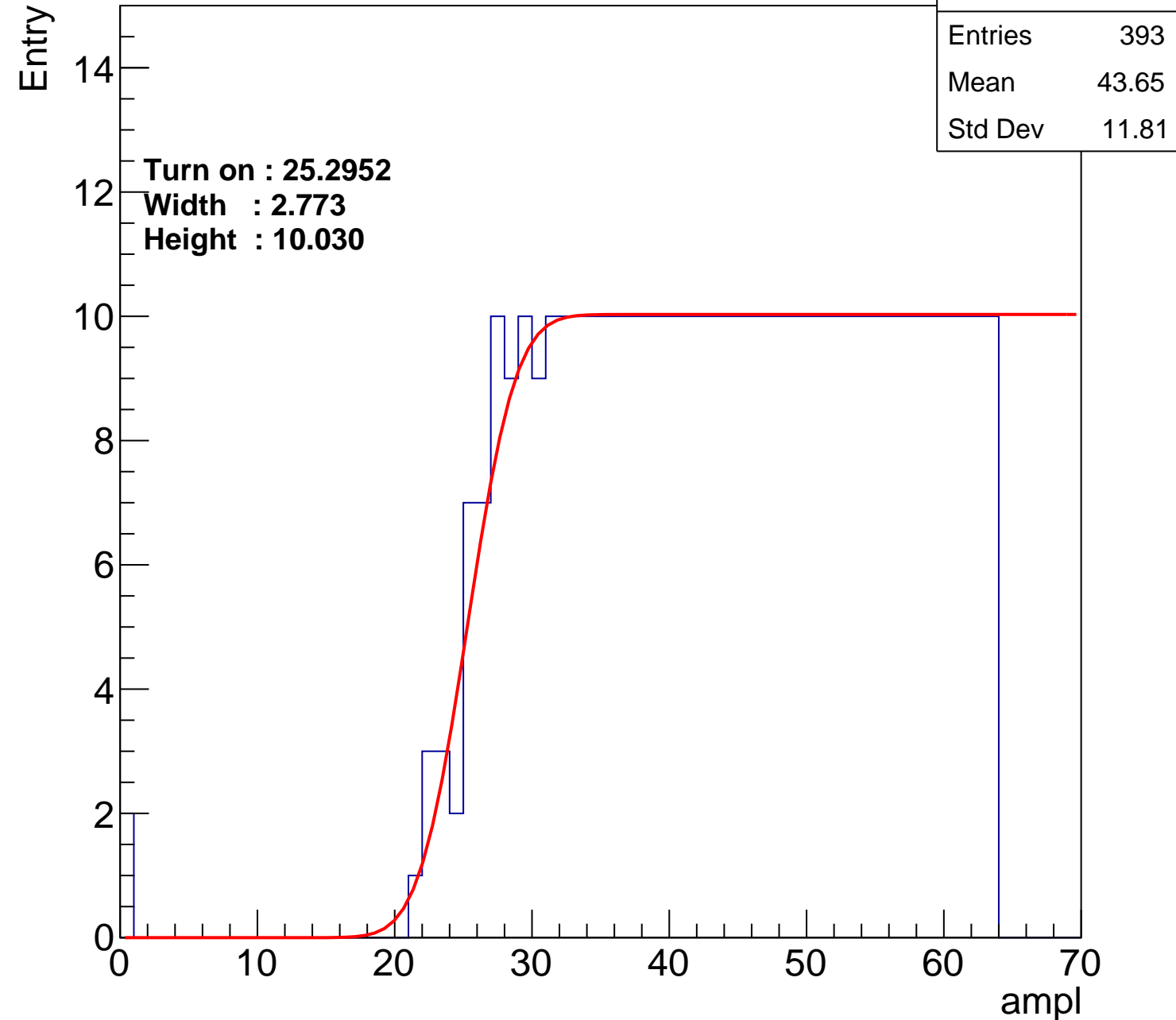
Width : 2.773

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.02
Std Dev	10.96

Turn on : 27.7990

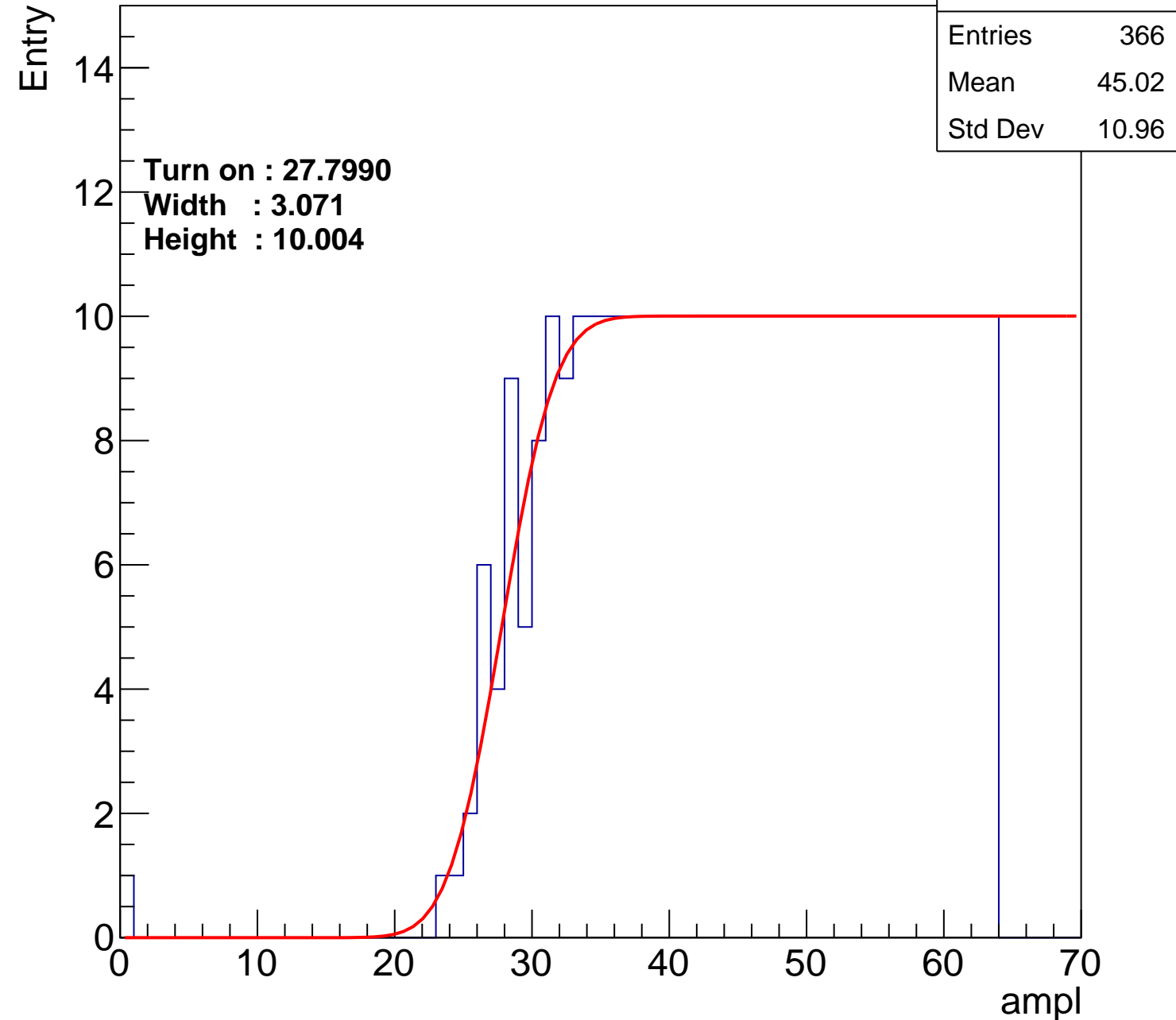
Width : 3.071

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.93
Std Dev	11.47

Turn on : 27.9237

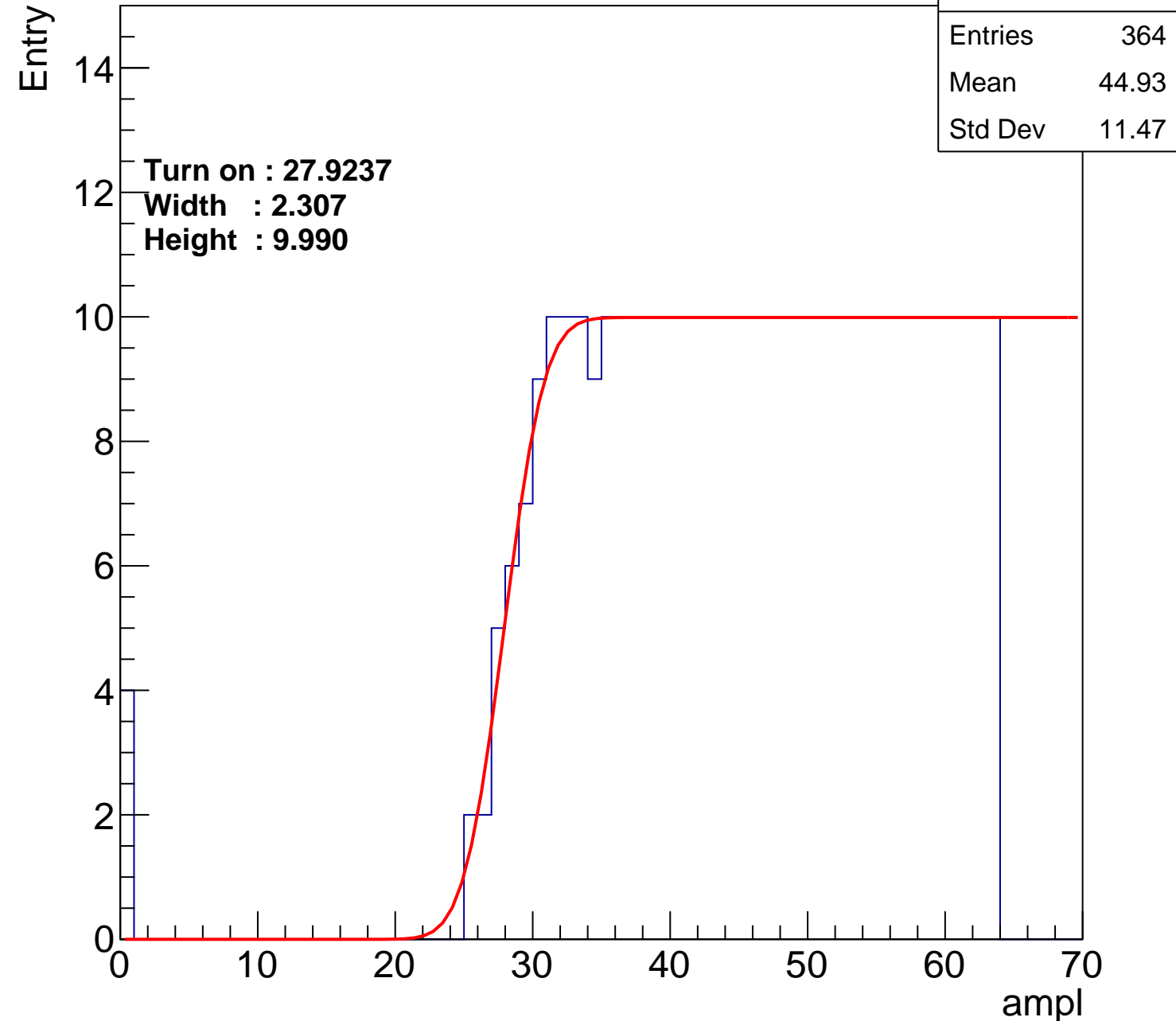
Width : 2.307

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.35
Std Dev	11.8

Turn on : 27.0927

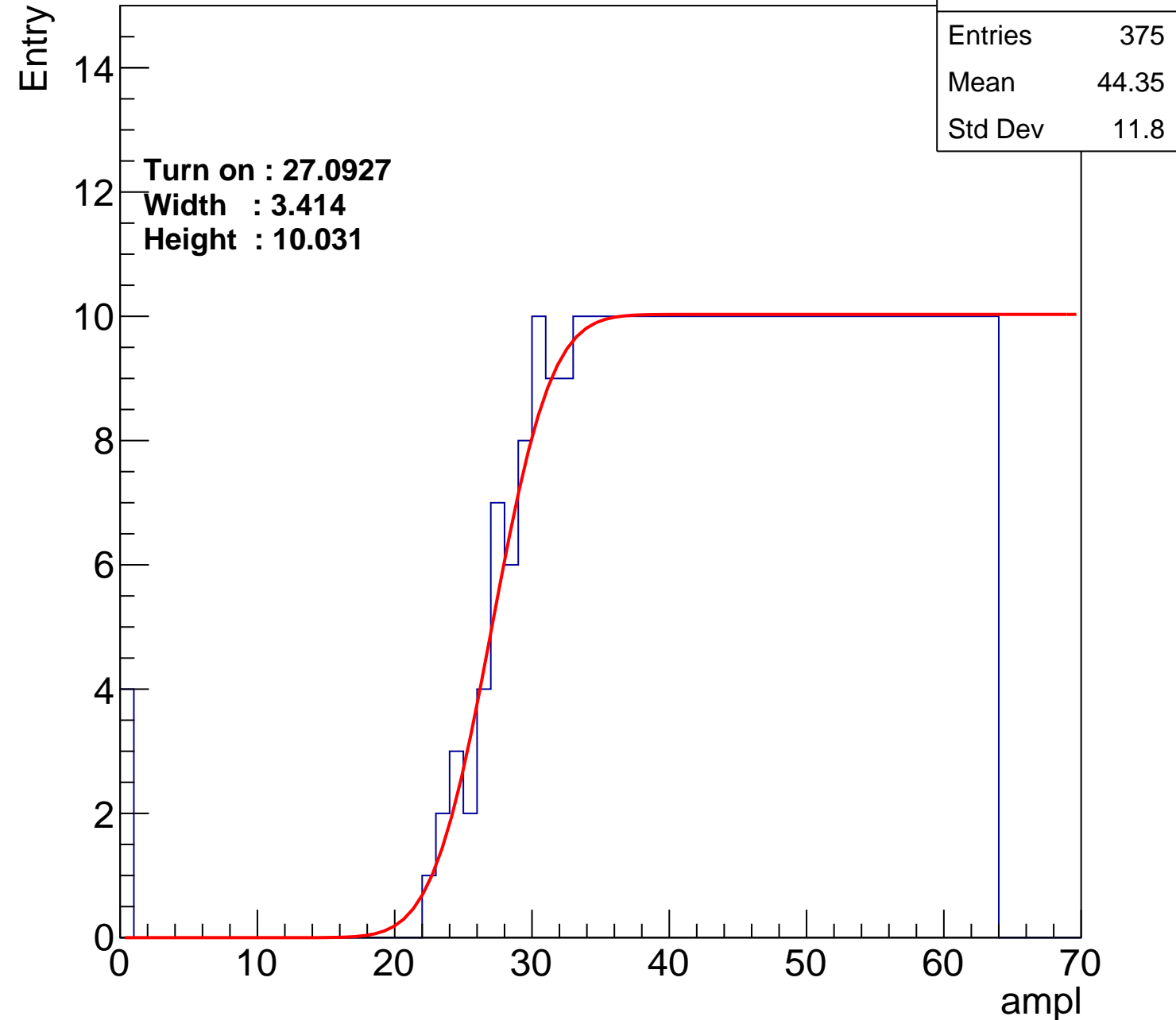
Width : 3.414

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.87
Std Dev	10.55

Turn on : 29.4260

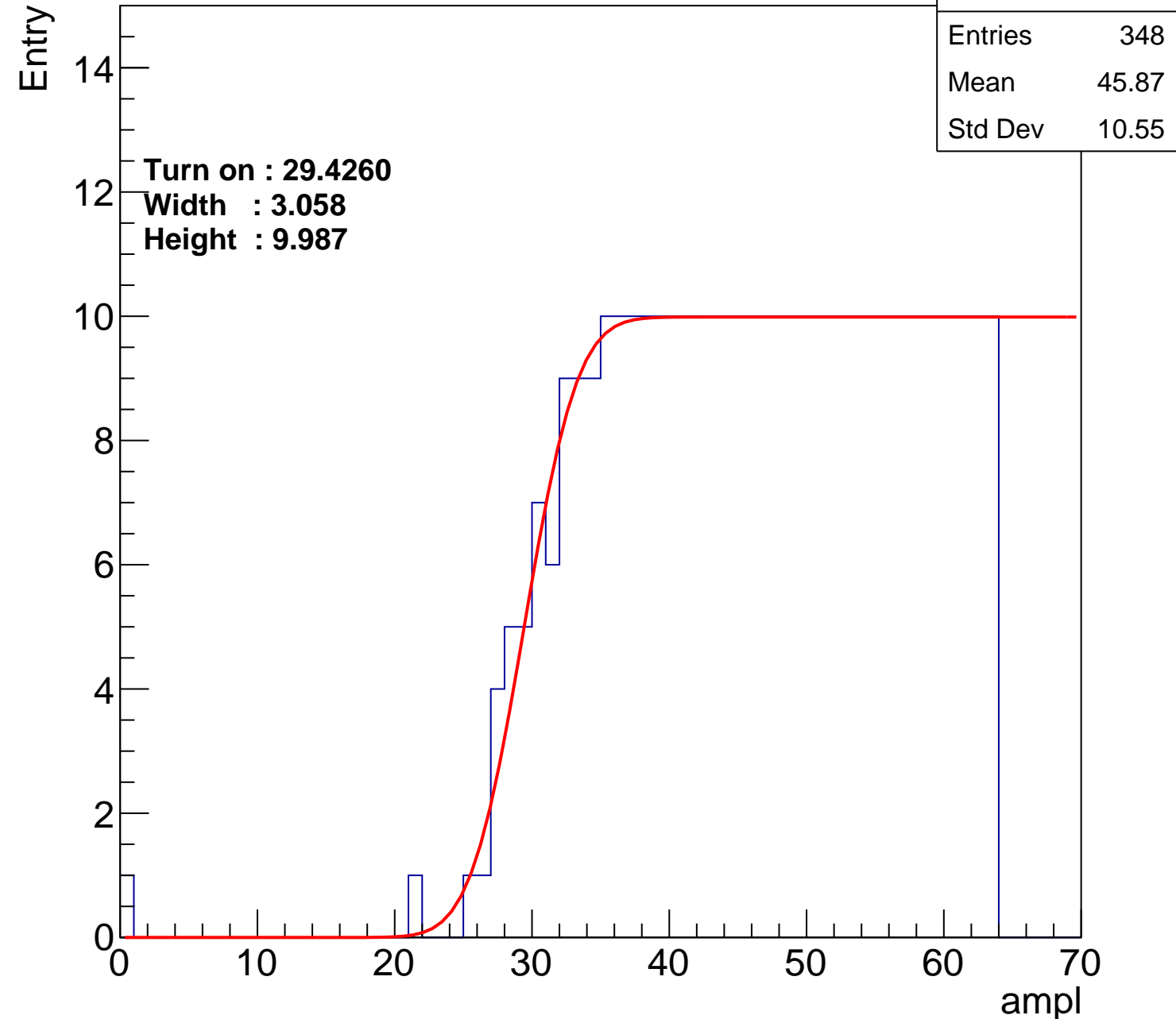
Width : 3.058

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch36

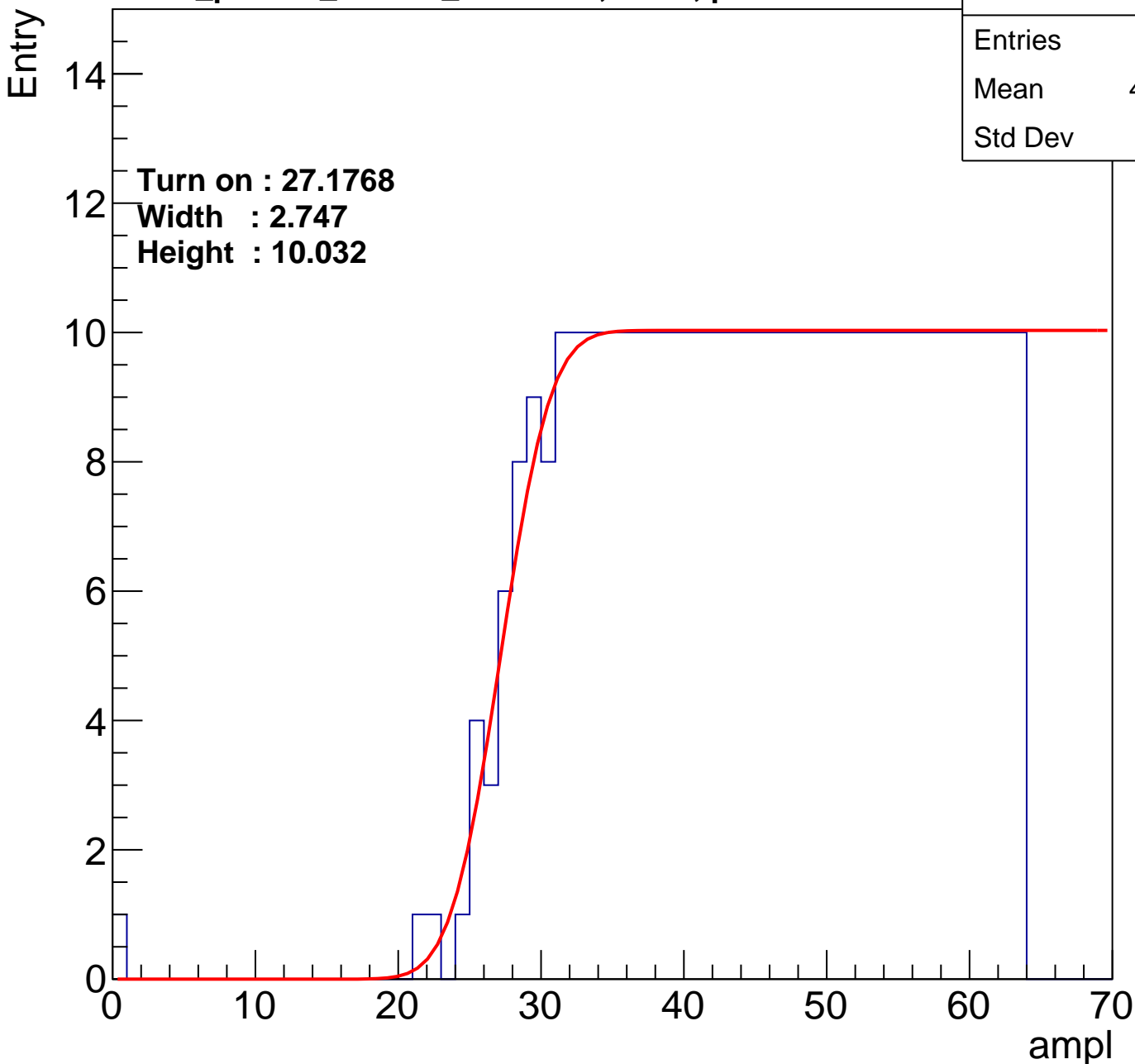
calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.74
Std Dev	11.1

Turn on : 27.1768

Width : 2.747

Height : 10.032



B0L002S, U16-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.89
Std Dev	11.07

Turn on : 27.7821

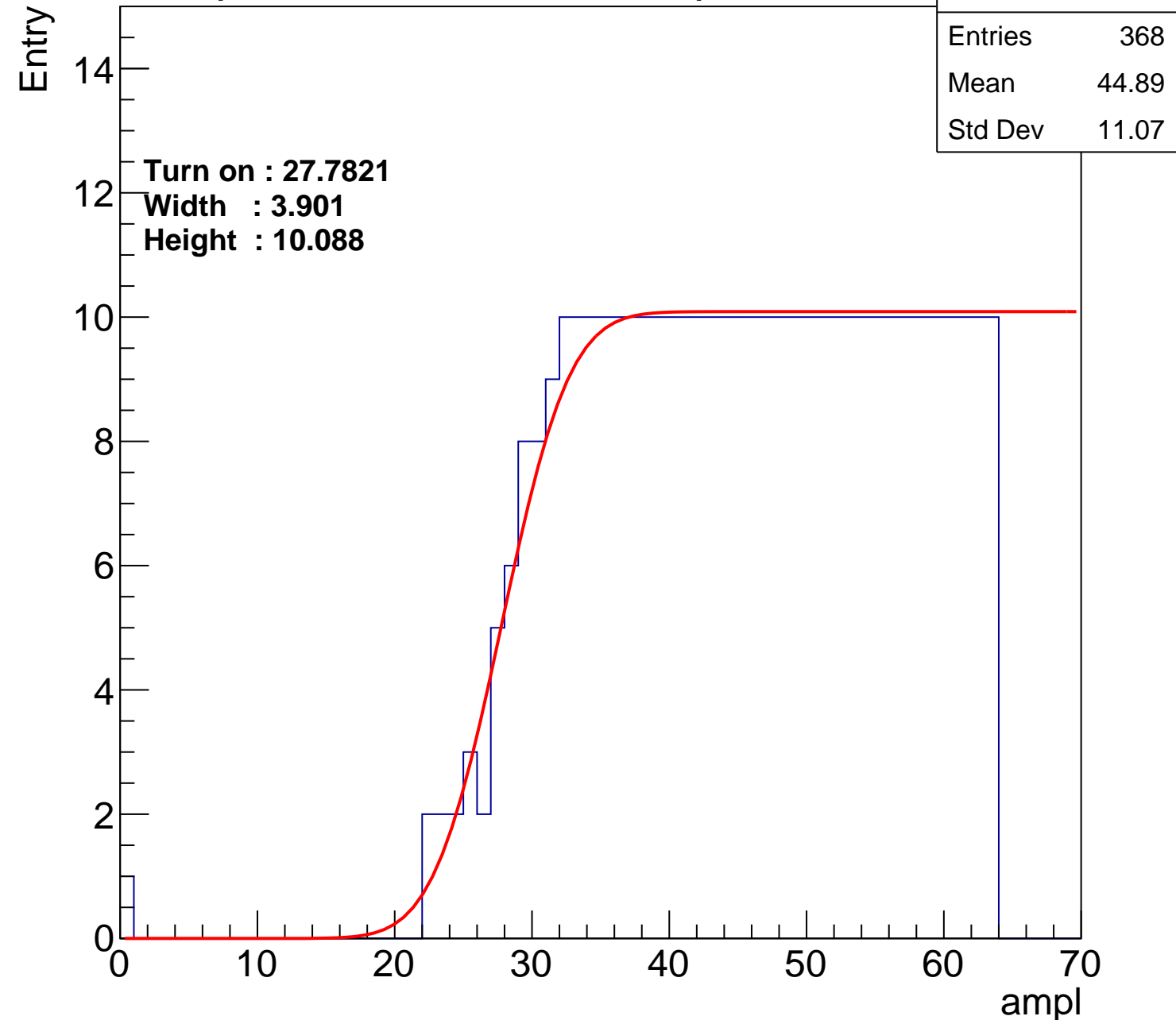
Width : 3.901

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch38

calib_packv5_042523_0143.root, FC#8, port C1

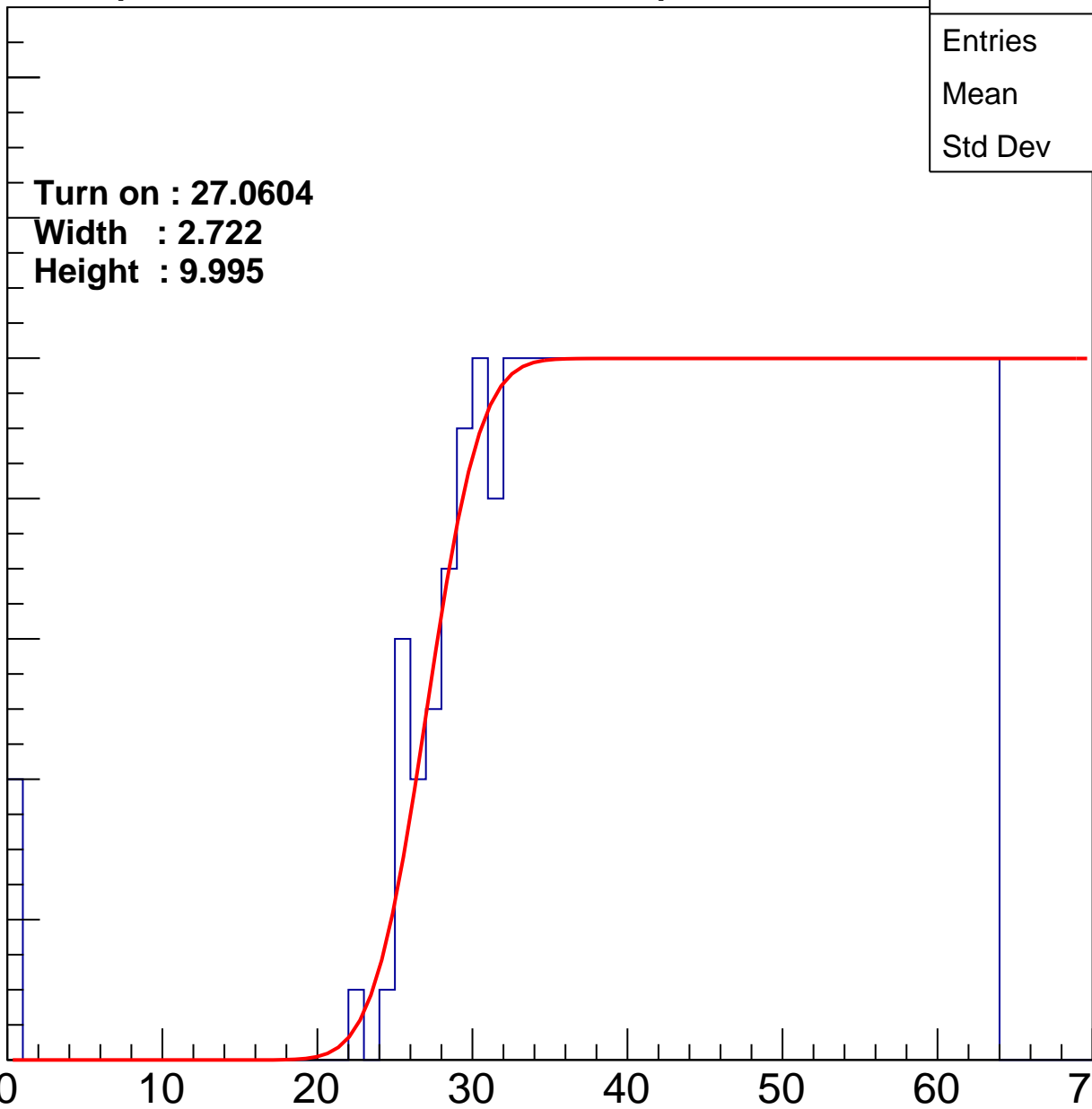
Entry

14
12
10
8
6
4
2
0

Turn on : 27.0604
Width : 2.722
Height : 9.995

Entries	375
Mean	44.37
Std Dev	11.75

ampl



B0L002S, U16-ch39

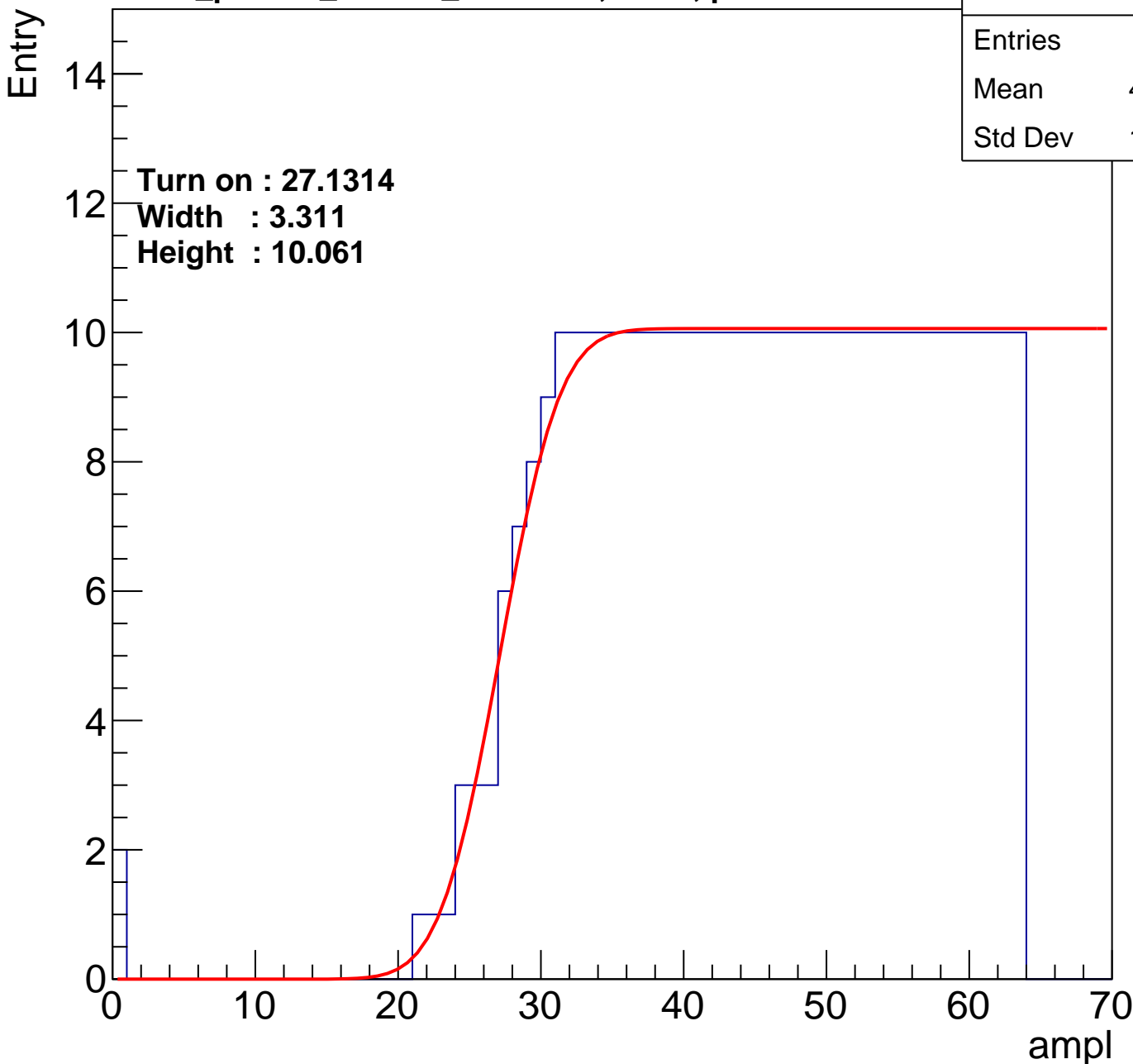
calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 27.1314

Width : 3.311

Height : 10.061

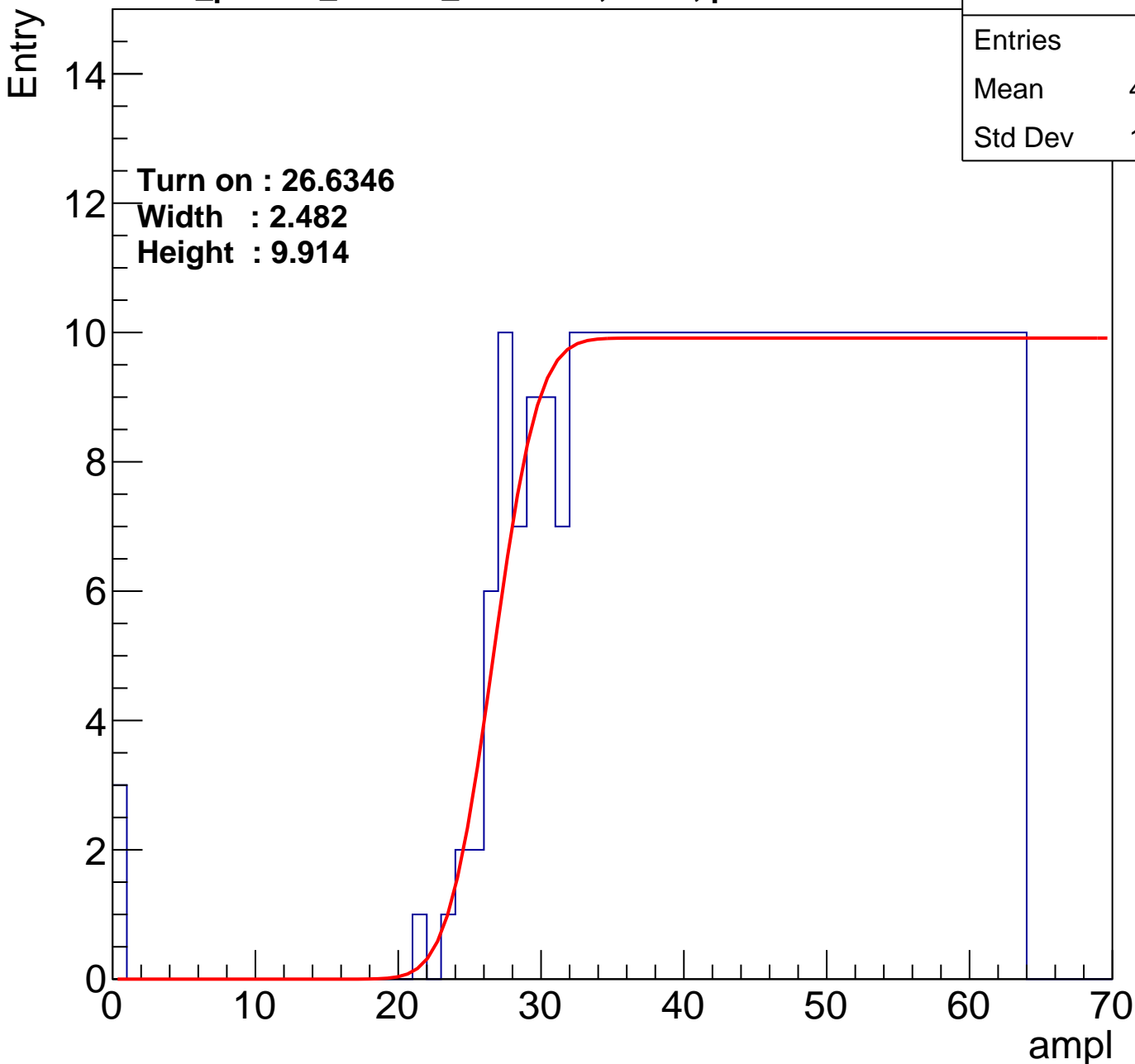


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.33
Std Dev	11.64

Height : 9.914



B0L002S, U16-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.43
Std Dev	11.64

Turn on : 27.0780

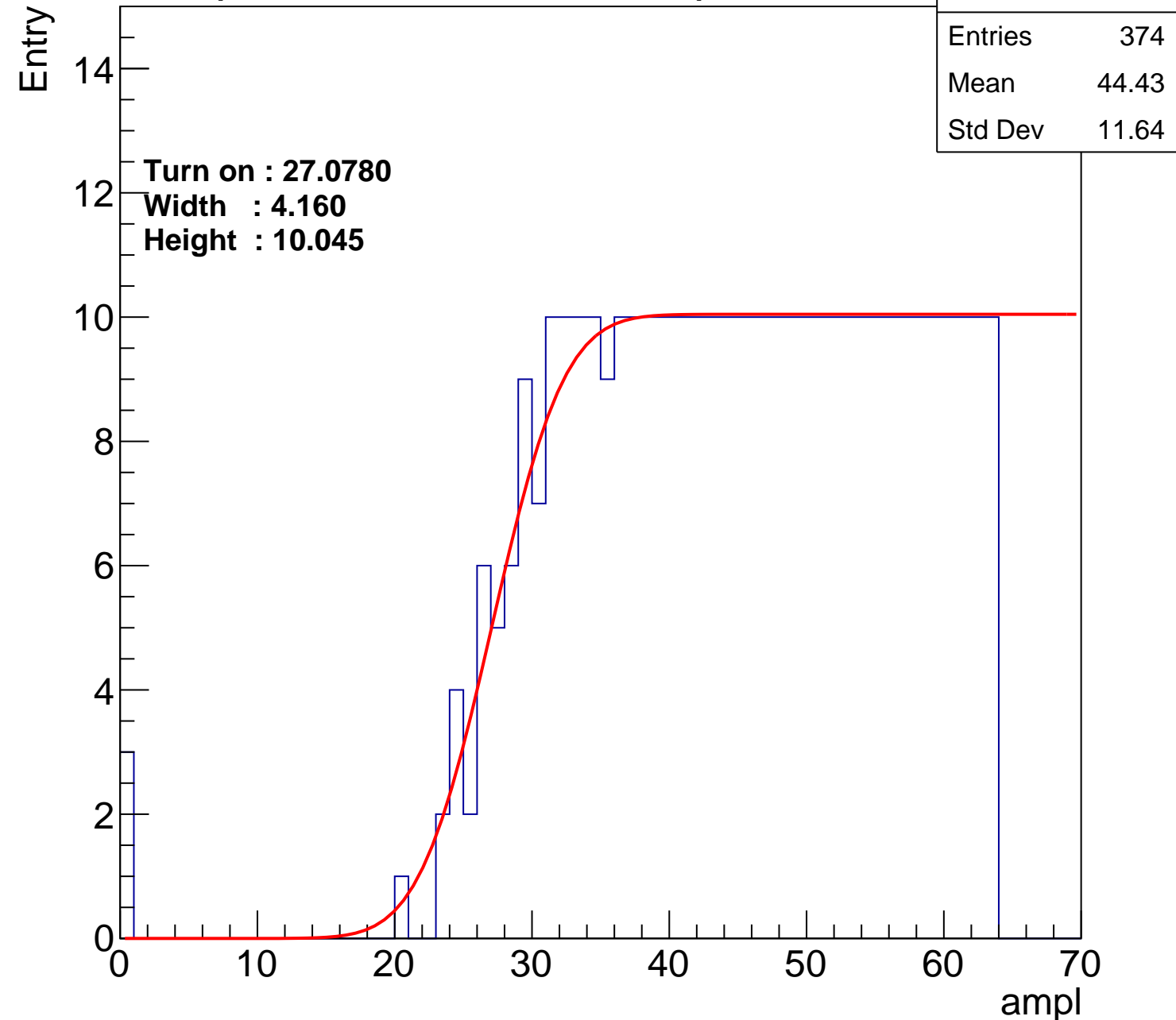
Width : 4.160

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch42

calib_packv5_042523_0143.root, FC#8, port C1

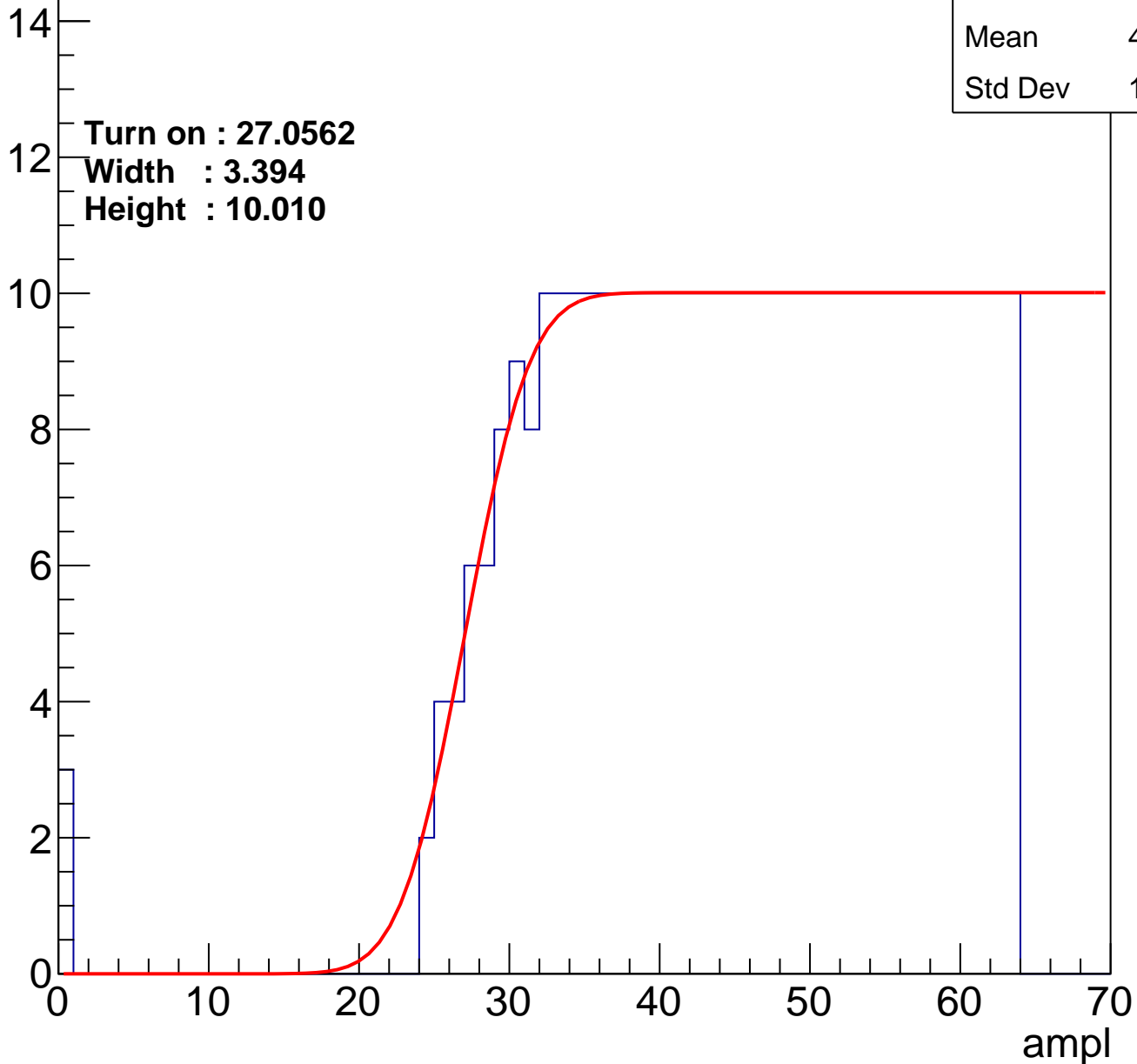
Entries	370
Mean	44.68
Std Dev	11.46

Turn on : 27.0562

Width : 3.394

Height : 10.010

Entry



B0L002S, U16-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.1056

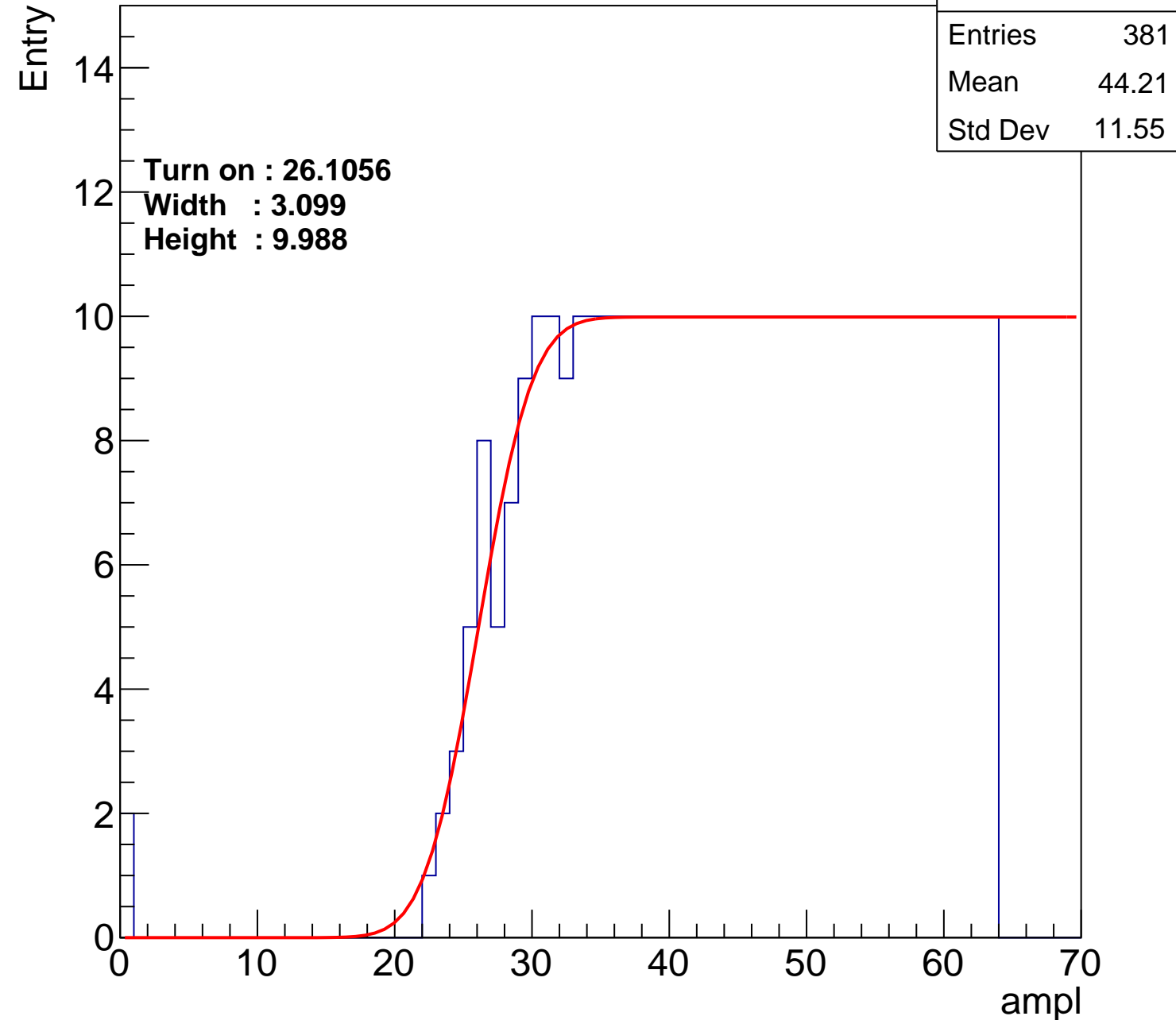
Width : 3.099

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch44

calib_packv5_042523_0143.root, FC#8, port C1

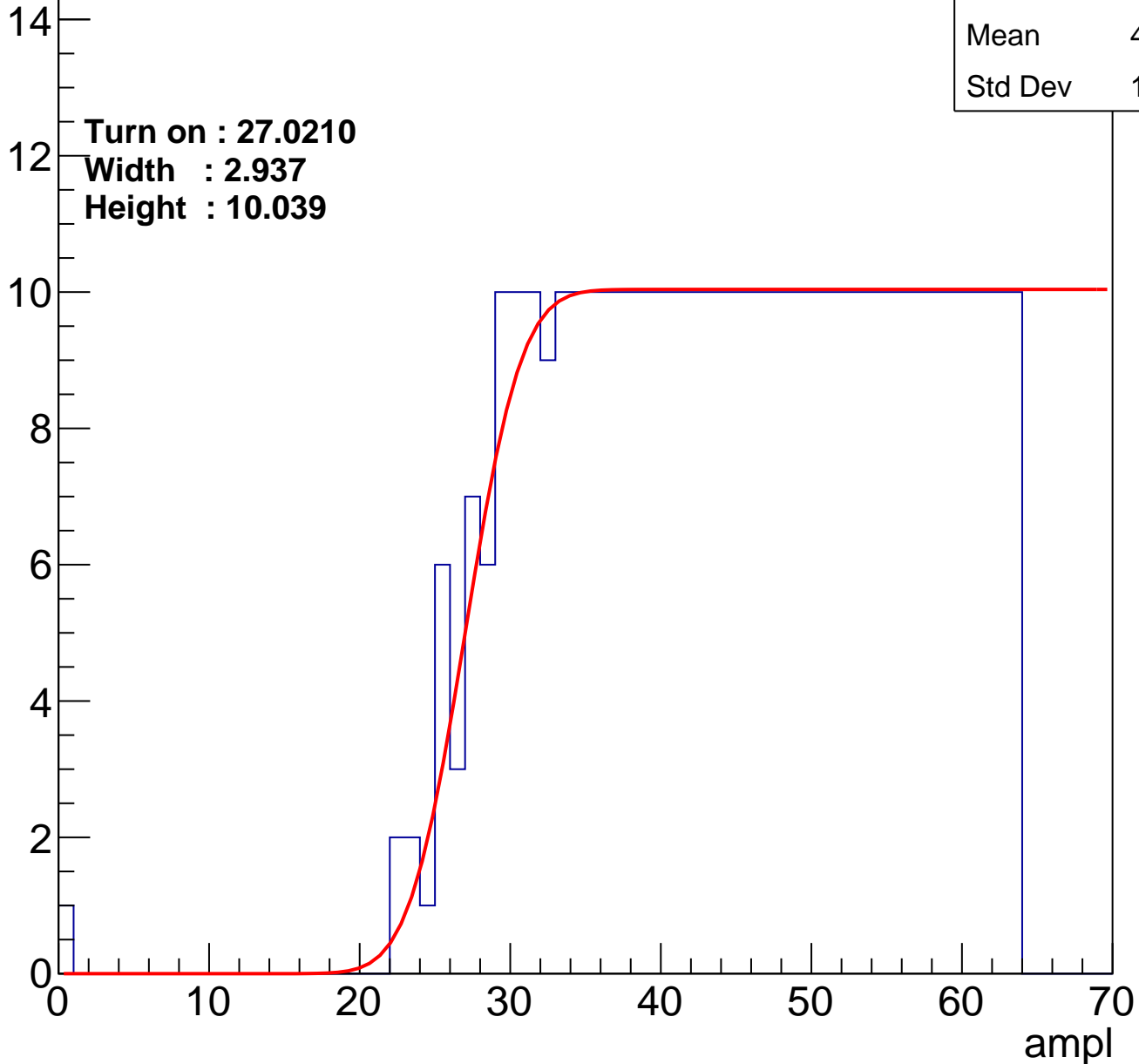
Entries	377
Mean	44.47
Std Dev	11.25

Turn on : 27.0210

Width : 2.937

Height : 10.039

Entry



B0L002S, U16-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.93
Std Dev	11.29

Turn on : 27.9087

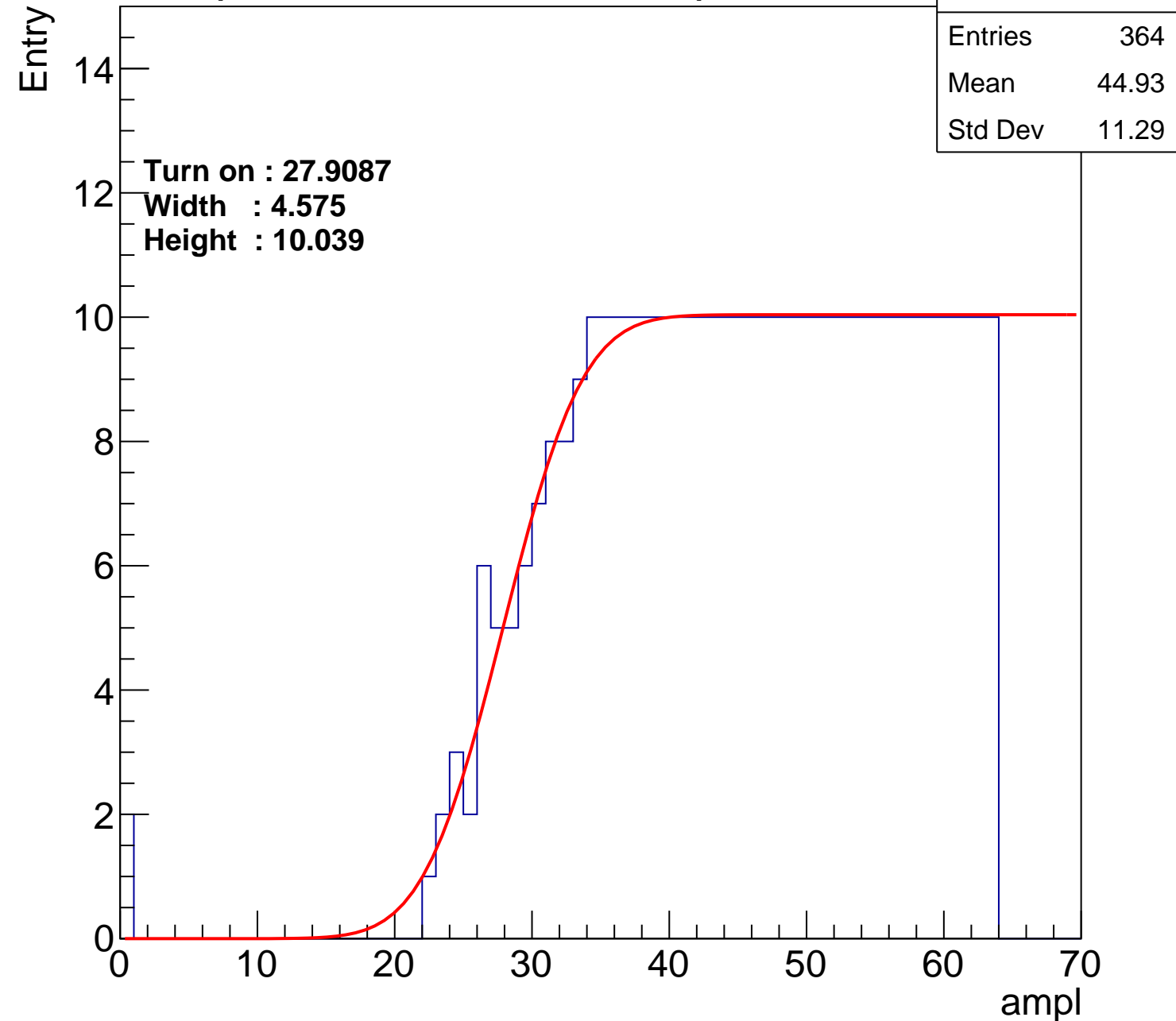
Width : 4.575

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch46

calib_packv5_042523_0143.root, FC#8, port C1

Entries	392
Mean	43.61
Std Dev	11.98

Turn on : 25.5744

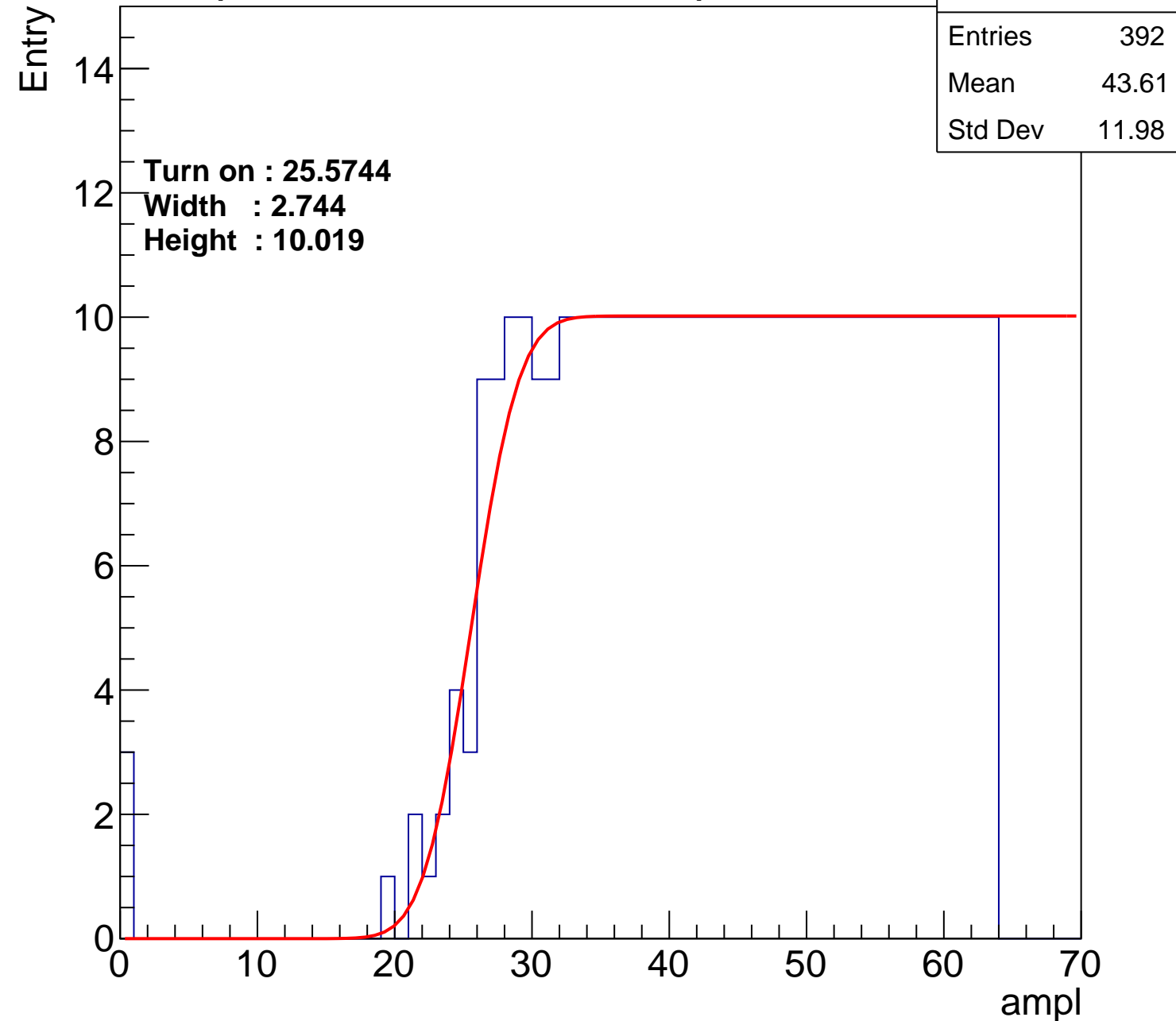
Width : 2.744

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.63
Std Dev	11.03

Turn on : 30.0825

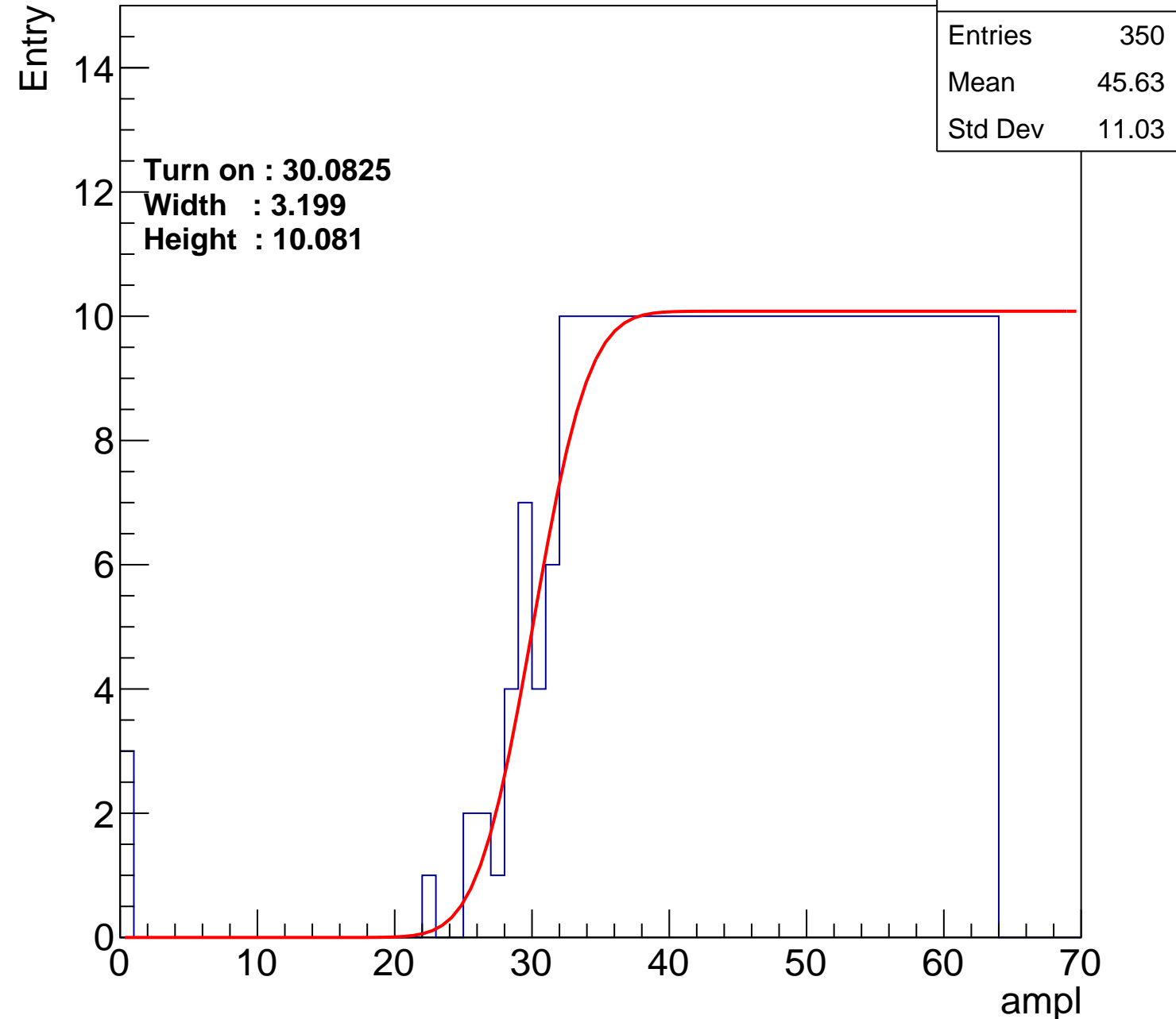
Width : 3.199

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.61
Std Dev	11.52

Turn on : 27.7727

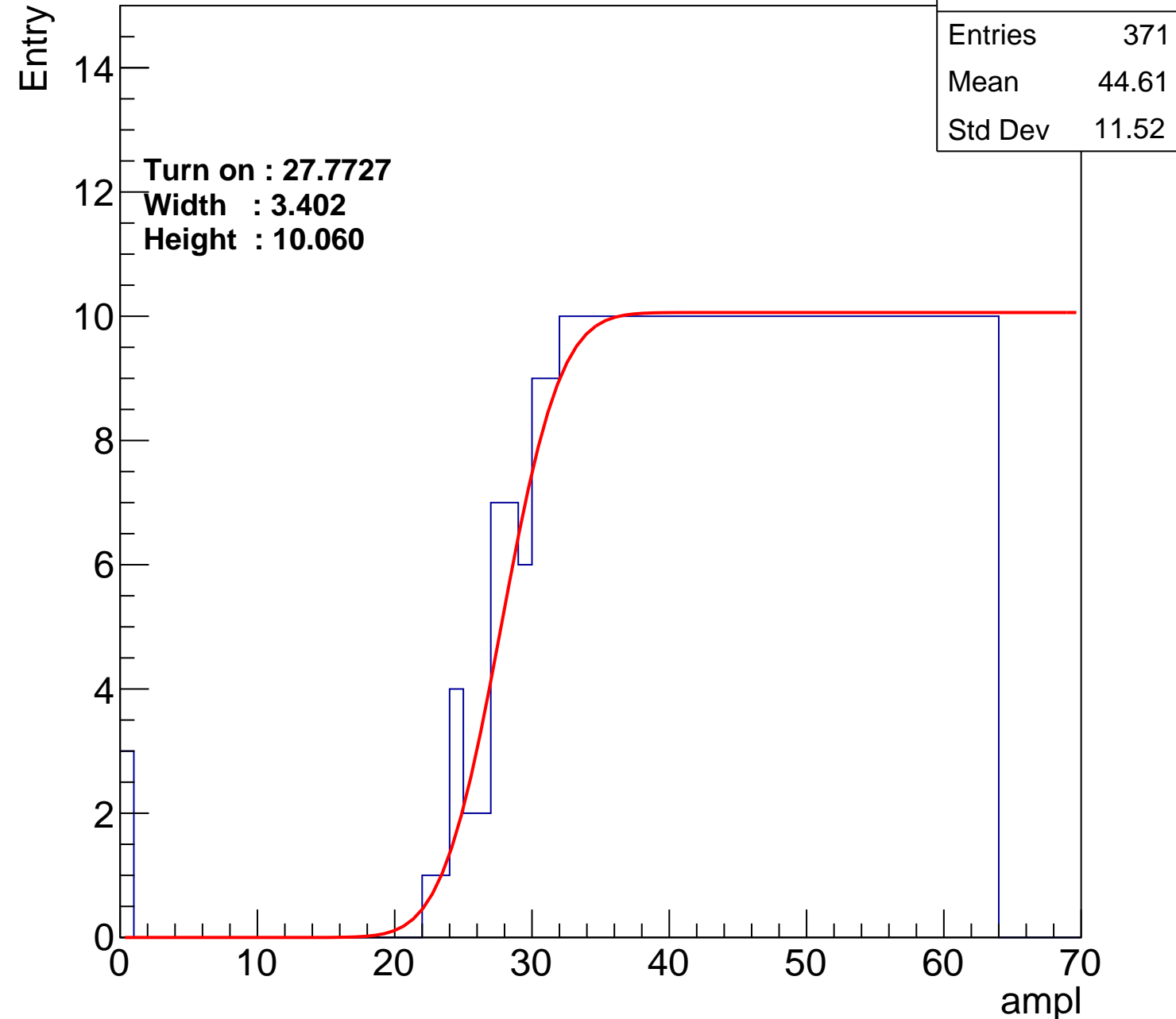
Width : 3.402

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.31
Std Dev	11.45

Turn on : 26.3568

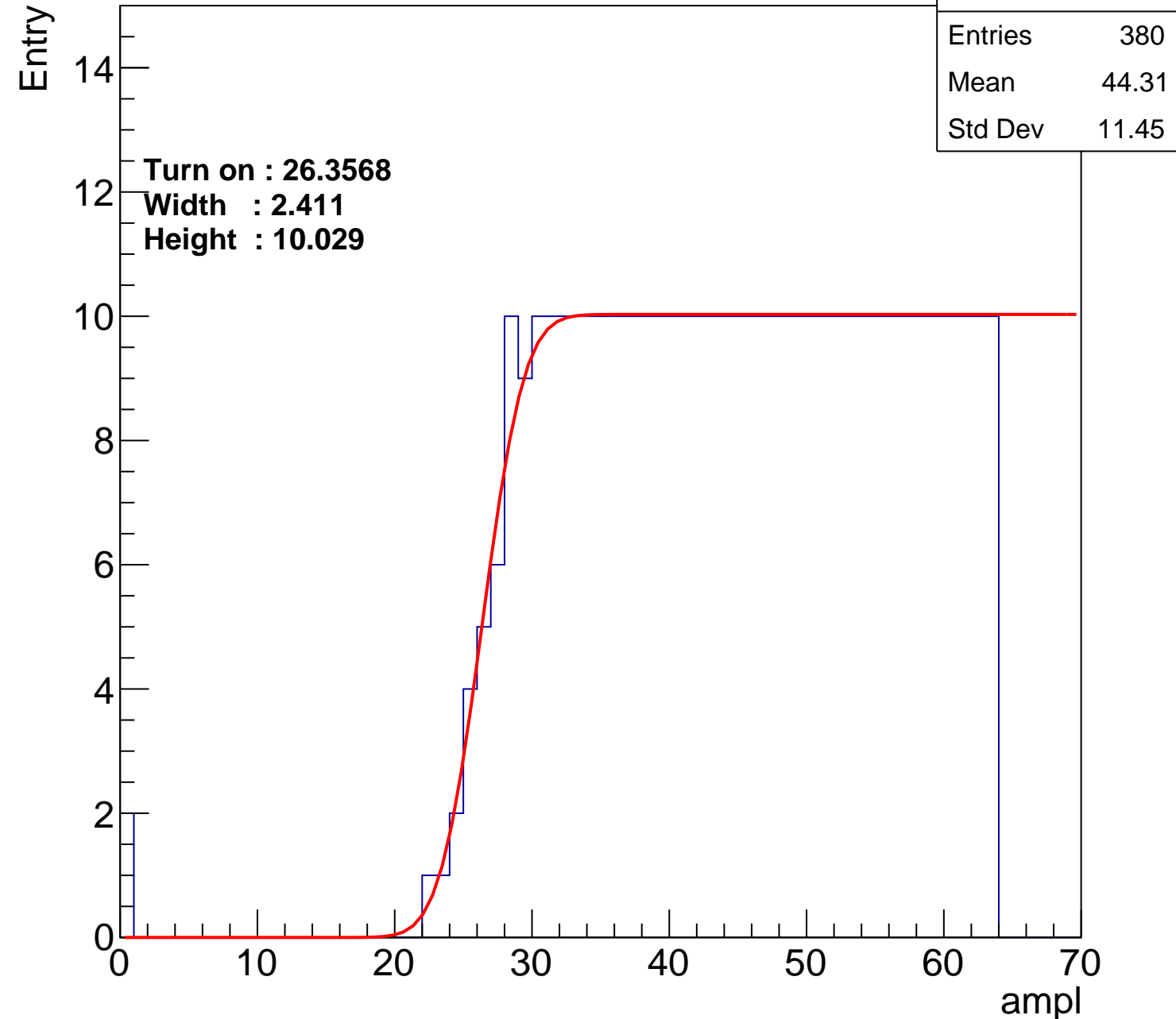
Width : 2.411

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.6436

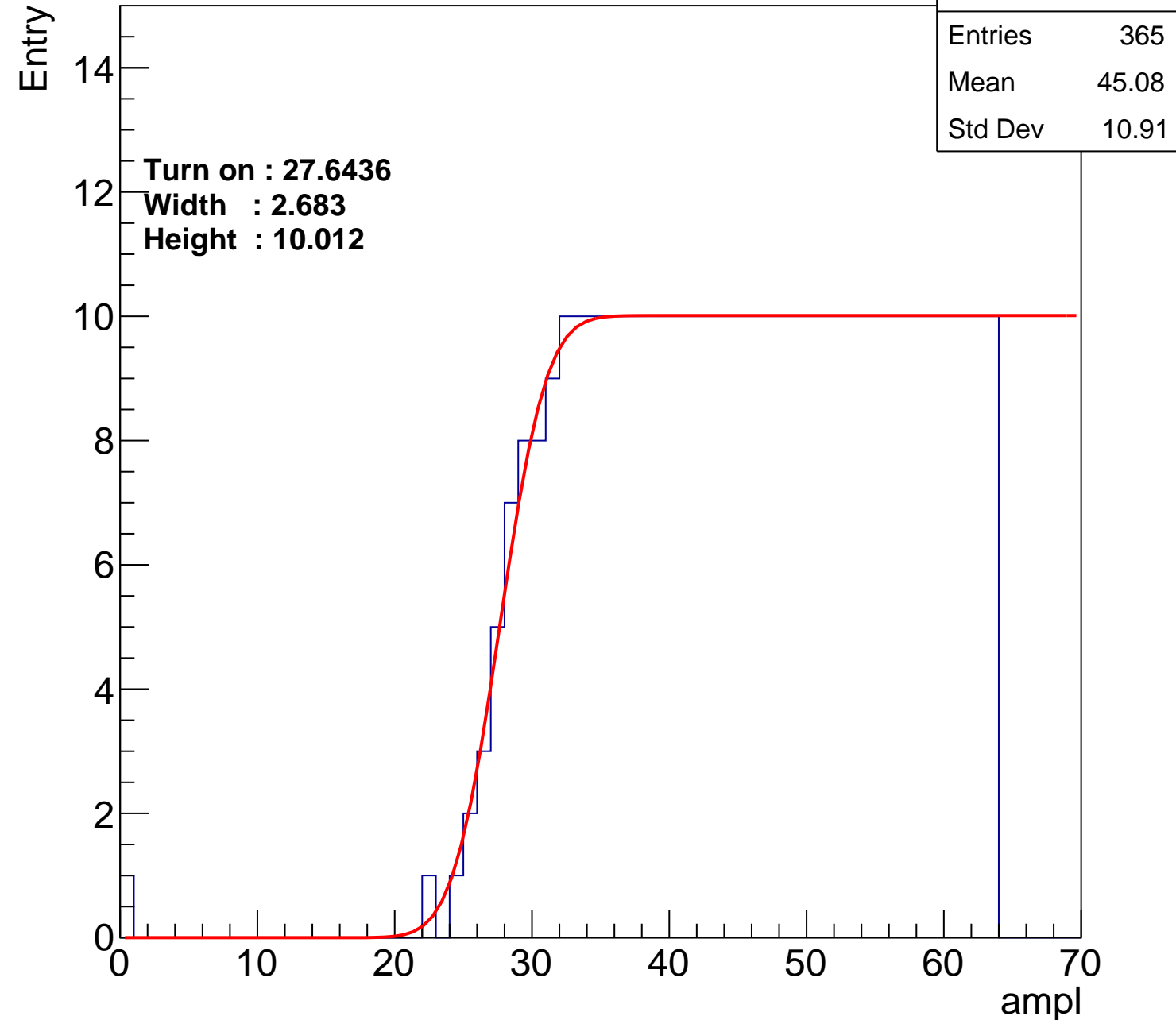
Width : 2.683

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch51

calib_packv5_042523_0143.root, FC#8, port C1

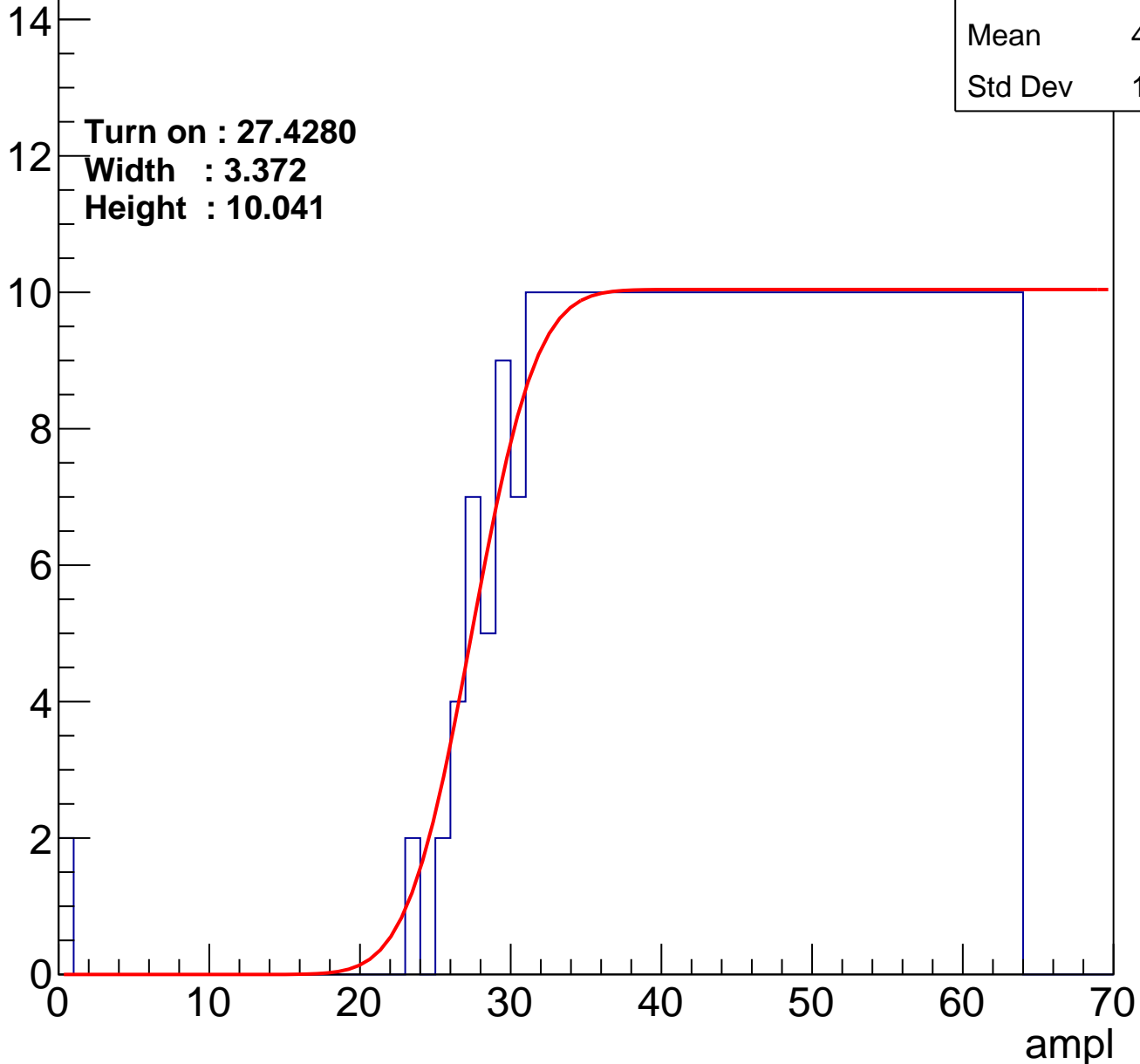
Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.4280

Width : 3.372

Height : 10.041

Entry



B0L002S, U16-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 26.3293

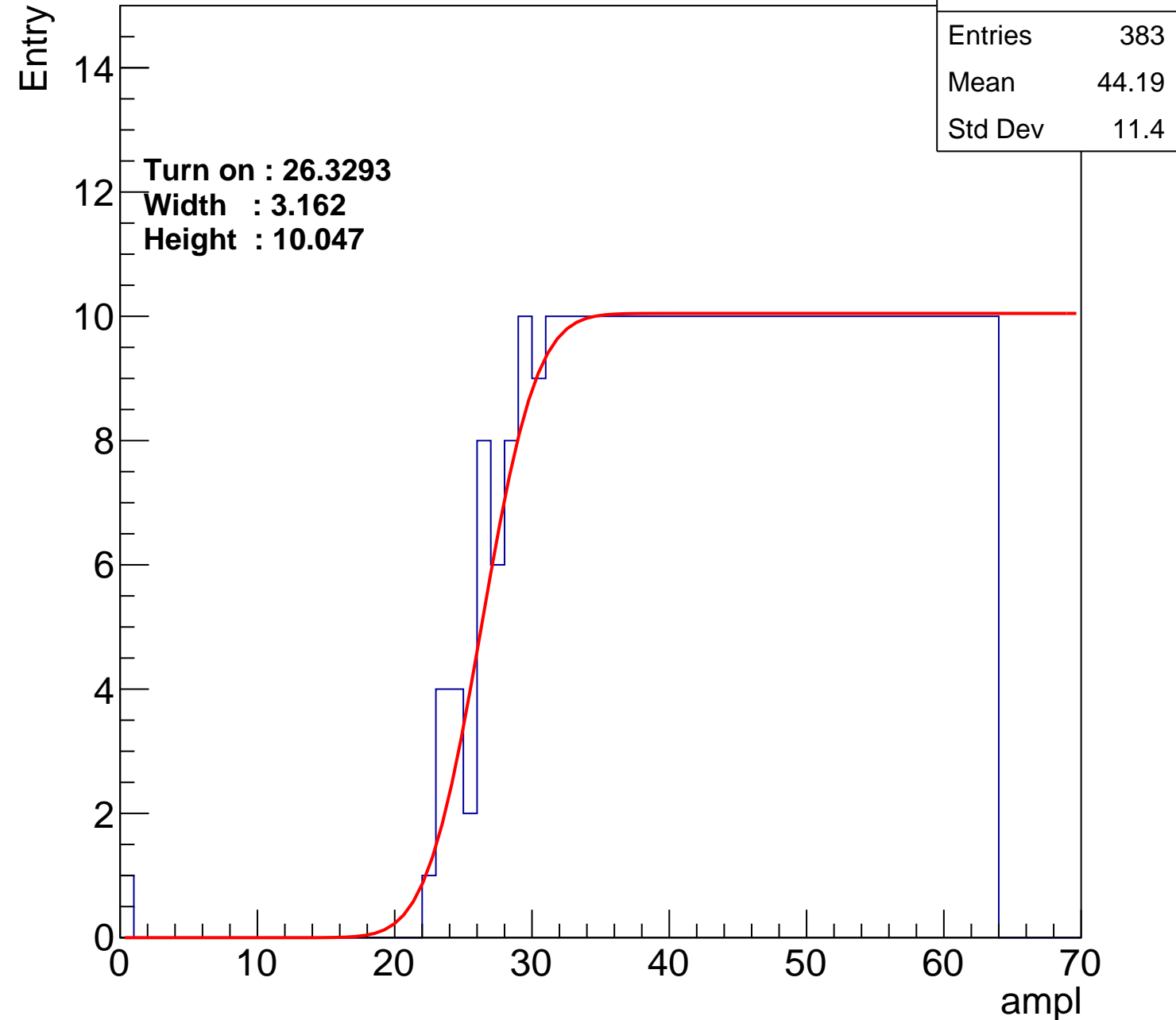
Width : 3.162

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.73
Std Dev	10.54

Turn on : 29.1842

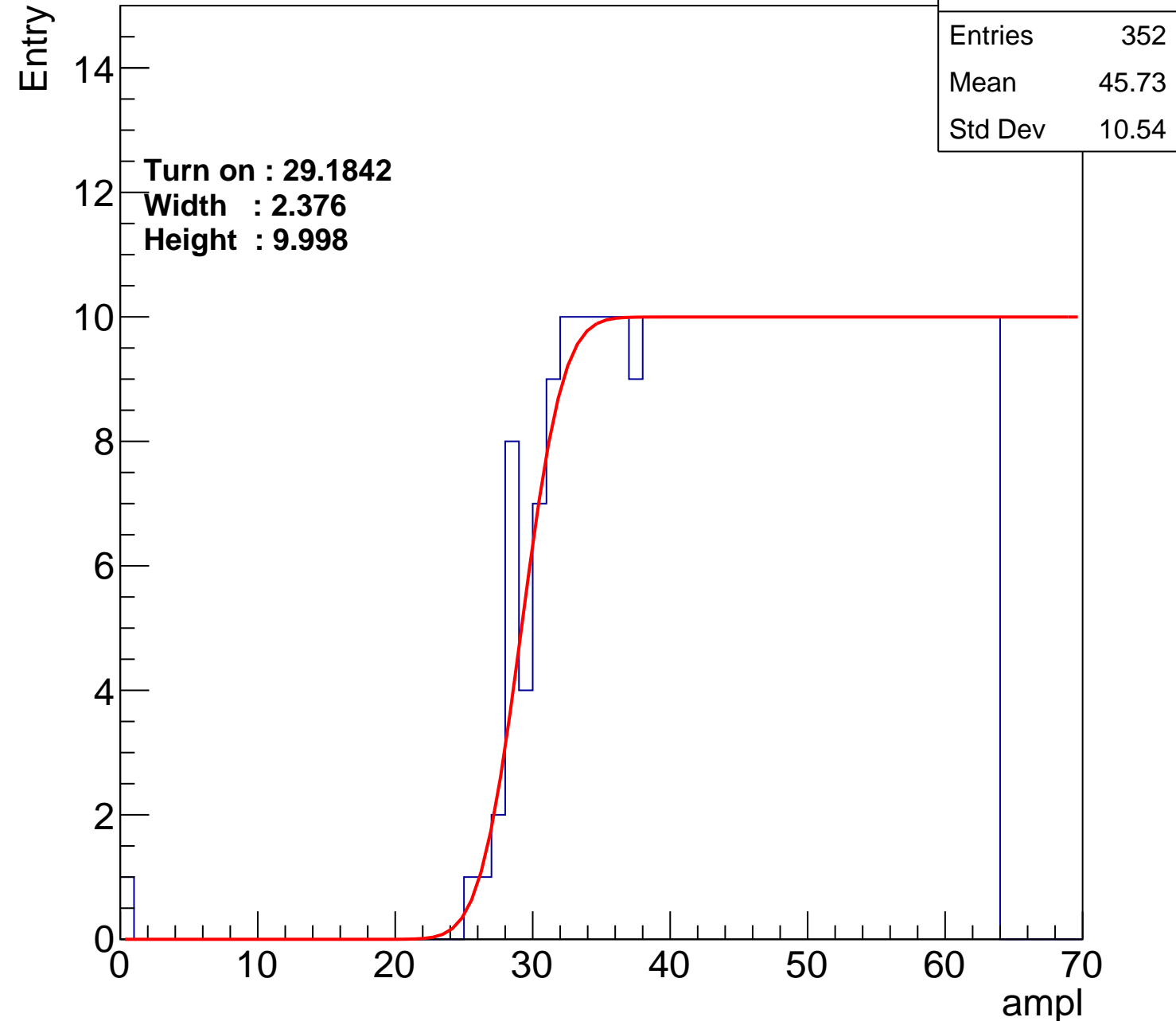
Width : 2.376

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	44.07
Std Dev	11.49

Turn on : 25.9845

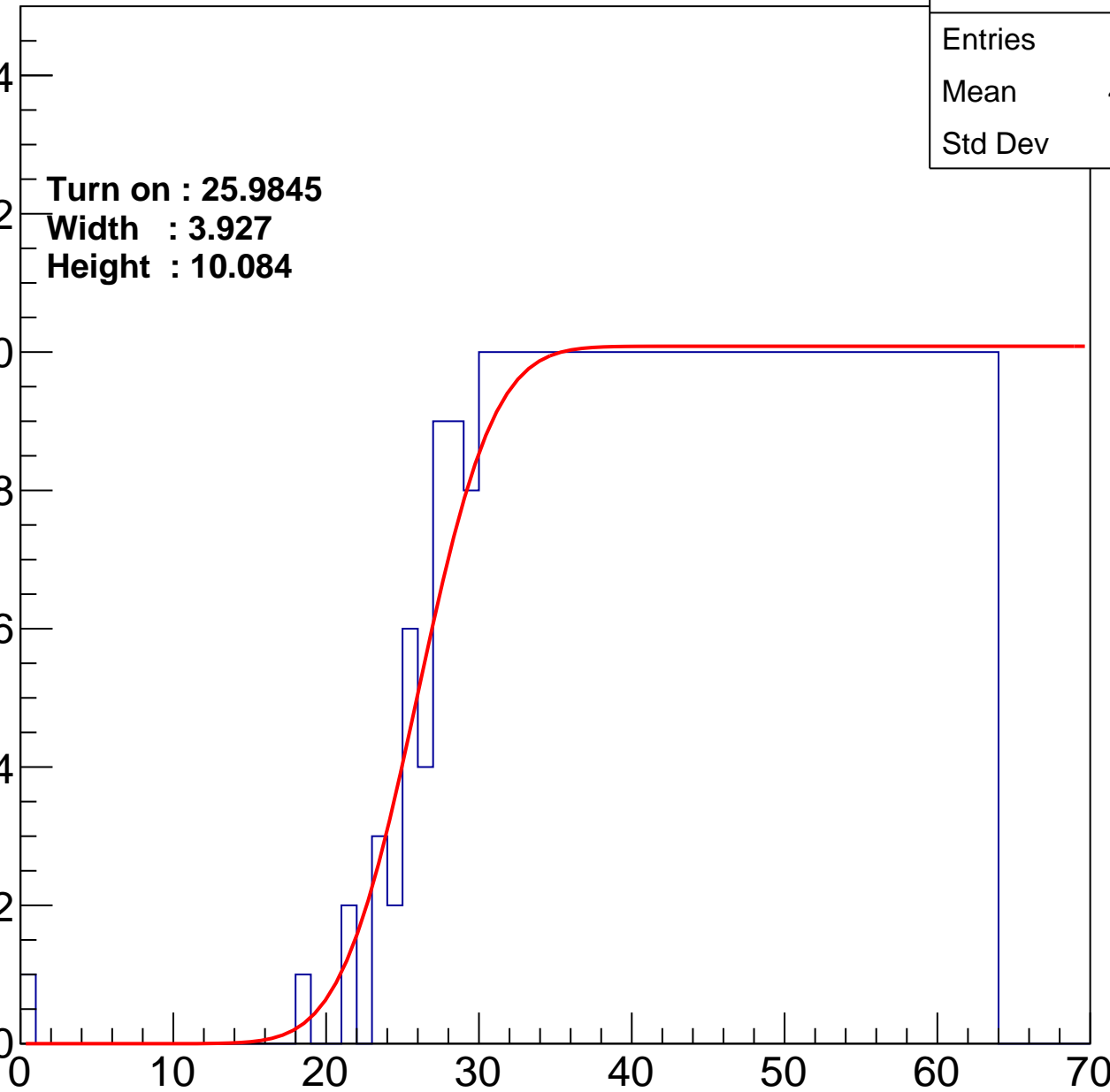
Width : 3.927

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.74
Std Dev	10.59

Turn on : 29.3986

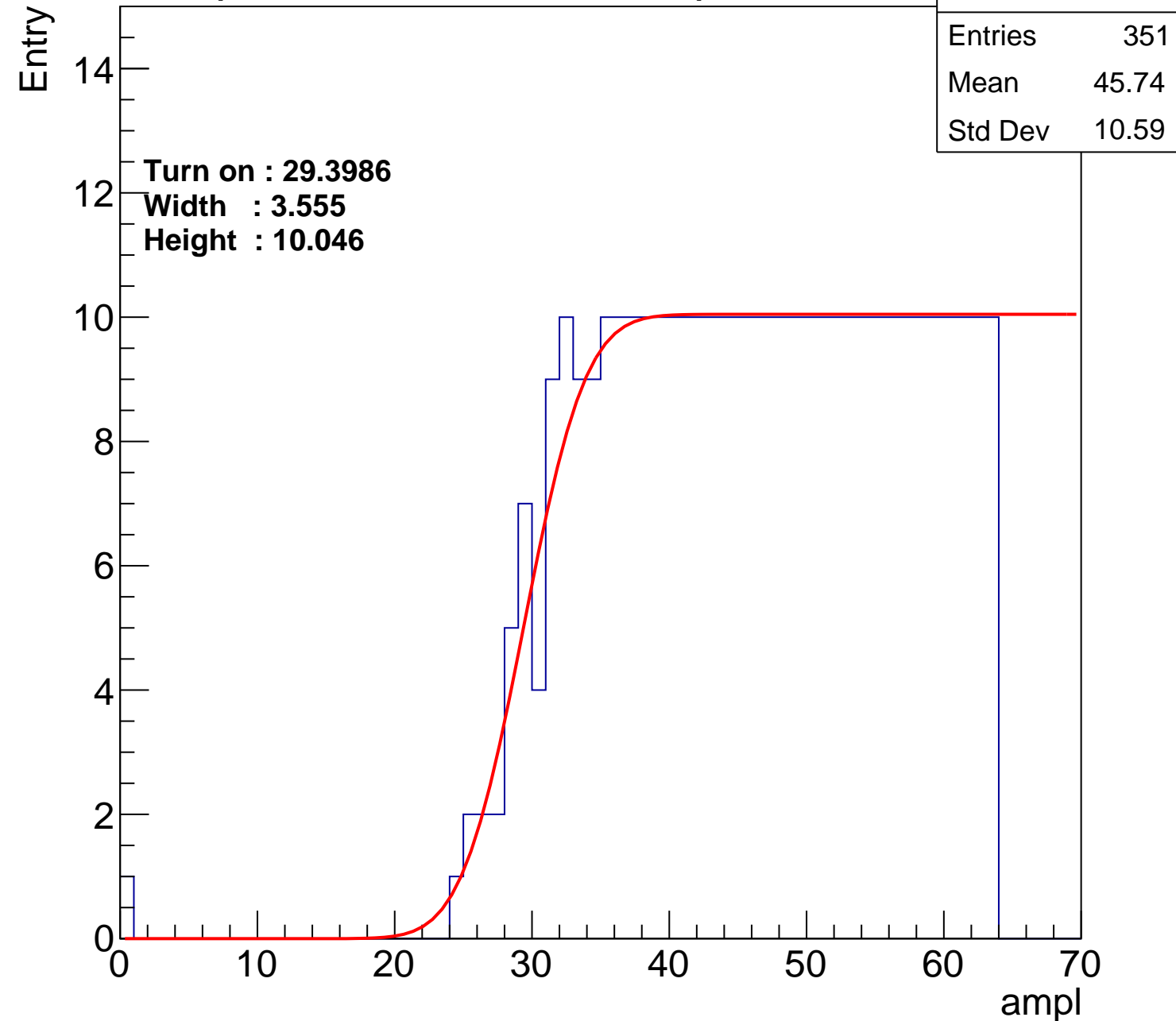
Width : 3.555

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch56

calib_packv5_042523_0143.root, FC#8, port C1

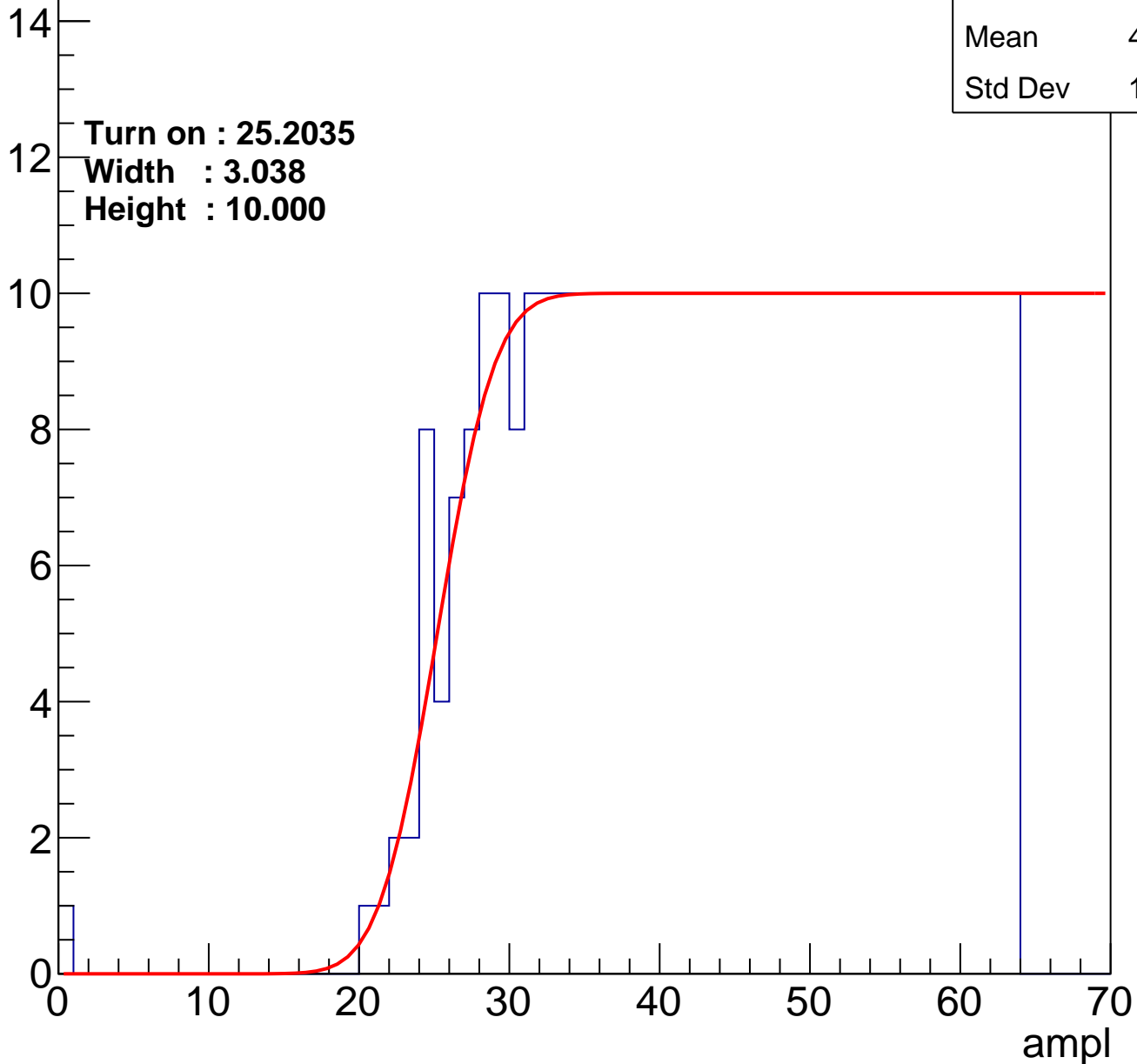
Entries	392
Mean	43.73
Std Dev	11.67

Turn on : 25.2035

Width : 3.038

Height : 10.000

Entry



B0L002S, U16-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.96
Std Dev	10.4

Turn on : 29.9912

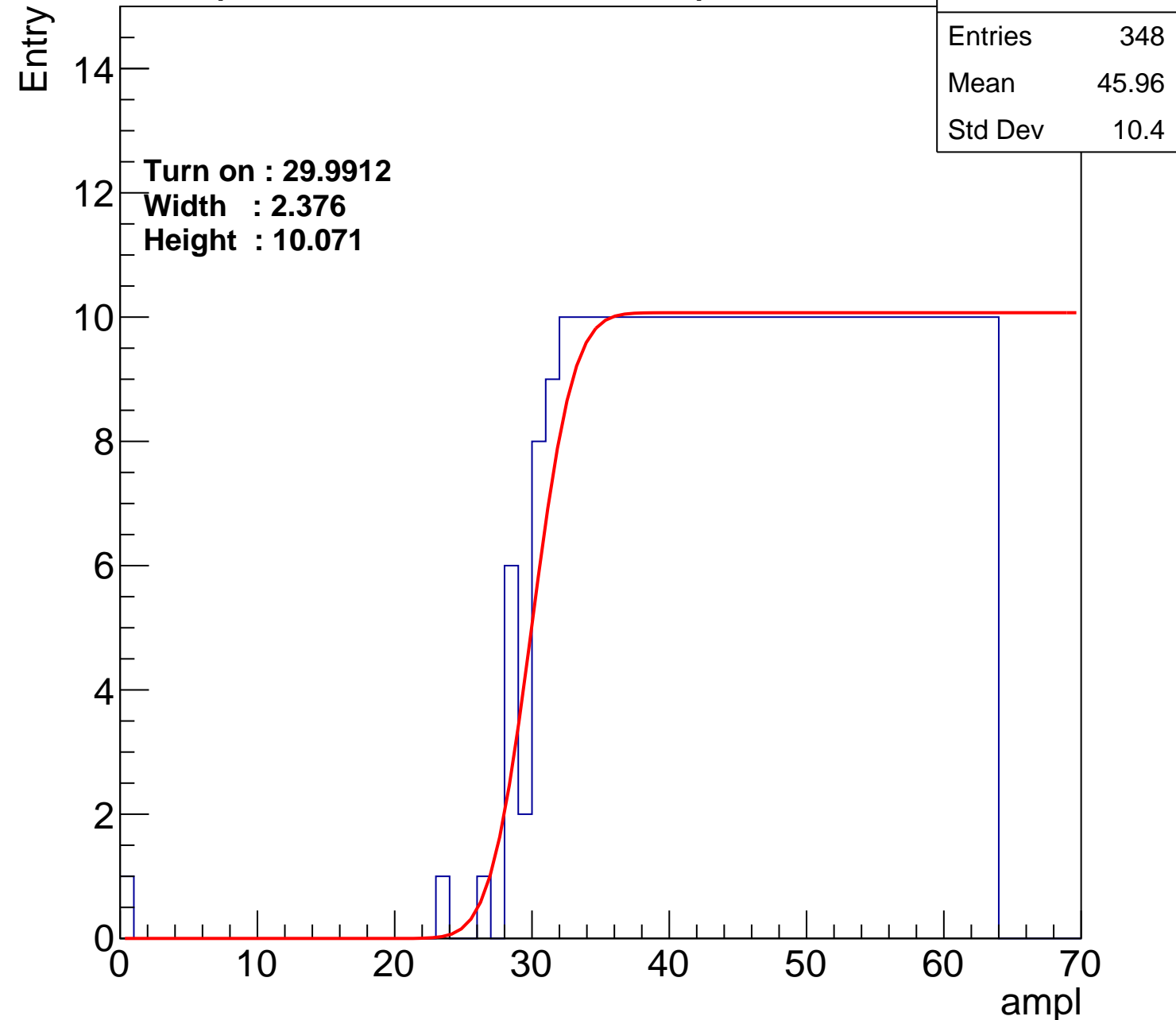
Width : 2.376

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch58

calib_packv5_042523_0143.root, FC#8, port C1

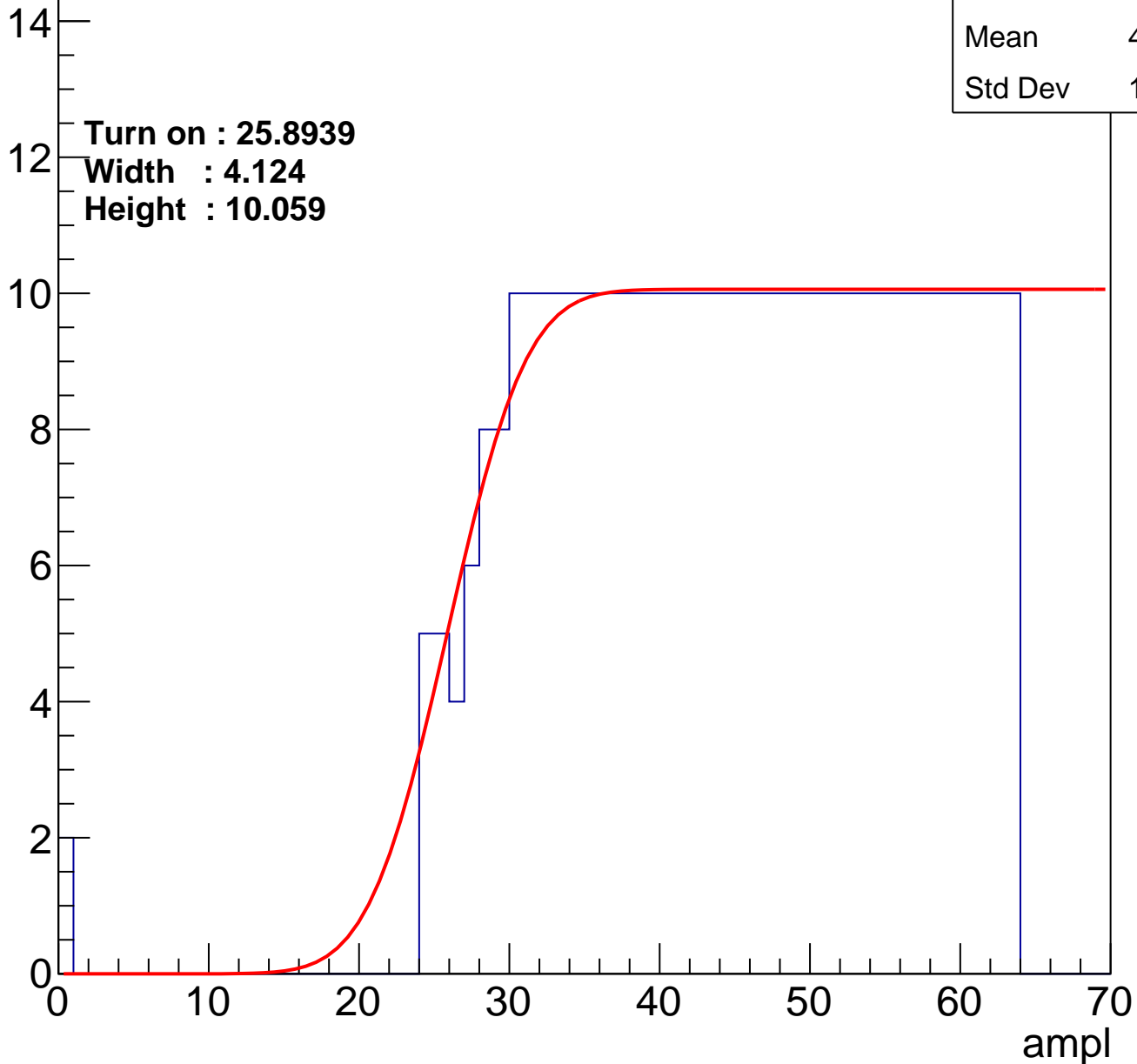
Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 25.8939

Width : 4.124

Height : 10.059

Entry



B0L002S, U16-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.55
Std Dev	11.5

Turn on : 27.5395

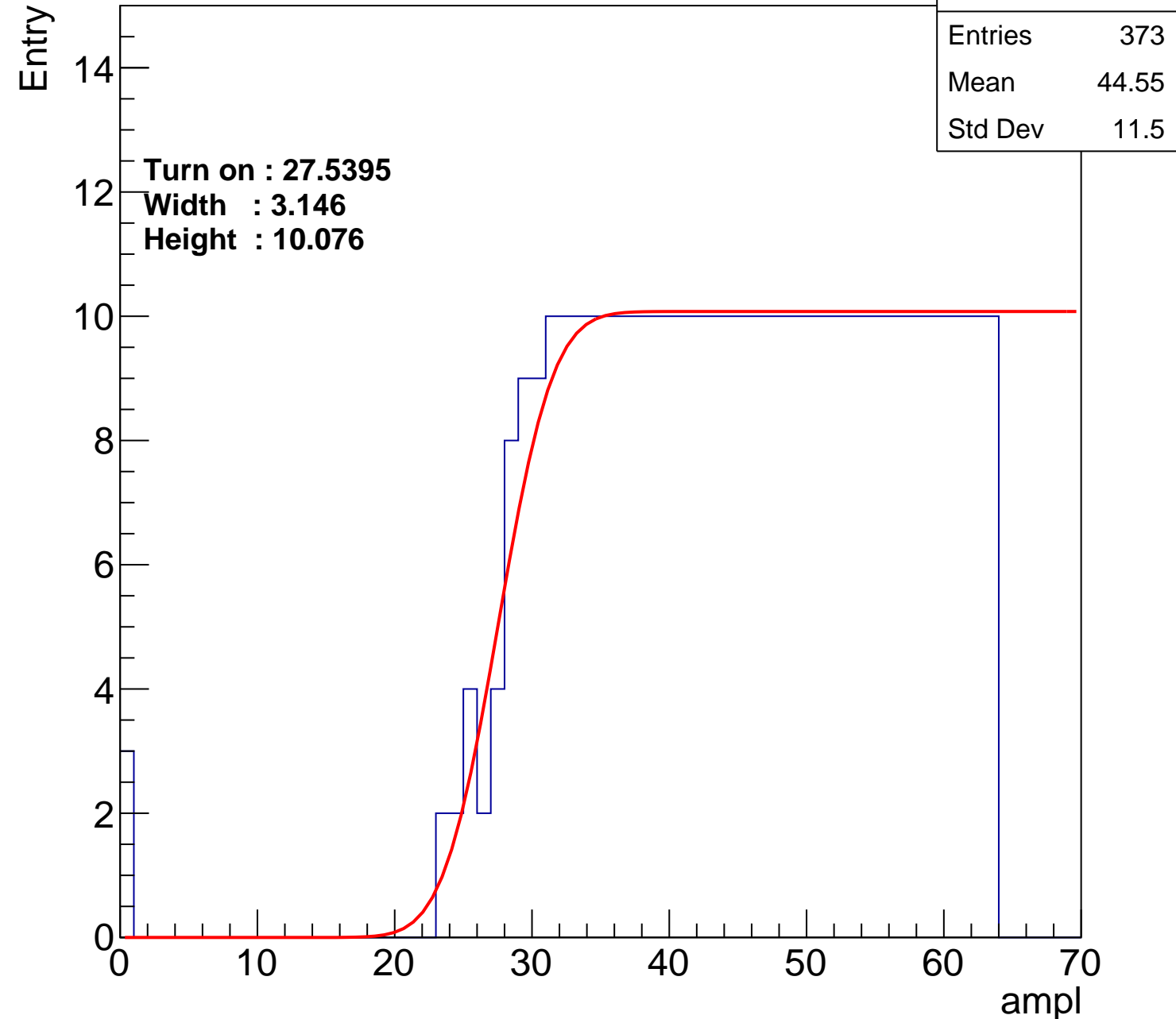
Width : 3.146

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch60

calib_packv5_042523_0143.root, FC#8, port C1

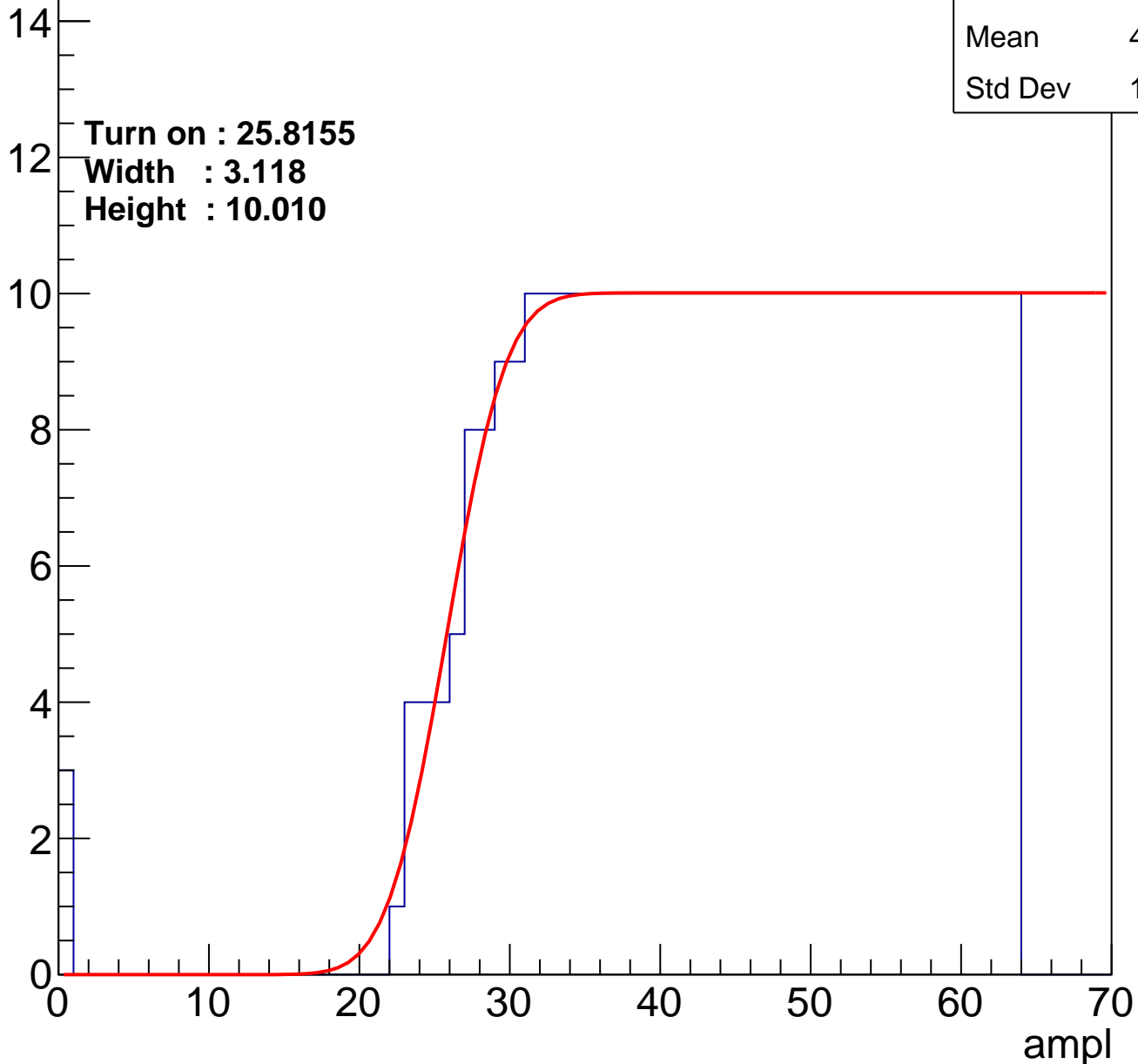
Entries	385
Mean	43.95
Std Dev	11.82

Turn on : 25.8155

Width : 3.118

Height : 10.010

Entry



B0L002S, U16-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.95
Std Dev	11.17

Turn on : 28.1568

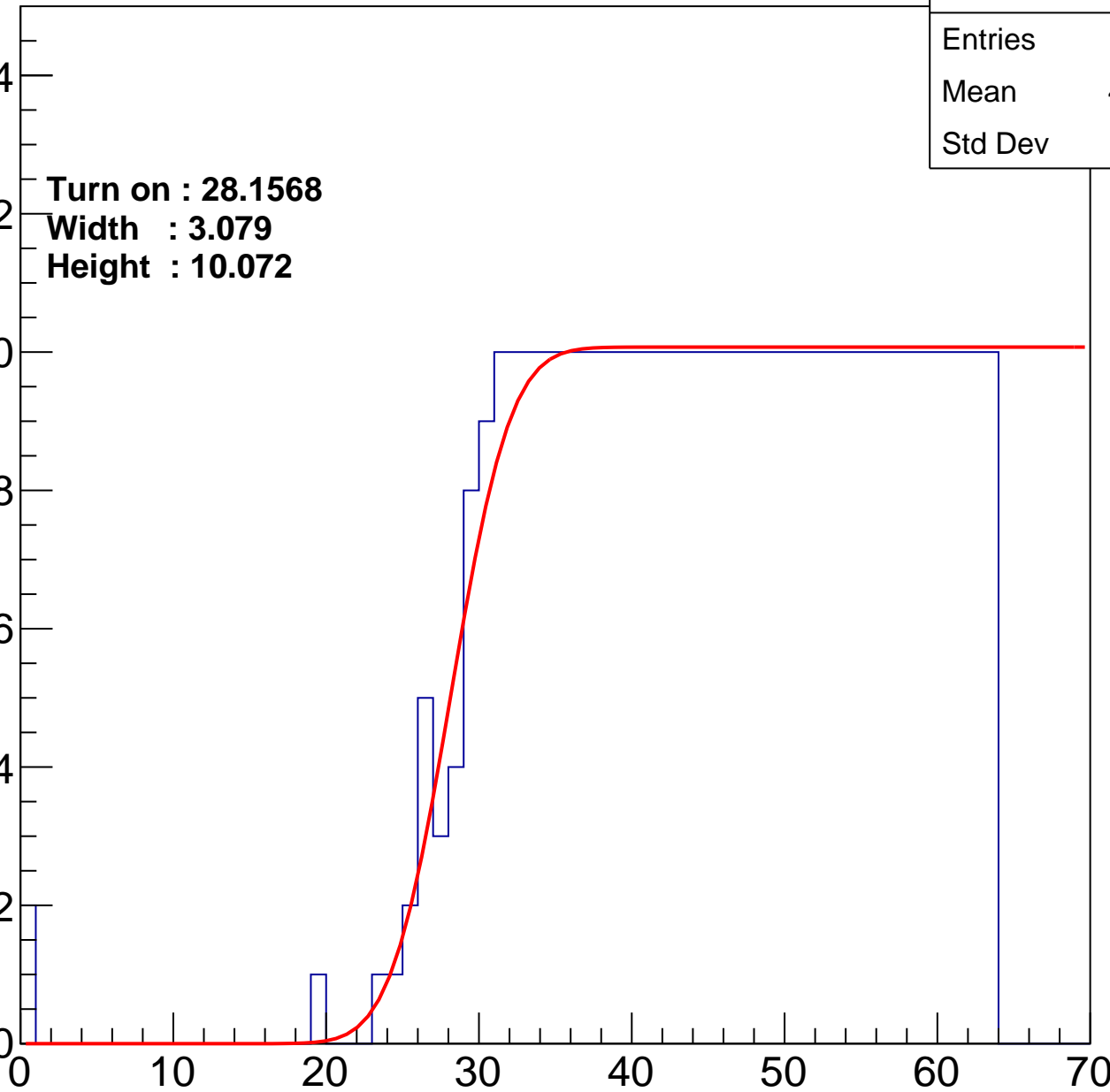
Width : 3.079

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.51
Std Dev	11.87

Turn on : 27.3842

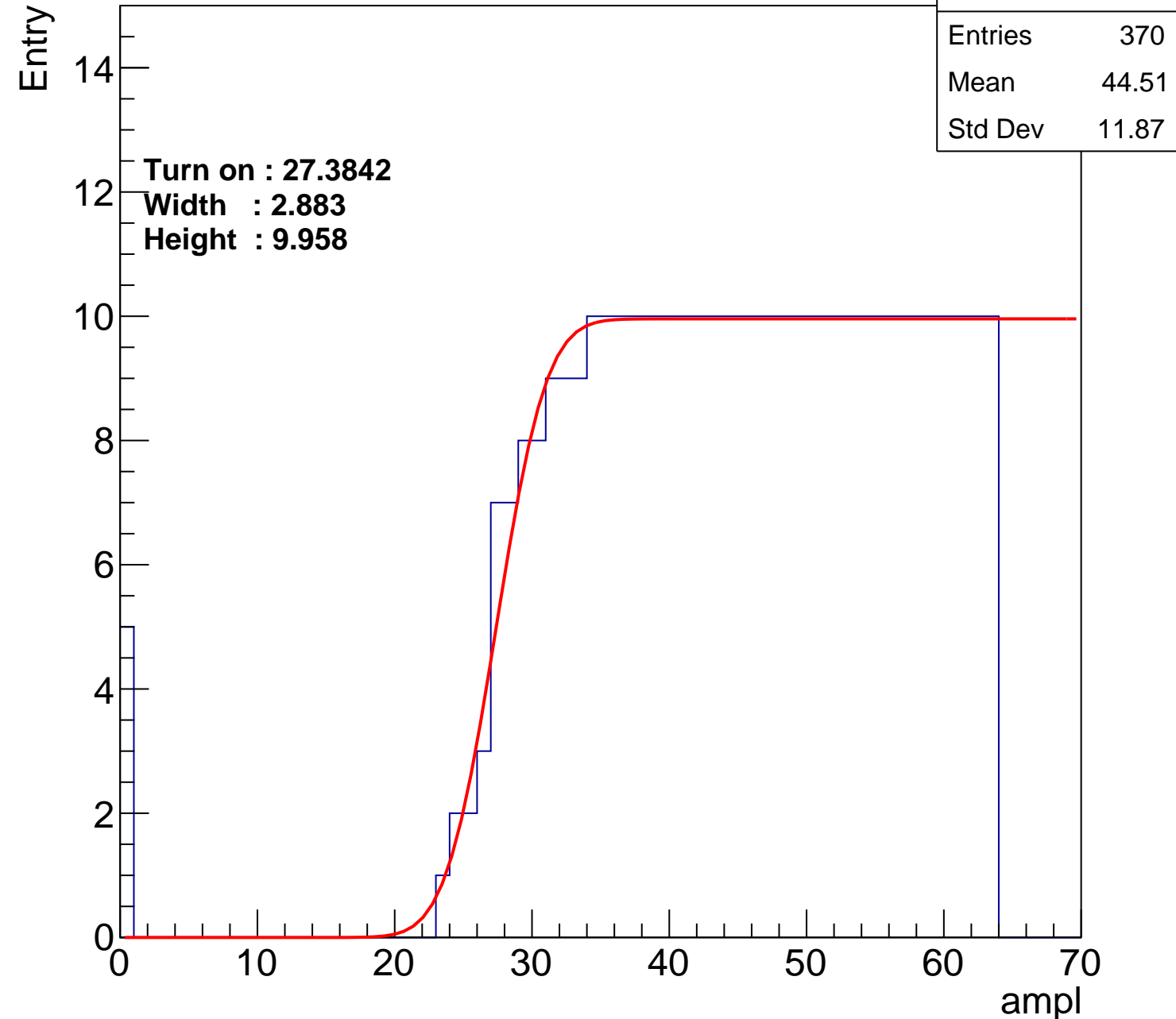
Width : 2.883

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	393
Mean	43.5
Std Dev	12.18

Turn on : 25.4673

Width : 2.697

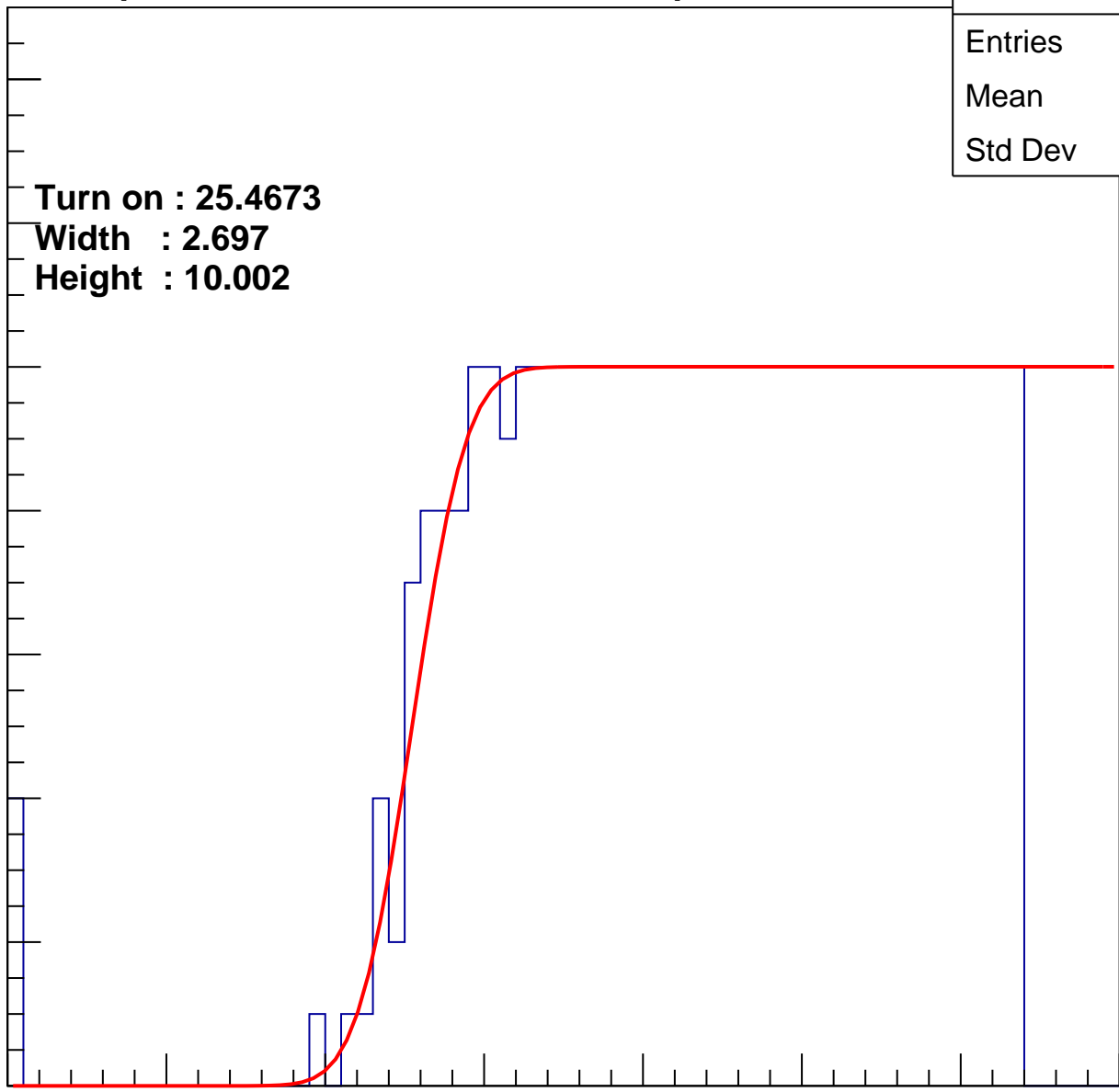
Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U16-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.3
Std Dev	11.51

Turn on : 26.7160

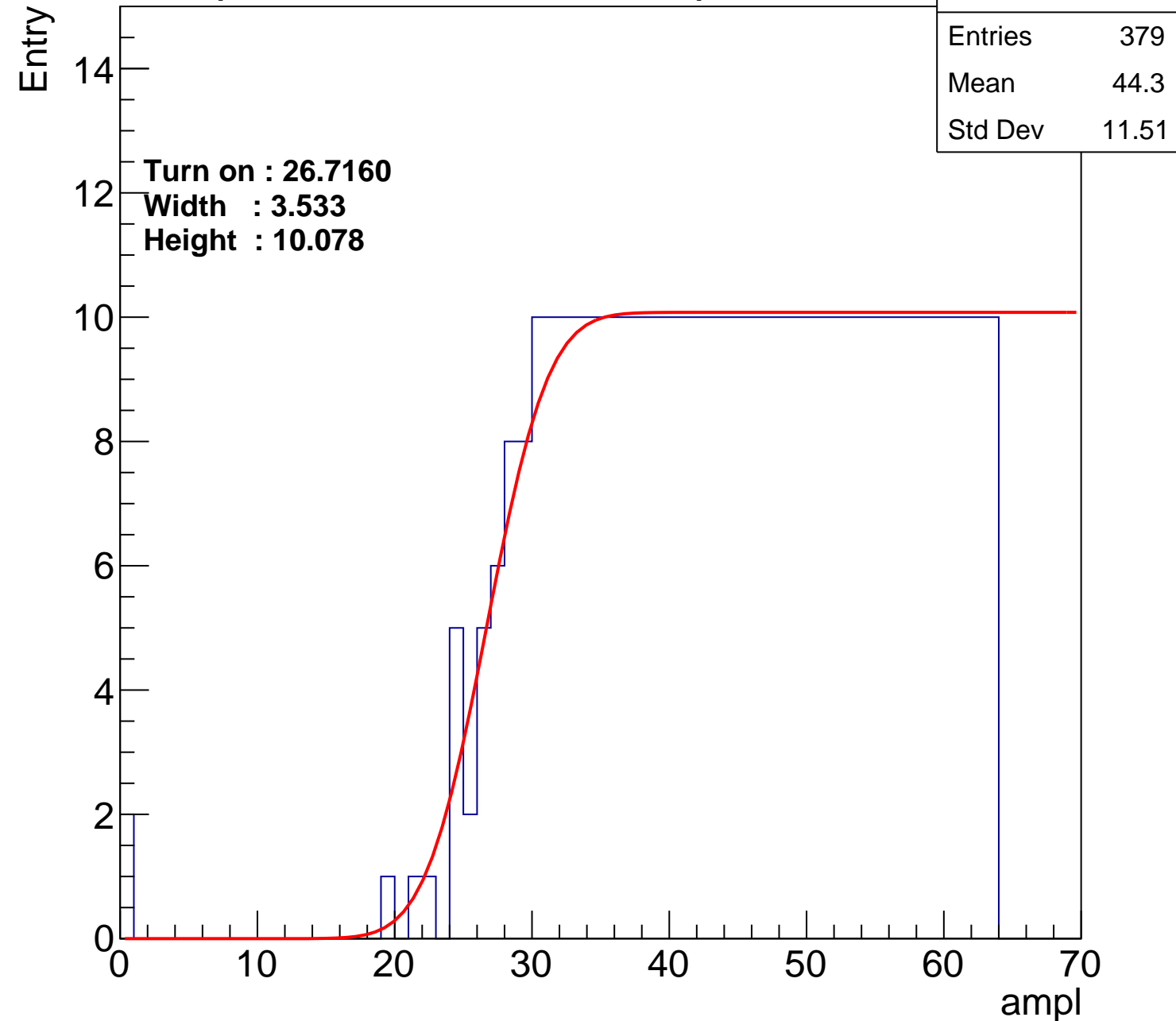
Width : 3.533

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch65

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.27
Std Dev	11.82

Turn on : 27.4549

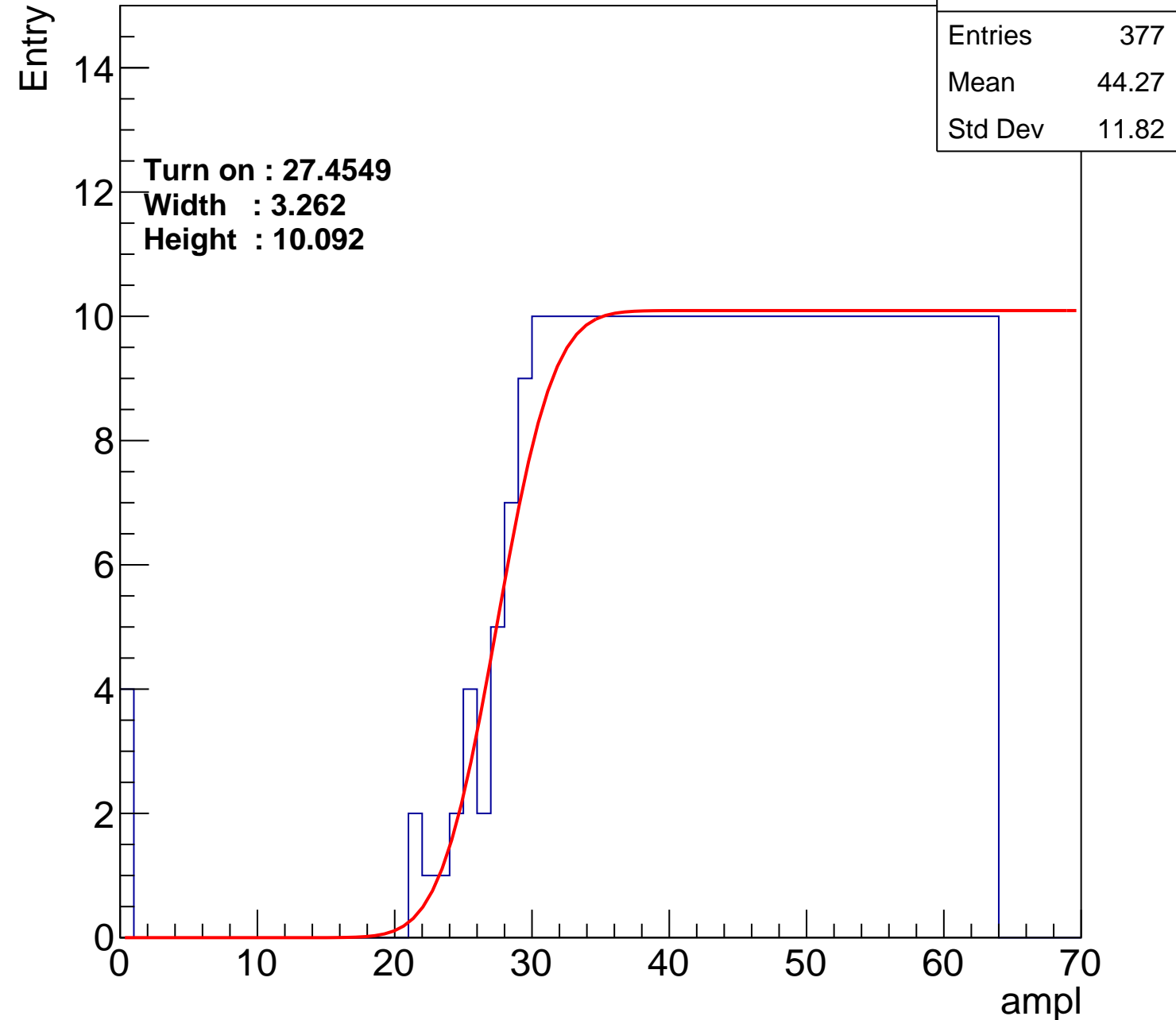
Width : 3.262

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch66

calib_packv5_042523_0143.root, FC#8, port C1

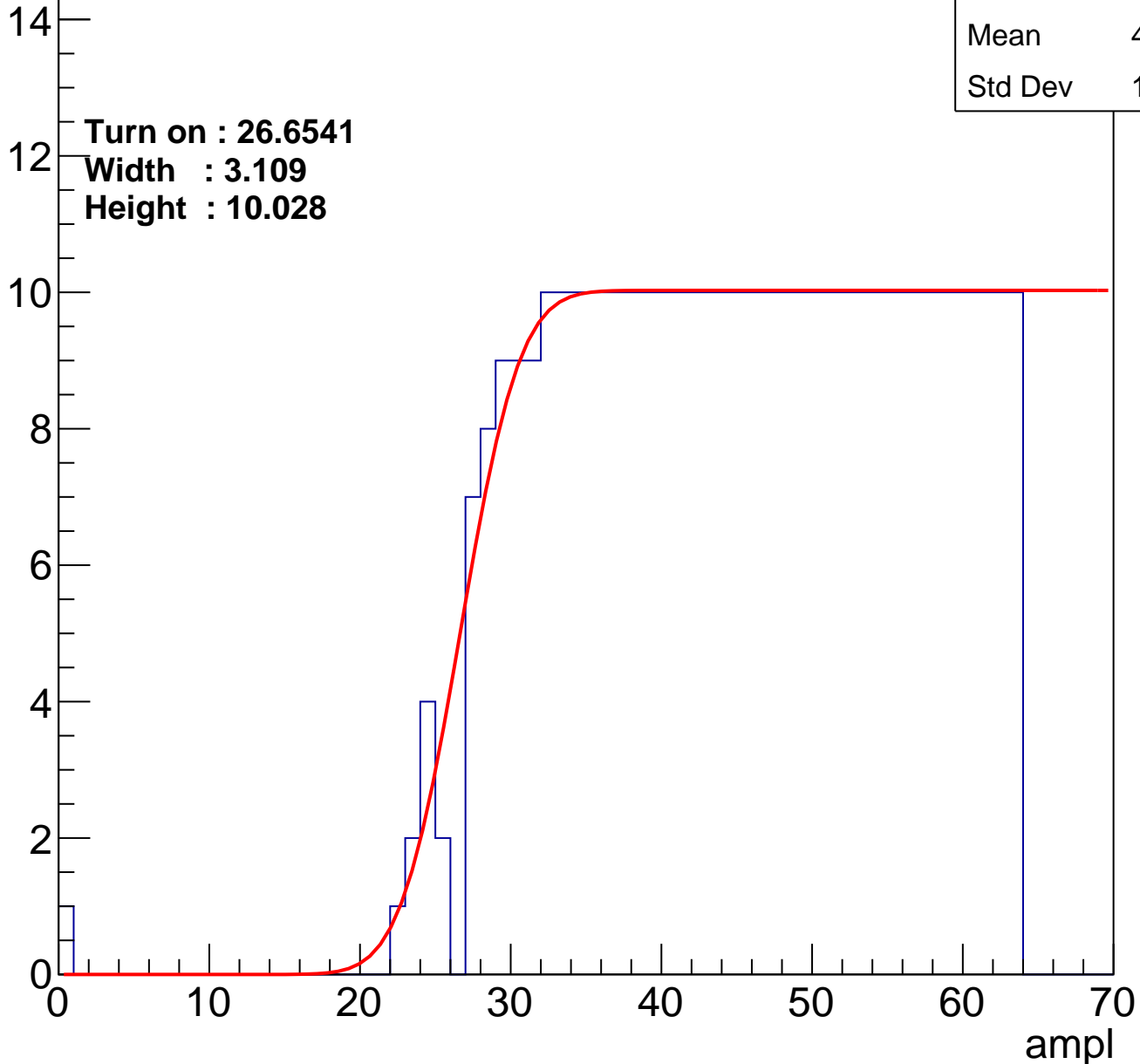
Entries	372
Mean	44.72
Std Dev	11.12

Turn on : 26.6541

Width : 3.109

Height : 10.028

Entry



B0L002S, U16-ch67

calib_packv5_042523_0143.root, FC#8, port C1

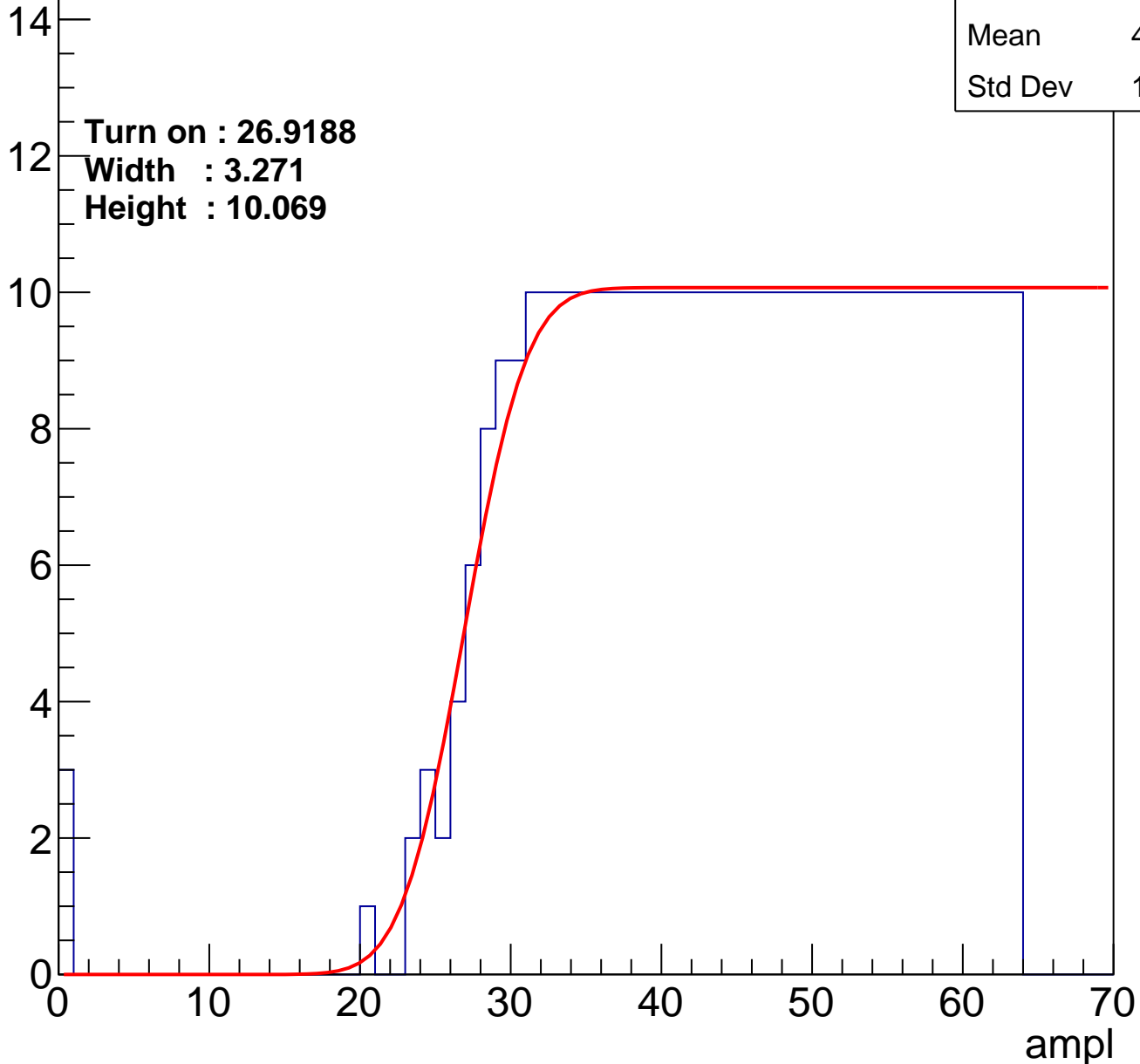
Entries	377
Mean	44.35
Std Dev	11.62

Turn on : 26.9188

Width : 3.271

Height : 10.069

Entry



B0L002S, U16-ch68

calib_packv5_042523_0143.root, FC#8, port C1

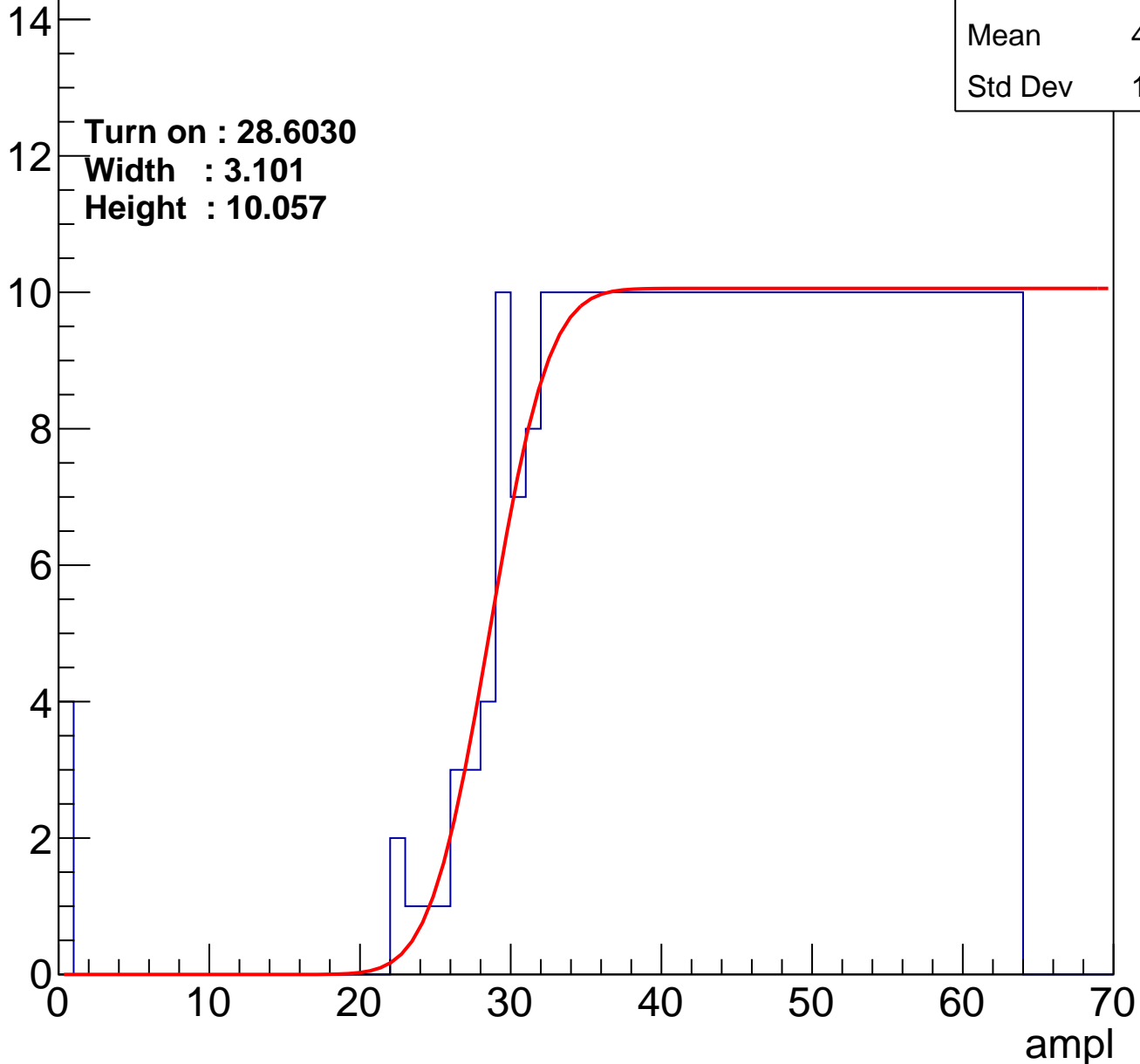
Entries	364
Mean	44.88
Std Dev	11.56

Turn on : 28.6030

Width : 3.101

Height : 10.057

Entry



B0L002S, U16-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.56
Std Dev	11.17

Turn on : 26.7575

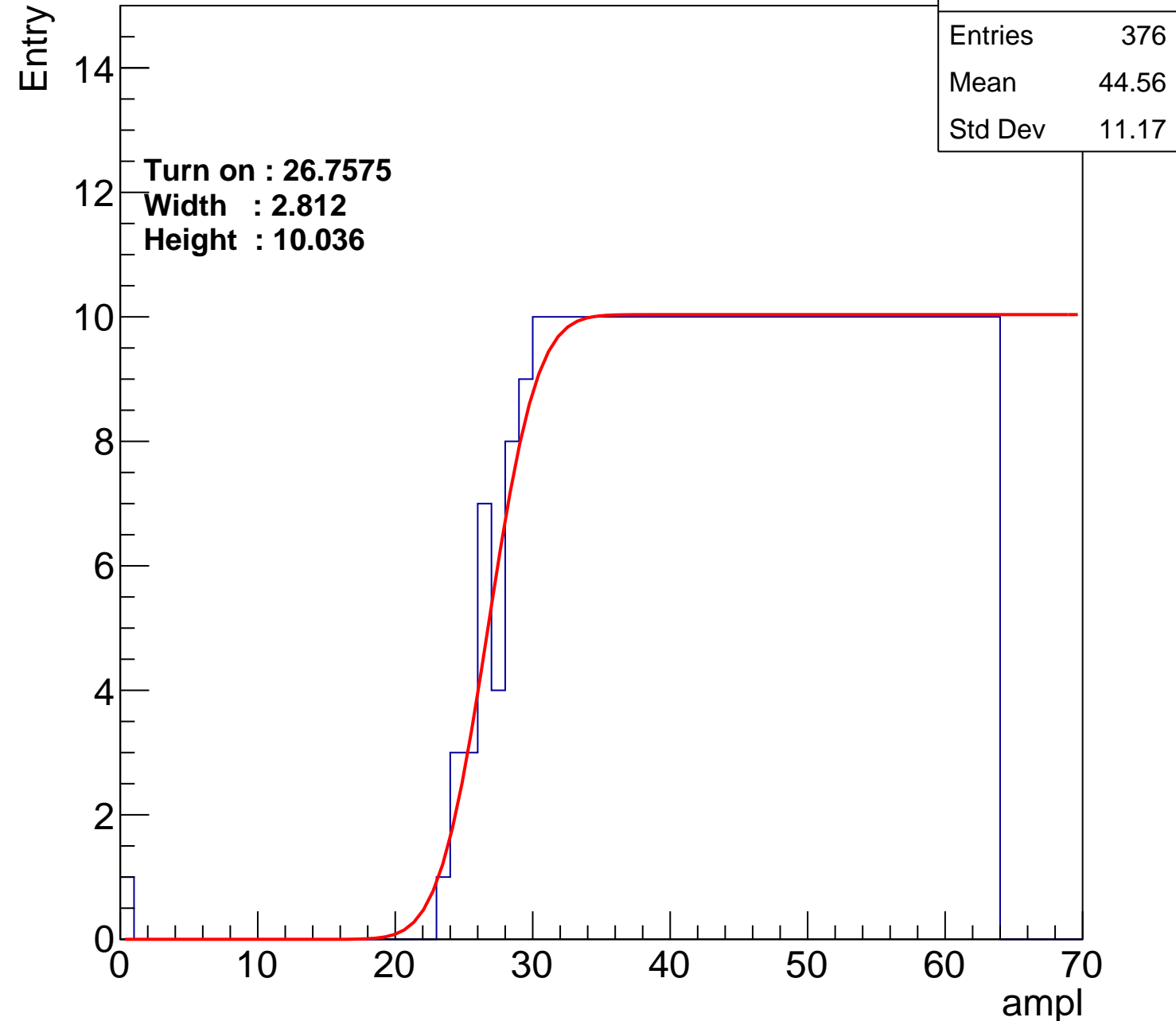
Width : 2.812

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch70

calib_packv5_042523_0143.root, FC#8, port C1

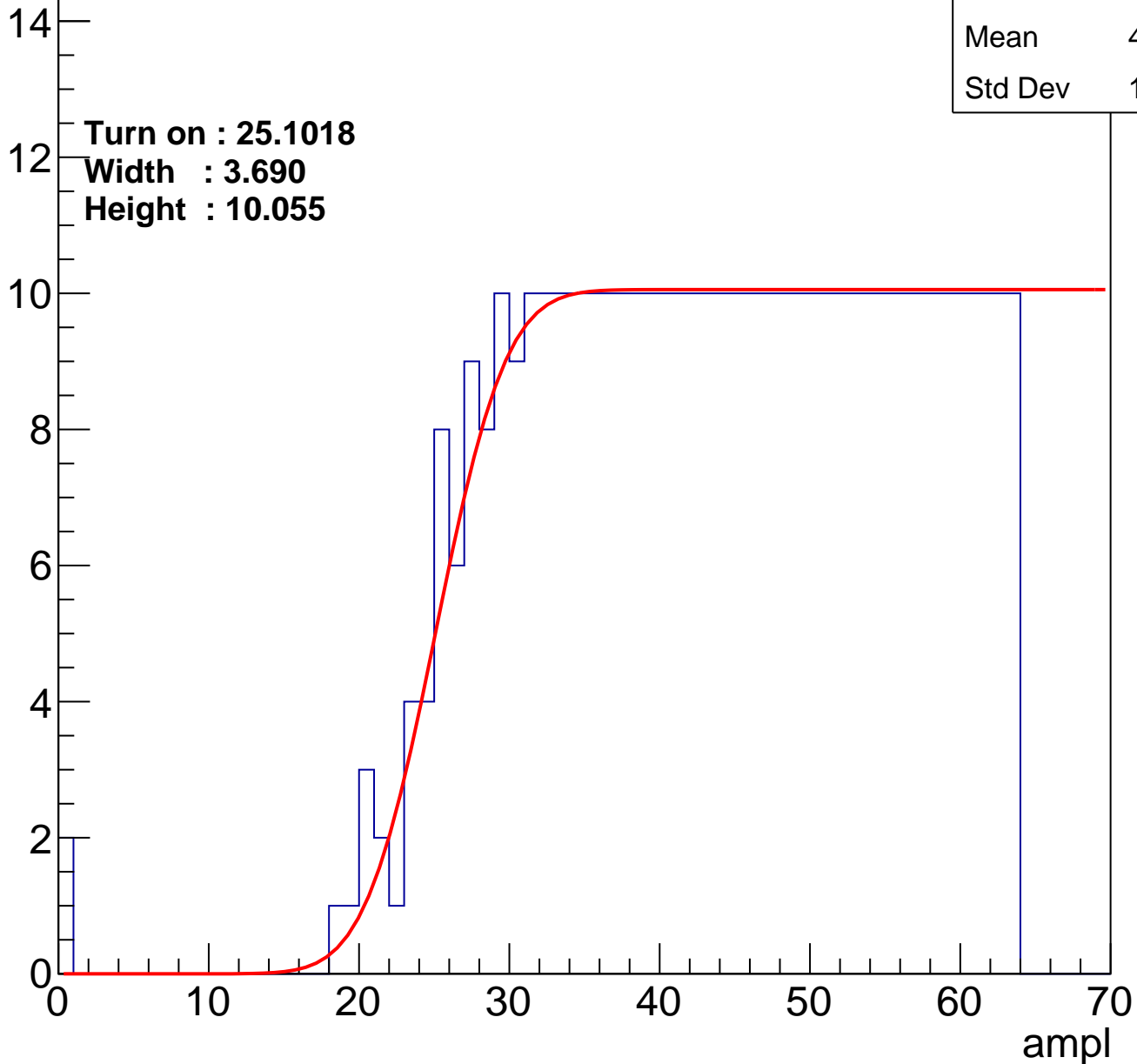
Entries	398
Mean	43.32
Std Dev	12.07

Turn on : 25.1018

Width : 3.690

Height : 10.055

Entry



B0L002S, U16-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.11
Std Dev	11.61

Turn on : 26.3143

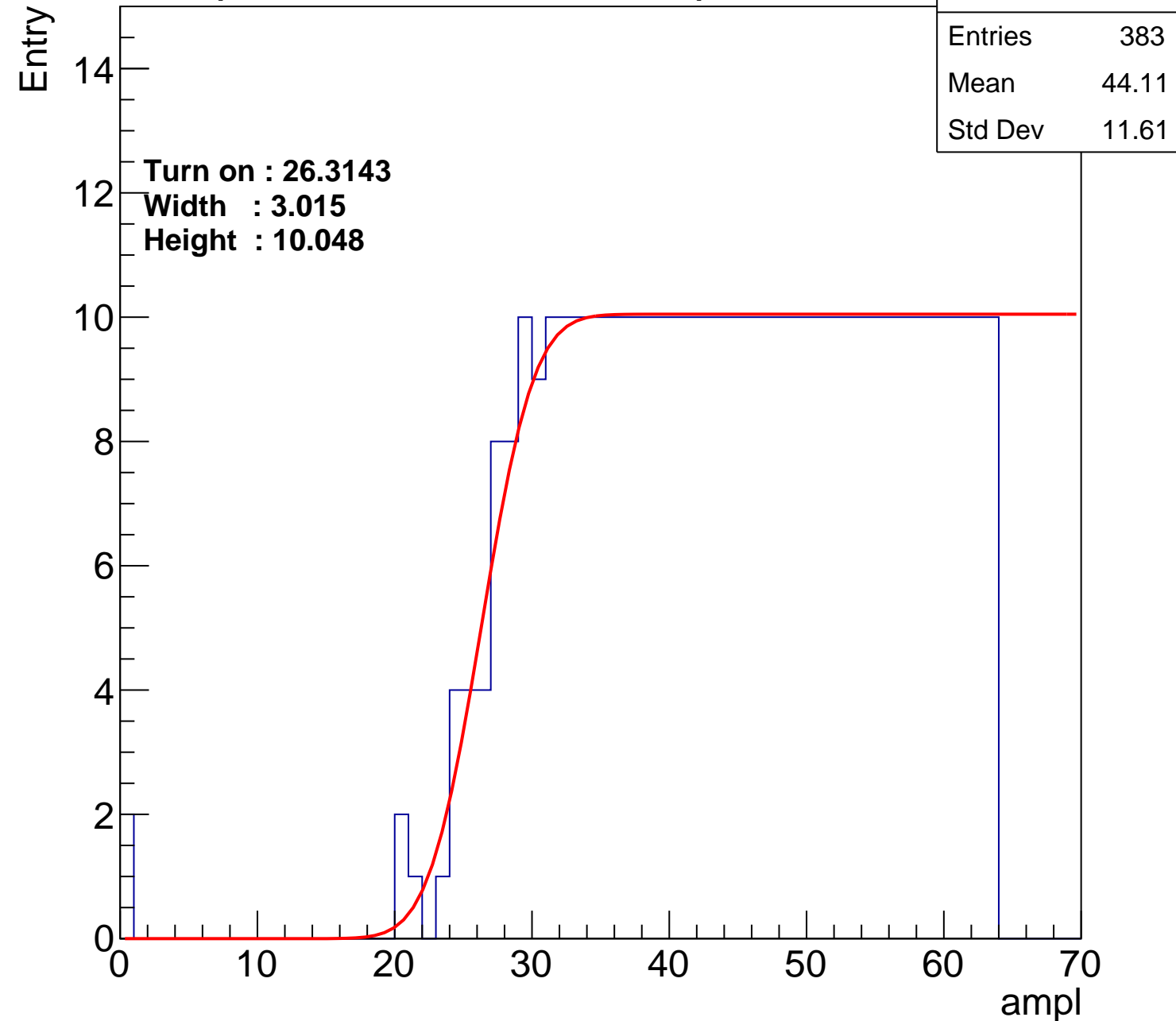
Width : 3.015

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.95
Std Dev	11.35

Turn on : 27.8037

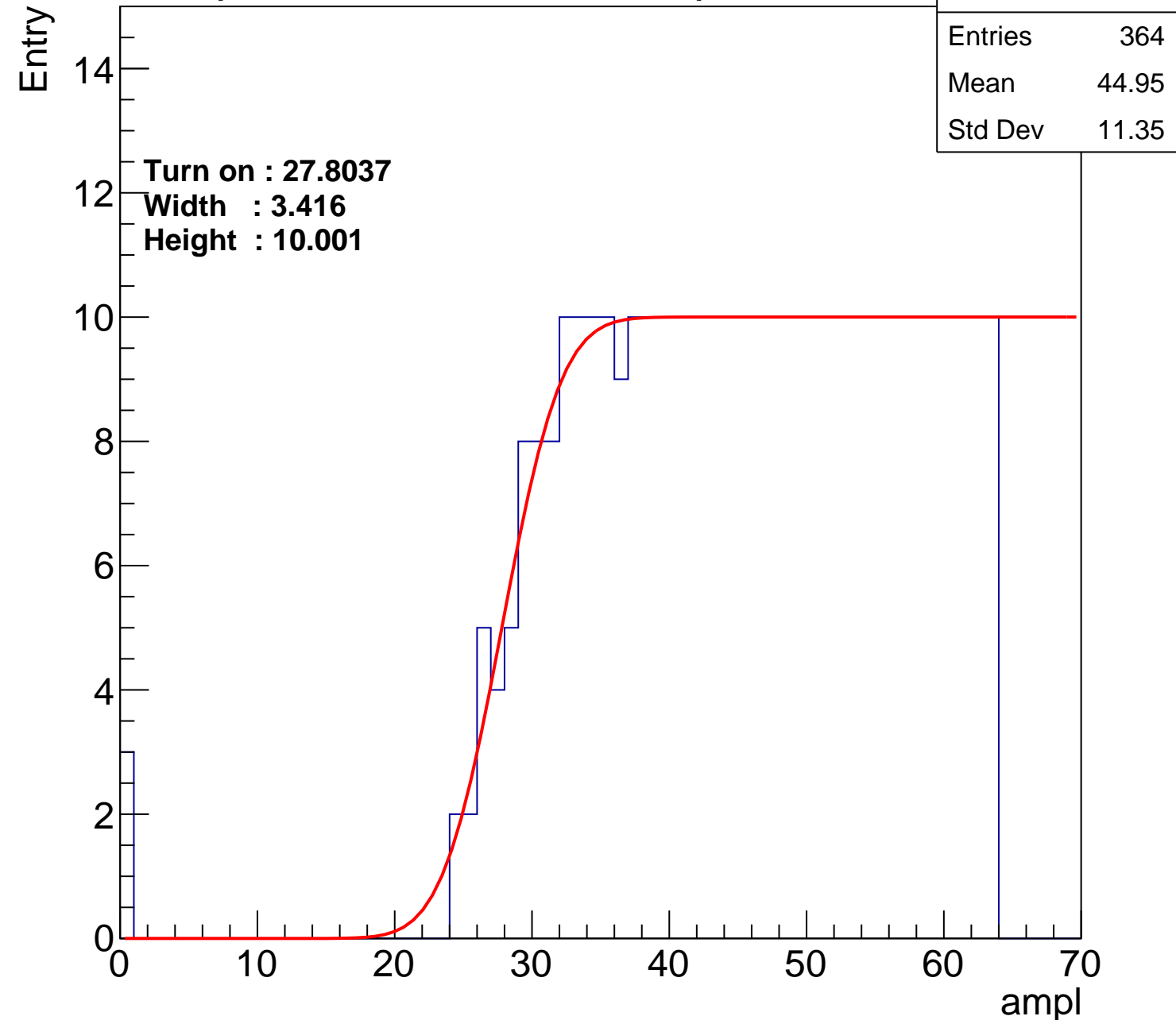
Width : 3.416

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch73

calib_packv5_042523_0143.root, FC#8, port C1

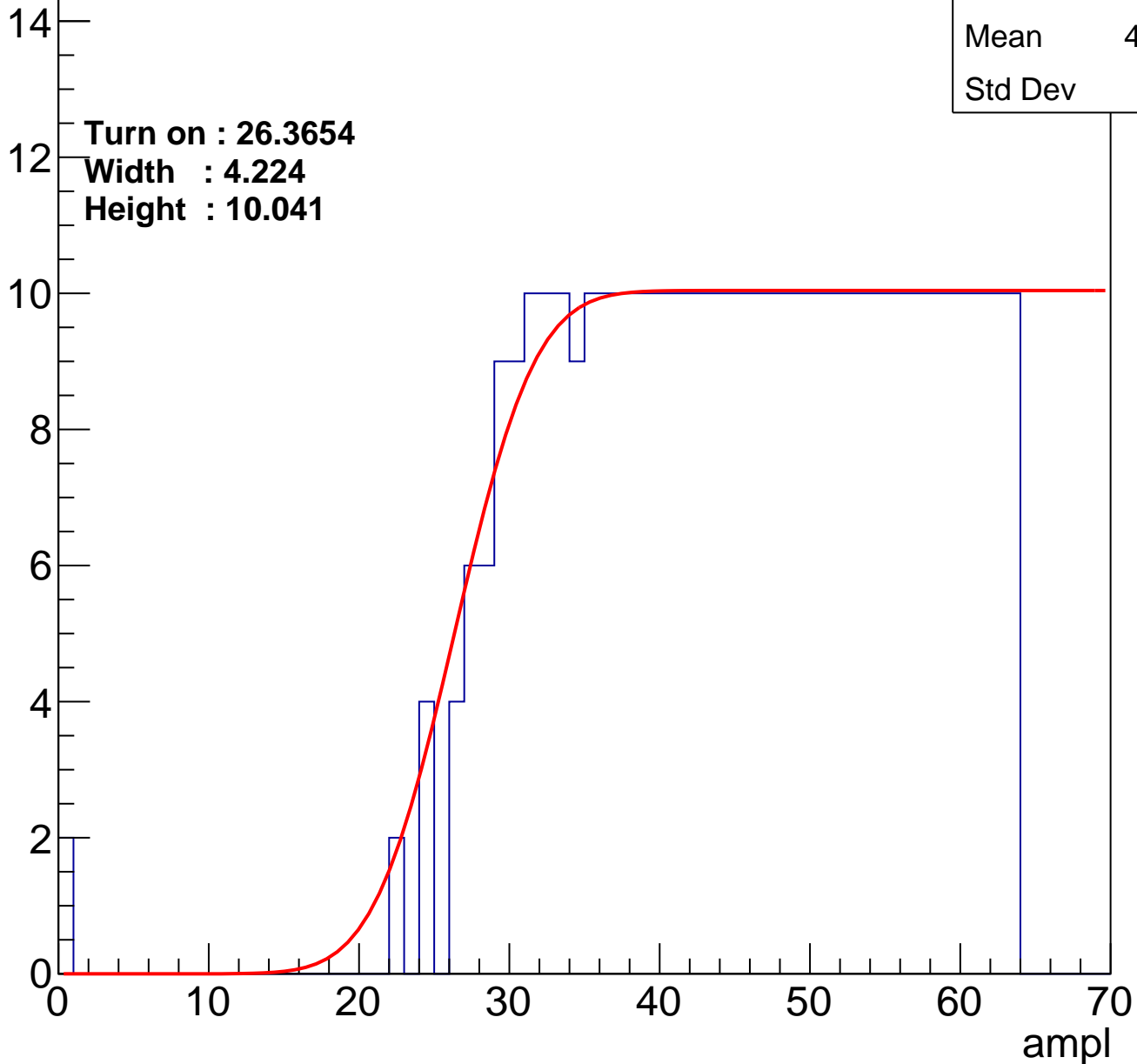
Entries	371
Mean	44.69
Std Dev	11.3

Turn on : 26.3654

Width : 4.224

Height : 10.041

Entry



B0L002S, U16-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	43.97
Std Dev	11.84

Turn on : 25.8888

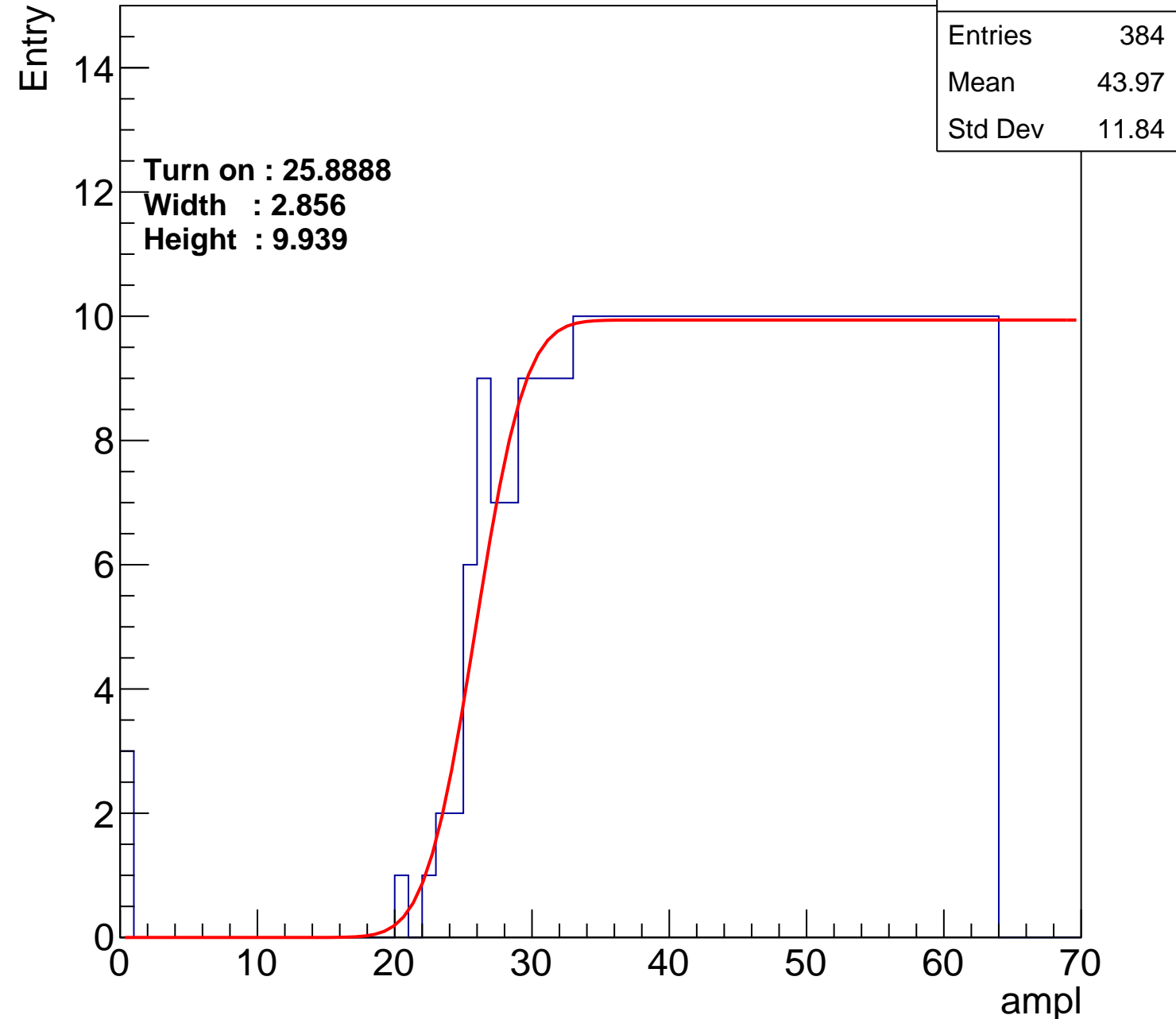
Width : 2.856

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.43
Std Dev	11.91

Turn on : 27.6300

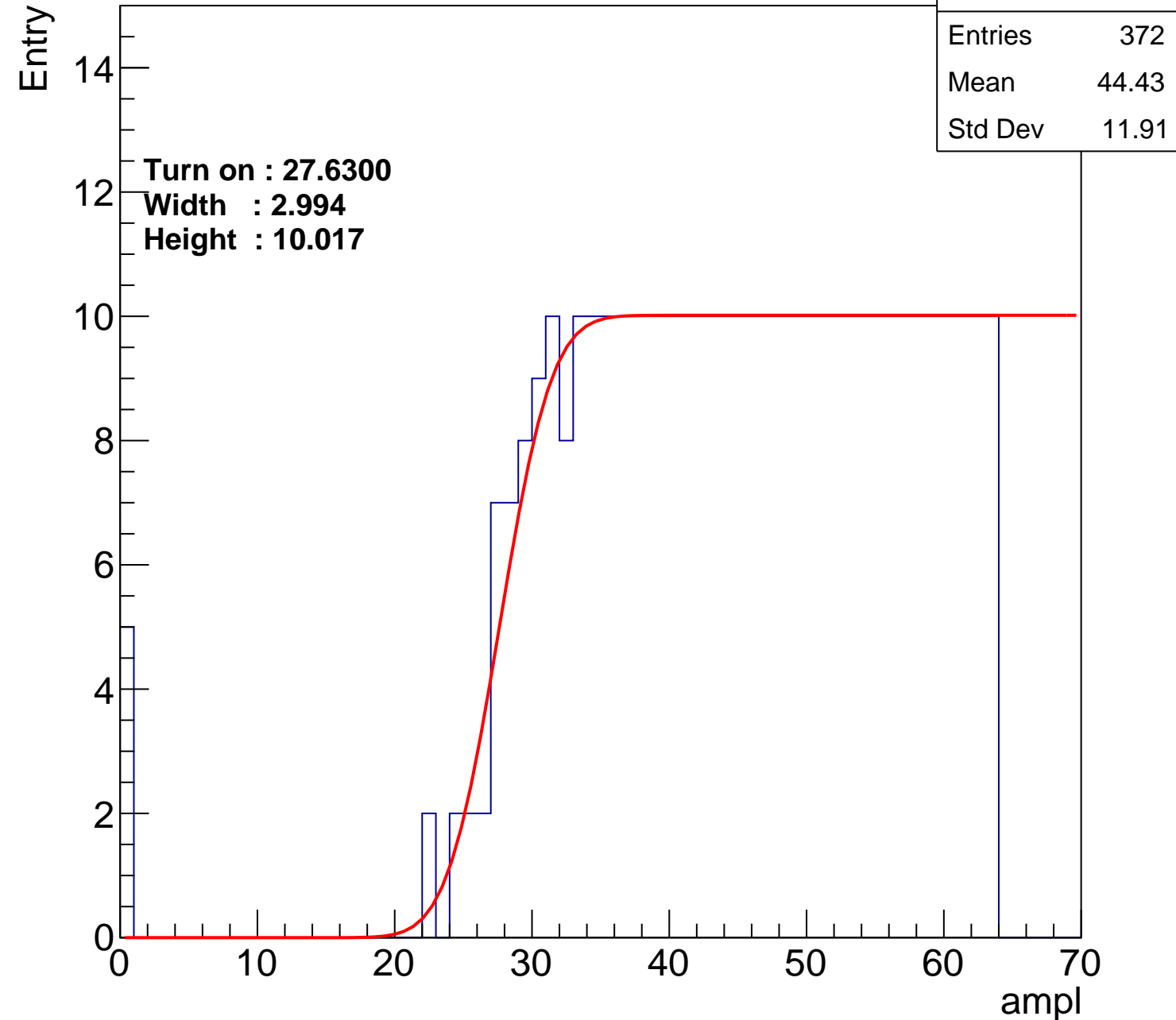
Width : 2.994

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch76

calib_packv5_042523_0143.root, FC#8, port C1

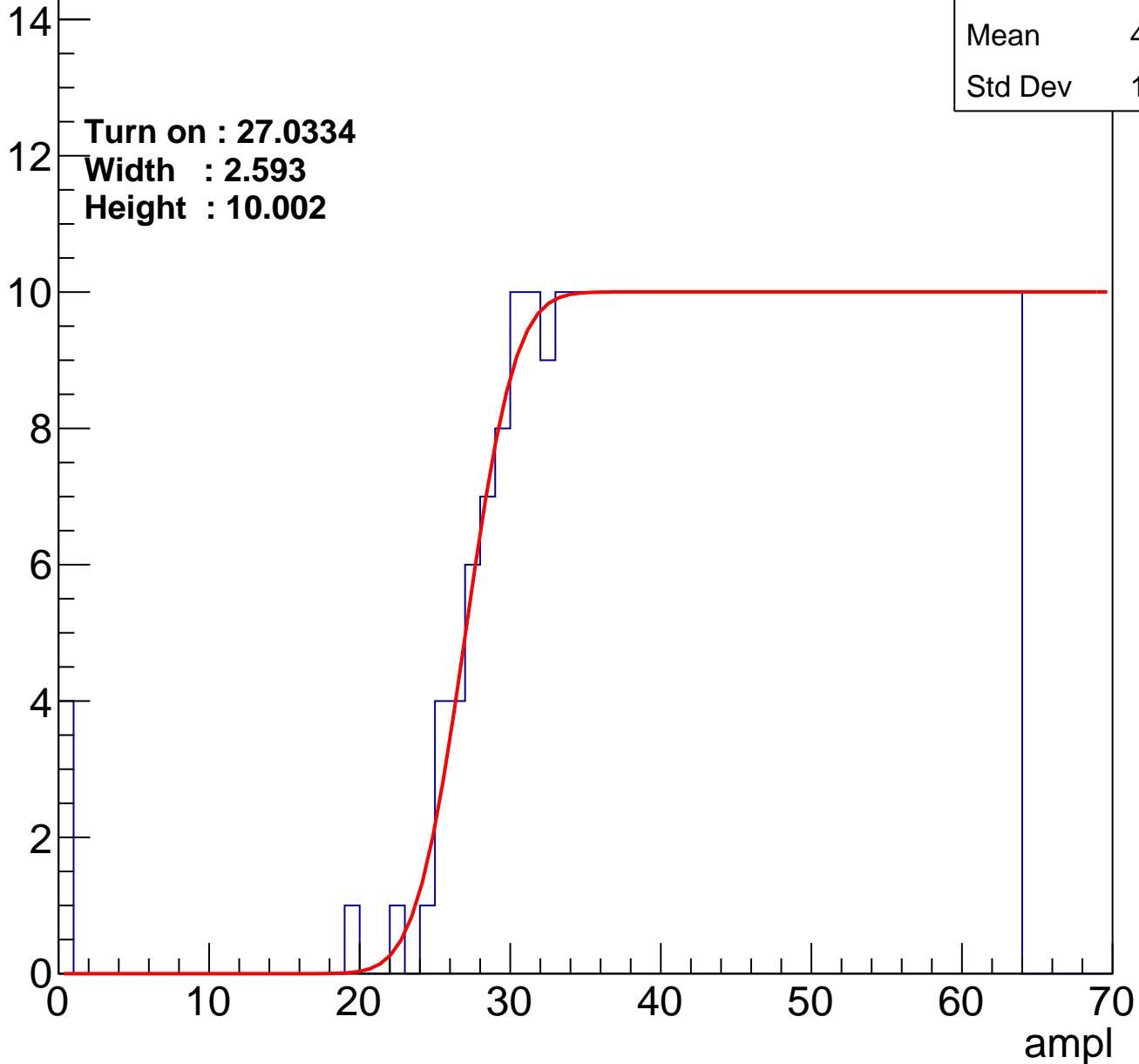
Entries	375
Mean	44.37
Std Dev	11.77

Turn on : 27.0334

Width : 2.593

Height : 10.002

Entry



B0L002S, U16-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.97
Std Dev	11.13

Turn on : 27.6444

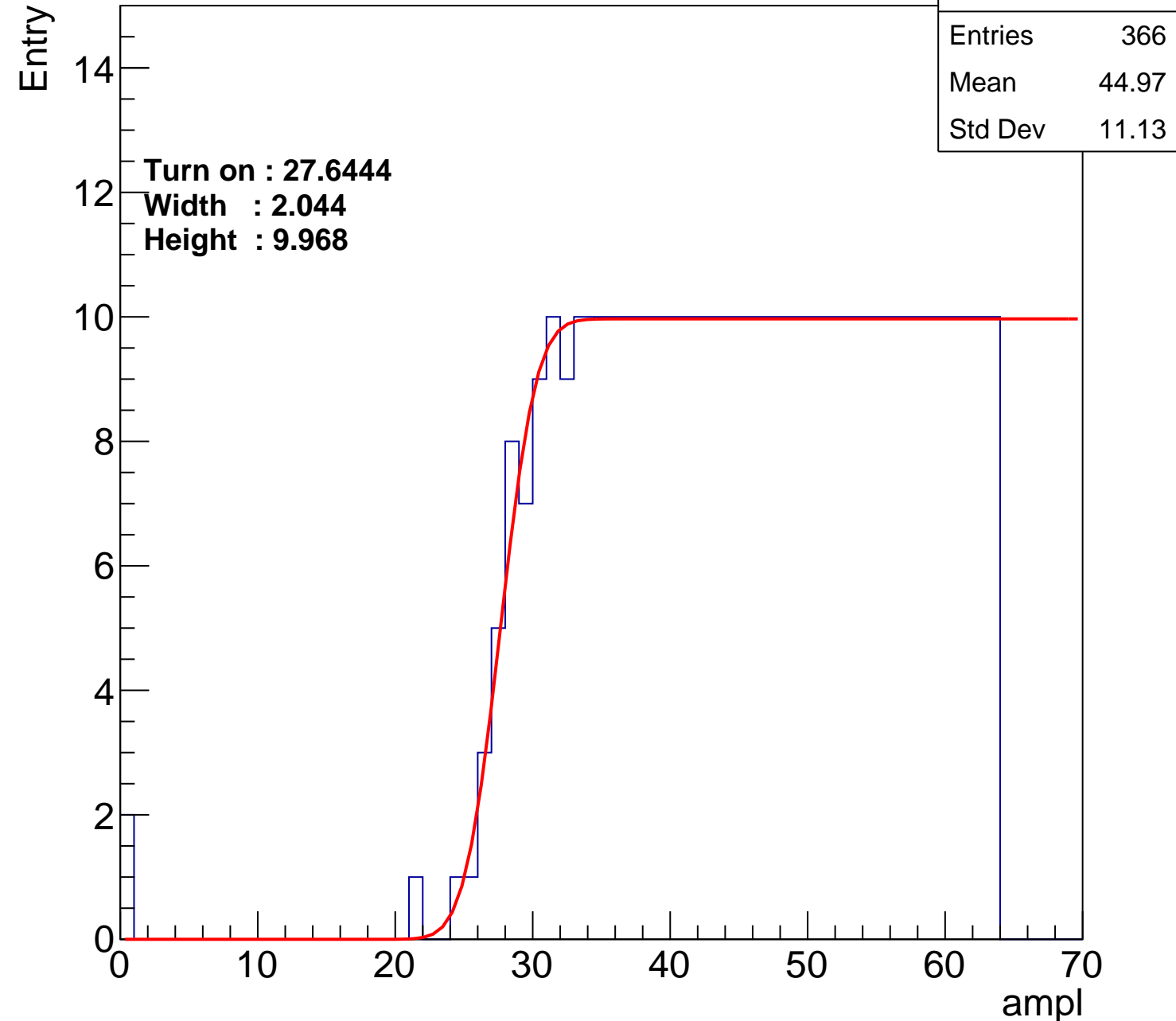
Width : 2.044

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch78

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.24
Std Dev	11.07

Turn on : 28.3580

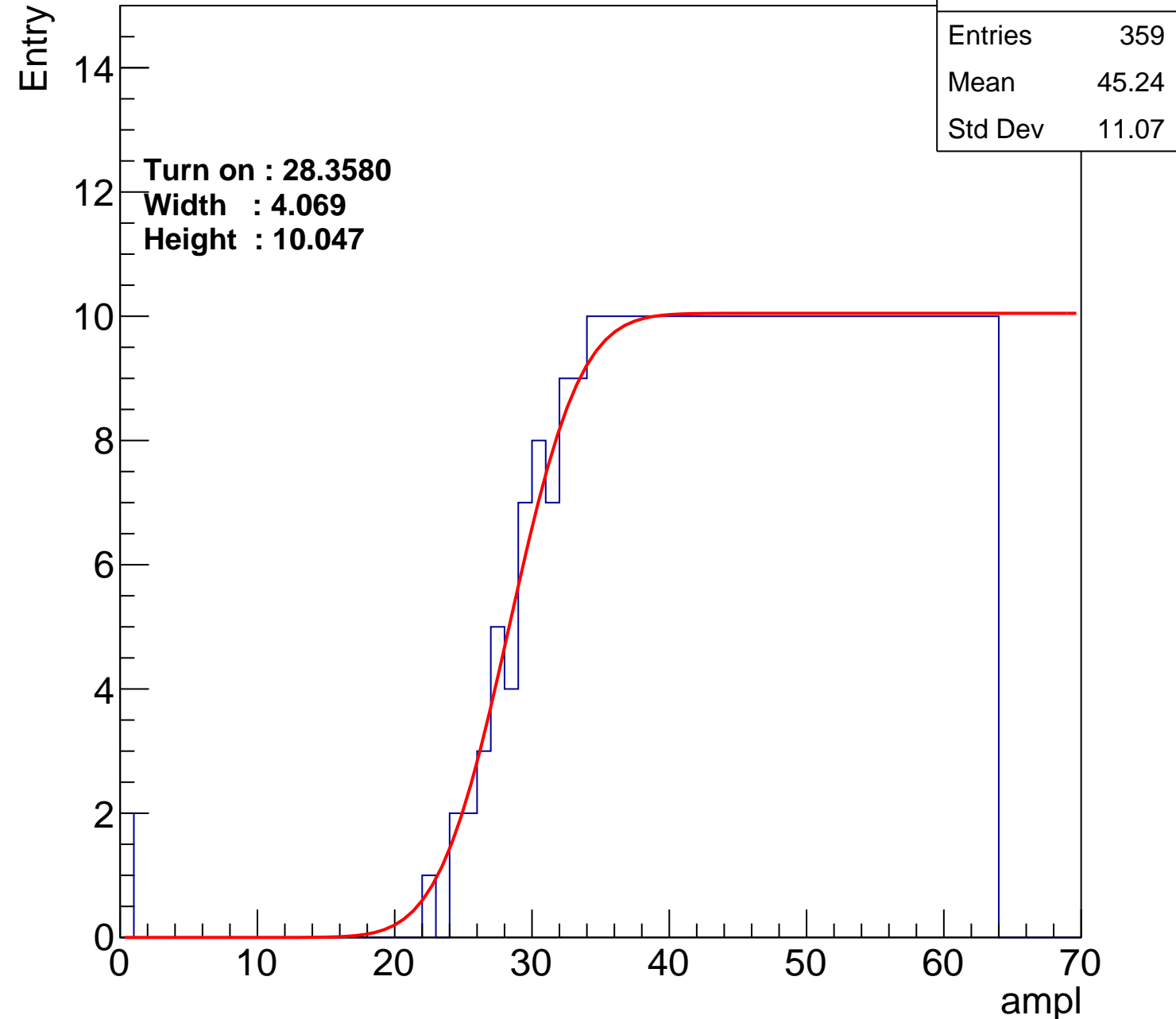
Width : 4.069

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch79

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.03
Std Dev	11.13

Turn on : 27.7631

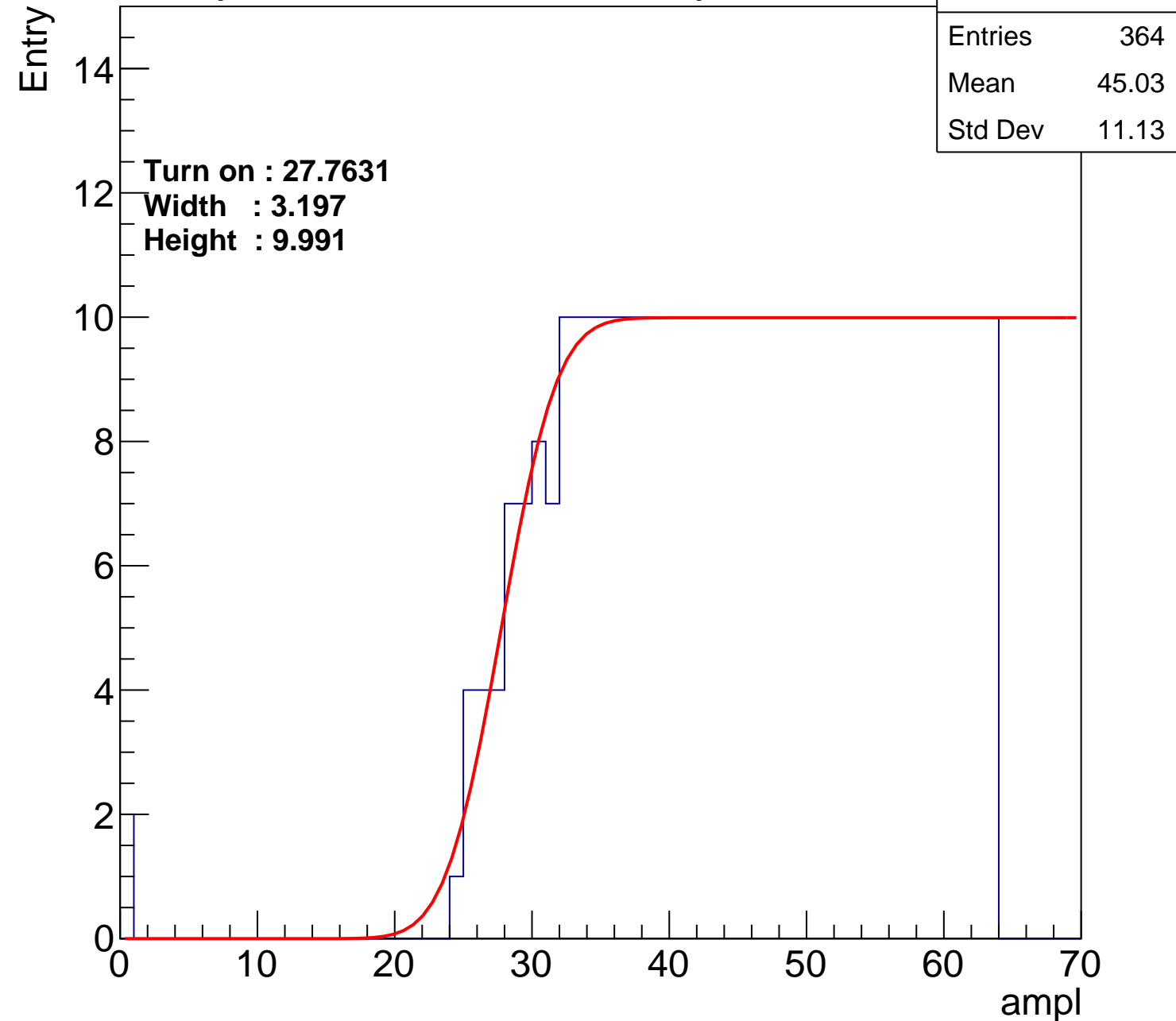
Width : 3.197

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.27
Std Dev	11.63

Turn on : 26.6155

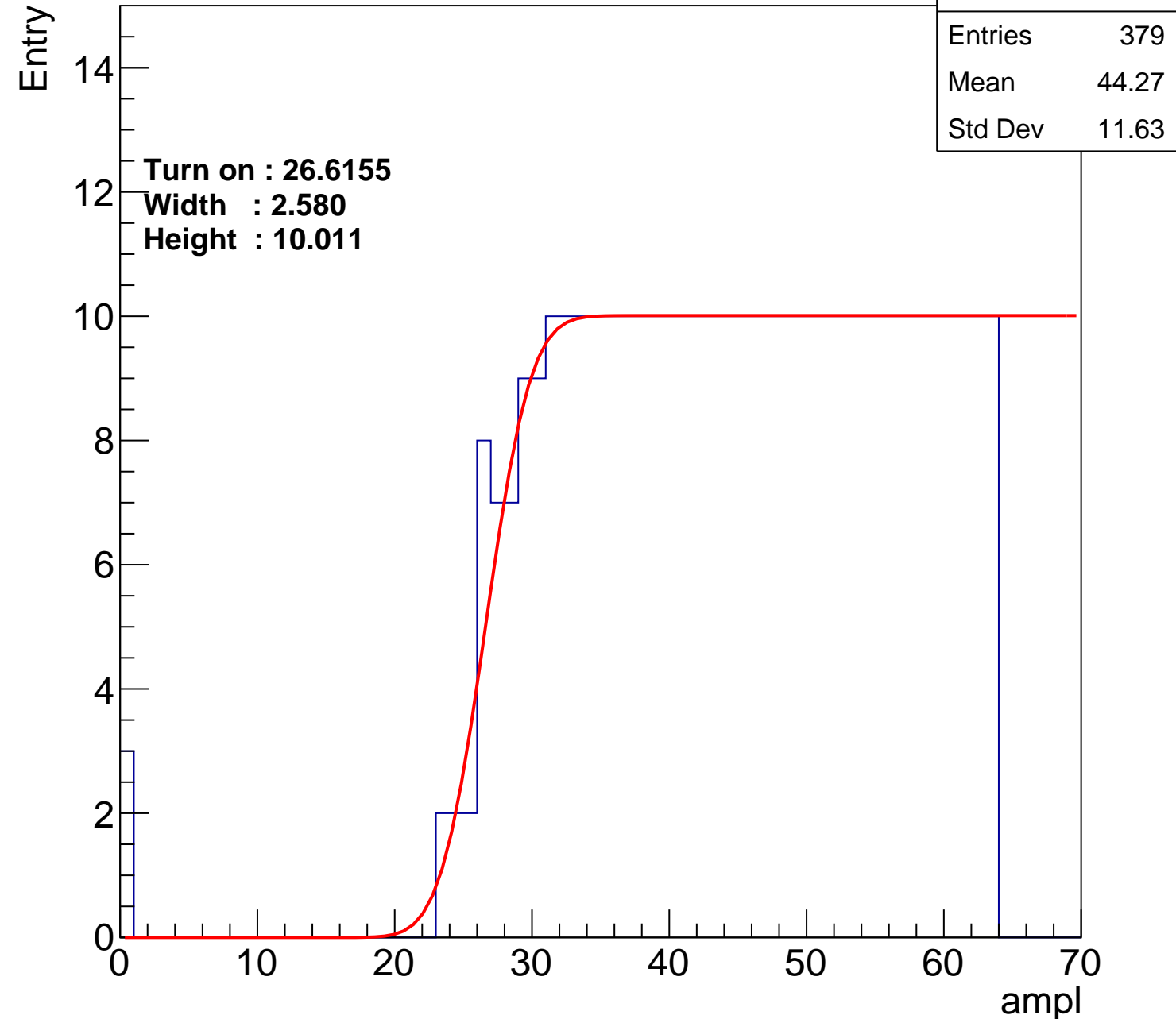
Width : 2.580

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch81

calib_packv5_042523_0143.root, FC#8, port C1

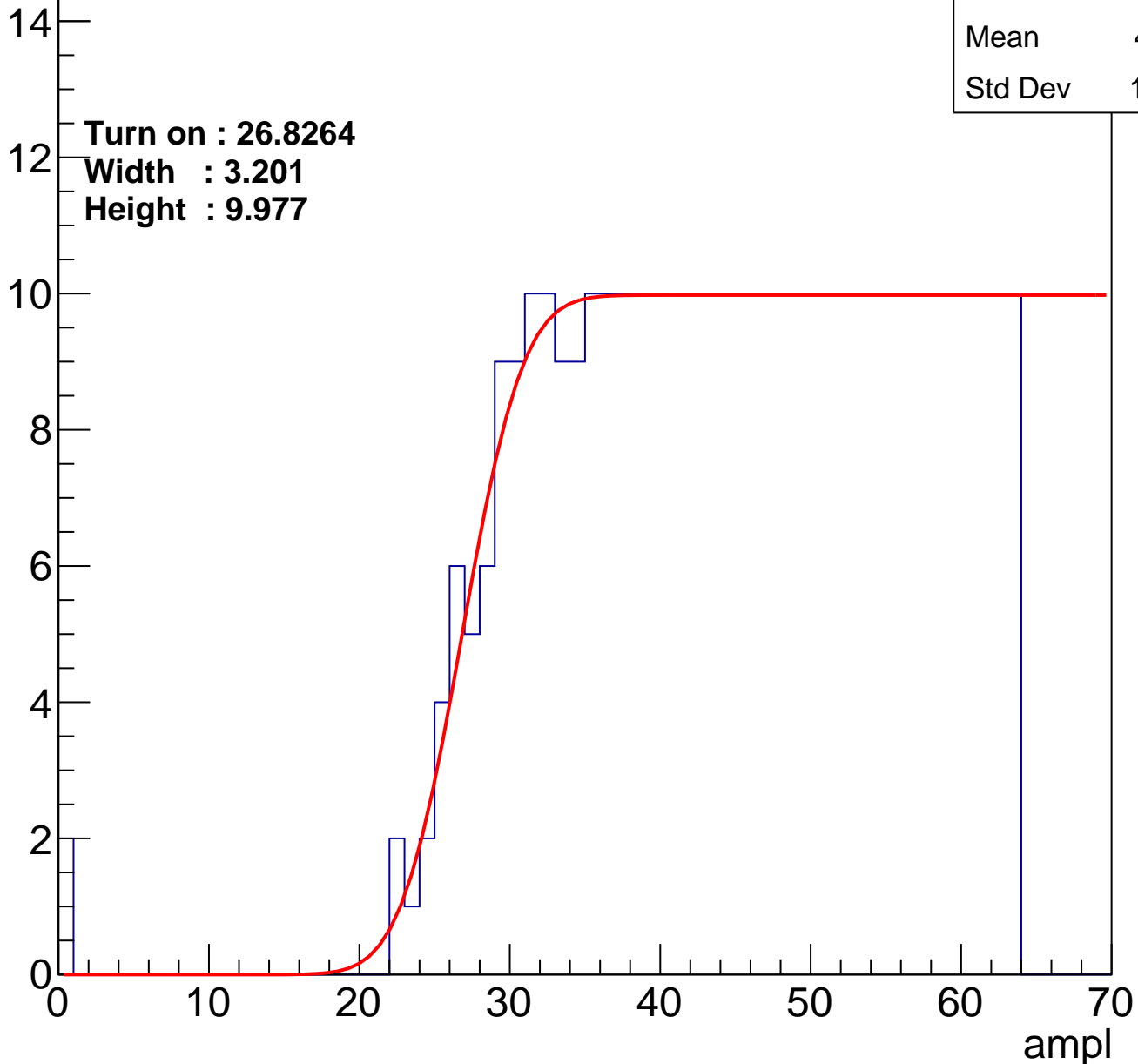
Entries	374
Mean	44.51
Std Dev	11.42

Turn on : 26.8264

Width : 3.201

Height : 9.977

Entry



B0L002S, U16-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	43.97
Std Dev	11.95

Turn on : 25.7560

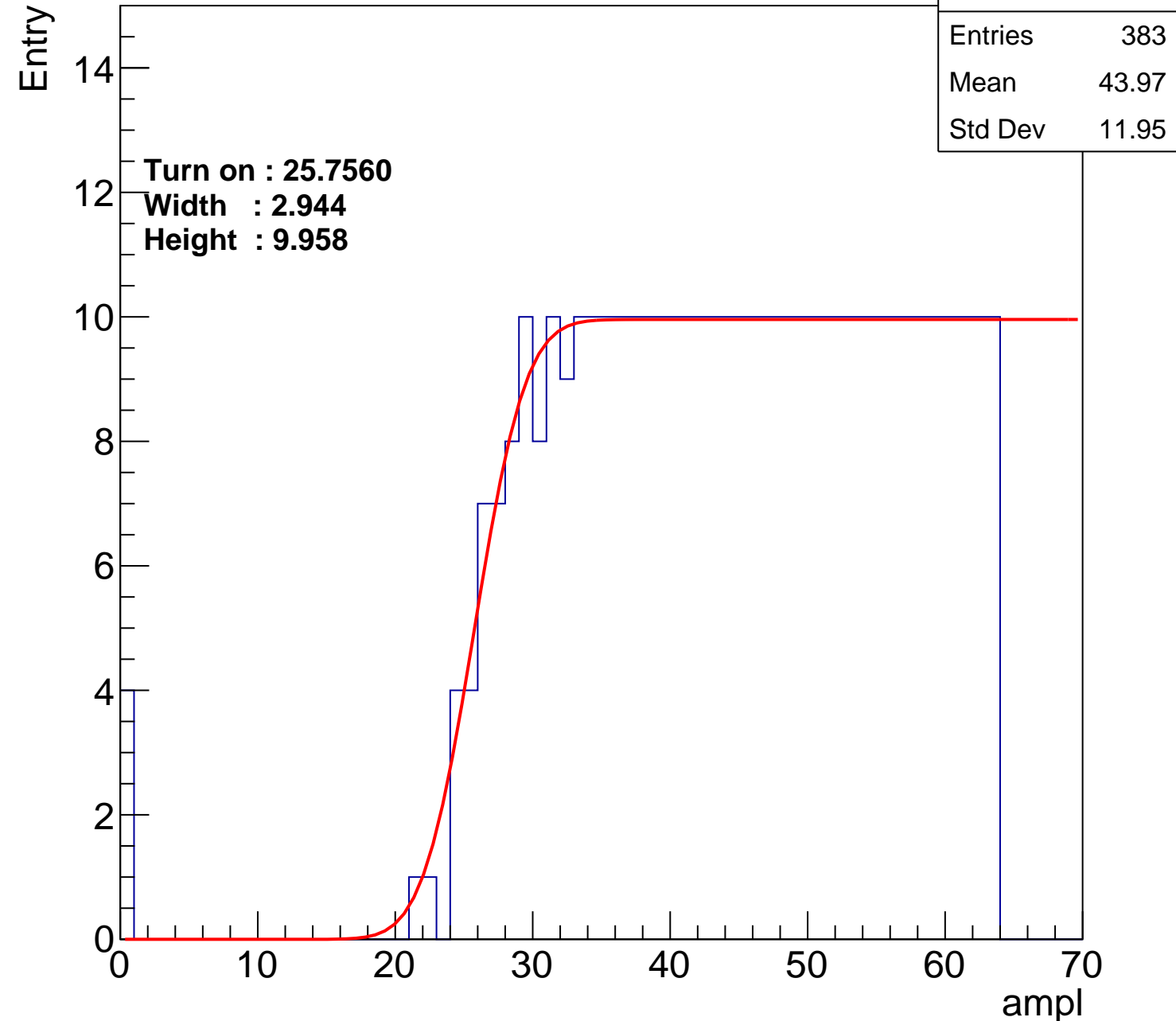
Width : 2.944

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch83

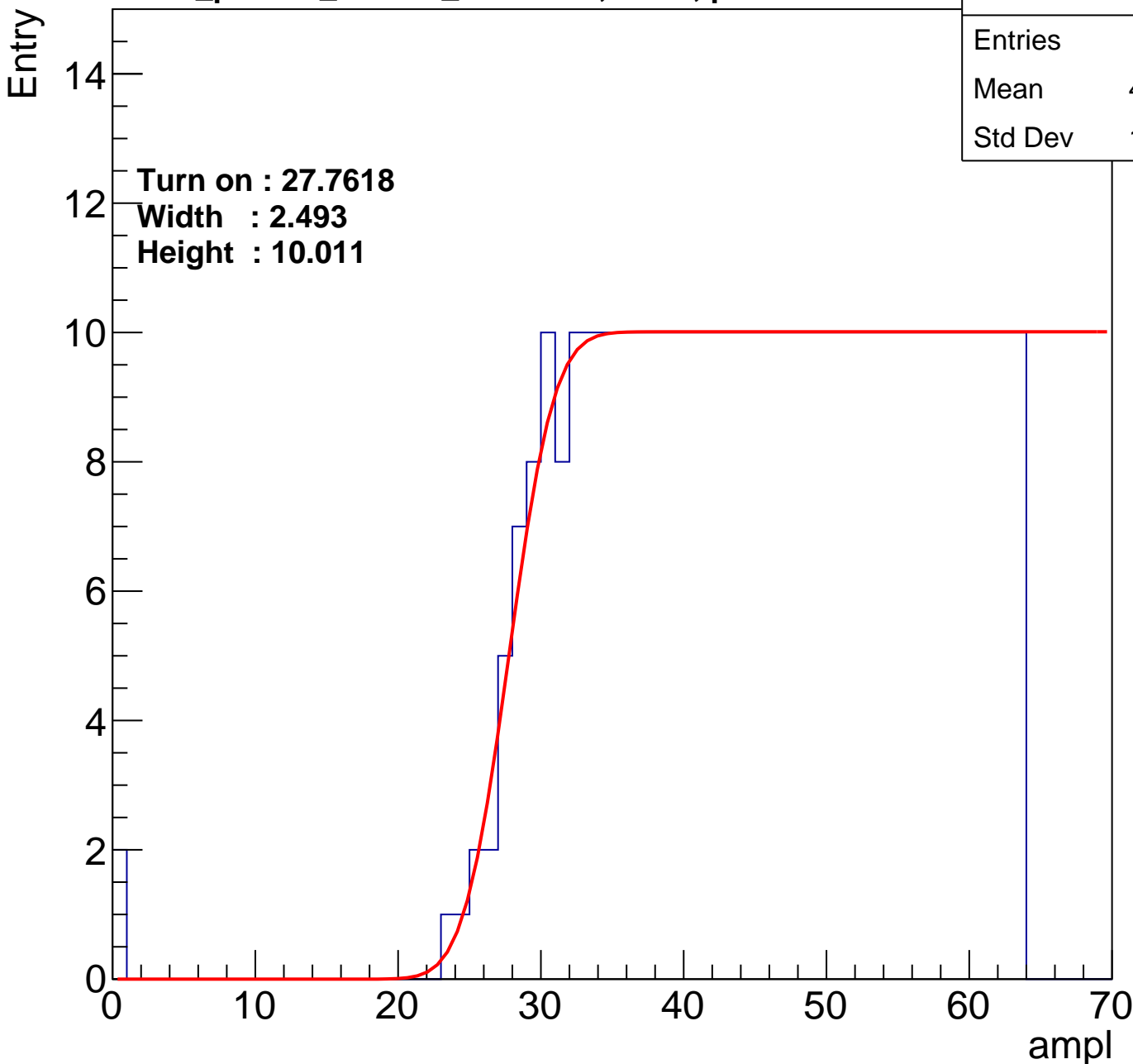
calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.97
Std Dev	11.12

Turn on : 27.7618

Width : 2.493

Height : 10.011



B0L002S, U16-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.33
Std Dev	11.02

Turn on : 27.9412

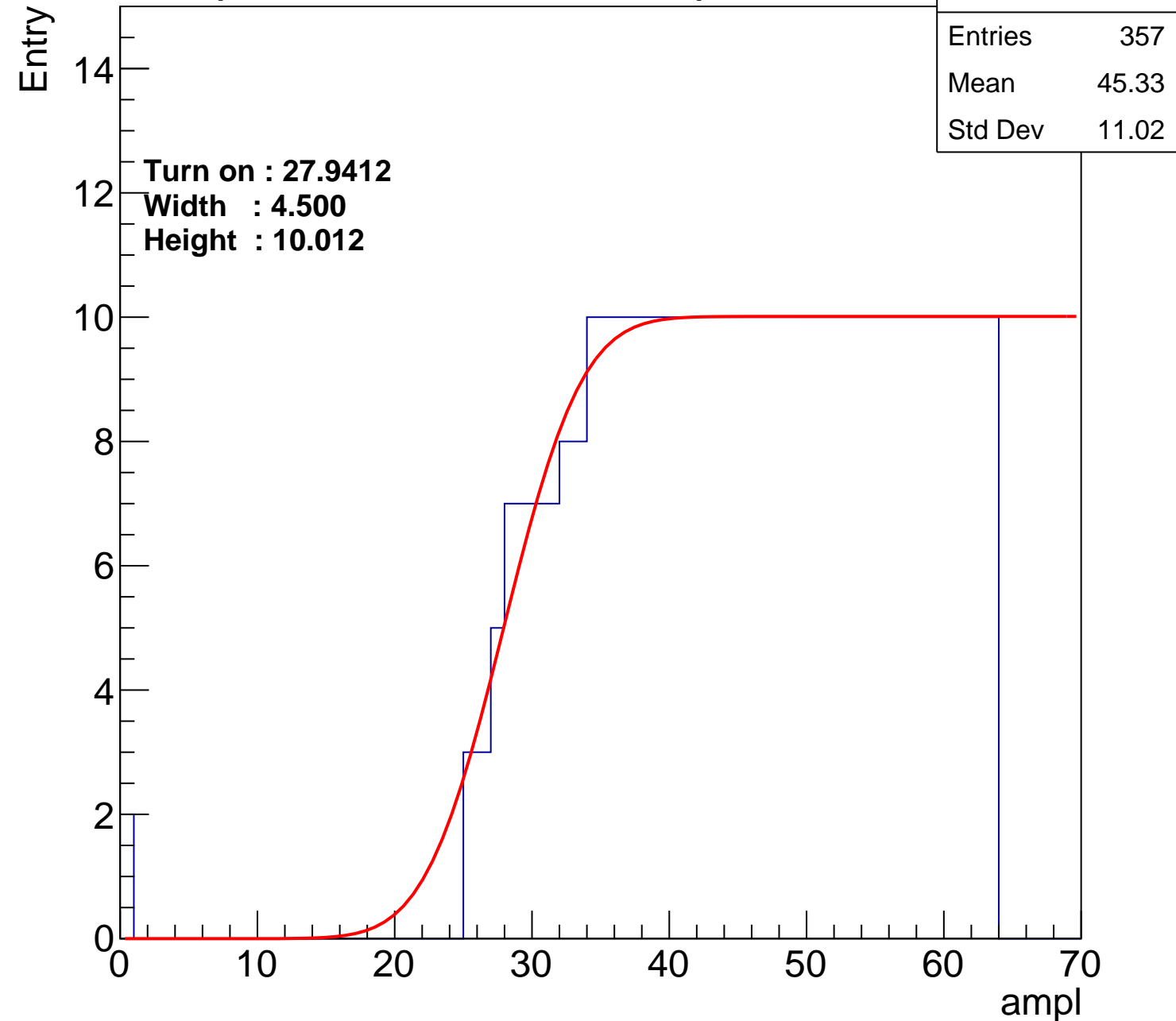
Width : 4.500

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.26
Std Dev	11.65

Turn on : 26.6505

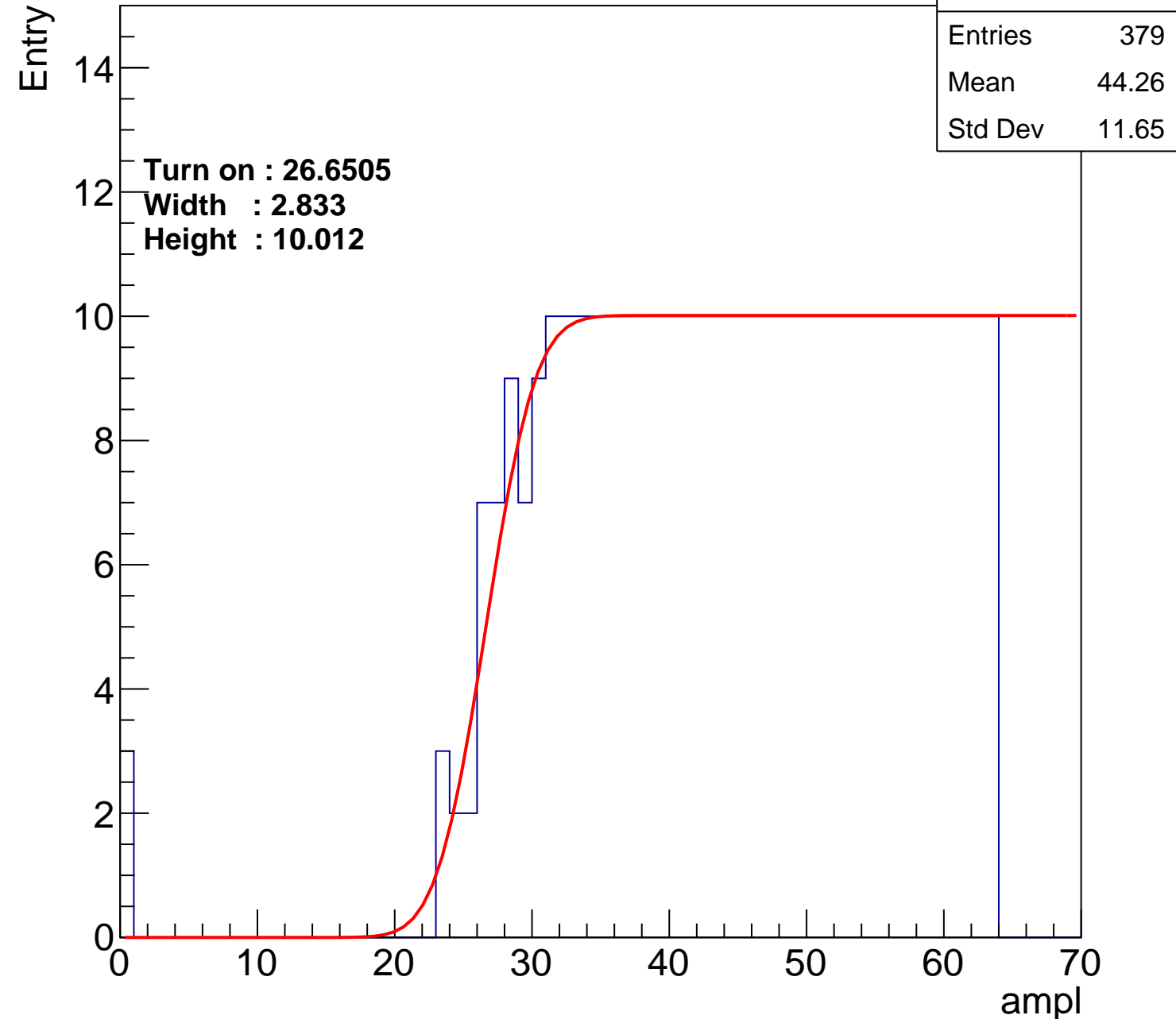
Width : 2.833

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.66
Std Dev	11.67

Turn on : 27.5683

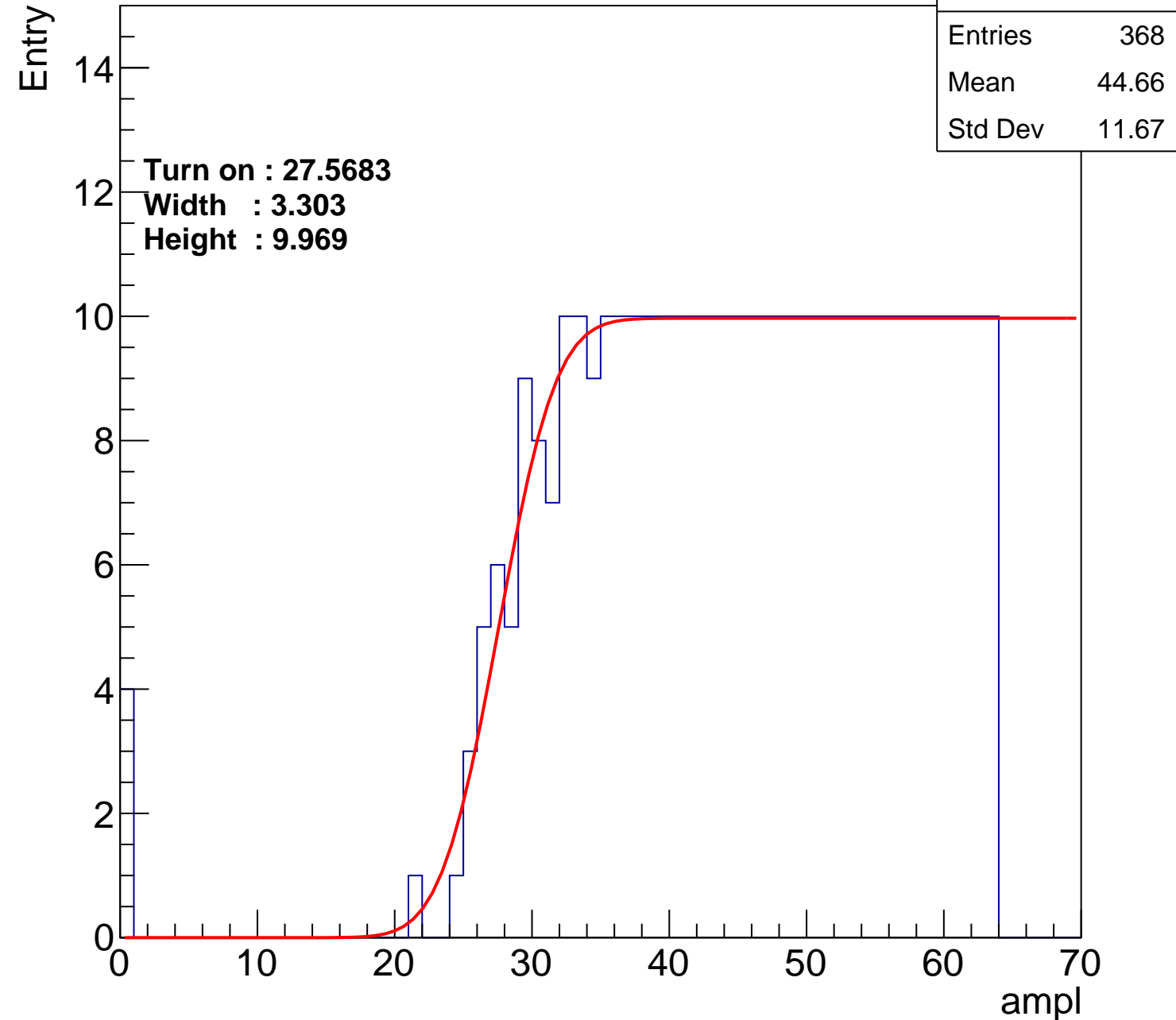
Width : 3.303

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.18
Std Dev	11.6

Turn on : 26.3978

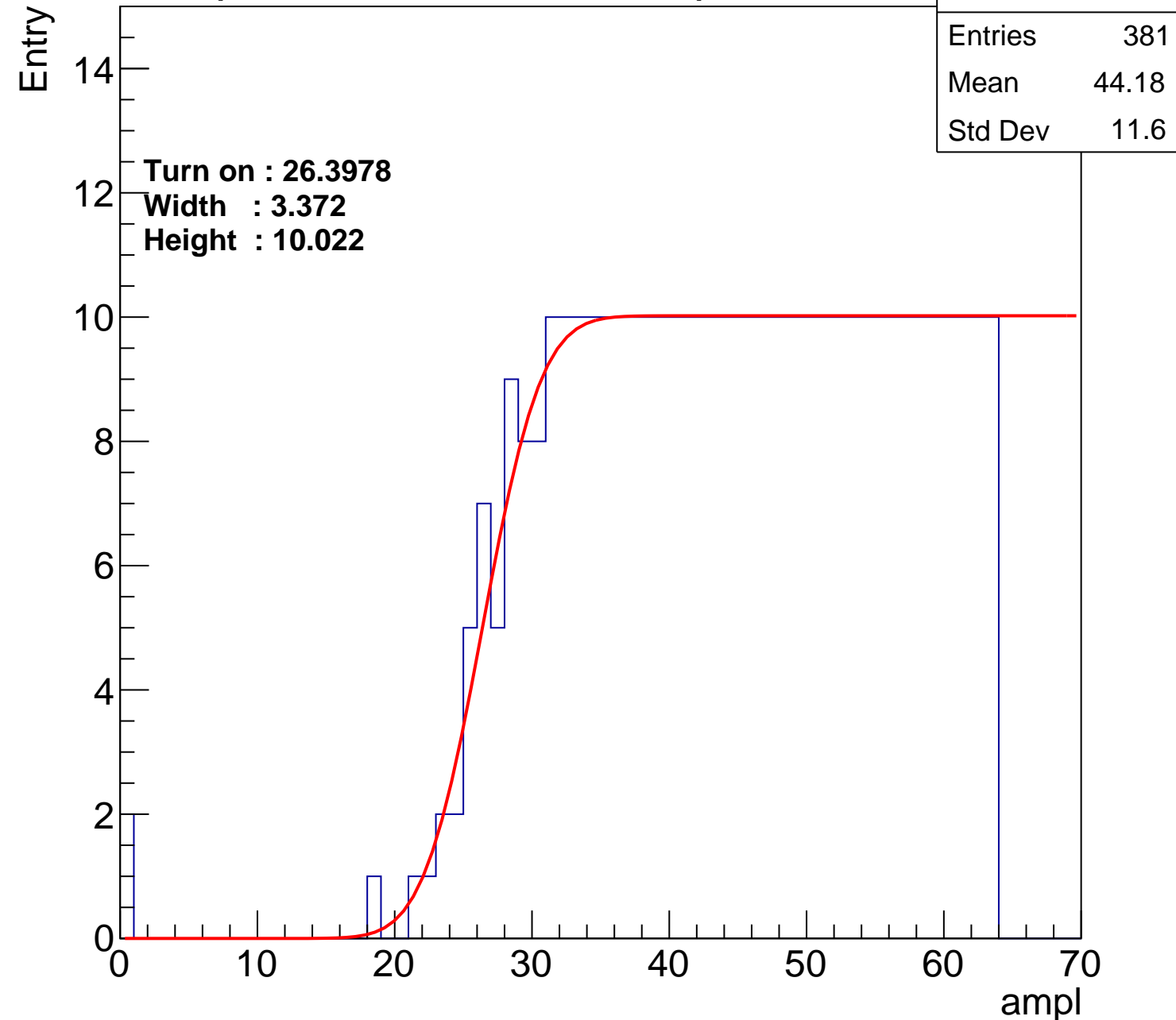
Width : 3.372

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch88

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.01
Std Dev	11.12

Turn on : 27.5568

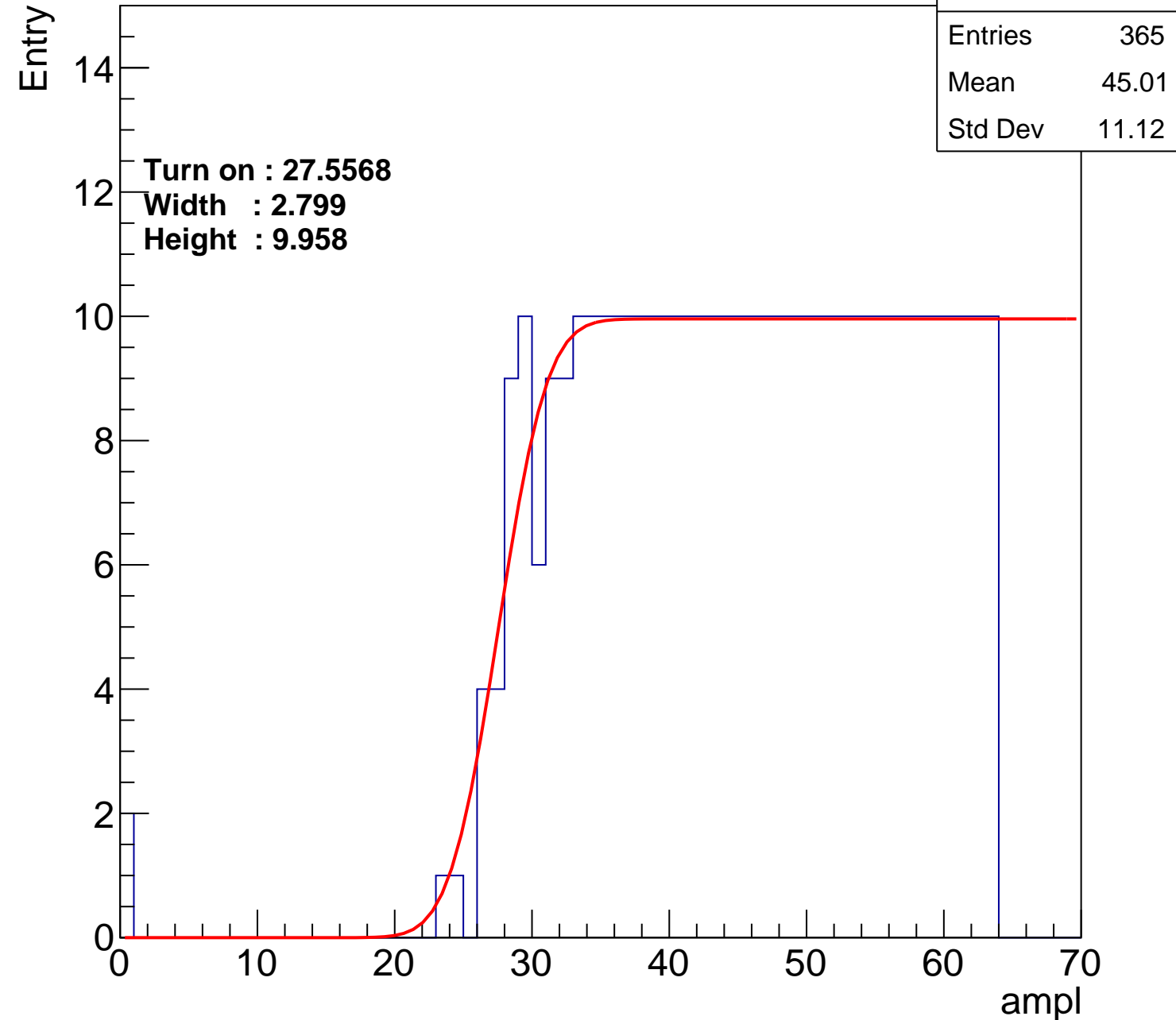
Width : 2.799

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.79
Std Dev	10.95

Turn on : 29.9627

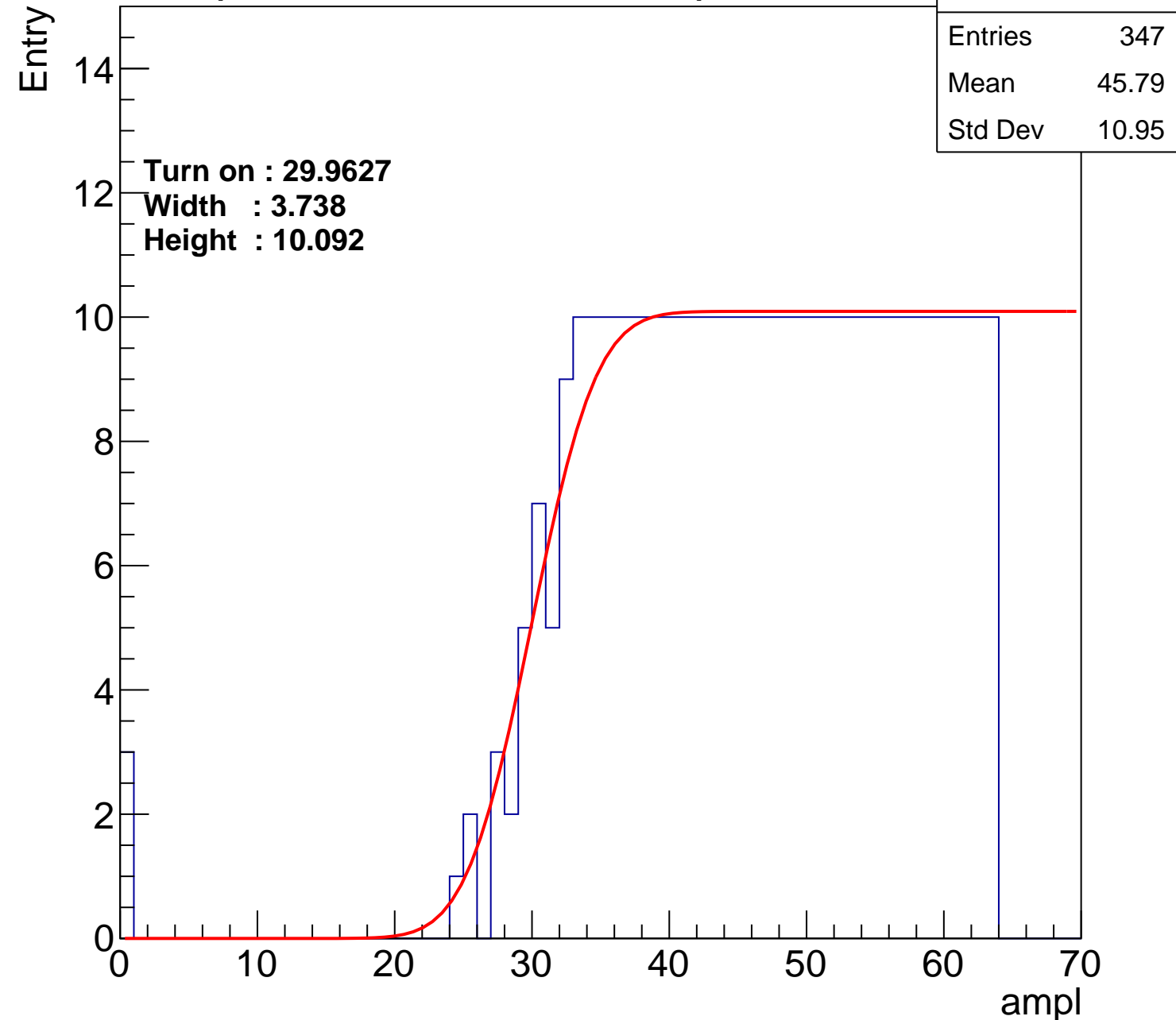
Width : 3.738

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch90

calib_packv5_042523_0143.root, FC#8, port C1

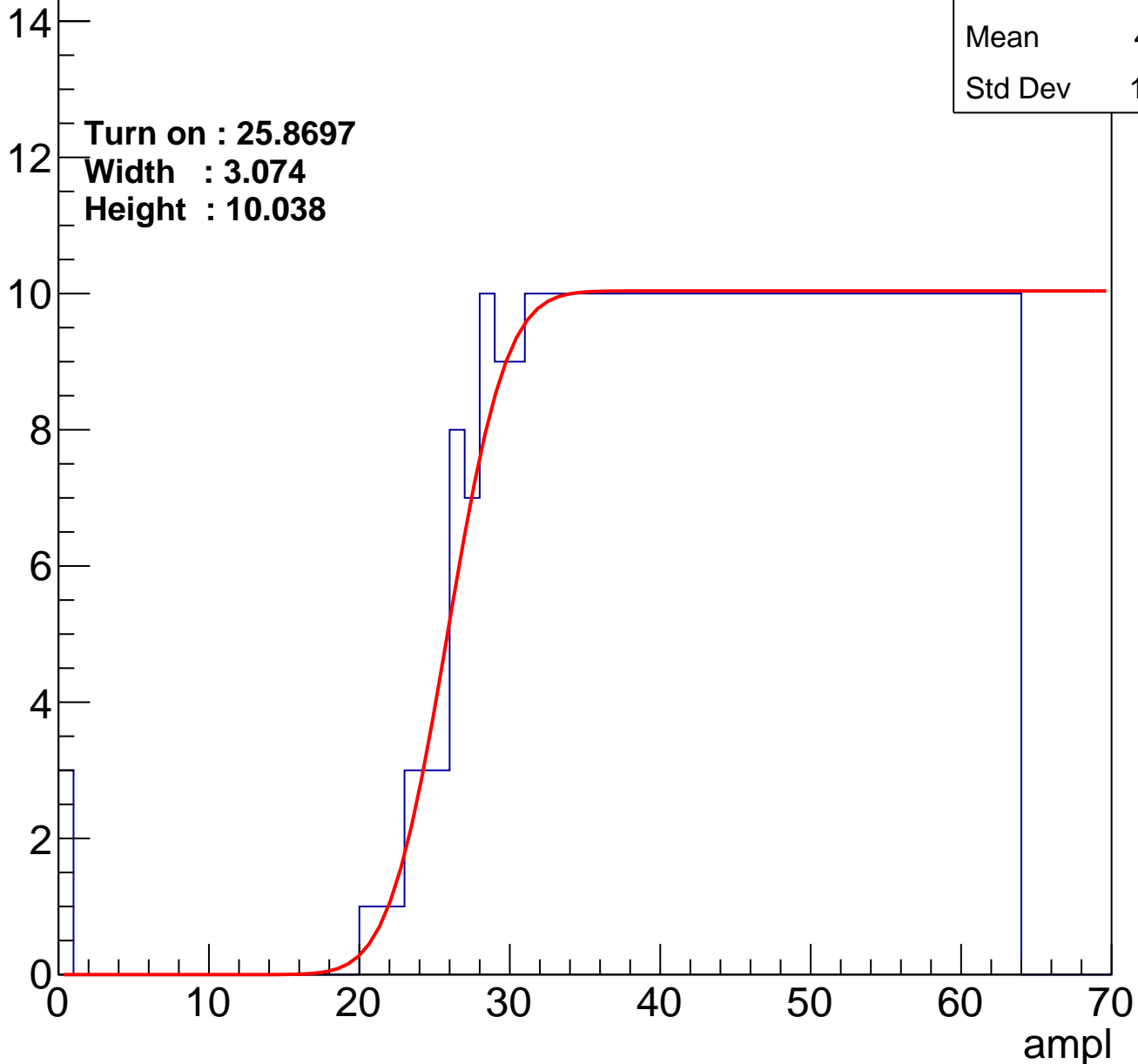
Entries	388
Mean	43.81
Std Dev	11.89

Turn on : 25.8697

Width : 3.074

Height : 10.038

Entry



B0L002S, U16-ch91

calib_packv5_042523_0143.root, FC#8, port C1

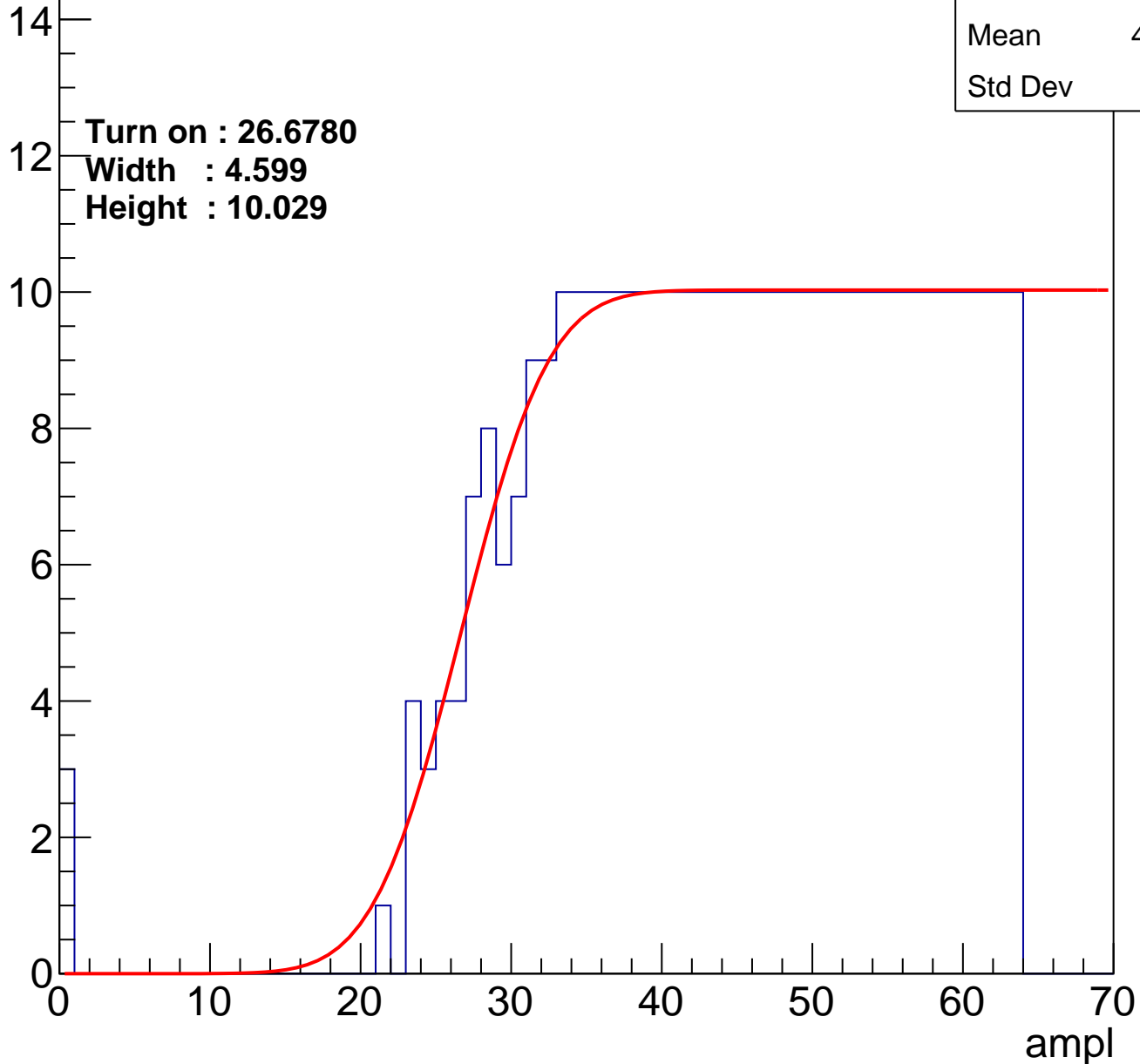
Entries	375
Mean	44.35
Std Dev	11.7

Turn on : 26.6780

Width : 4.599

Height : 10.029

Entry



B0L002S, U16-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.14
Std Dev	11.21

Turn on : 28.4807

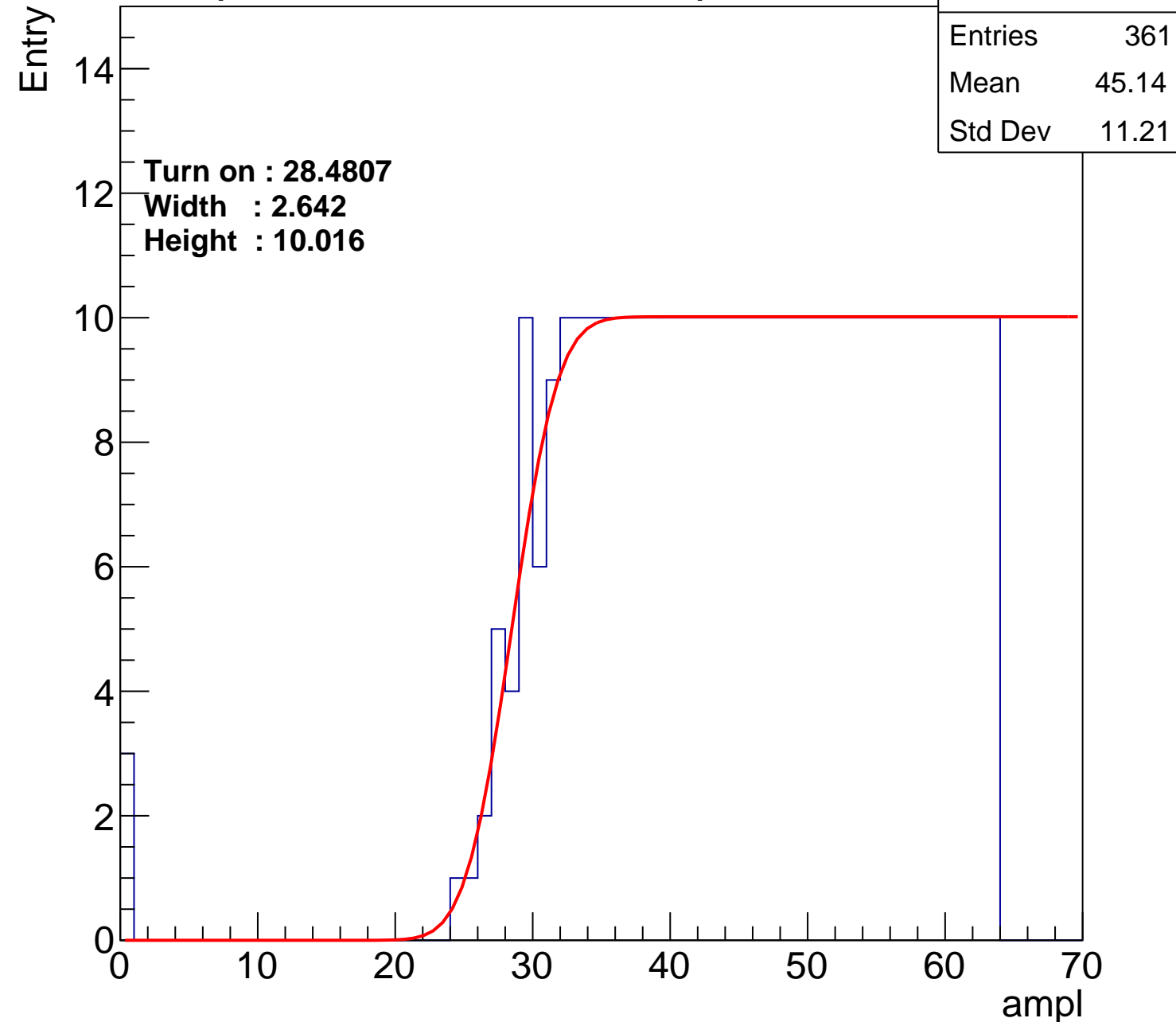
Width : 2.642

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	44.9
Std Dev	11.71

Turn on : 28.8645

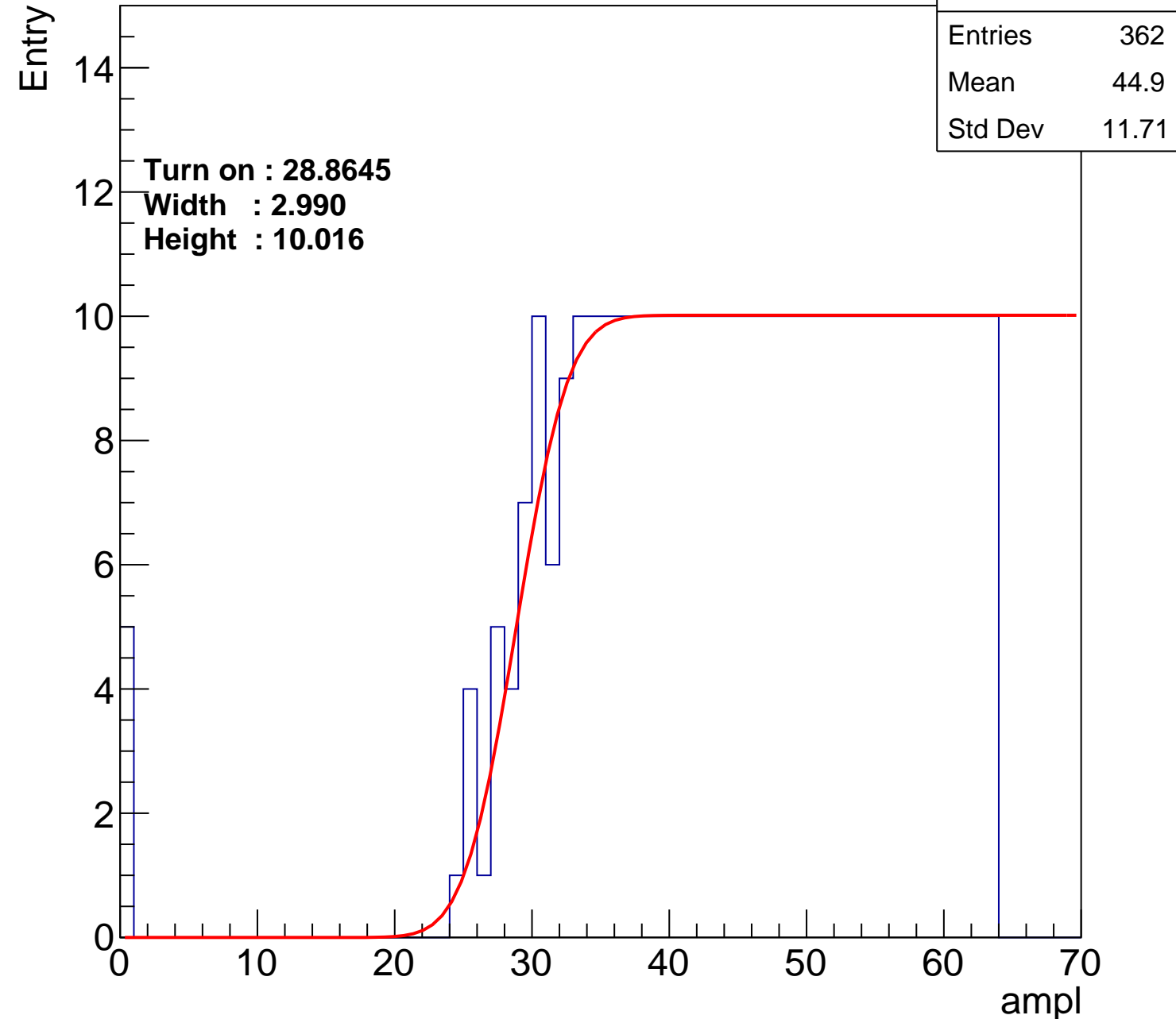
Width : 2.990

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.58
Std Dev	11.2

Turn on : 27.1099

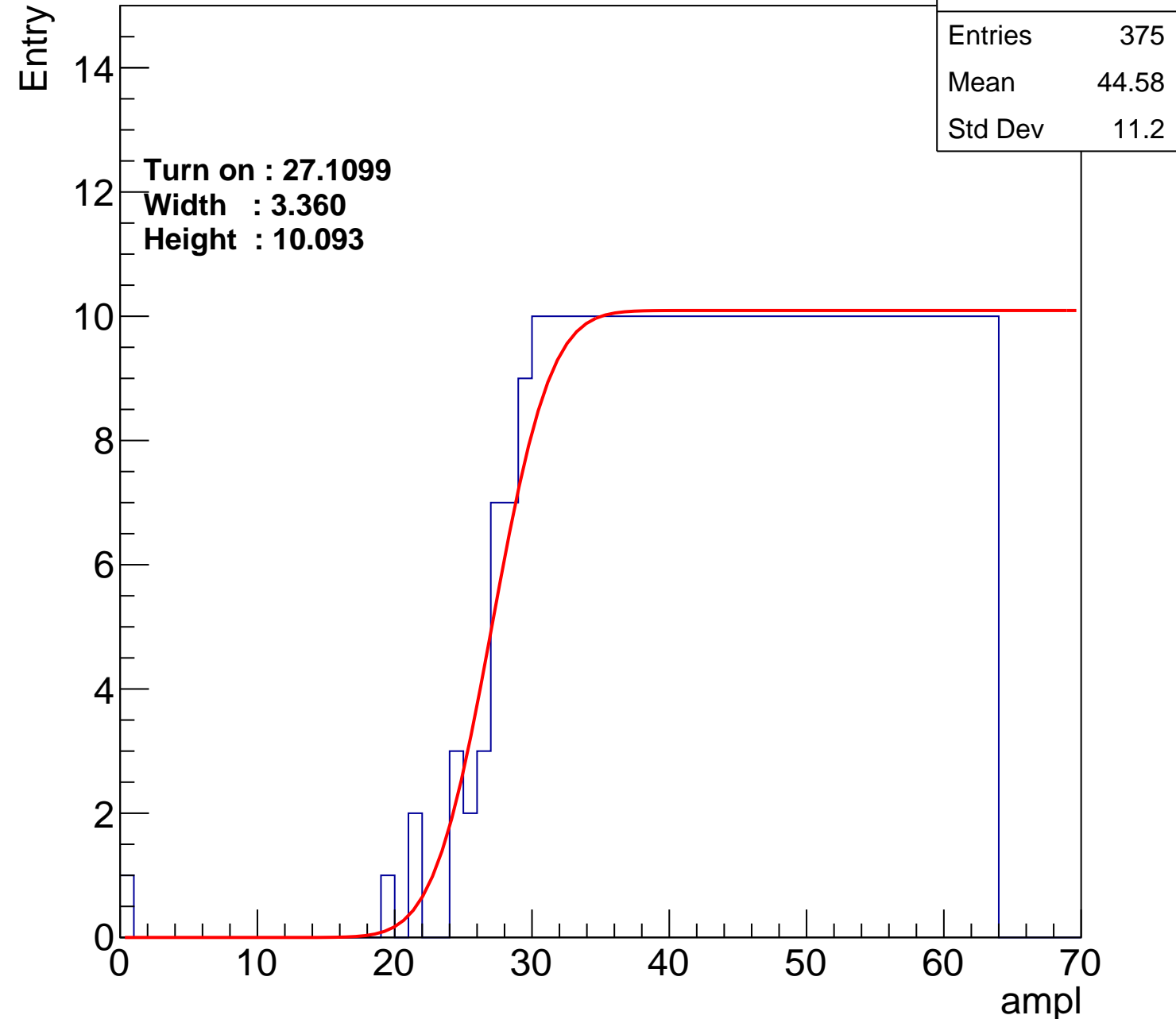
Width : 3.360

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch95

calib_packv5_042523_0143.root, FC#8, port C1

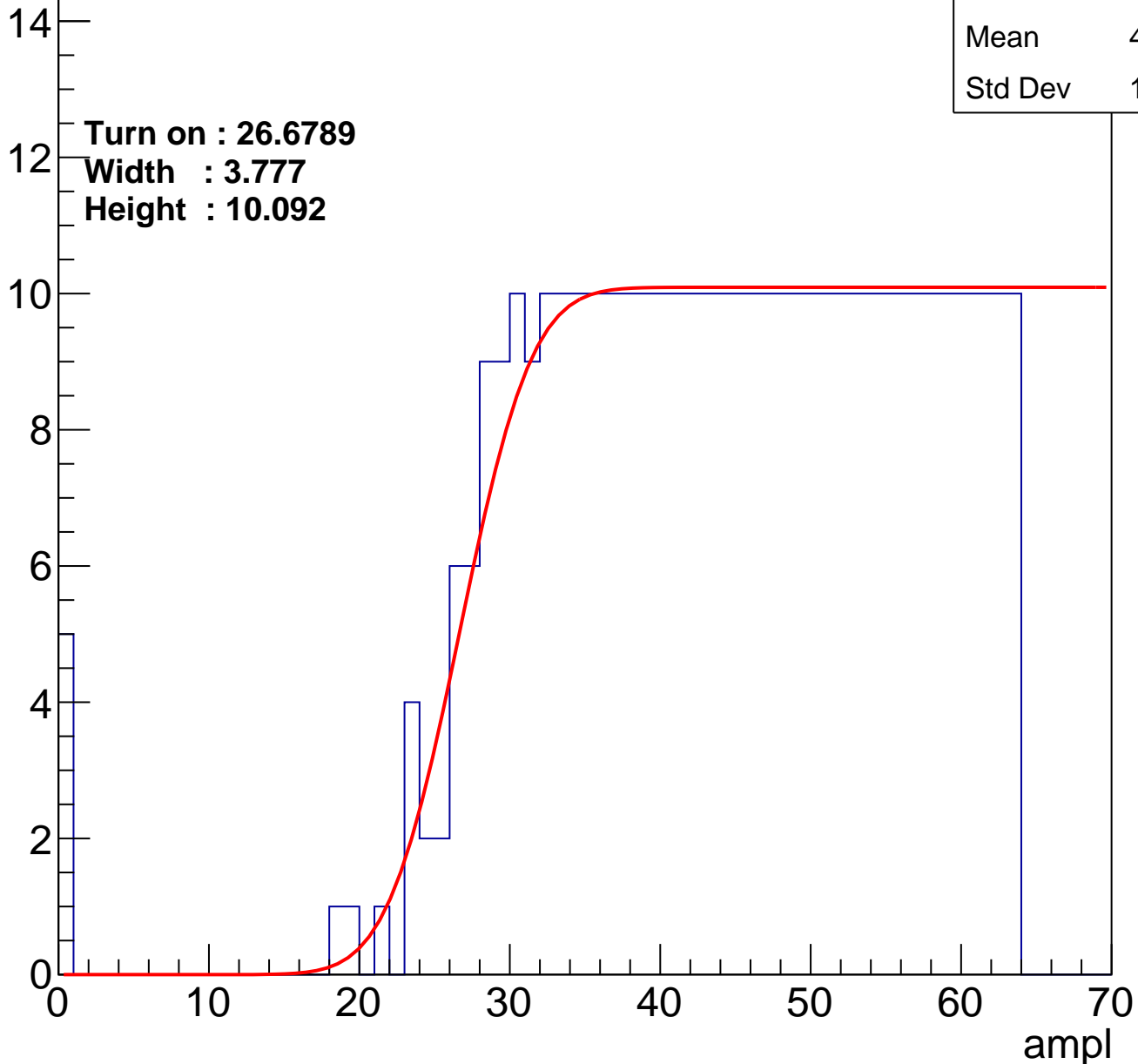
Entries	385
Mean	43.79
Std Dev	12.22

Turn on : 26.6789

Width : 3.777

Height : 10.092

Entry



B0L002S, U16-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.07
Std Dev	11.27

Turn on : 28.6846

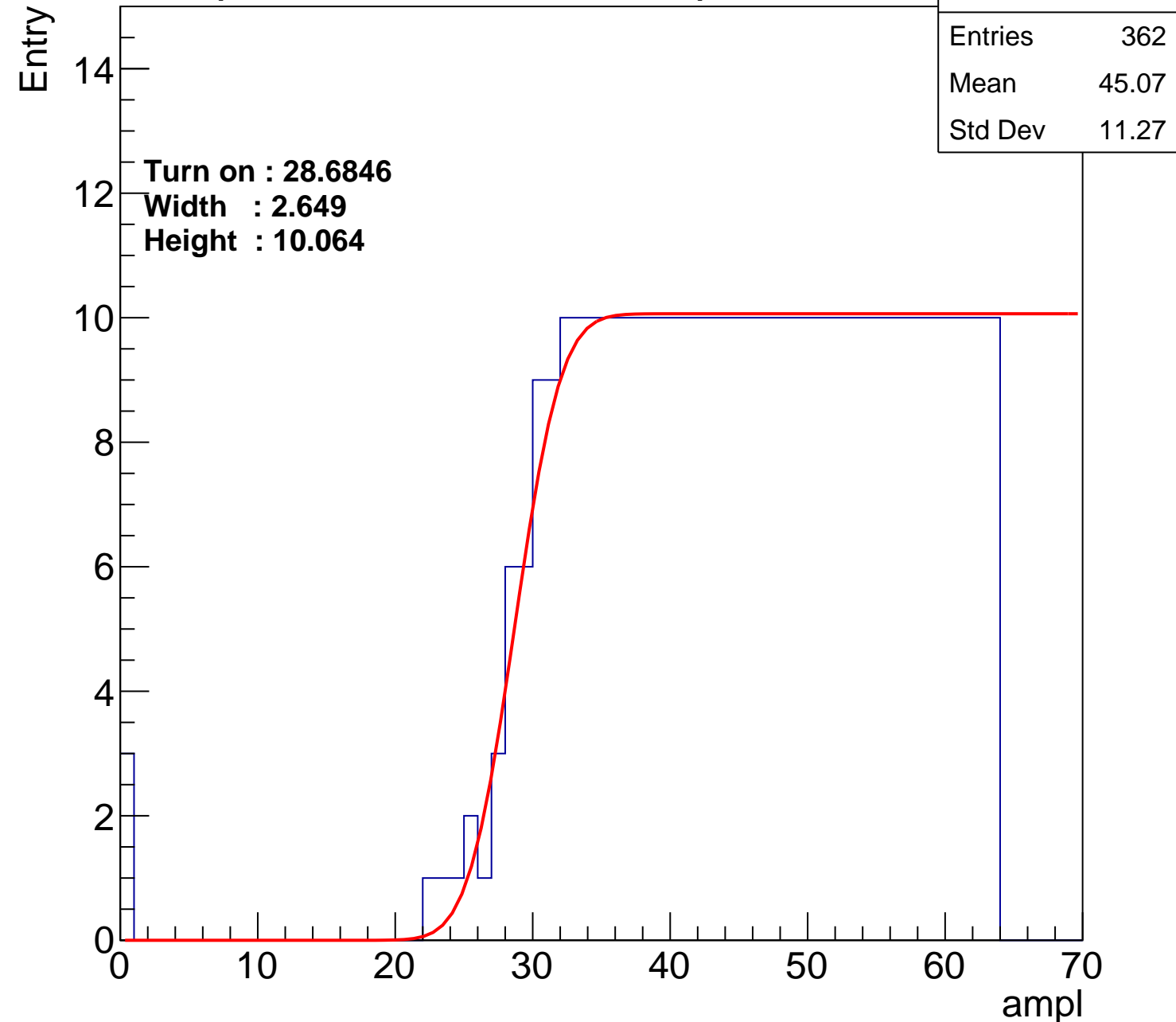
Width : 2.649

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.35
Std Dev	11

Turn on : 28.3165

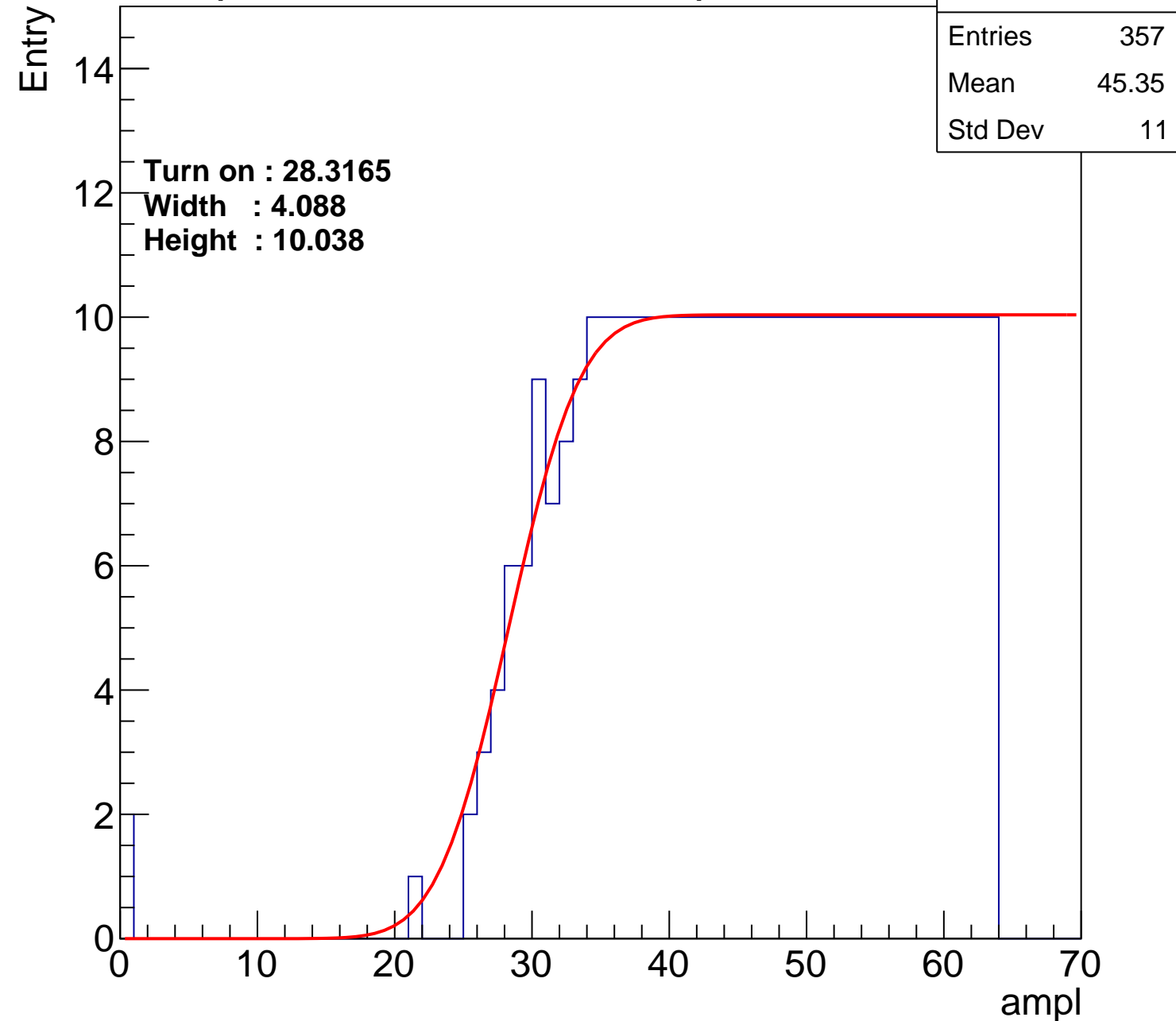
Width : 4.088

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch98

calib_packv5_042523_0143.root, FC#8, port C1

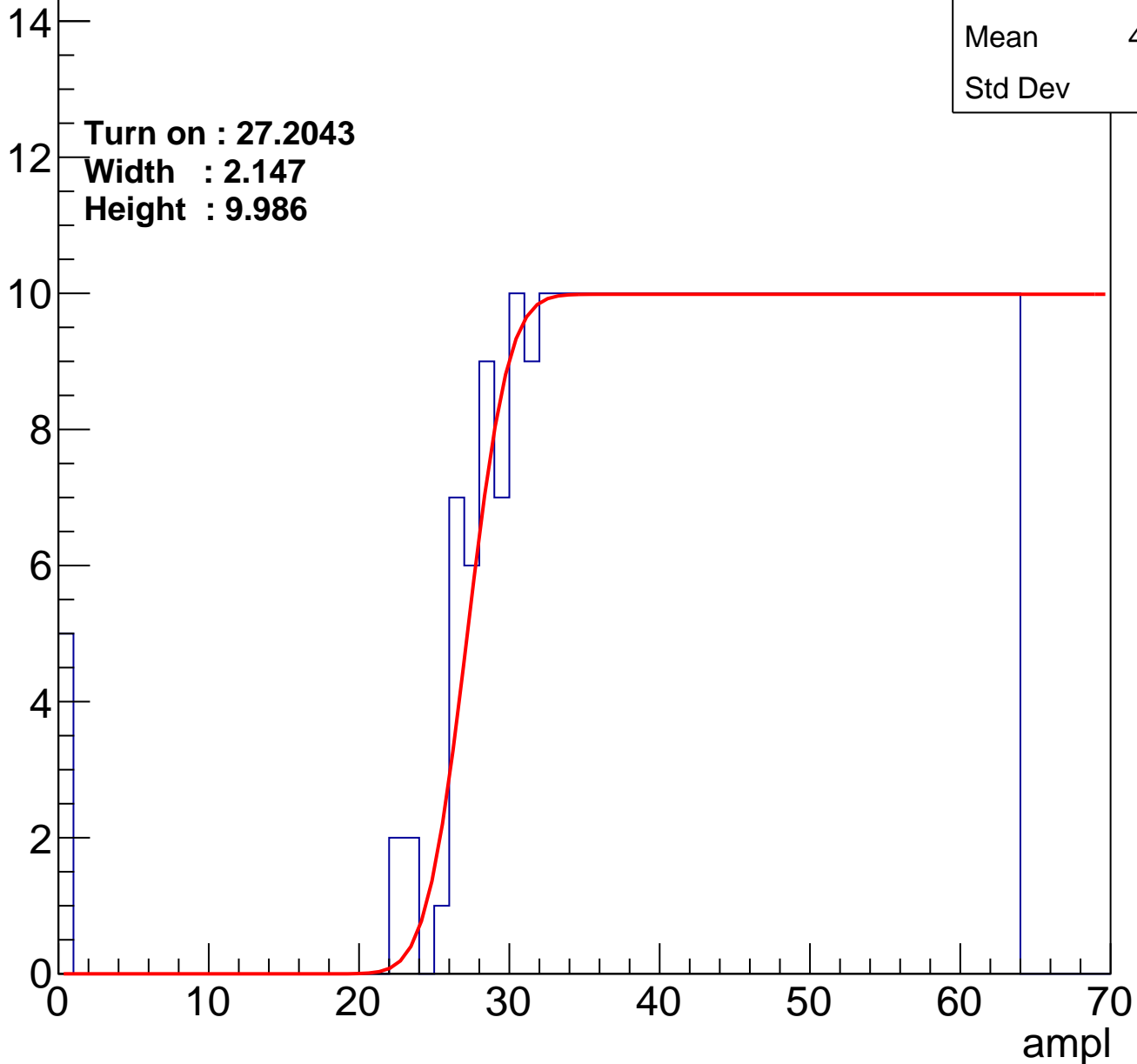
Entries	378
Mean	44.16
Std Dev	12

Turn on : 27.2043

Width : 2.147

Height : 9.986

Entry



B0L002S, U16-ch99

calib_packv5_042523_0143.root, FC#8, port C1

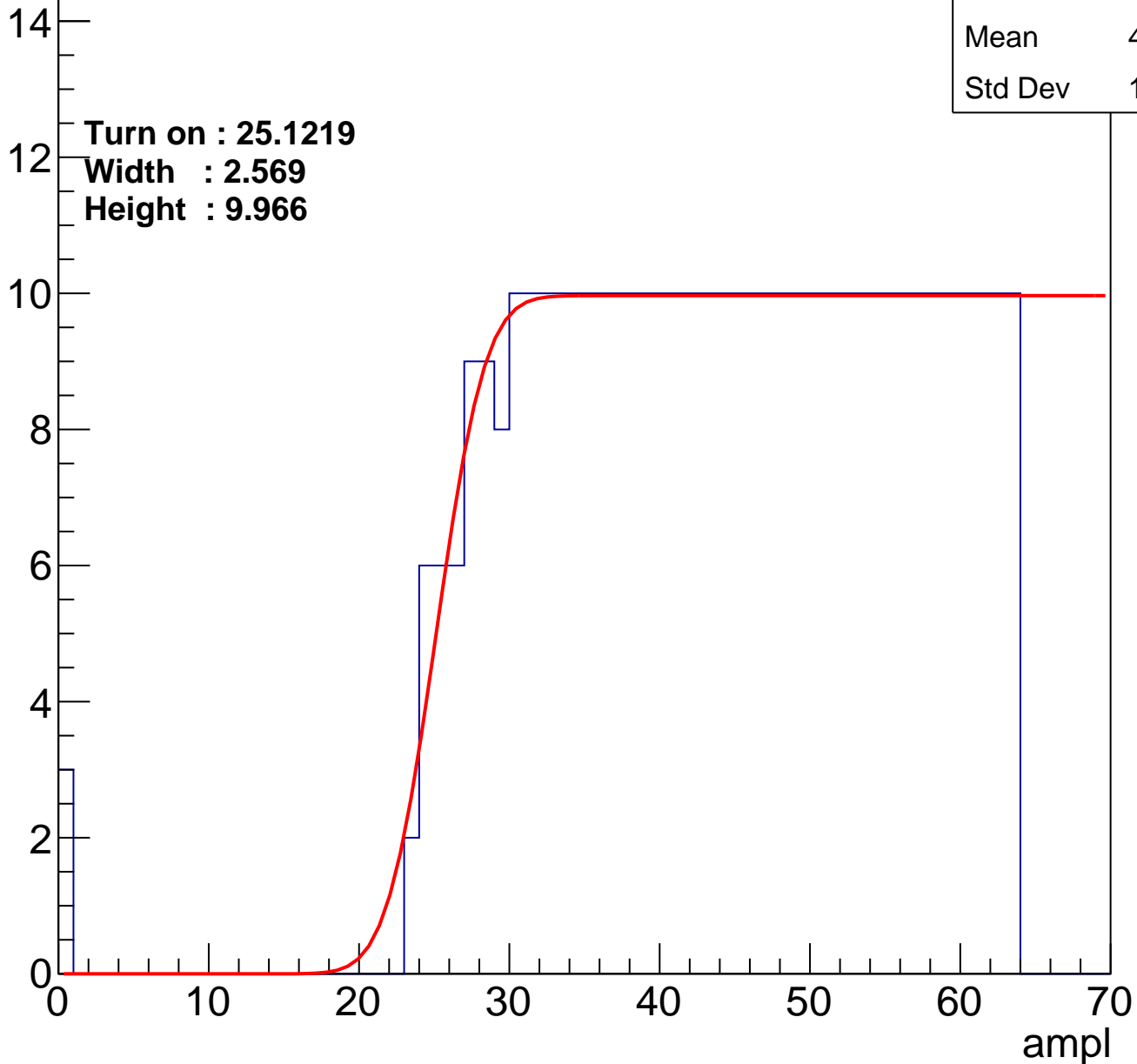
Entries	389
Mean	43.79
Std Dev	11.86

Turn on : 25.1219

Width : 2.569

Height : 9.966

Entry



B0L002S, U16-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	396
Mean	43.51
Std Dev	11.81

Turn on : 24.4896

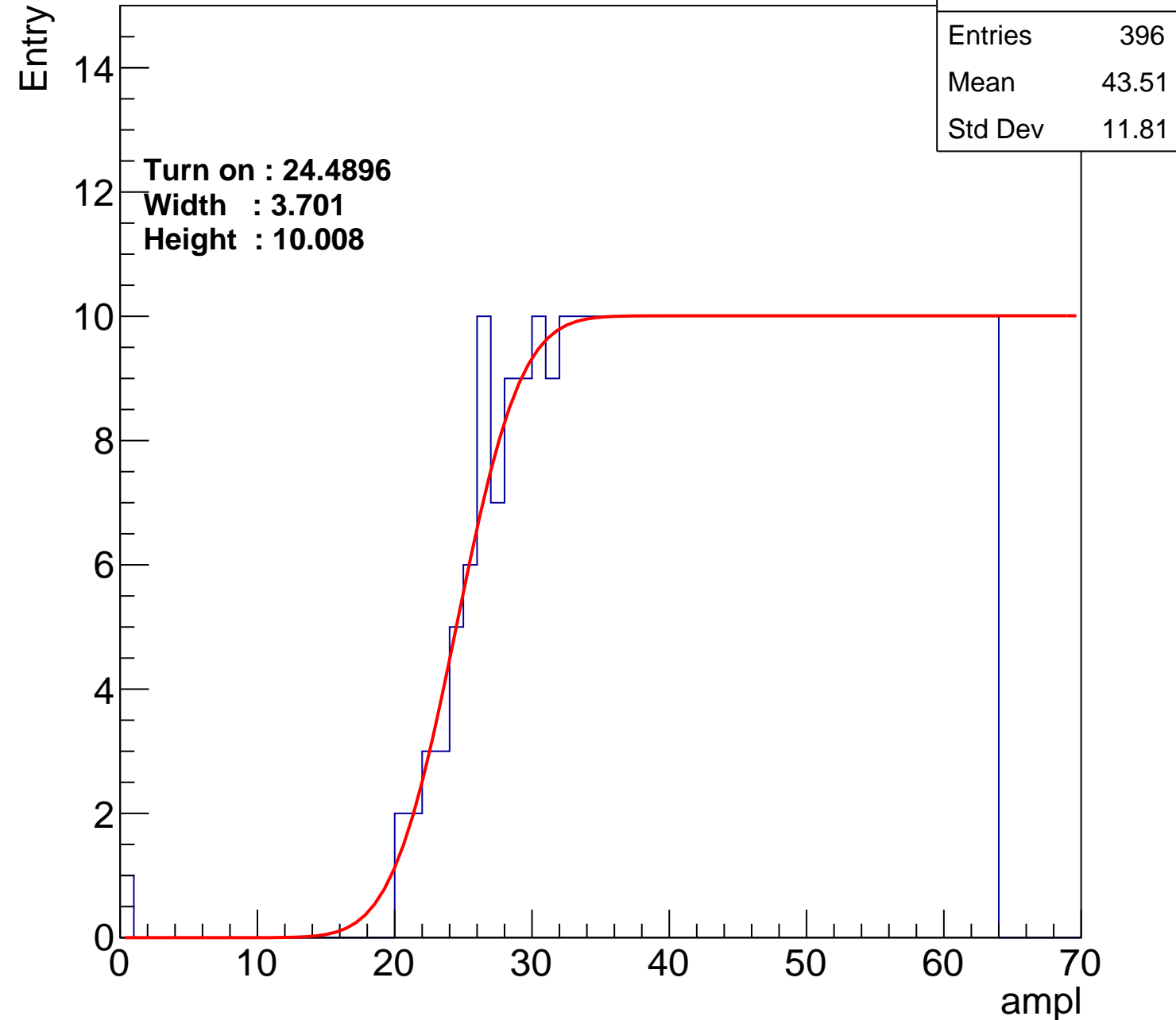
Width : 3.701

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch101

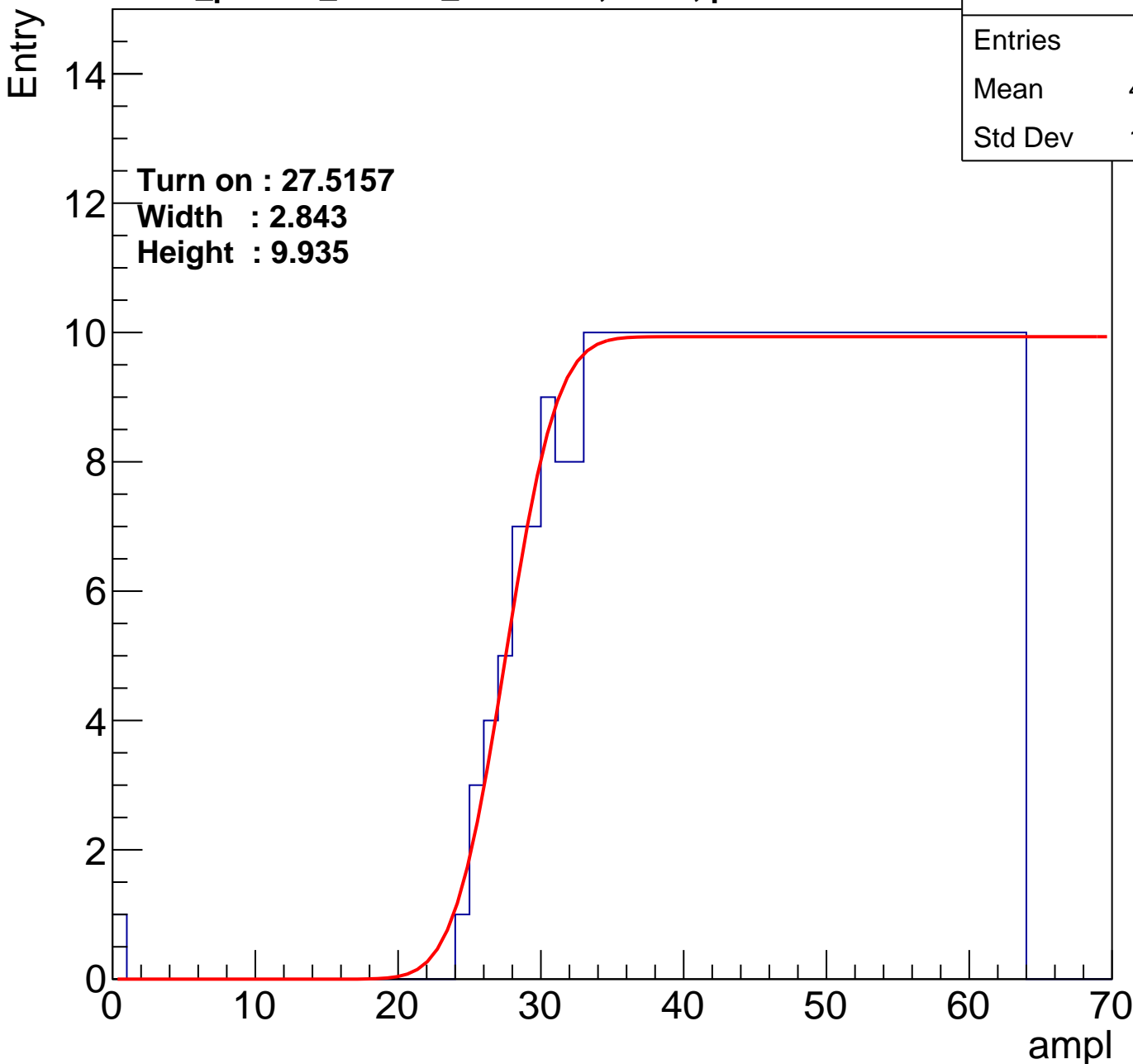
calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.15
Std Dev	10.89

Turn on : 27.5157

Width : 2.843

Height : 9.935



B0L002S, U16-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.04
Std Dev	11.77

Turn on : 26.0619

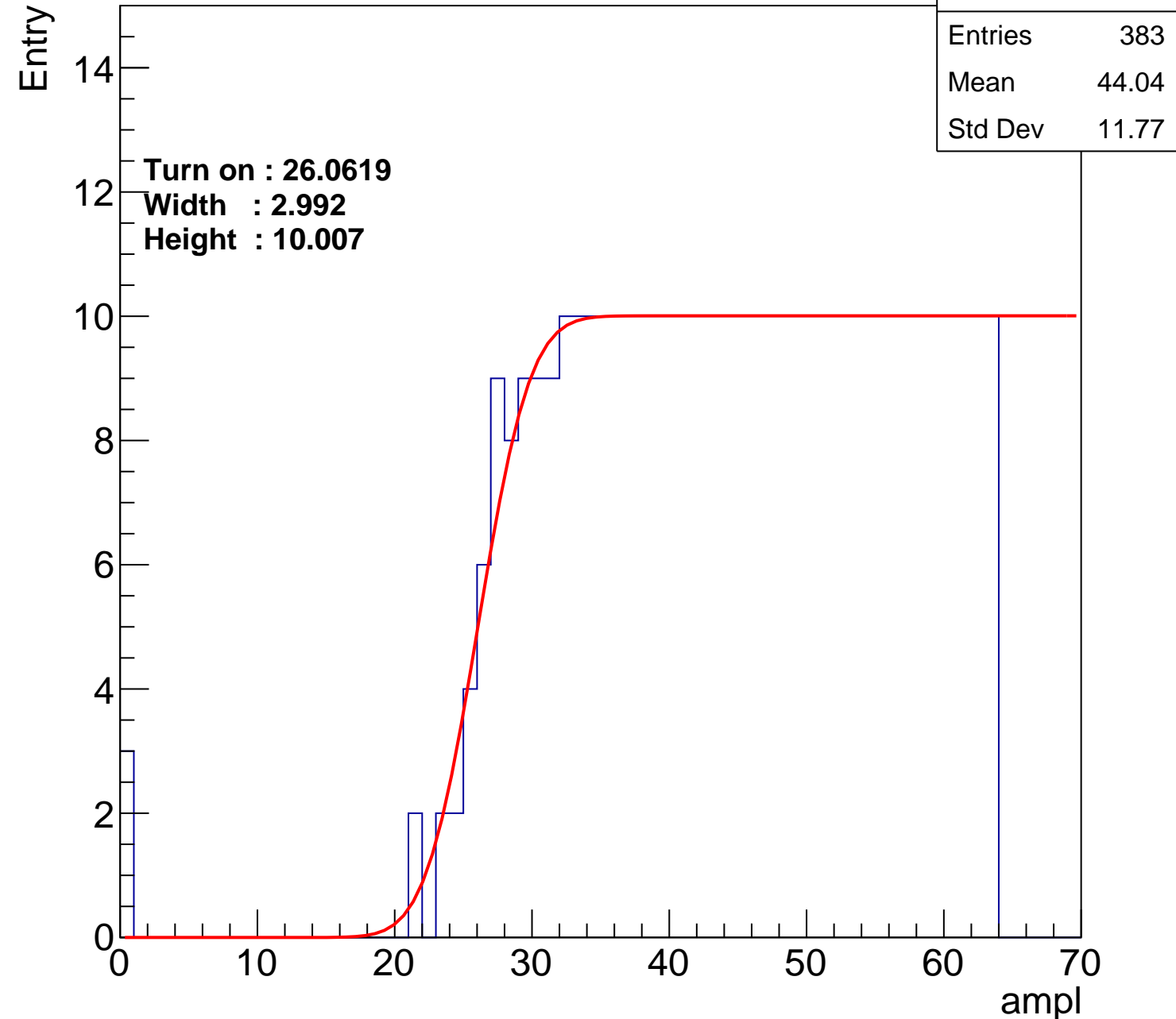
Width : 2.992

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.33
Std Dev	11.77

Turn on : 26.9863

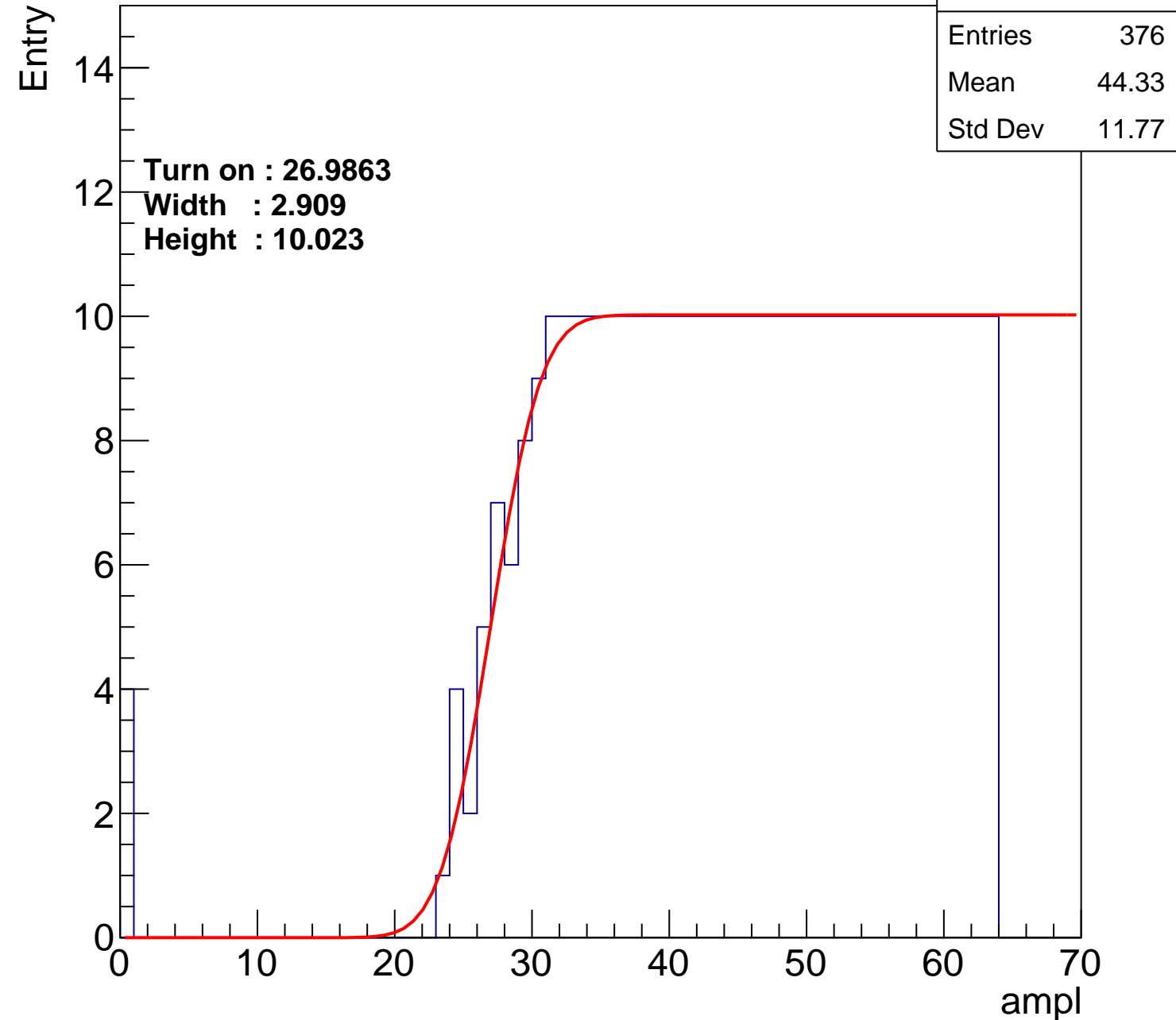
Width : 2.909

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.1
Std Dev	10.38

Turn on : 30.0829

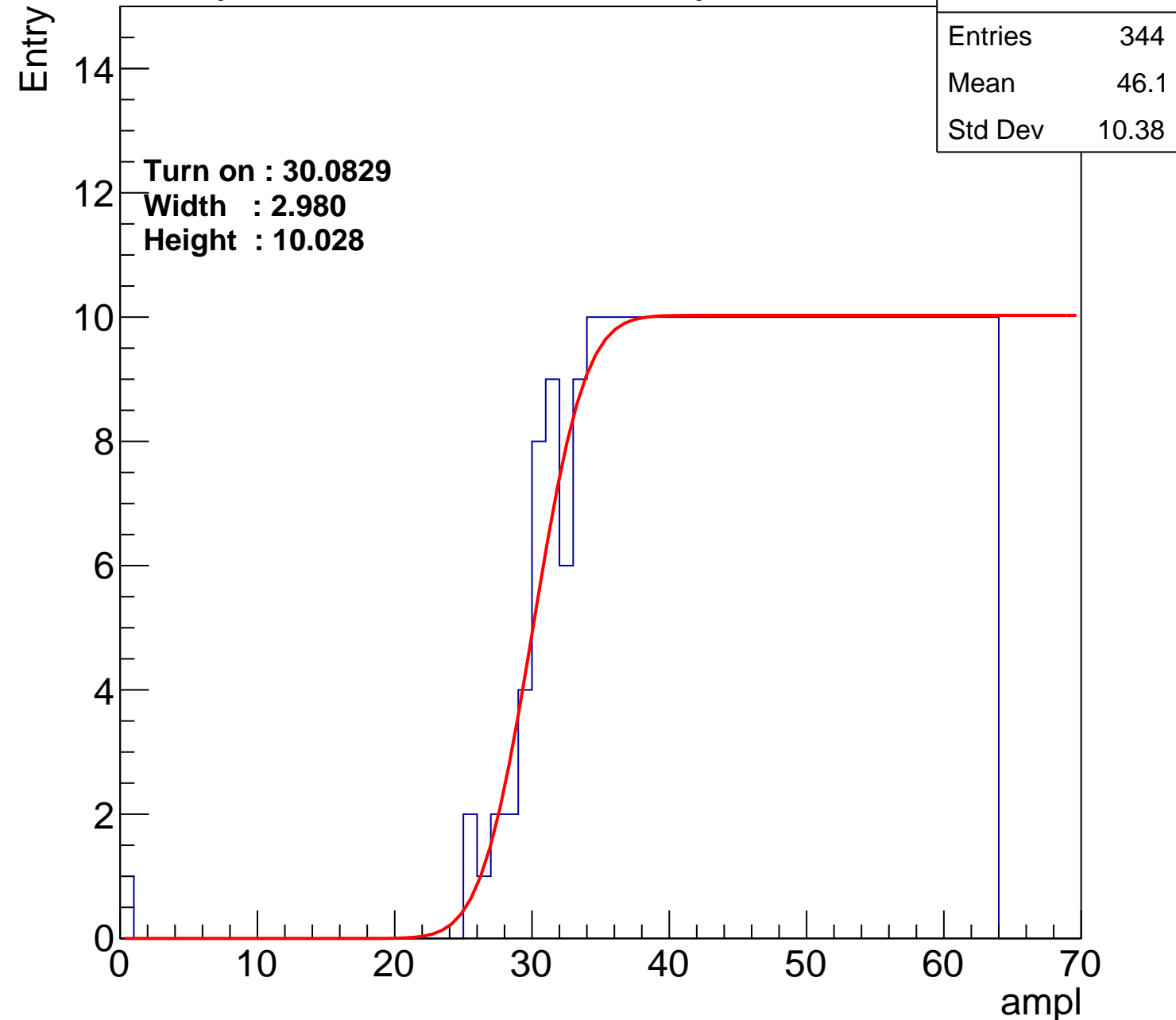
Width : 2.980

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch105

calib_packv5_042523_0143.root, FC#8, port C1

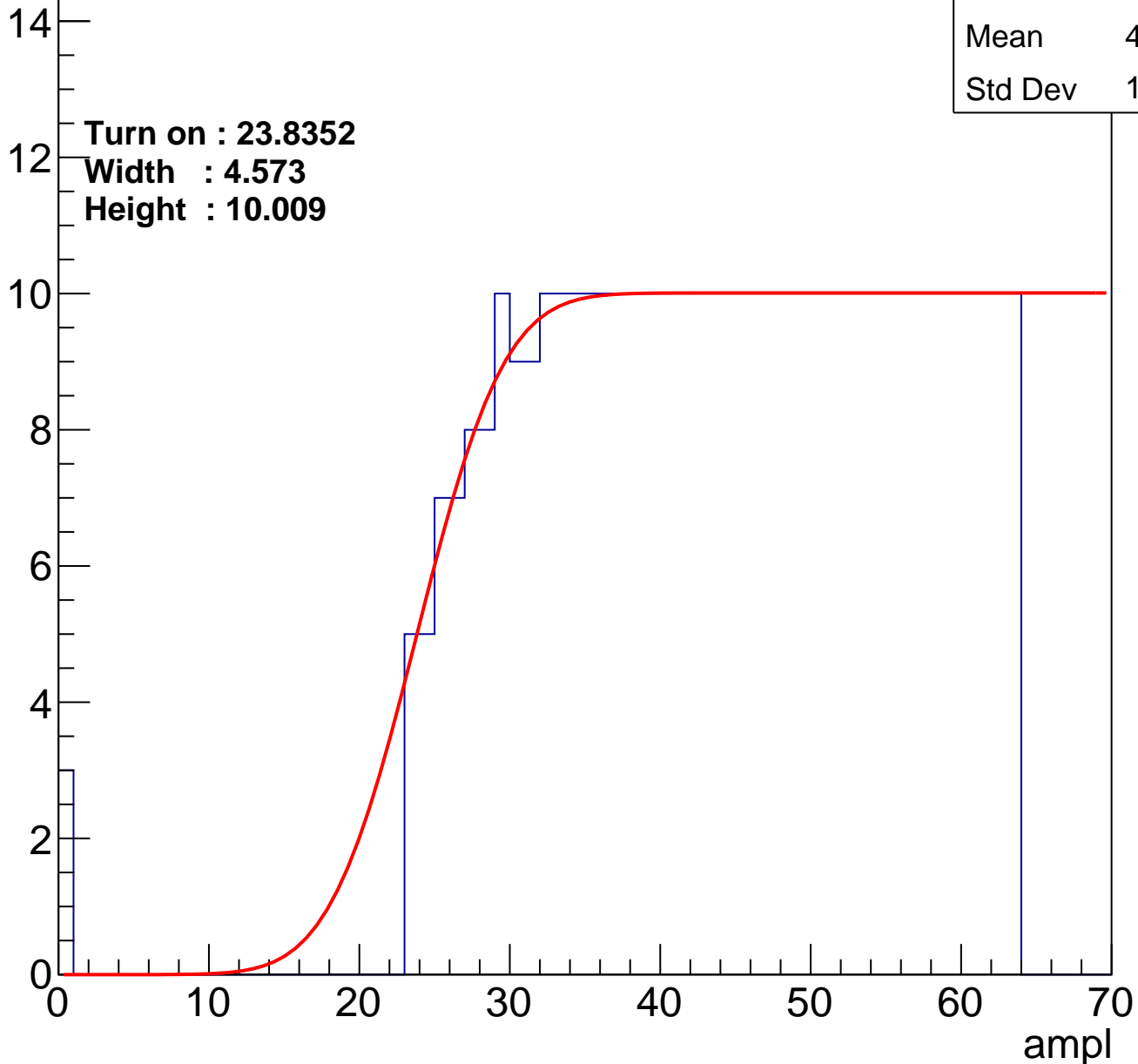
Entries	391
Mean	43.66
Std Dev	11.95

Turn on : 23.8352

Width : 4.573

Height : 10.009

Entry



B0L002S, U16-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	391
Mean	43.77
Std Dev	11.66

Turn on : 25.6143

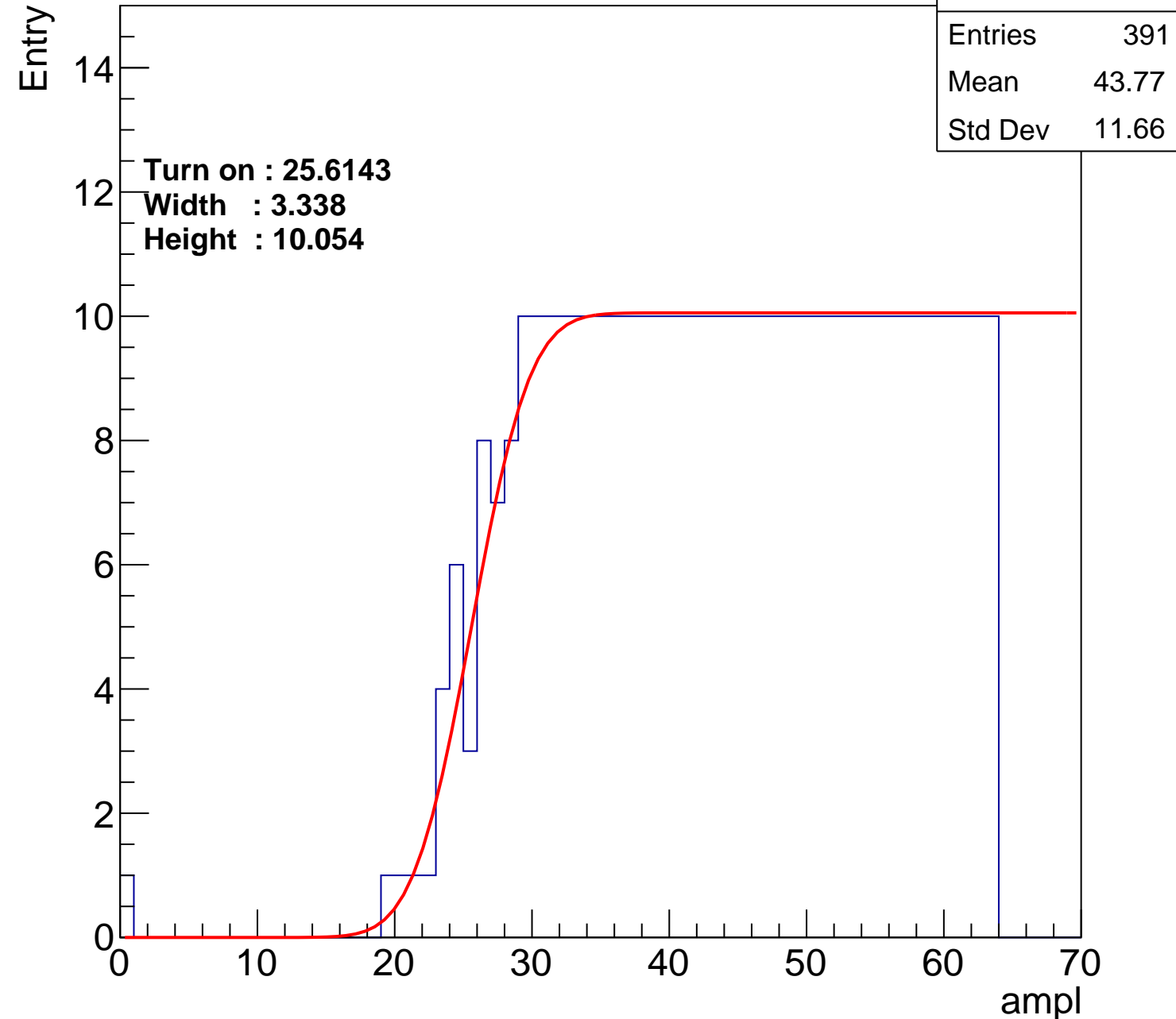
Width : 3.338

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.53
Std Dev	10.82

Turn on : 28.8397

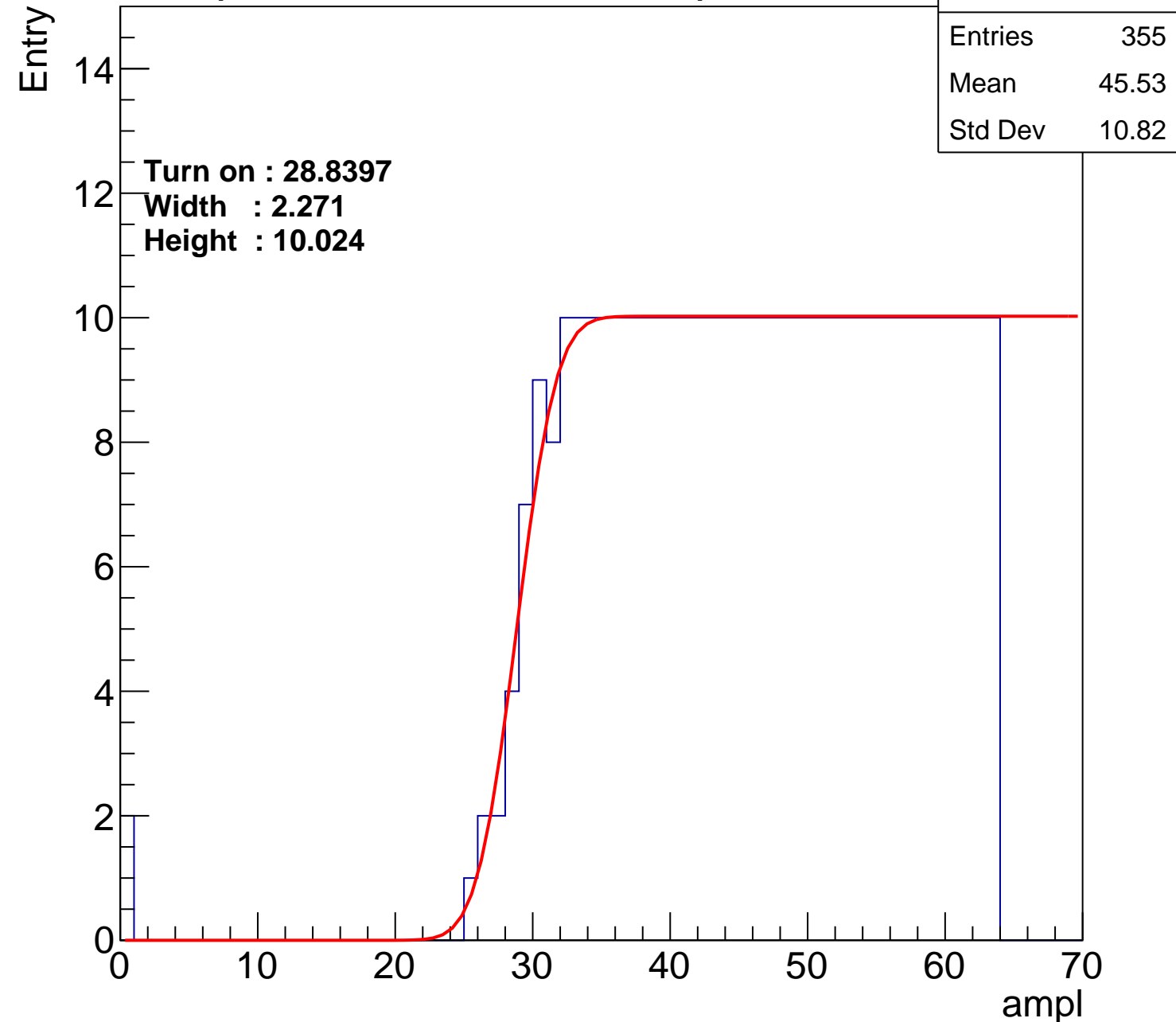
Width : 2.271

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.47
Std Dev	11.59

Turn on : 27.1492

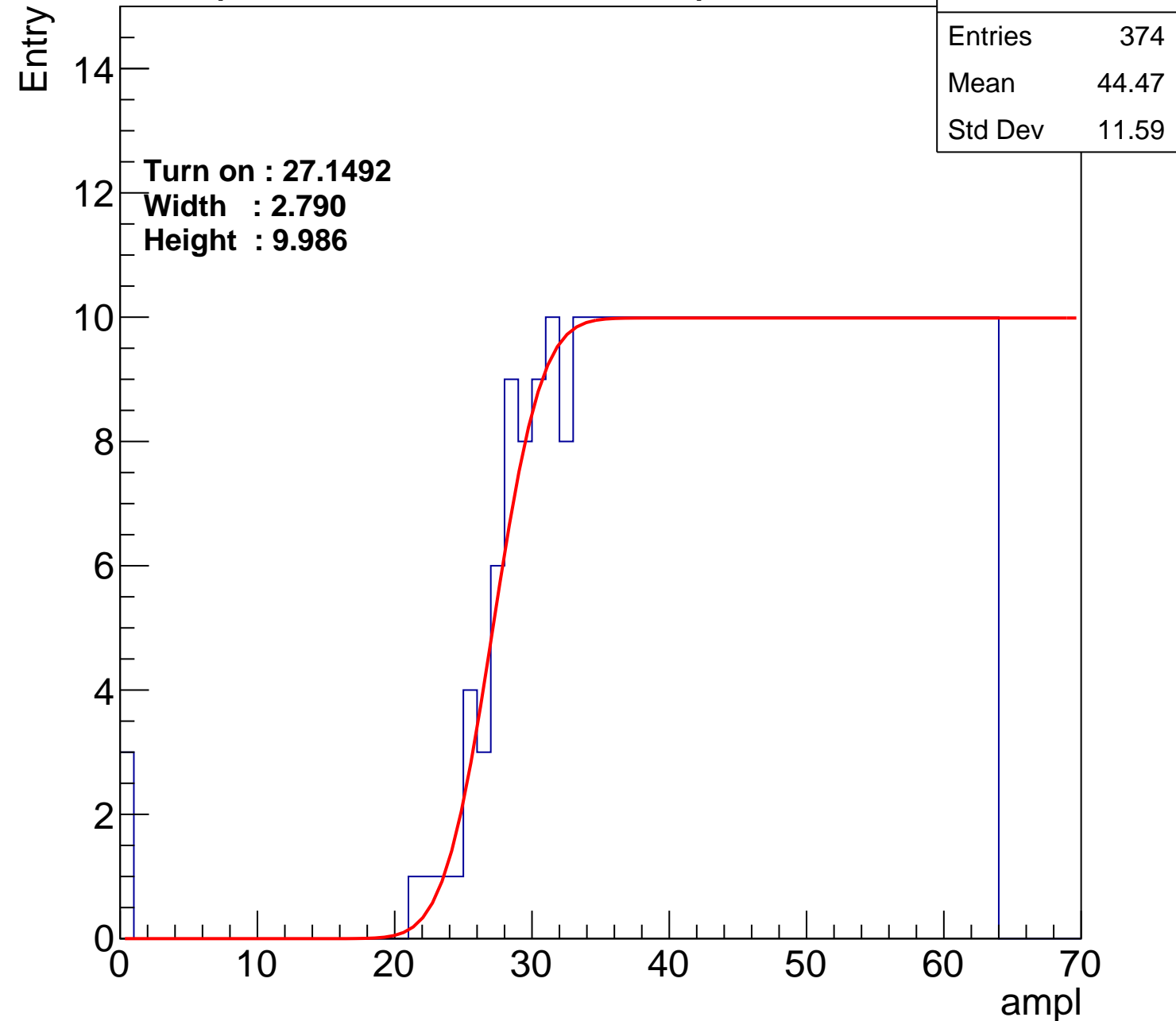
Width : 2.790

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.98
Std Dev	11.07

Turn on : 27.4447

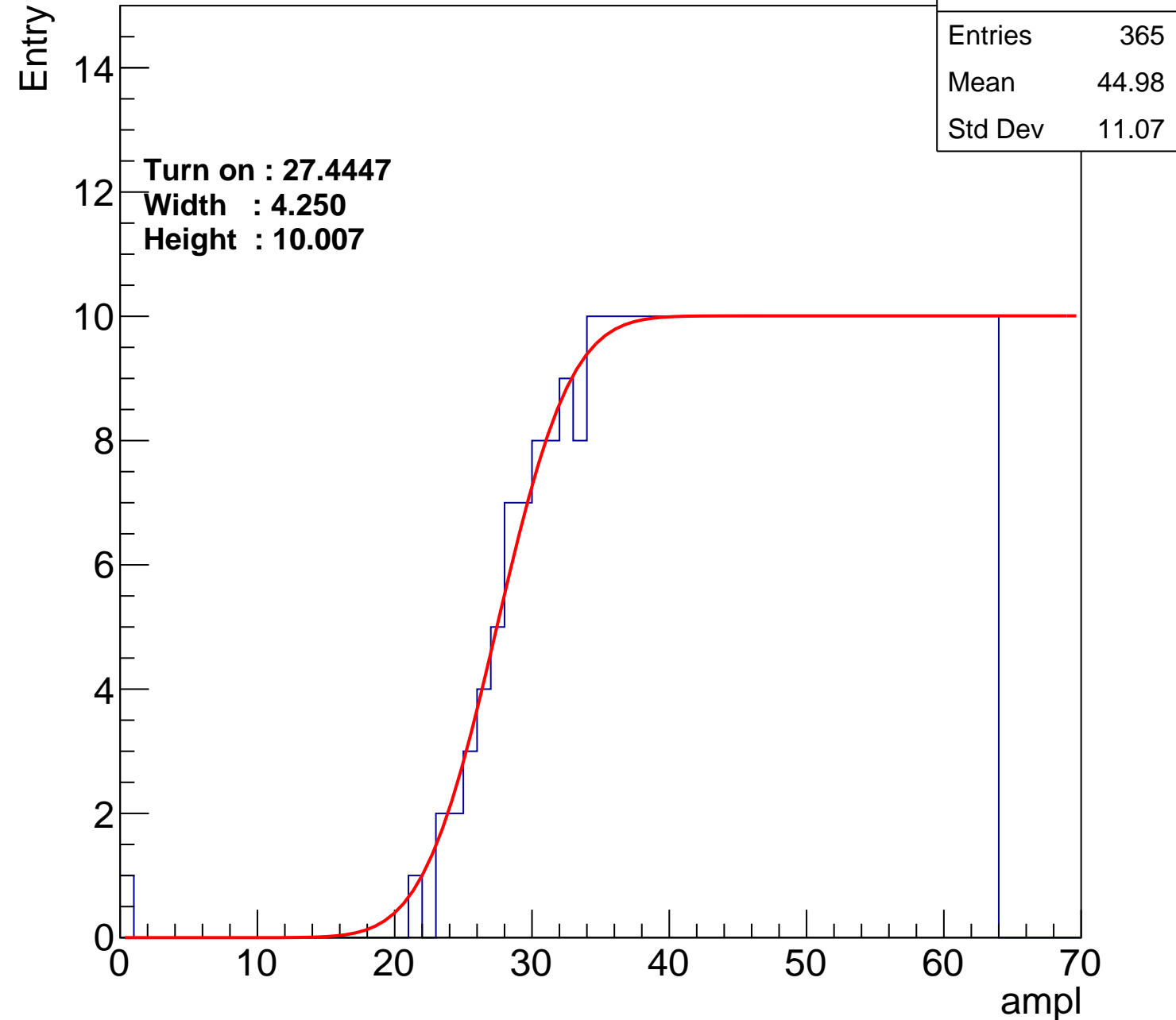
Width : 4.250

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.35
Std Dev	11.49

Turn on : 27.0710

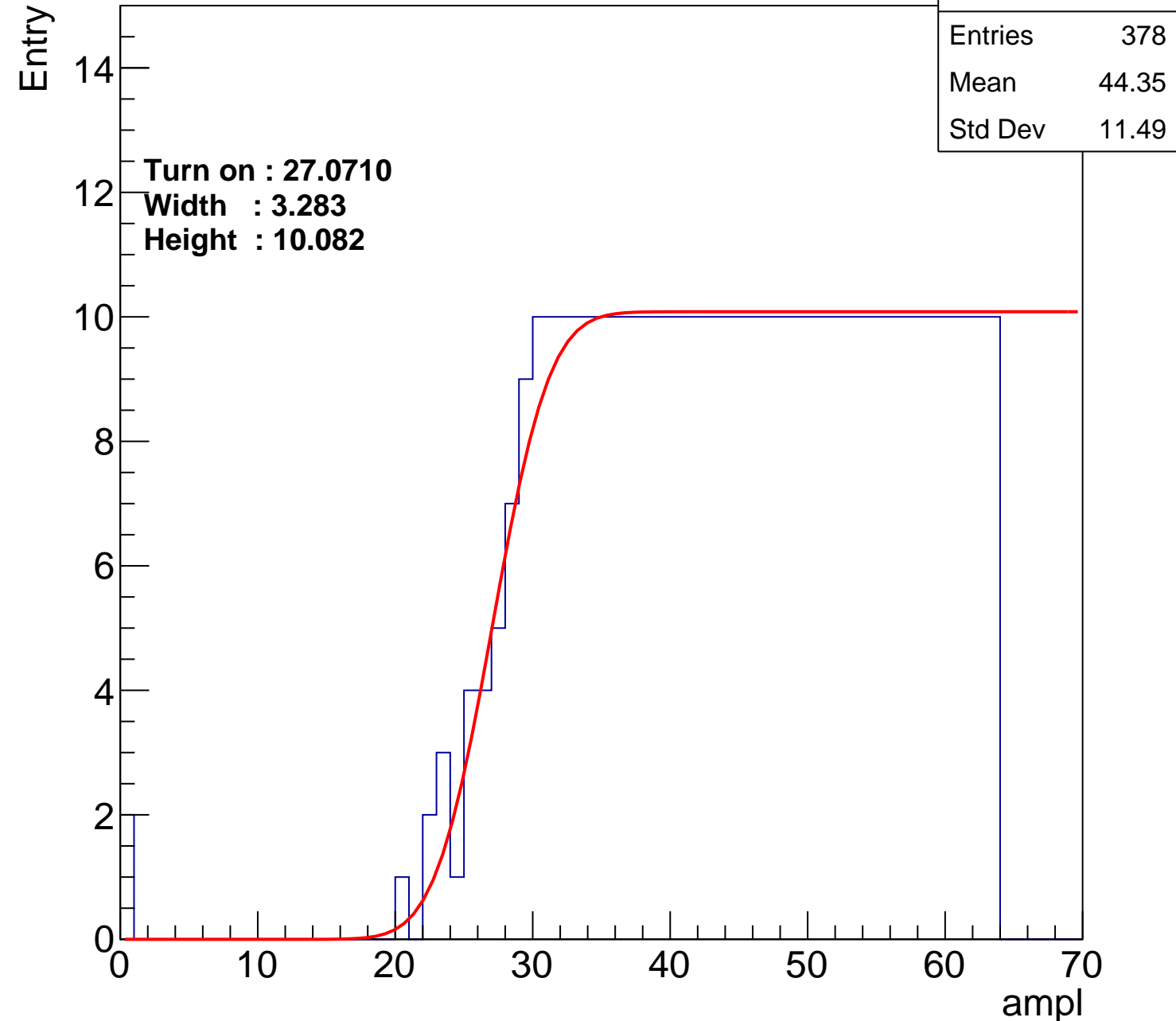
Width : 3.283

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.23
Std Dev	11.39

Turn on : 26.5145

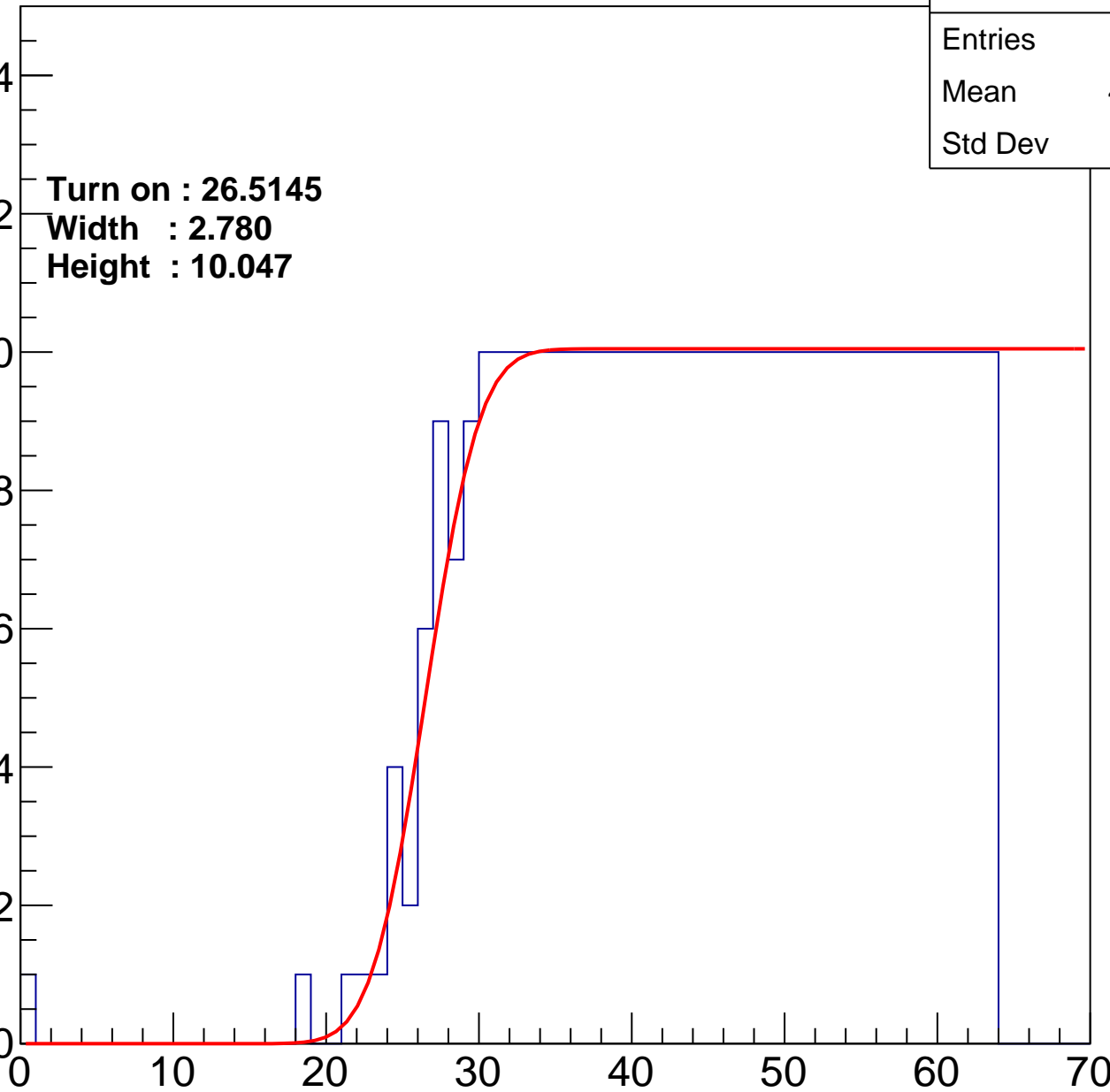
Width : 2.780

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch112

calib_packv5_042523_0143.root, FC#8, port C1

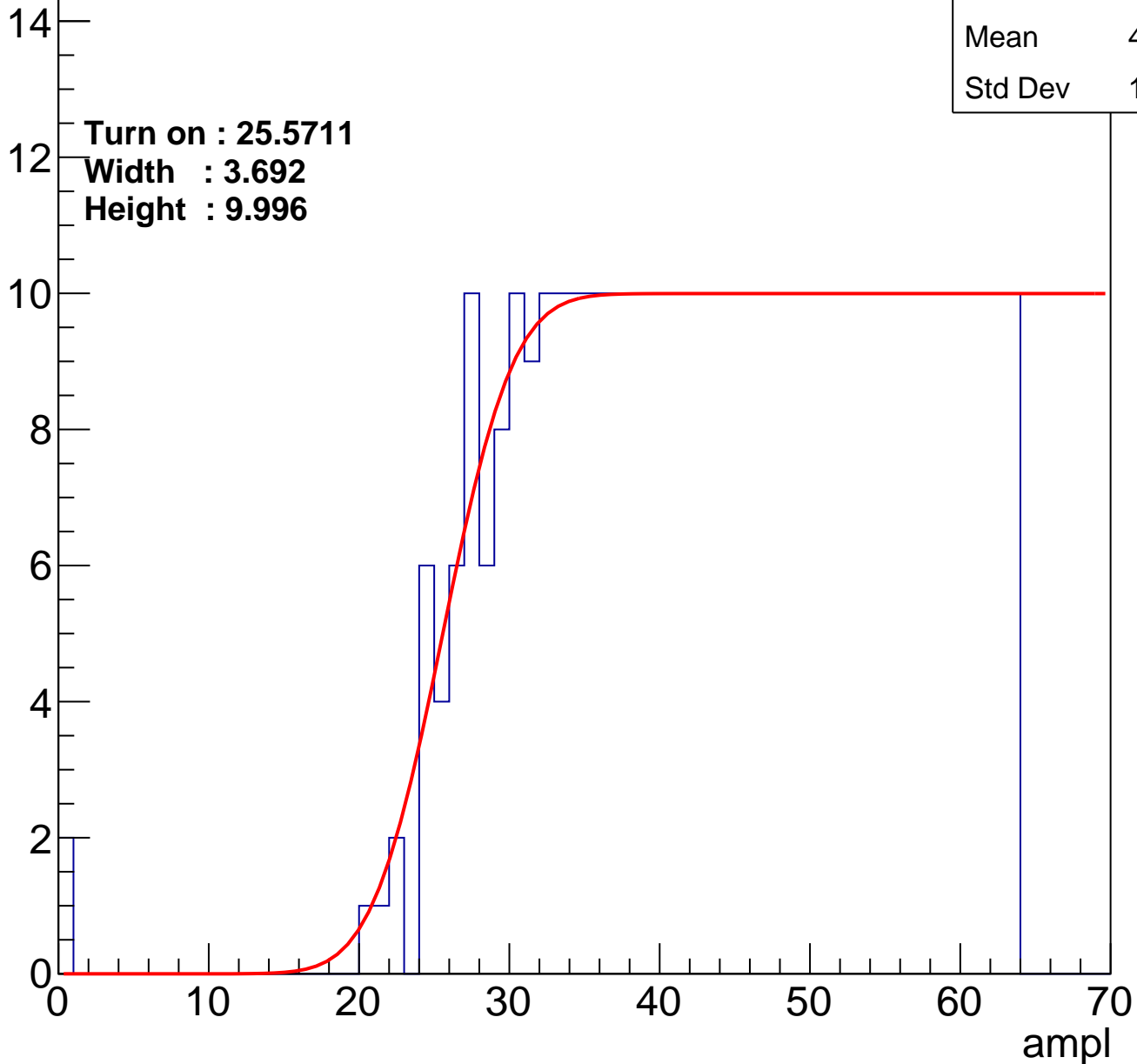
Entries	385
Mean	43.98
Std Dev	11.69

Turn on : 25.5711

Width : 3.692

Height : 9.996

Entry



B0L002S, U16-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44
Std Dev	12.26

Turn on : 26.5968

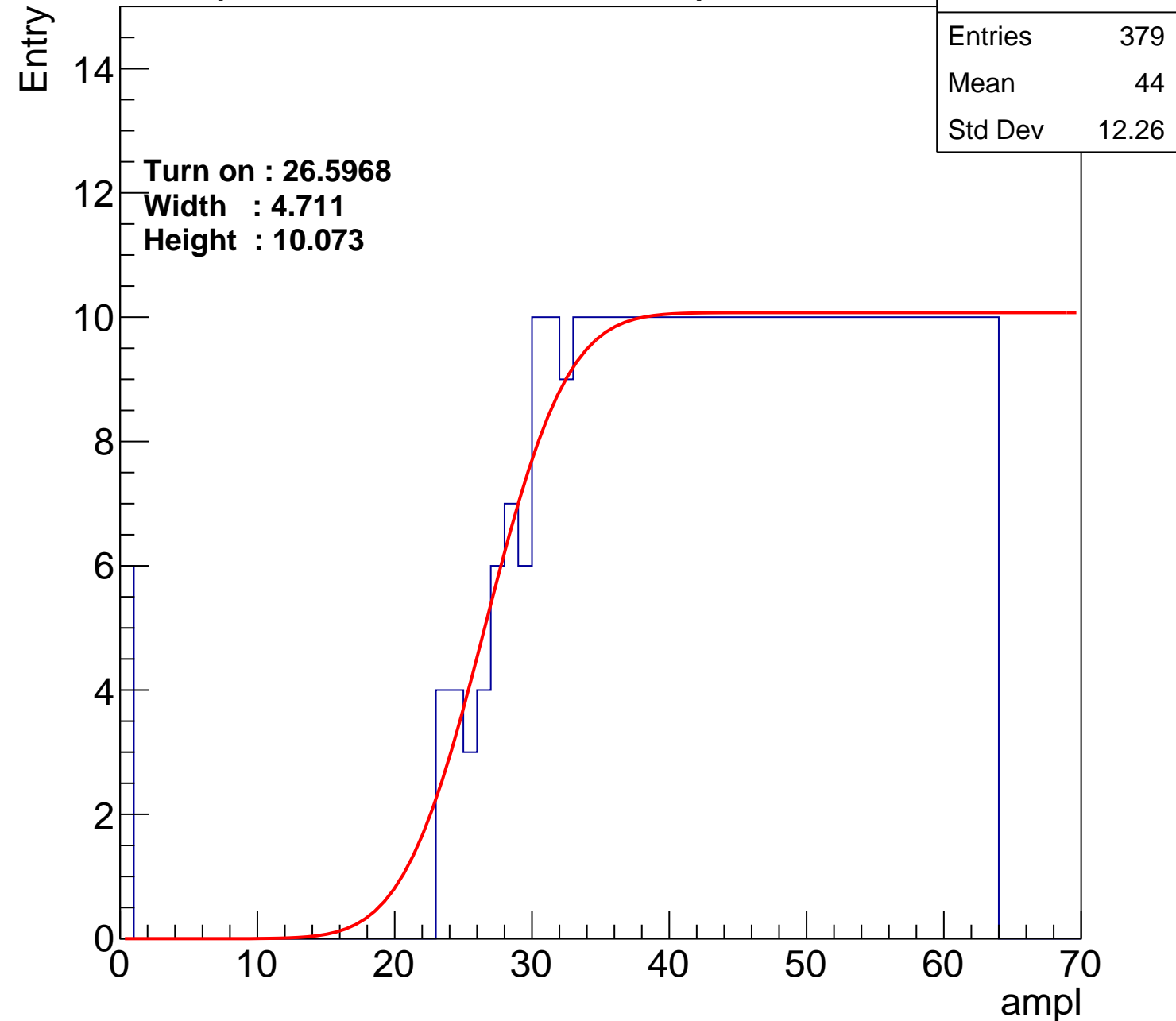
Width : 4.711

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.12
Std Dev	11.09

Turn on : 28.0128

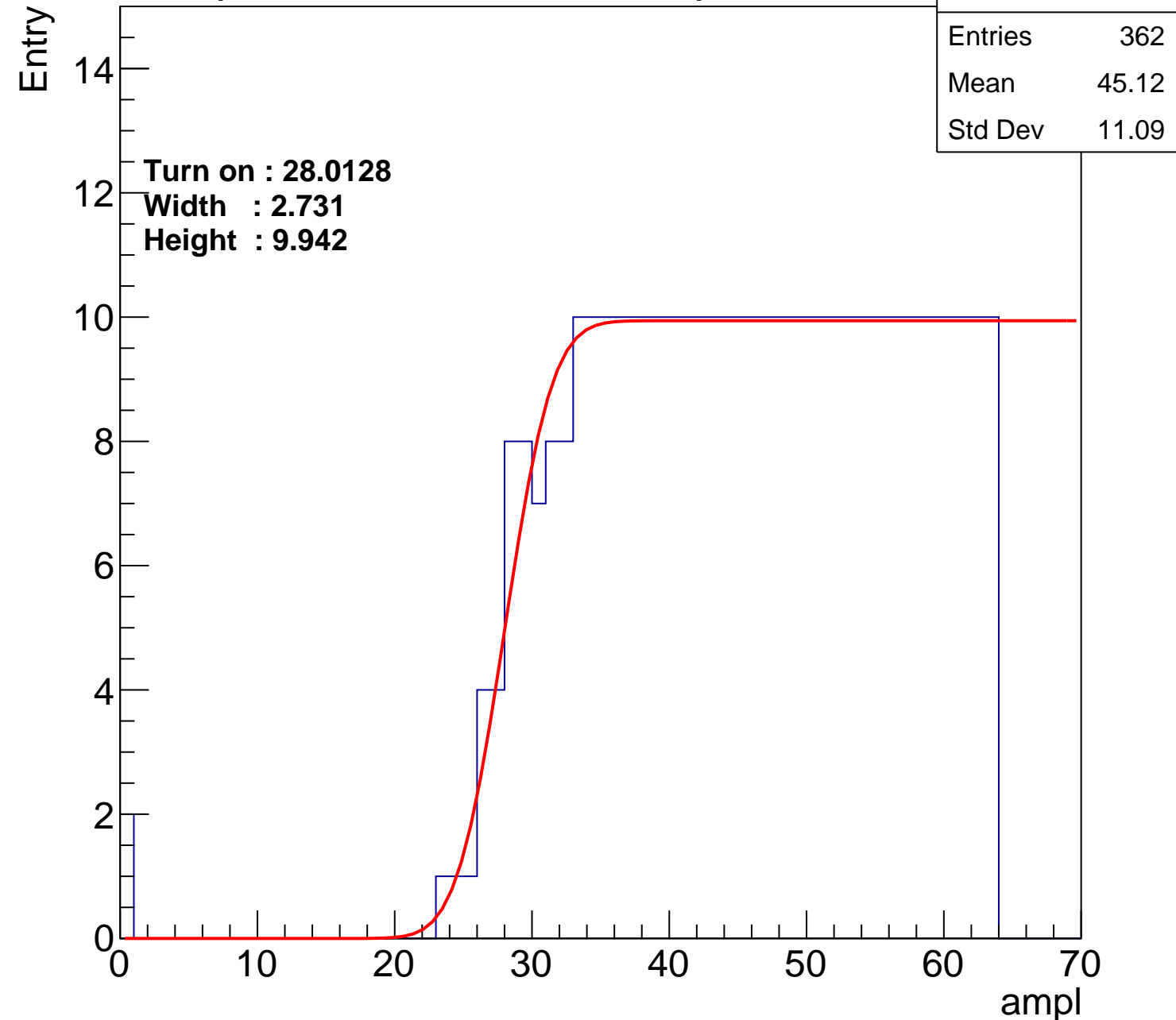
Width : 2.731

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.64
Std Dev	11.52

Turn on : 27.5283

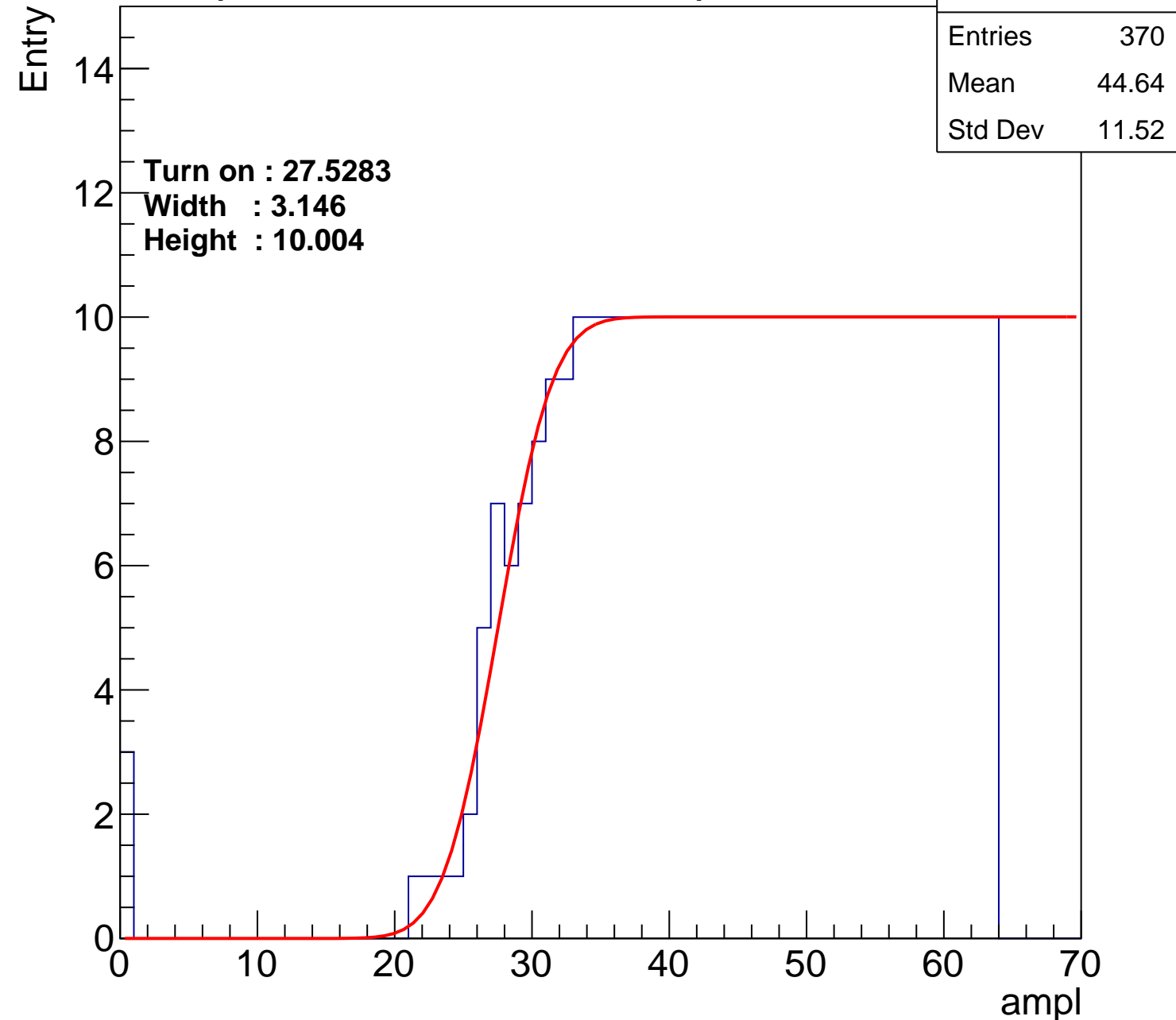
Width : 3.146

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.5858

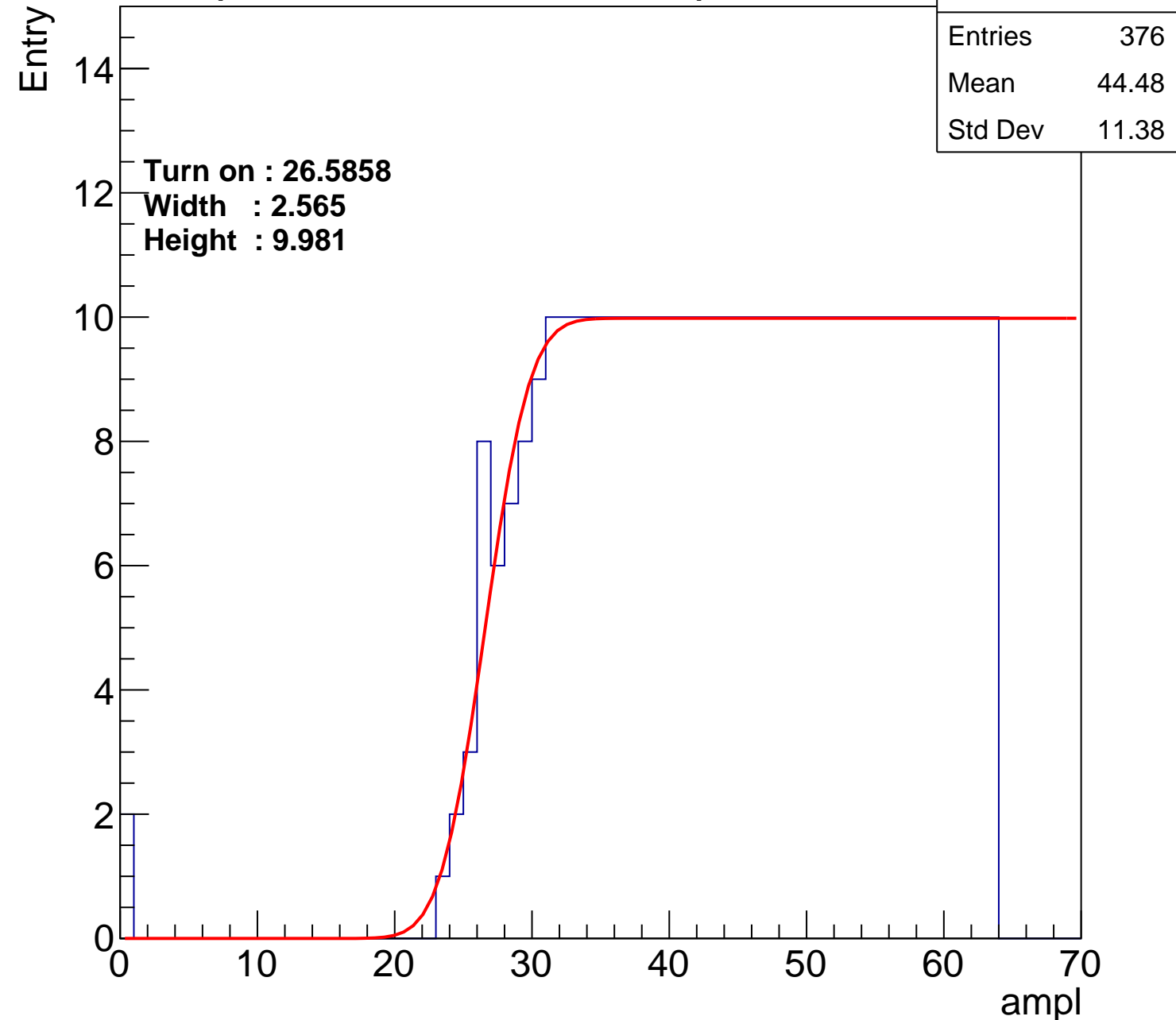
Width : 2.565

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.1
Std Dev	11.66

Turn on : 26.7946

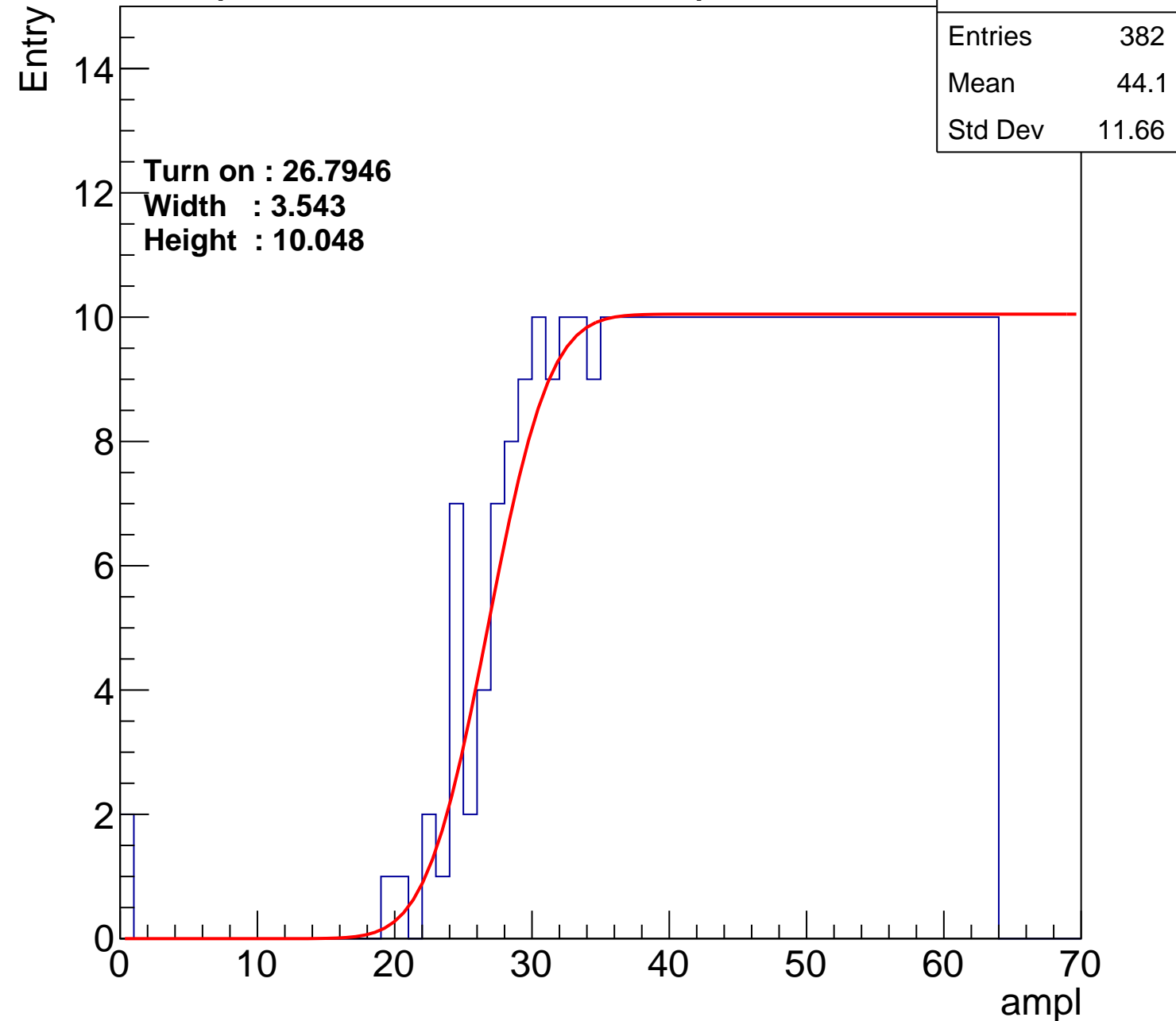
Width : 3.543

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
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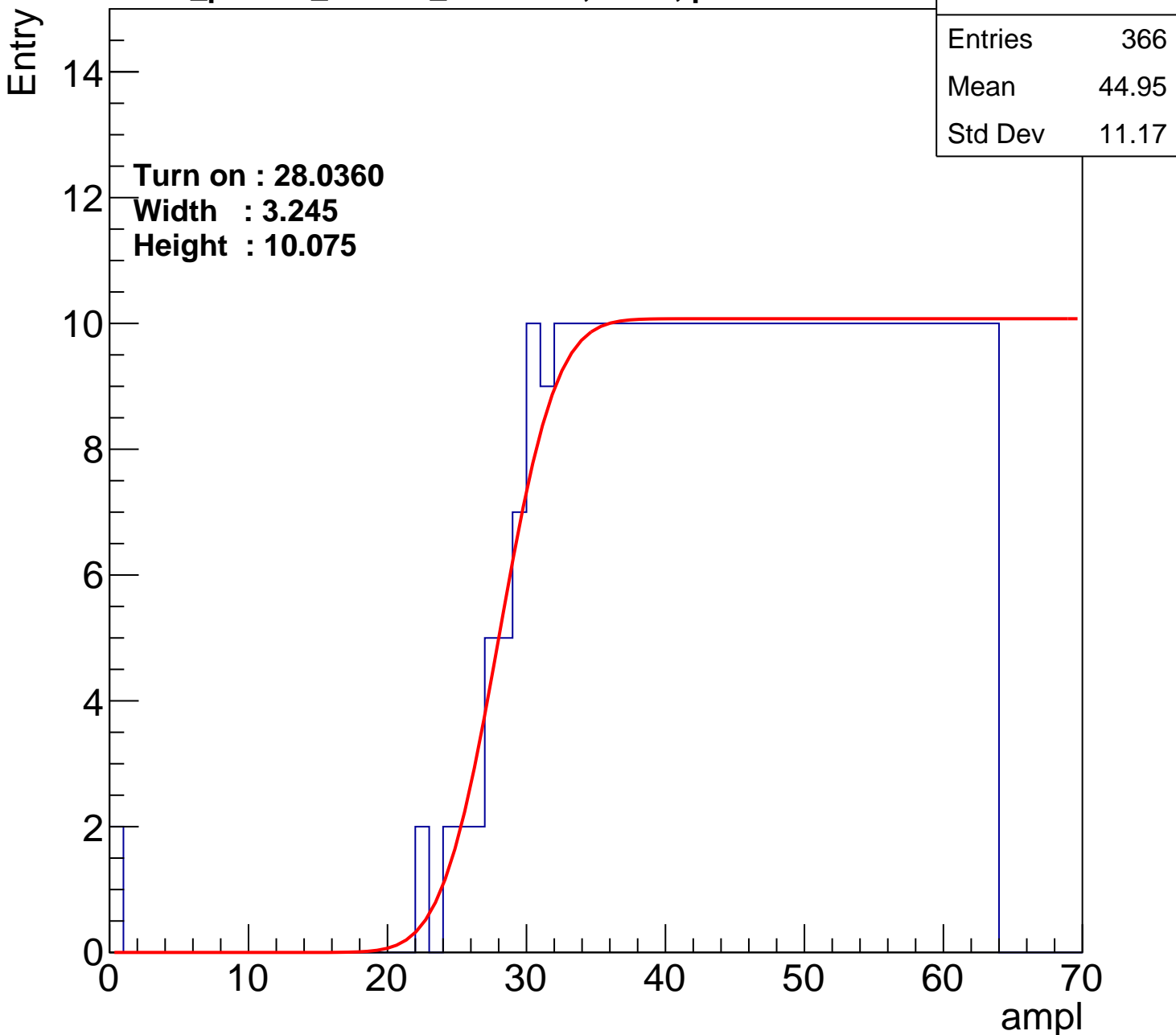
Mean	44.95
------	-------

Std Dev	11.17
---------	-------

Turn on : 28.0360

Width : 3.245

Height : 10.075



B0L002S, U16-ch119

calib_packv5_042523_0143.root, FC#8, port C1

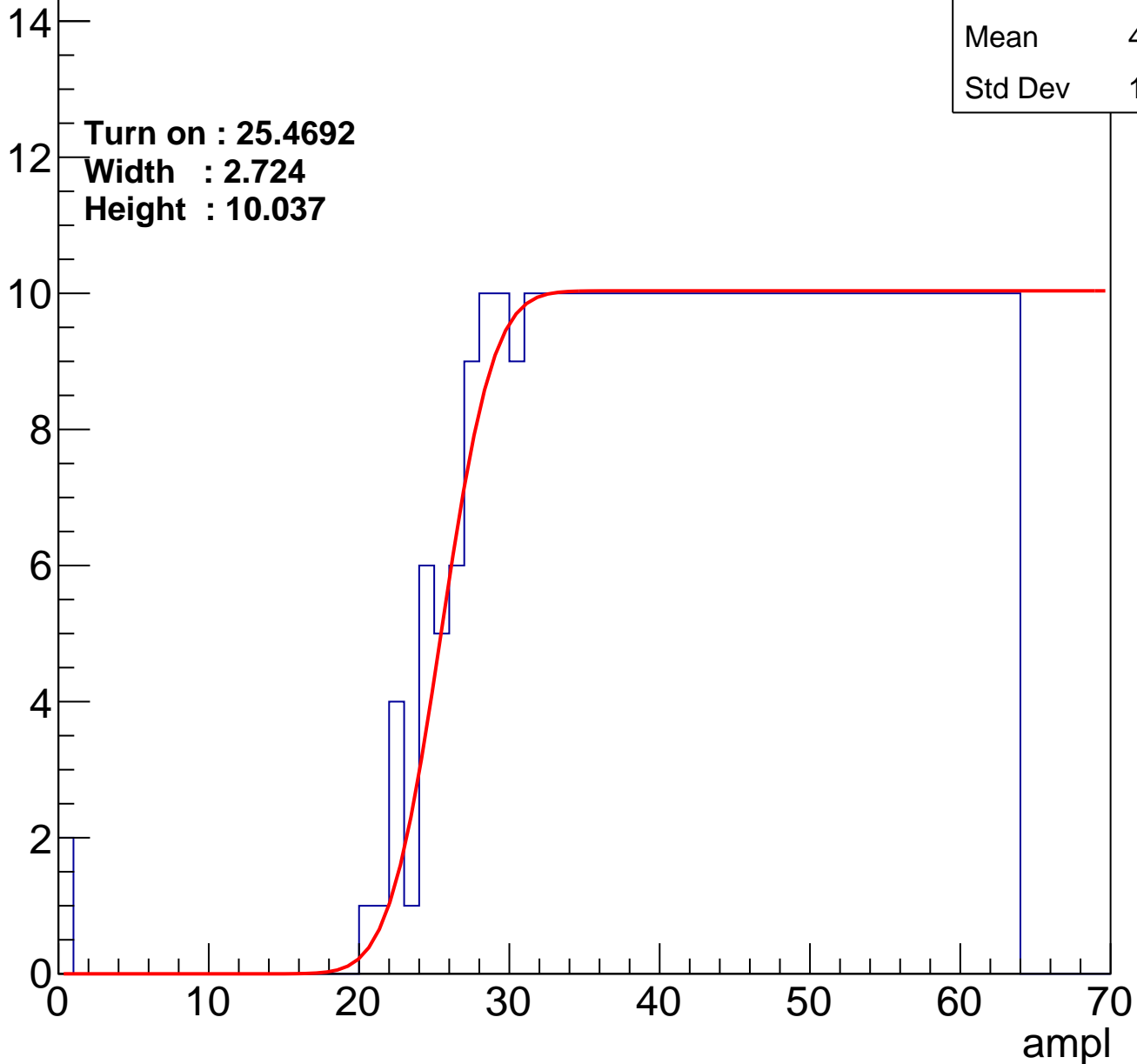
Entries	394
Mean	43.58
Std Dev	11.87

Turn on : 25.4692

Width : 2.724

Height : 10.037

Entry



B0L002S, U16-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.19
Std Dev	11.7

Turn on : 26.7954

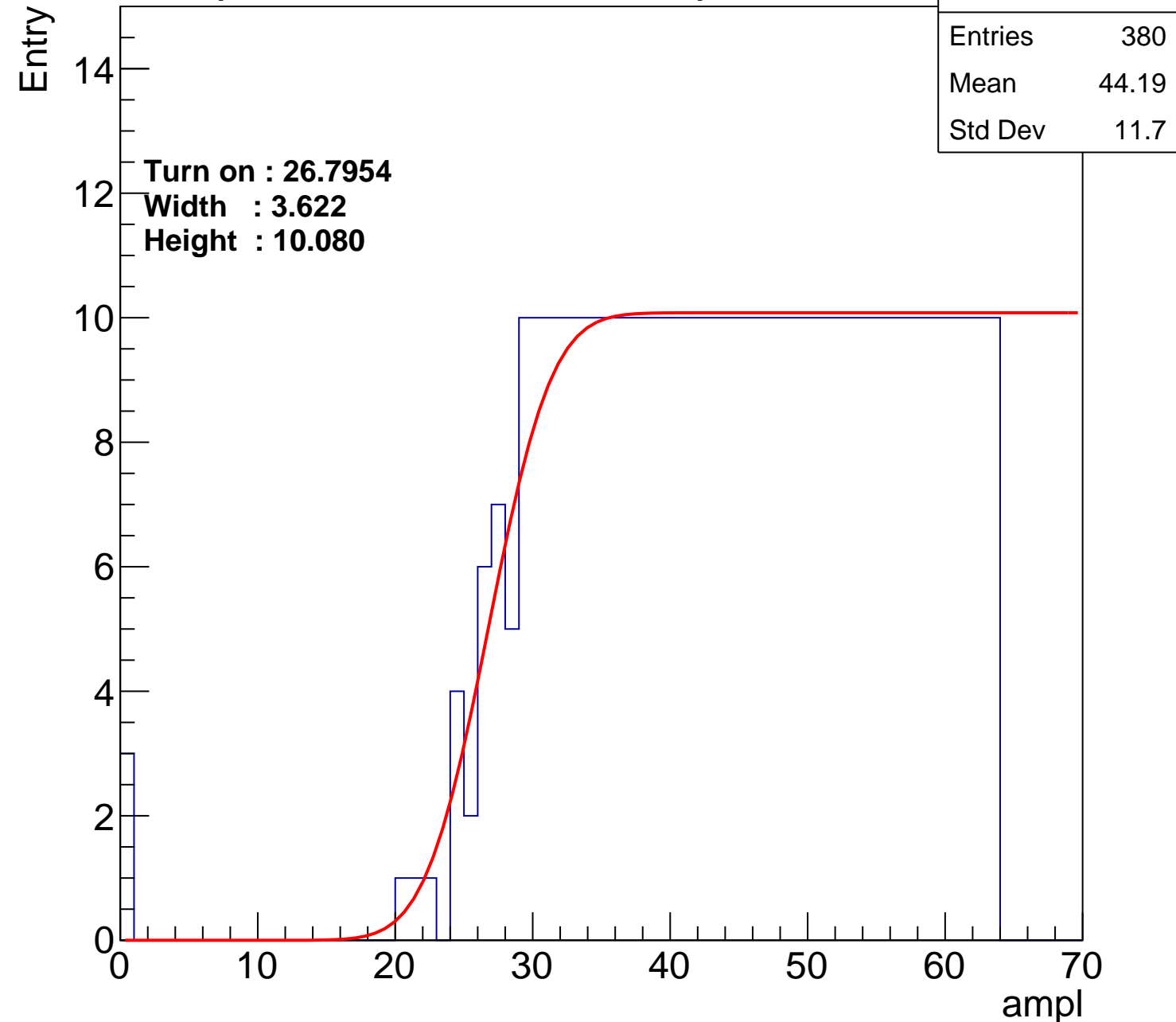
Width : 3.622

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	43.75
Std Dev	12.21

Turn on : 26.1737

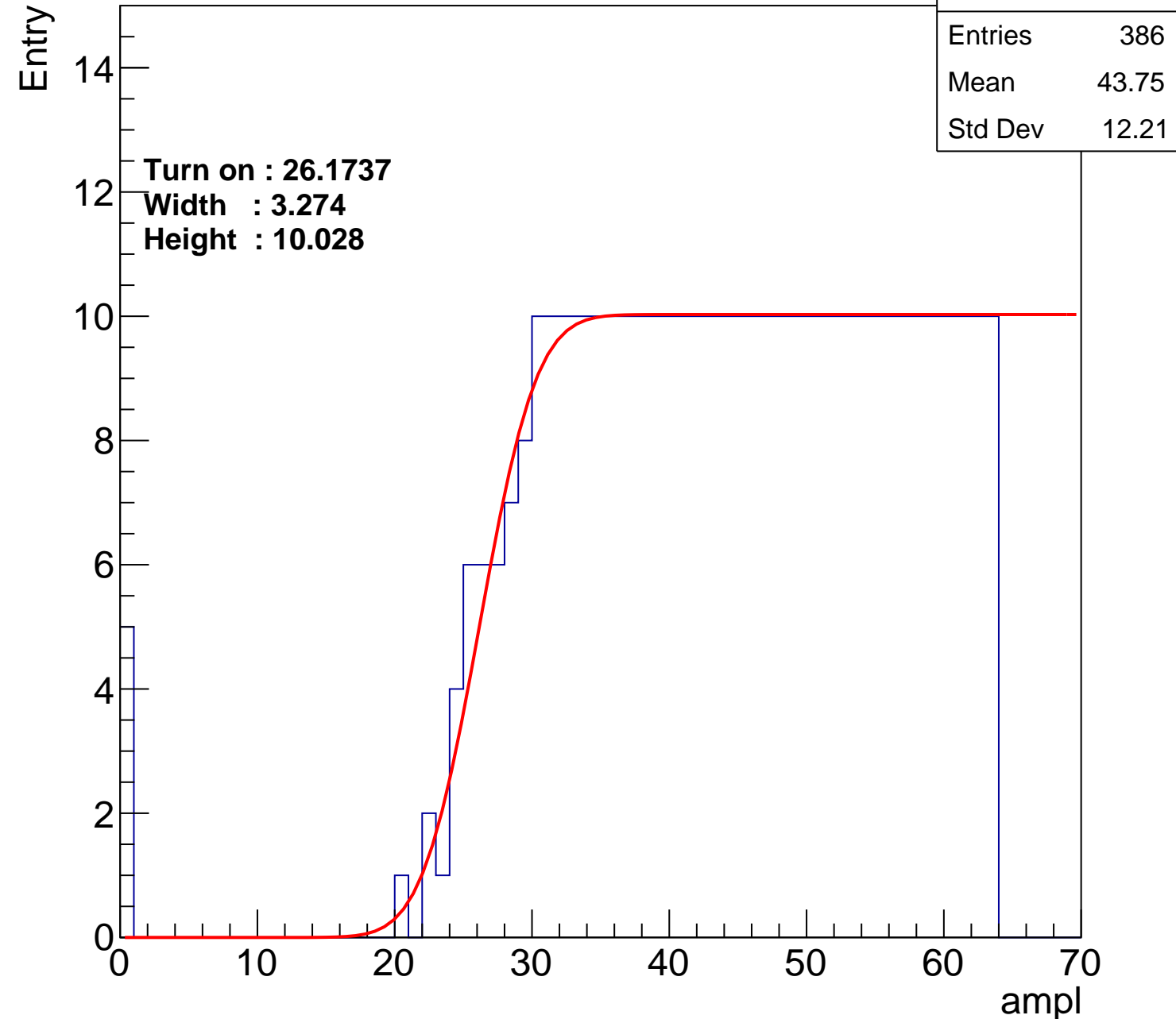
Width : 3.274

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.35
Std Dev	11.49

Turn on : 26.5717

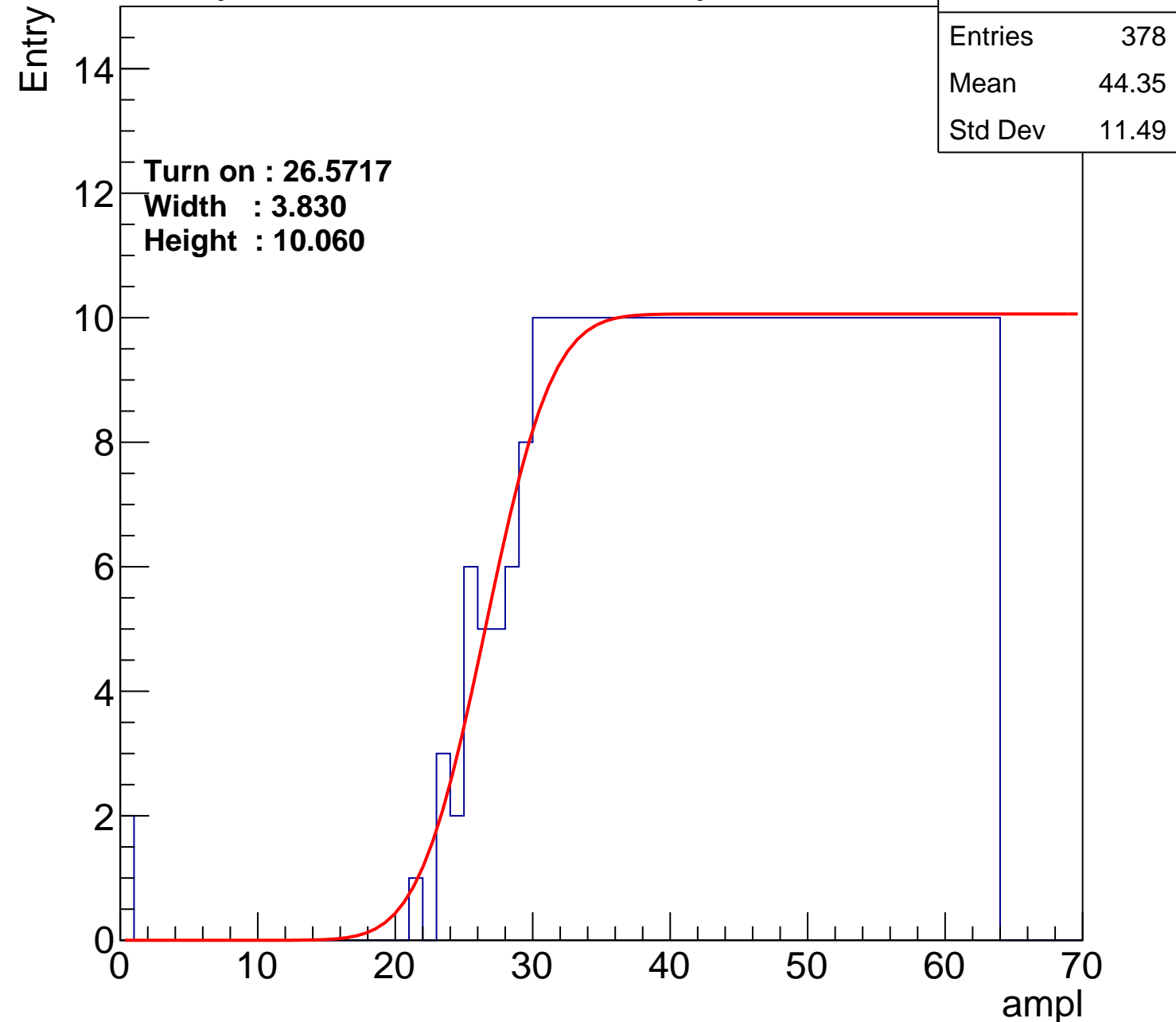
Width : 3.830

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.81
Std Dev	11.03

Turn on : 27.2646

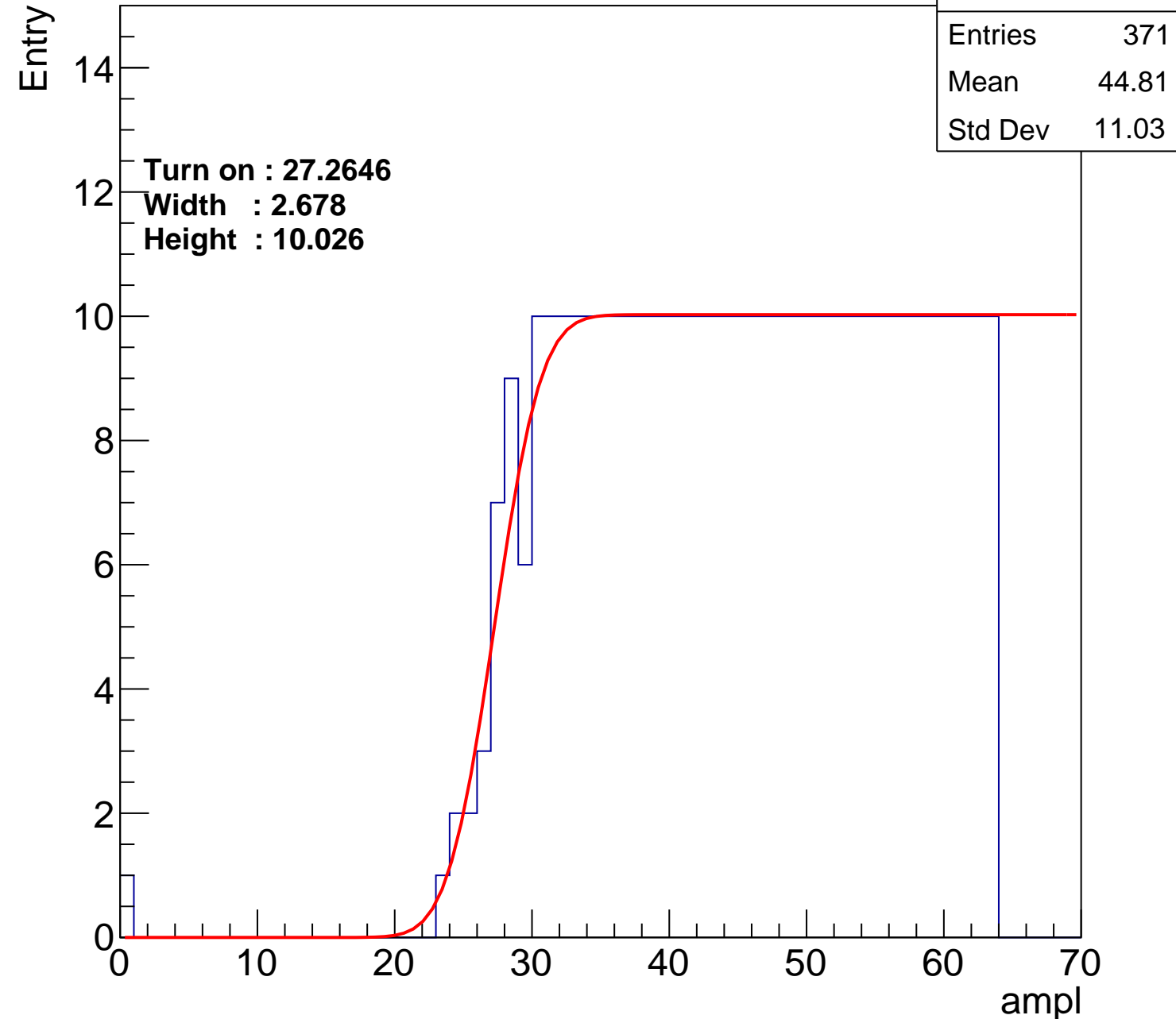
Width : 2.678

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.64
Std Dev	11.84

Turn on : 28.1622

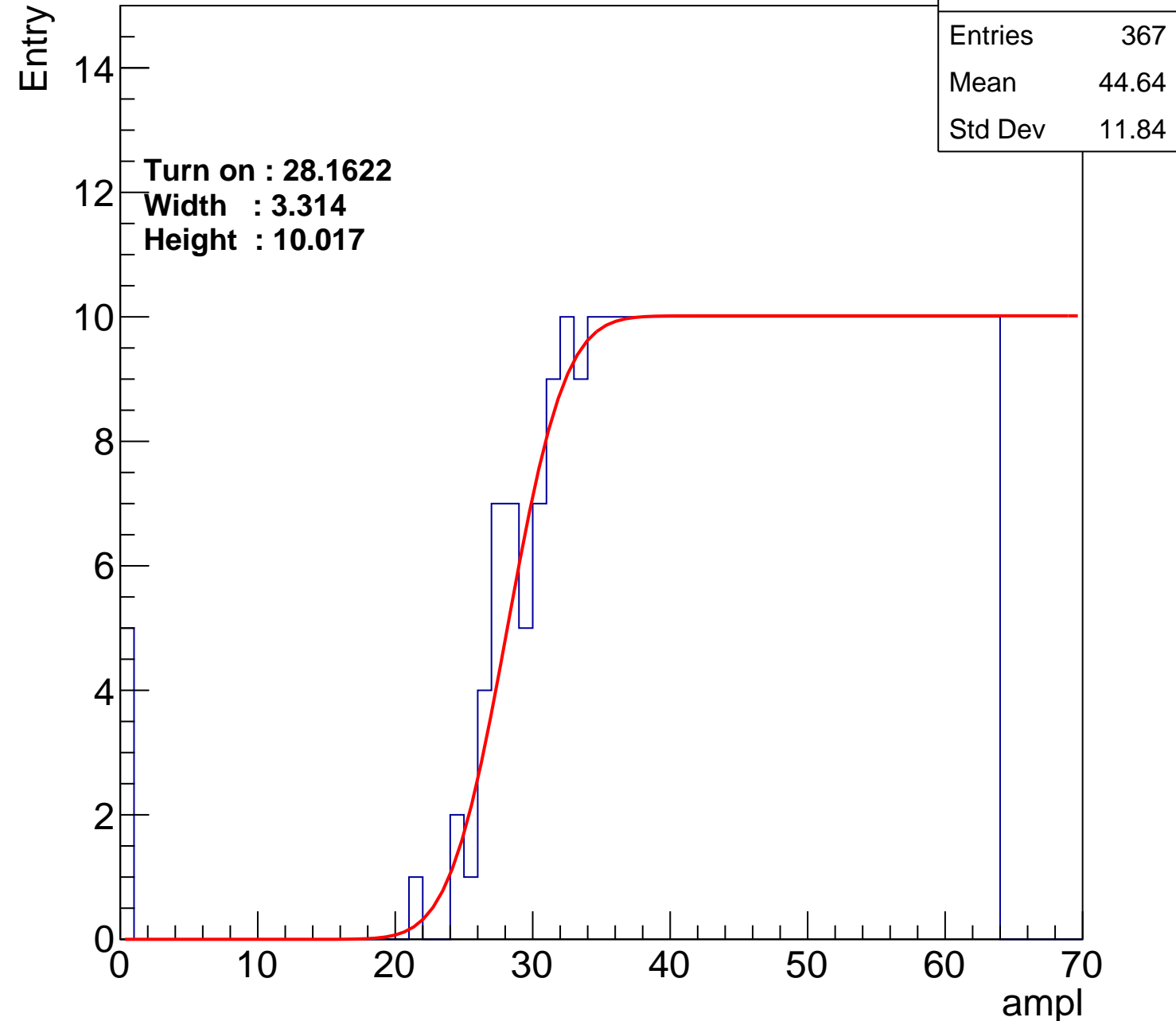
Width : 3.314

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.4
Std Dev	12.09

Turn on : 27.8285

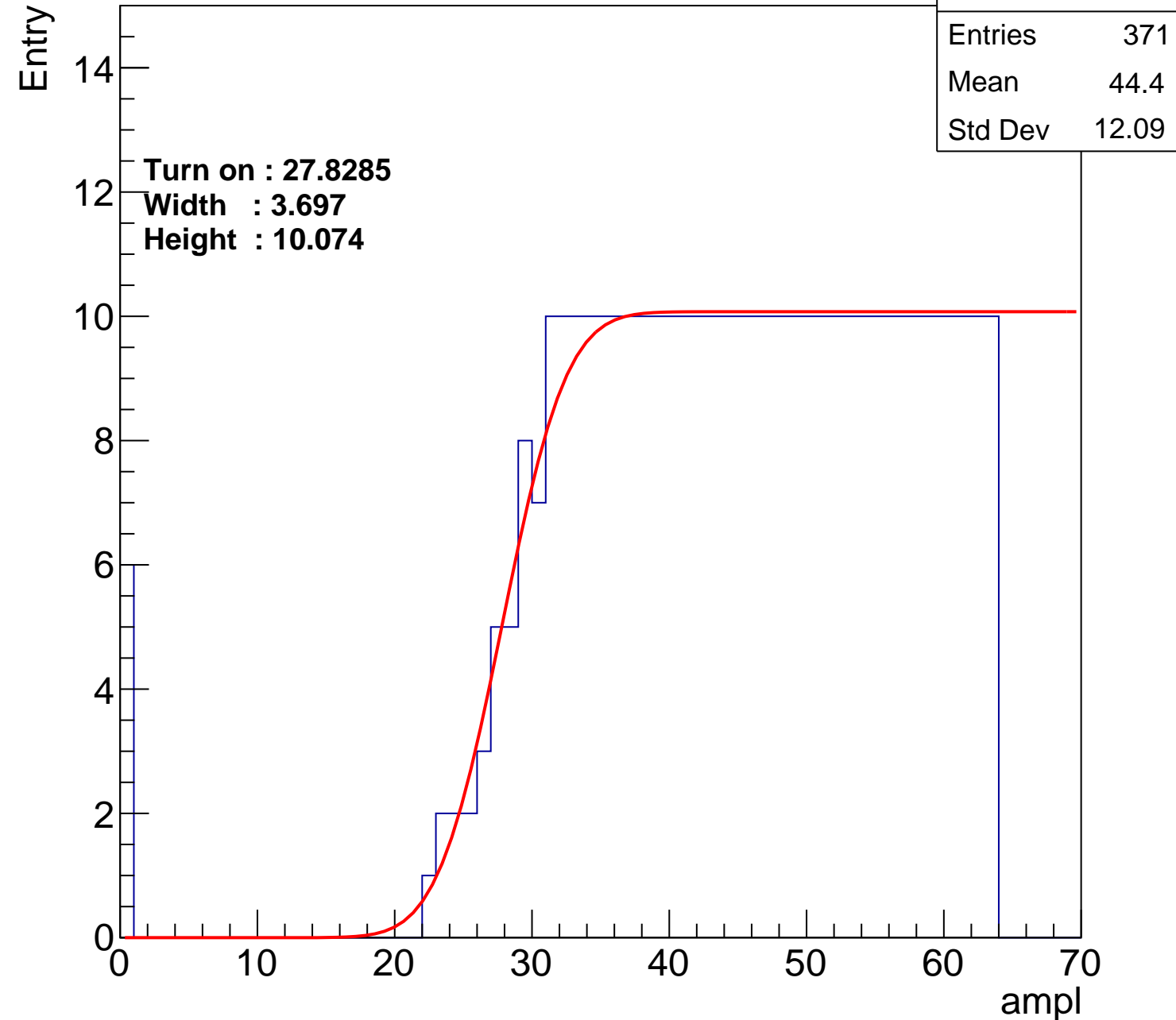
Width : 3.697

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch126

calib_packv5_042523_0143.root, FC#8, port C1

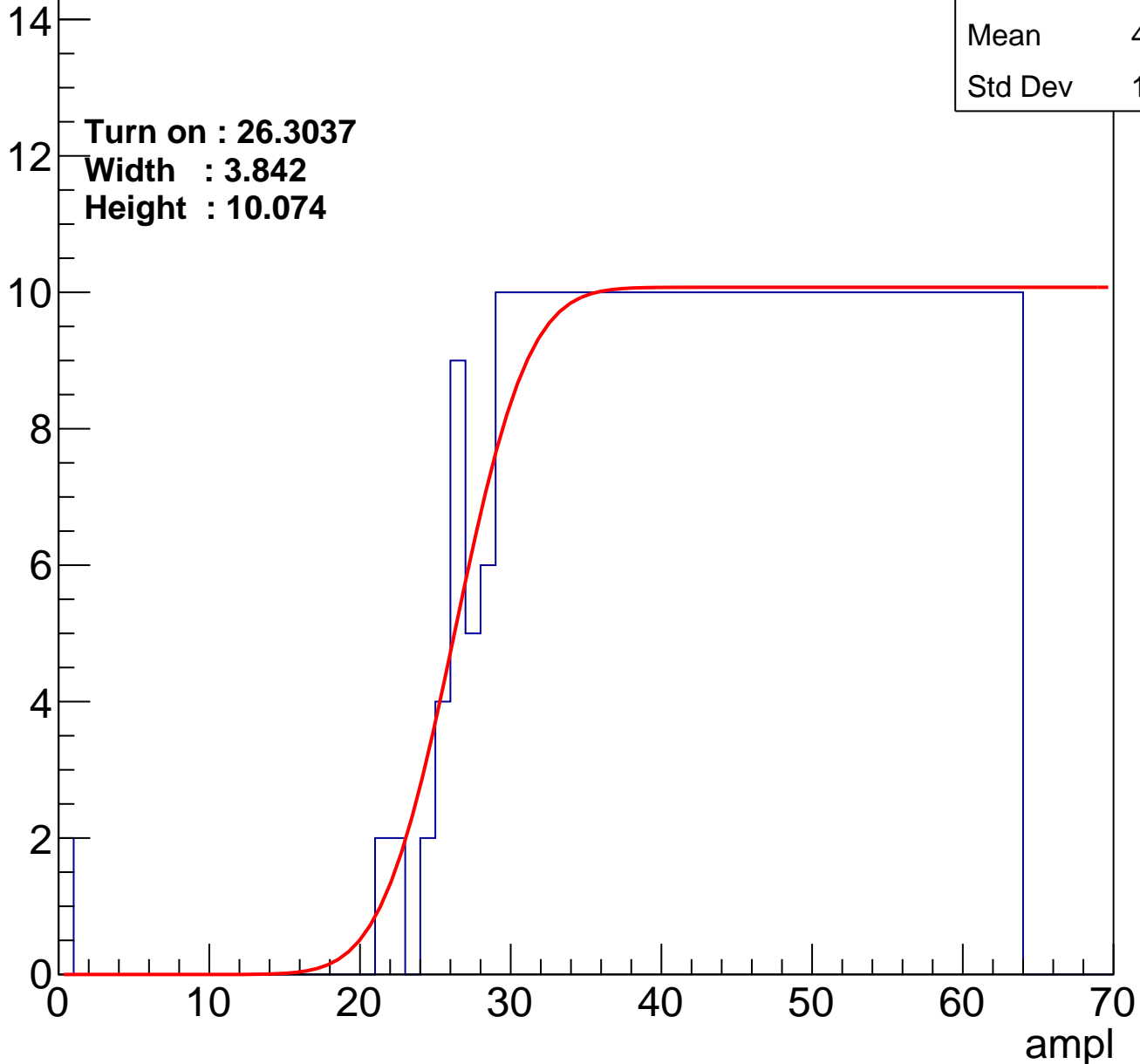
Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.3037

Width : 3.842

Height : 10.074

Entry



B0L002S, U16-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.2
Std Dev	11.28

Turn on : 29.1548

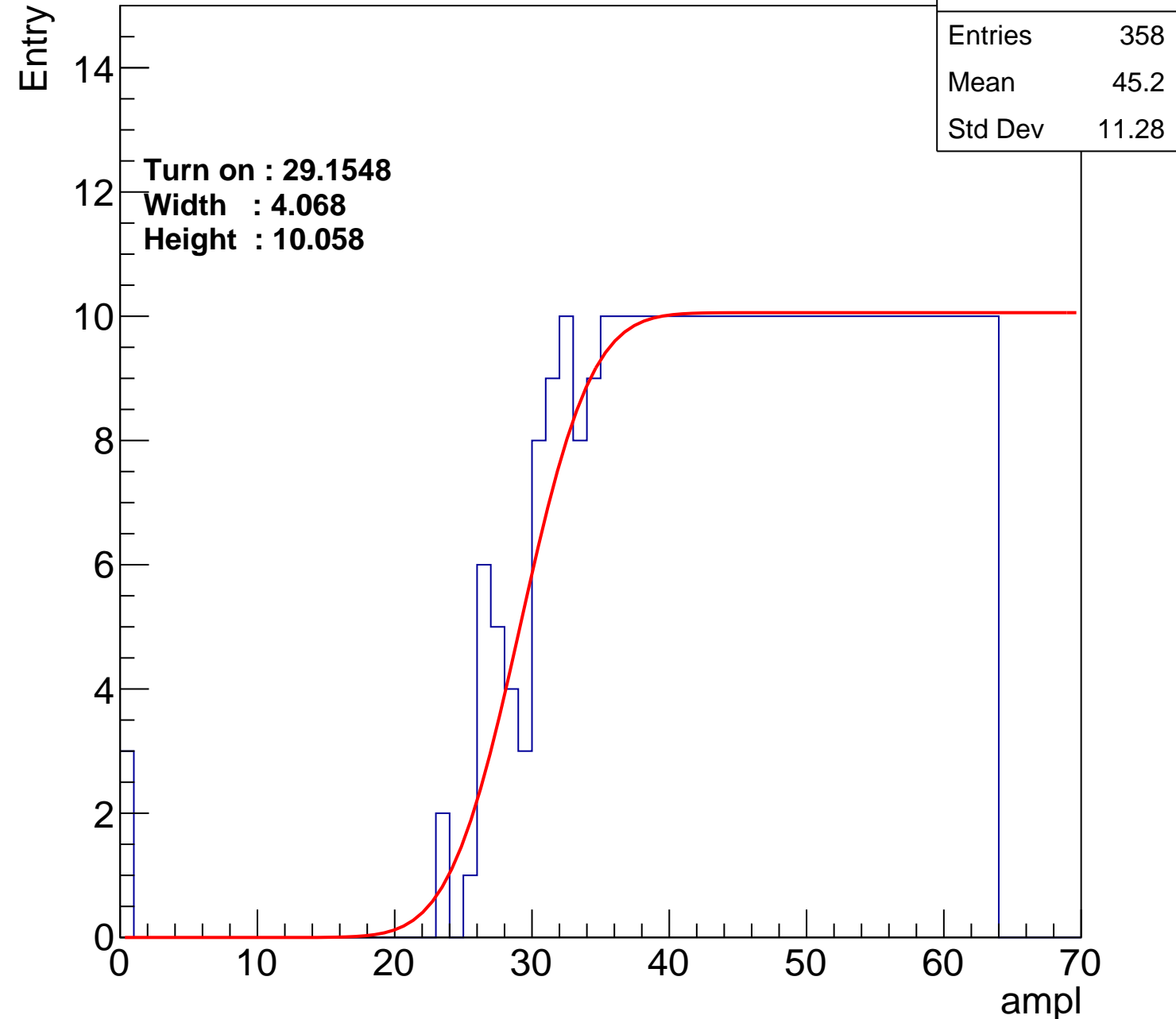
Width : 4.068

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U16-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.2
Std Dev	11.28

Turn on : 29.1548

Width : 4.068

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl

