

B1L103S, U26-ch0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.37
Std Dev	12.16

Turn on : 25.3039

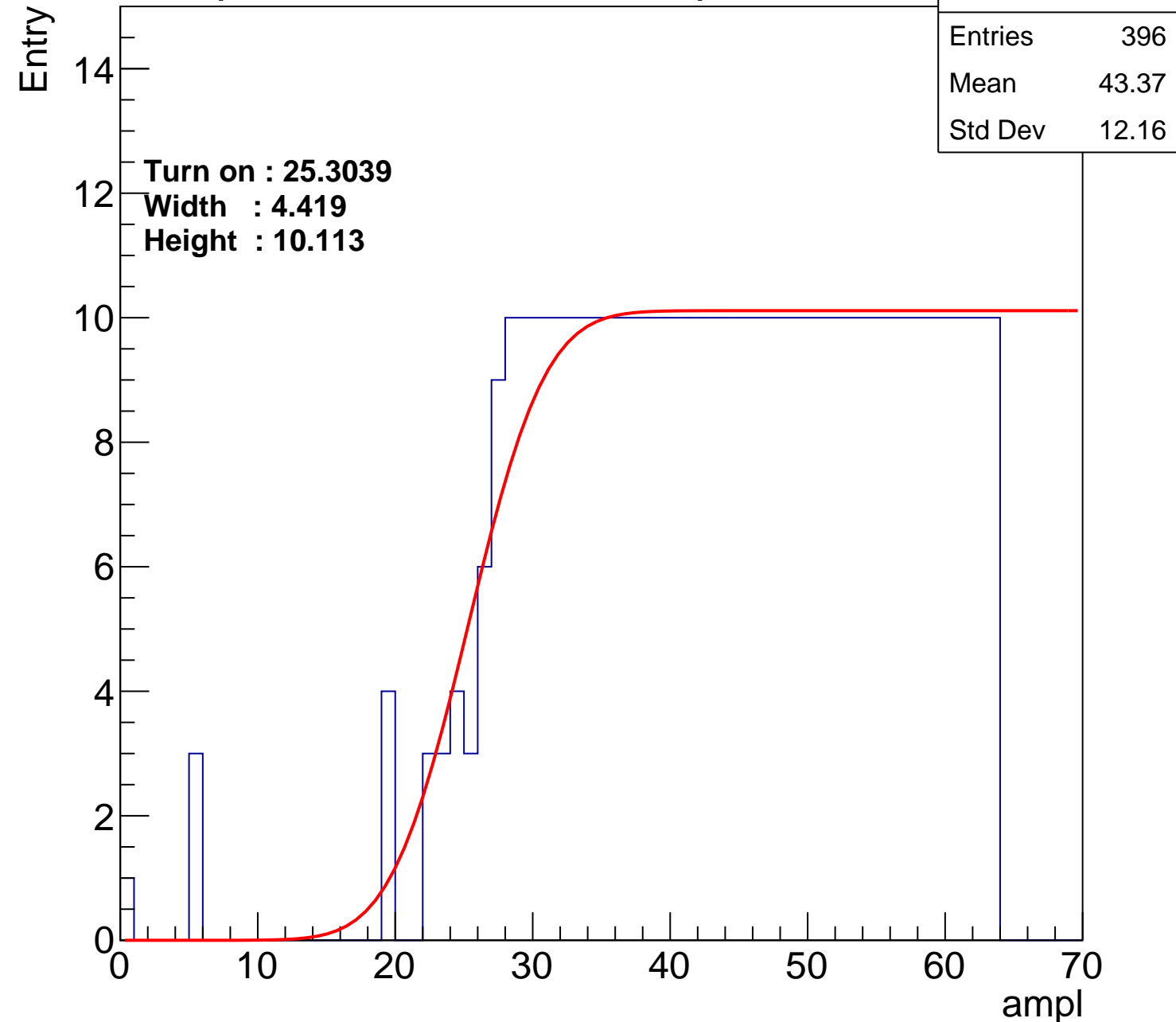
Width : 4.419

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch1

calib_packv5_042523_0143.root, FC#7, port C2

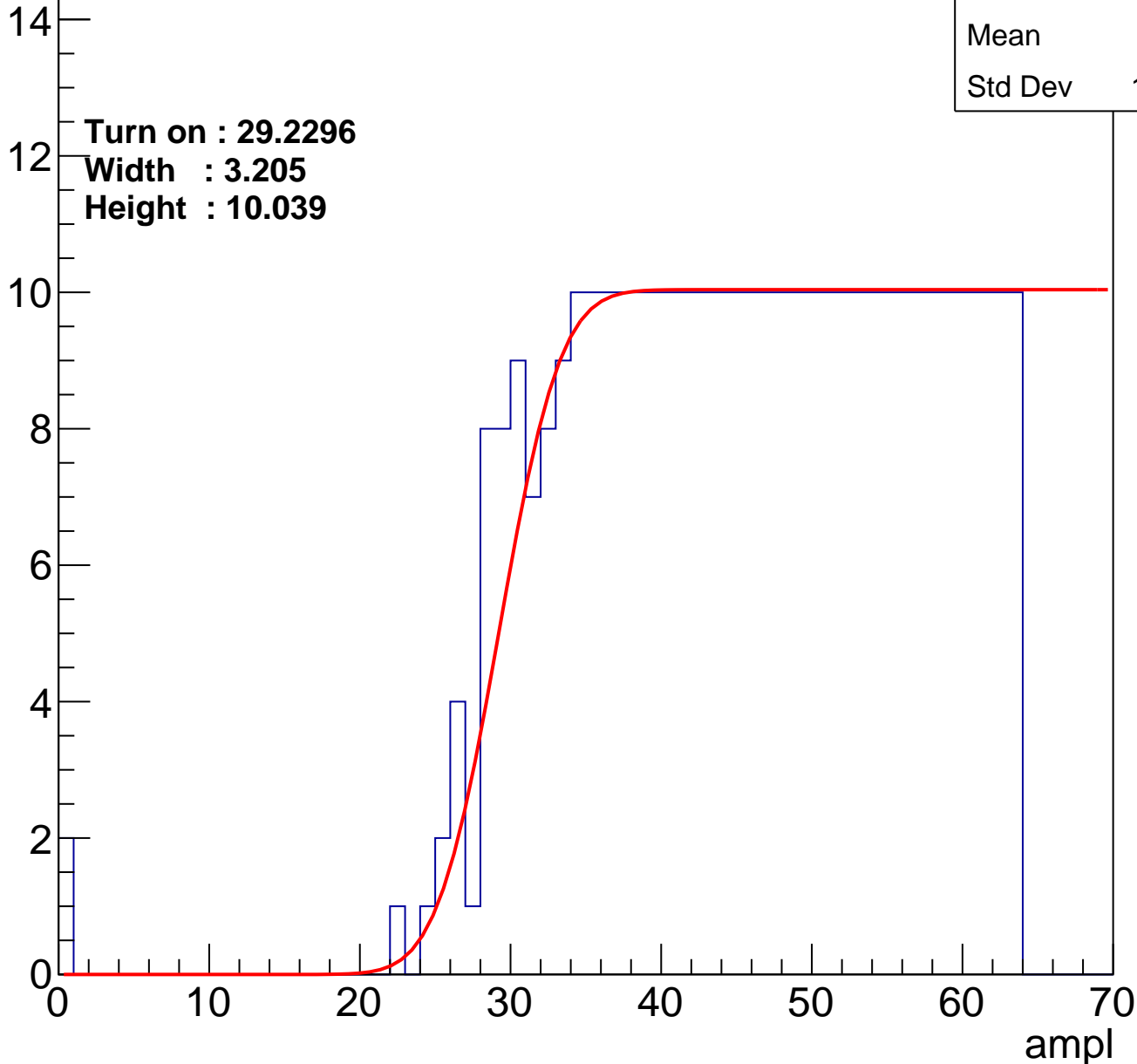
Entries	360
Mean	45.2
Std Dev	11.07

Turn on : 29.2296

Width : 3.205

Height : 10.039

Entry



B1L103S, U26-ch2

calib_packv5_042523_0143.root, FC#7, port C2

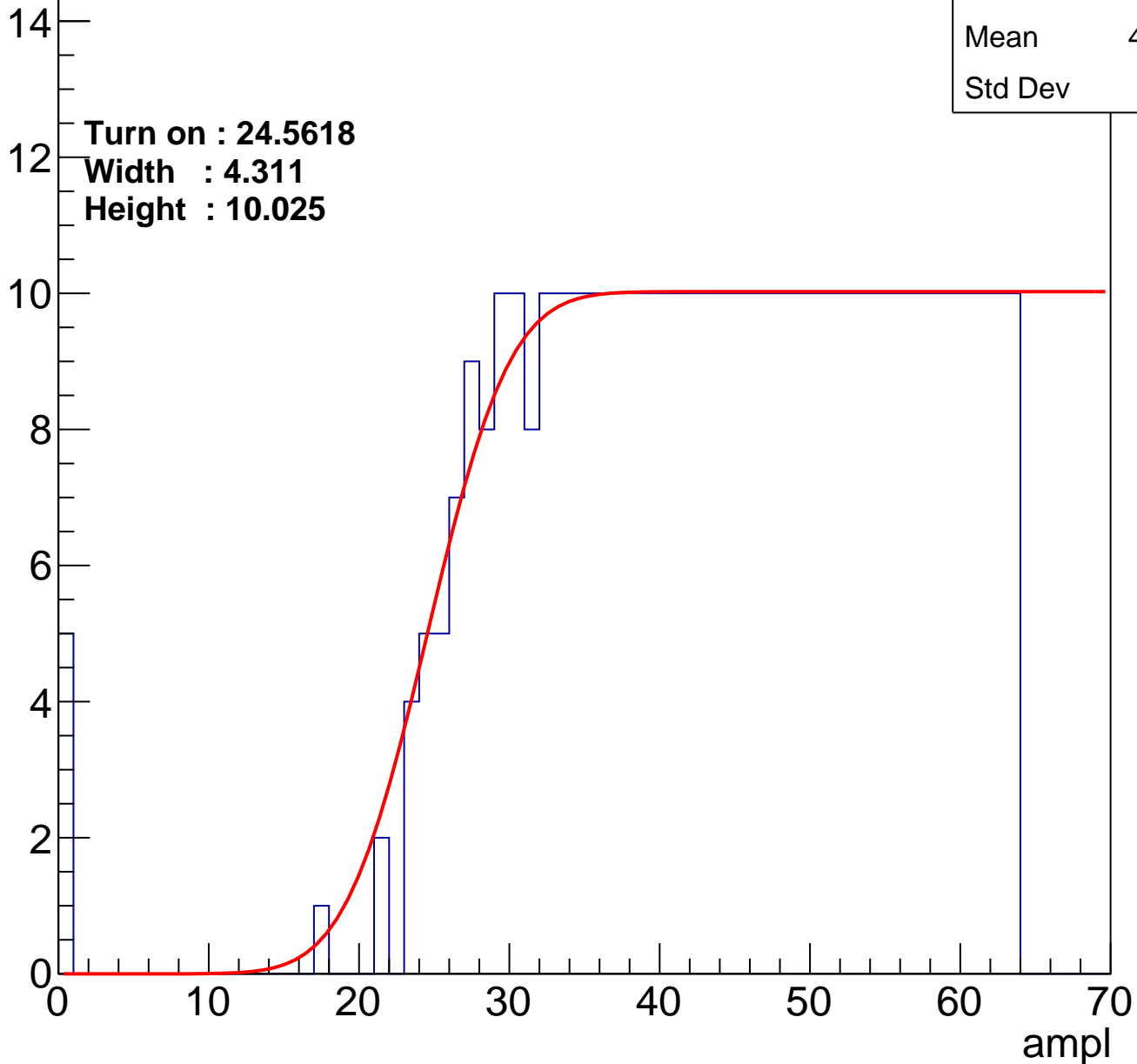
Entries	394
Mean	43.36
Std Dev	12.4

Turn on : 24.5618

Width : 4.311

Height : 10.025

Entry



B1L103S, U26-ch3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.34
Std Dev	11.77

Turn on : 27.2027

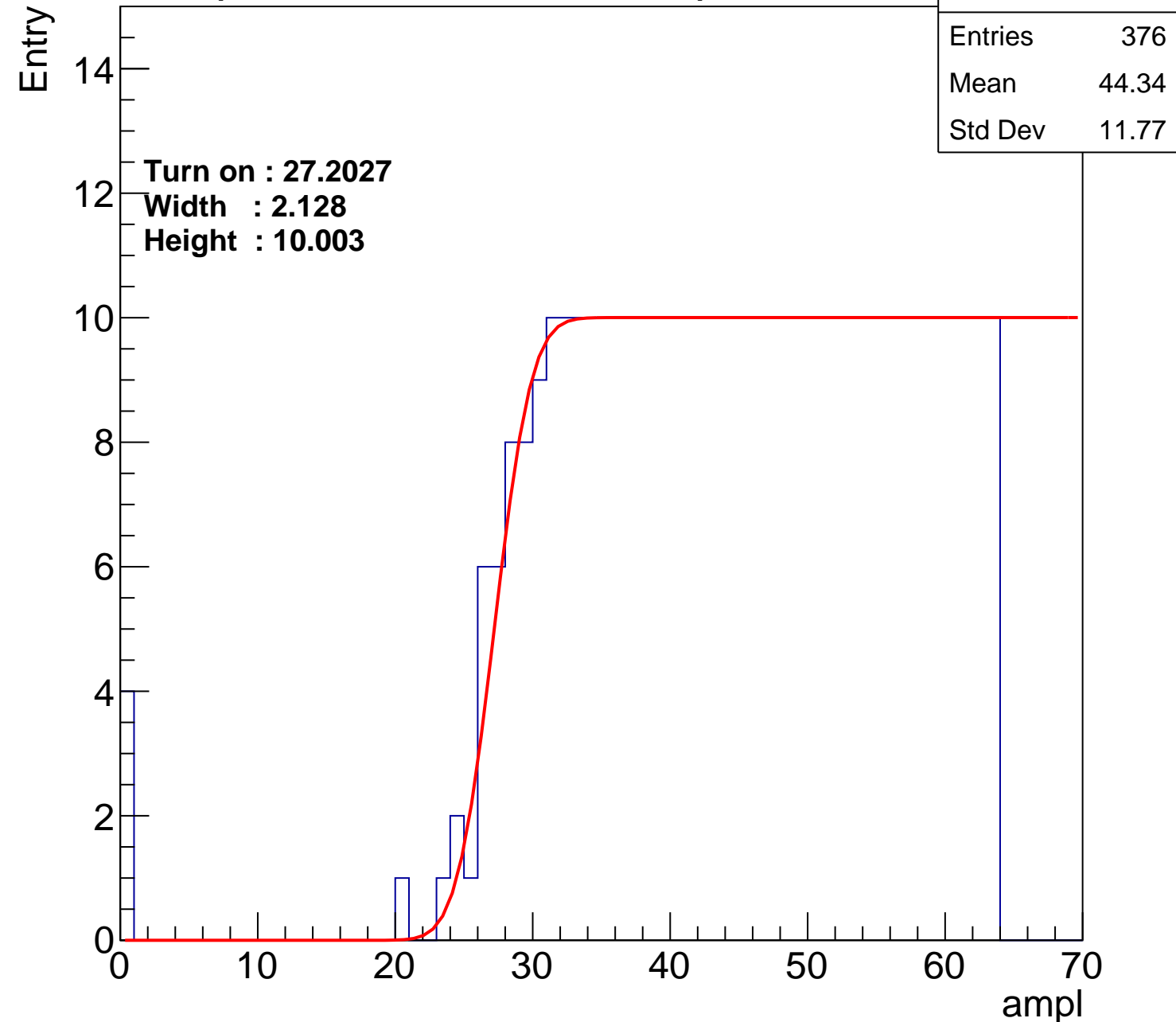
Width : 2.128

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.53
Std Dev	11.5

Turn on : 27.3818

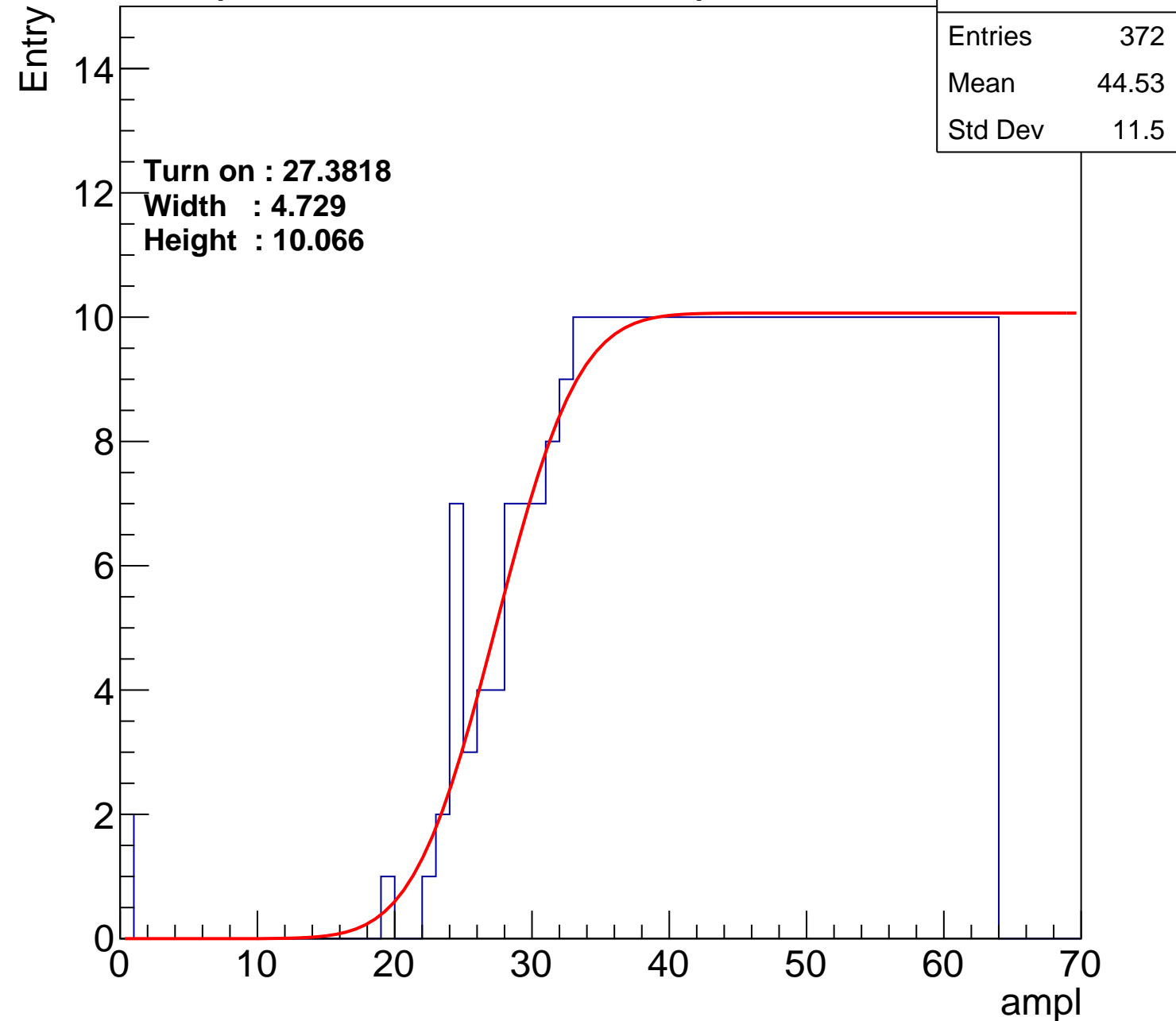
Width : 4.729

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.35
Std Dev	11.48

Turn on : 26.6274

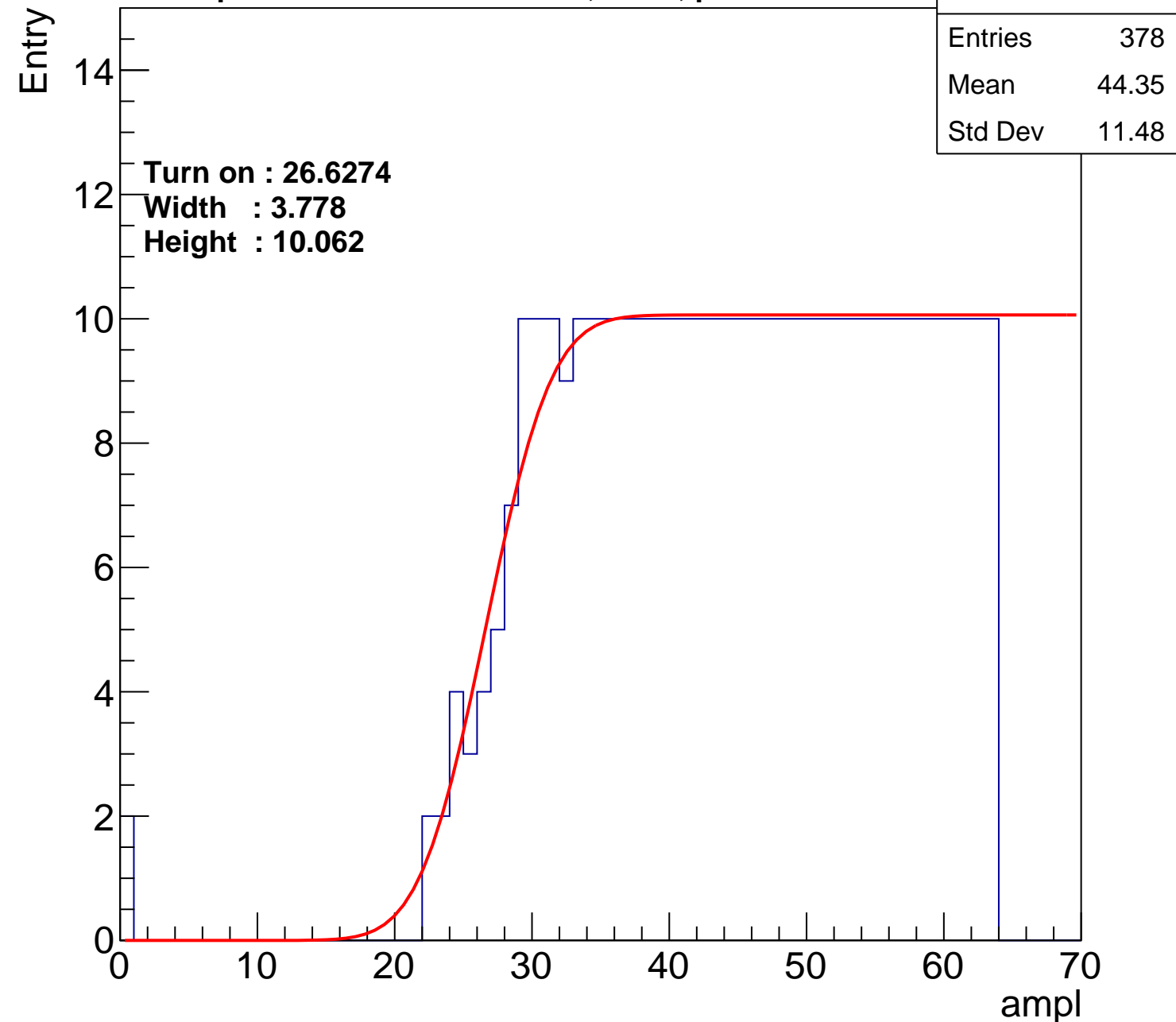
Width : 3.778

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.54
Std Dev	11.71

Turn on : 27.6037

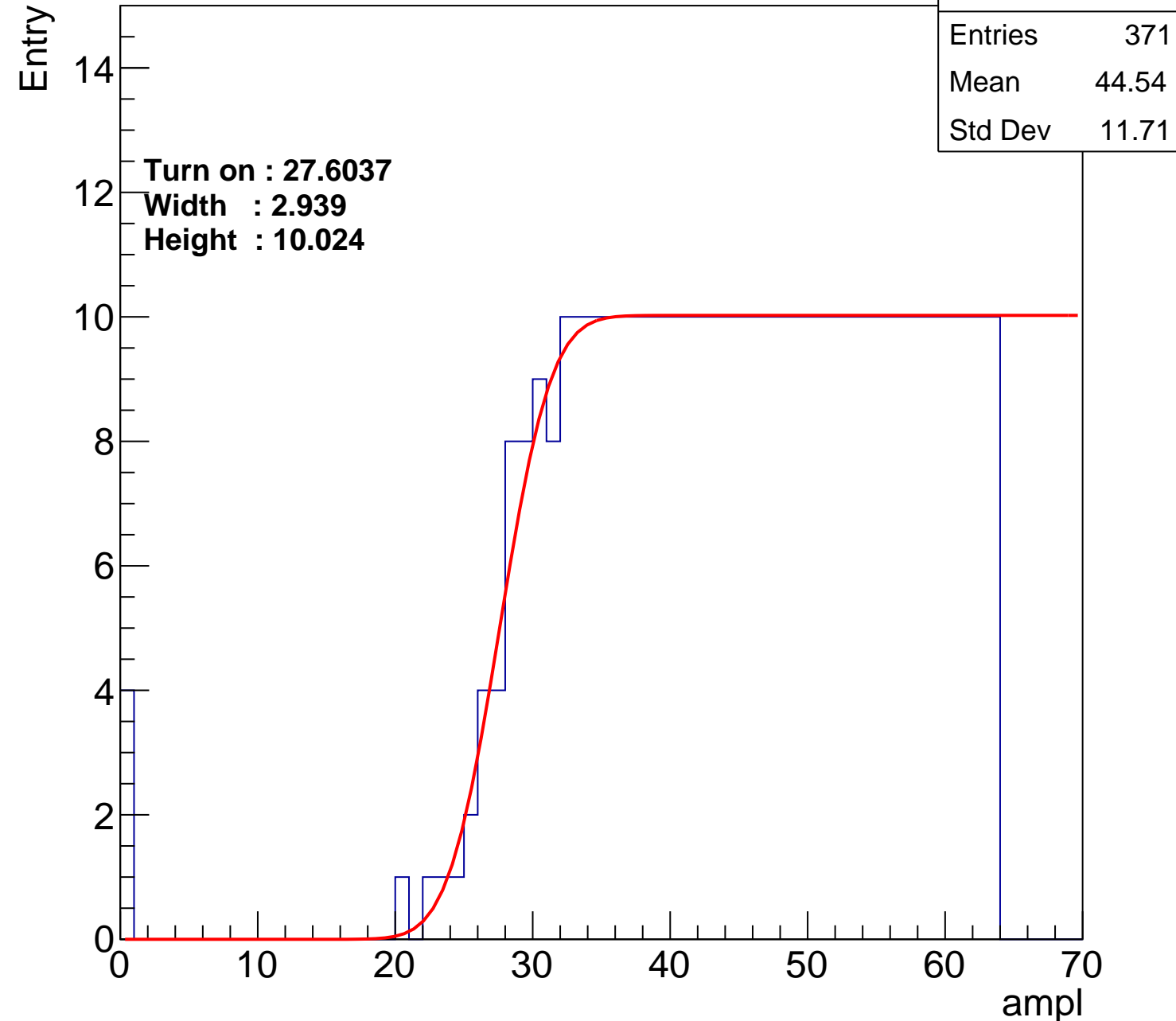
Width : 2.939

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch7

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.95
Std Dev	11.03

Turn on : 27.5705

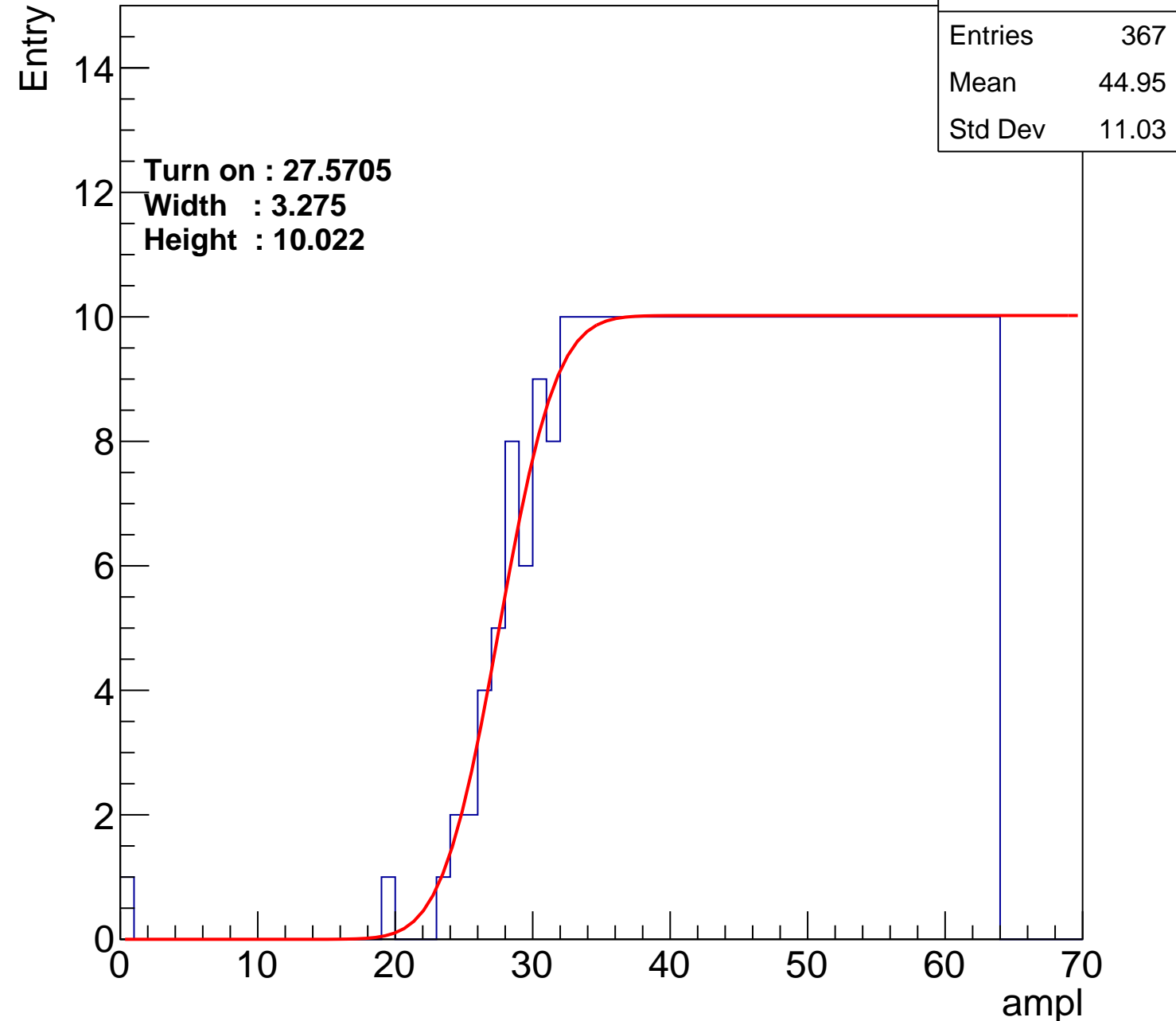
Width : 3.275

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch8

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.18
Std Dev	11.47

Turn on : 25.3739

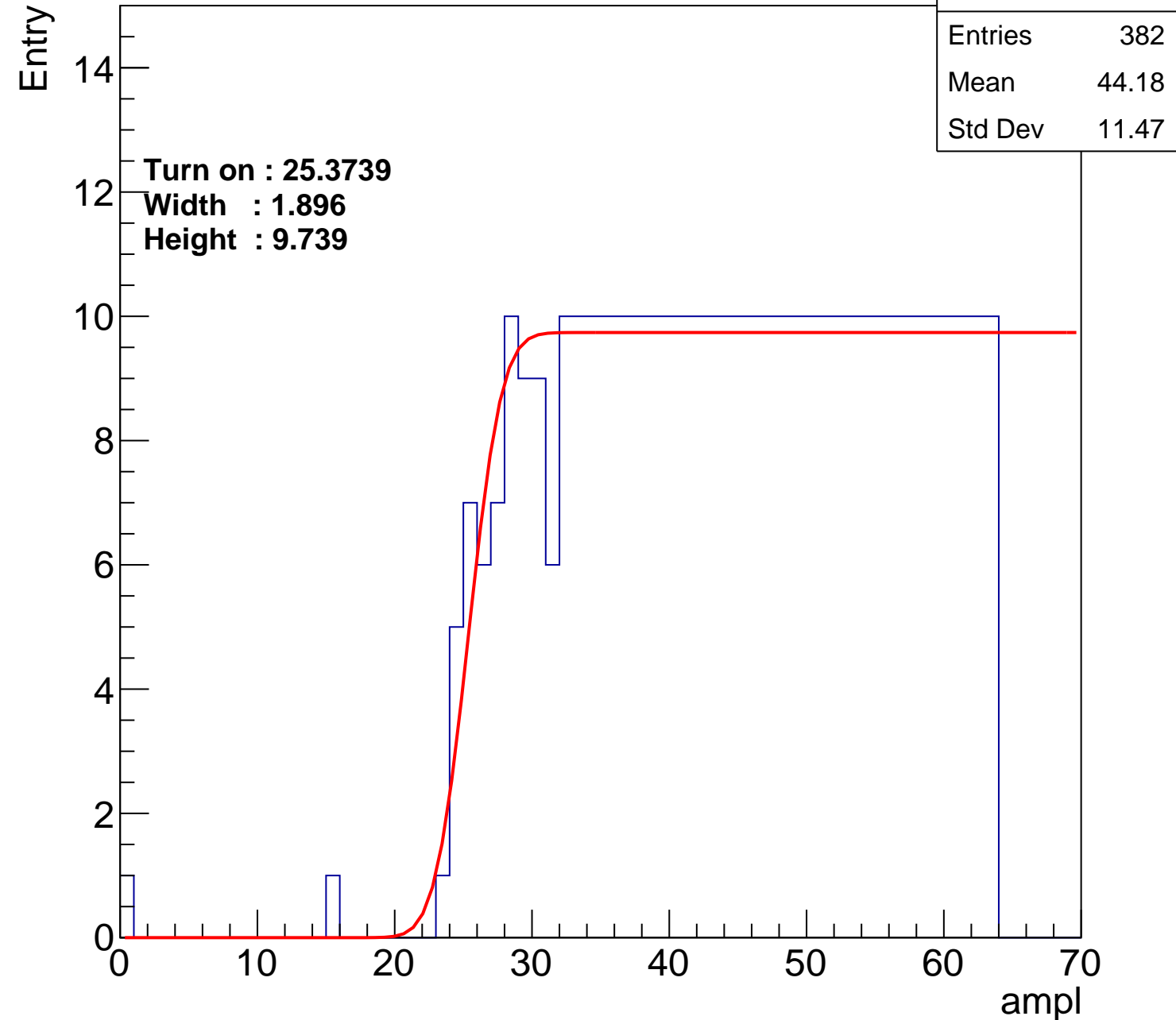
Width : 1.896

Height : 9.739

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	399
Mean	43.13
Std Dev	12.49

Turn on : 24.6634

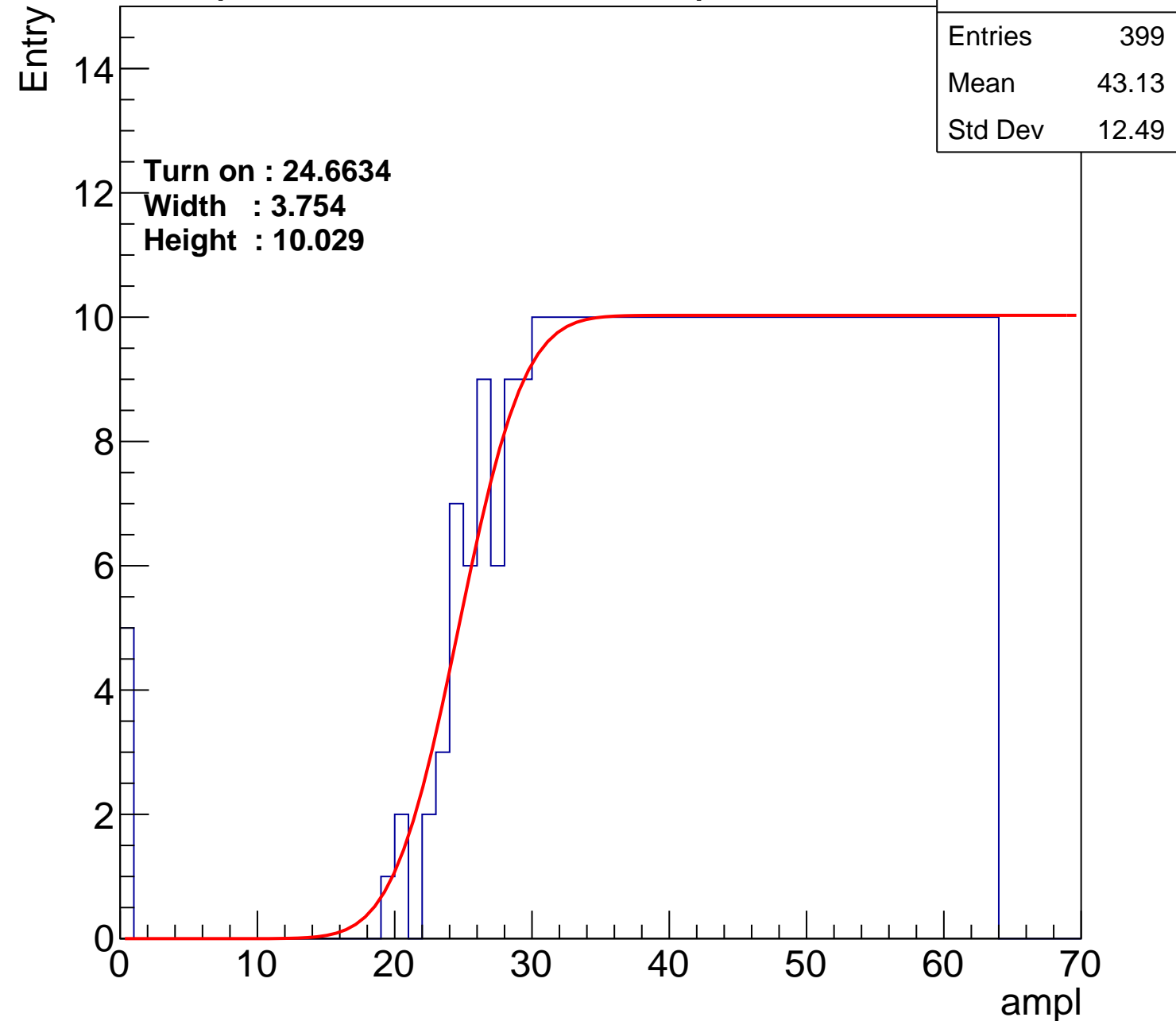
Width : 3.754

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch10

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.57
Std Dev	11.2

Turn on : 26.5149

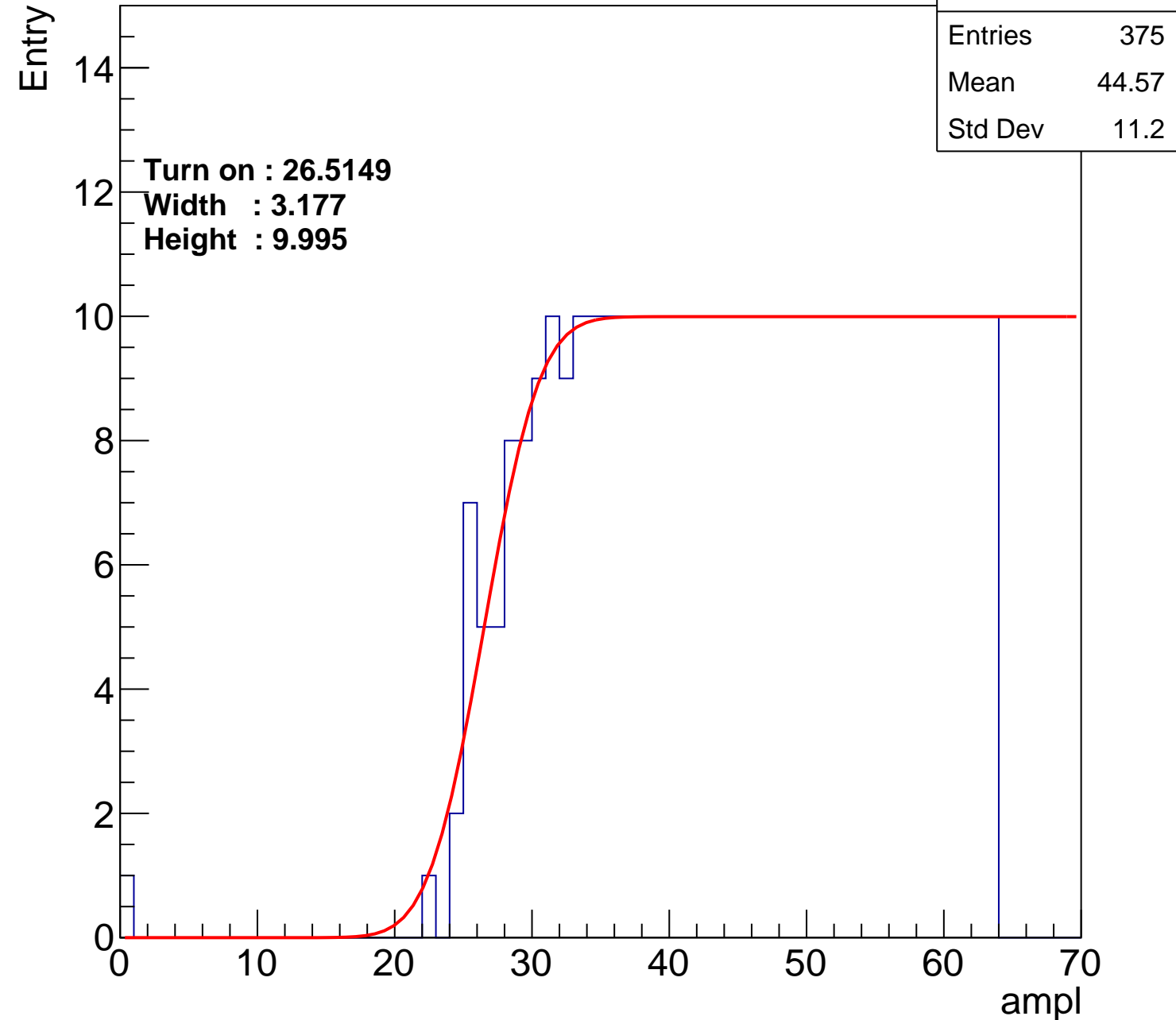
Width : 3.177

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch11

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.84
Std Dev	12.25

Turn on : 26.3192

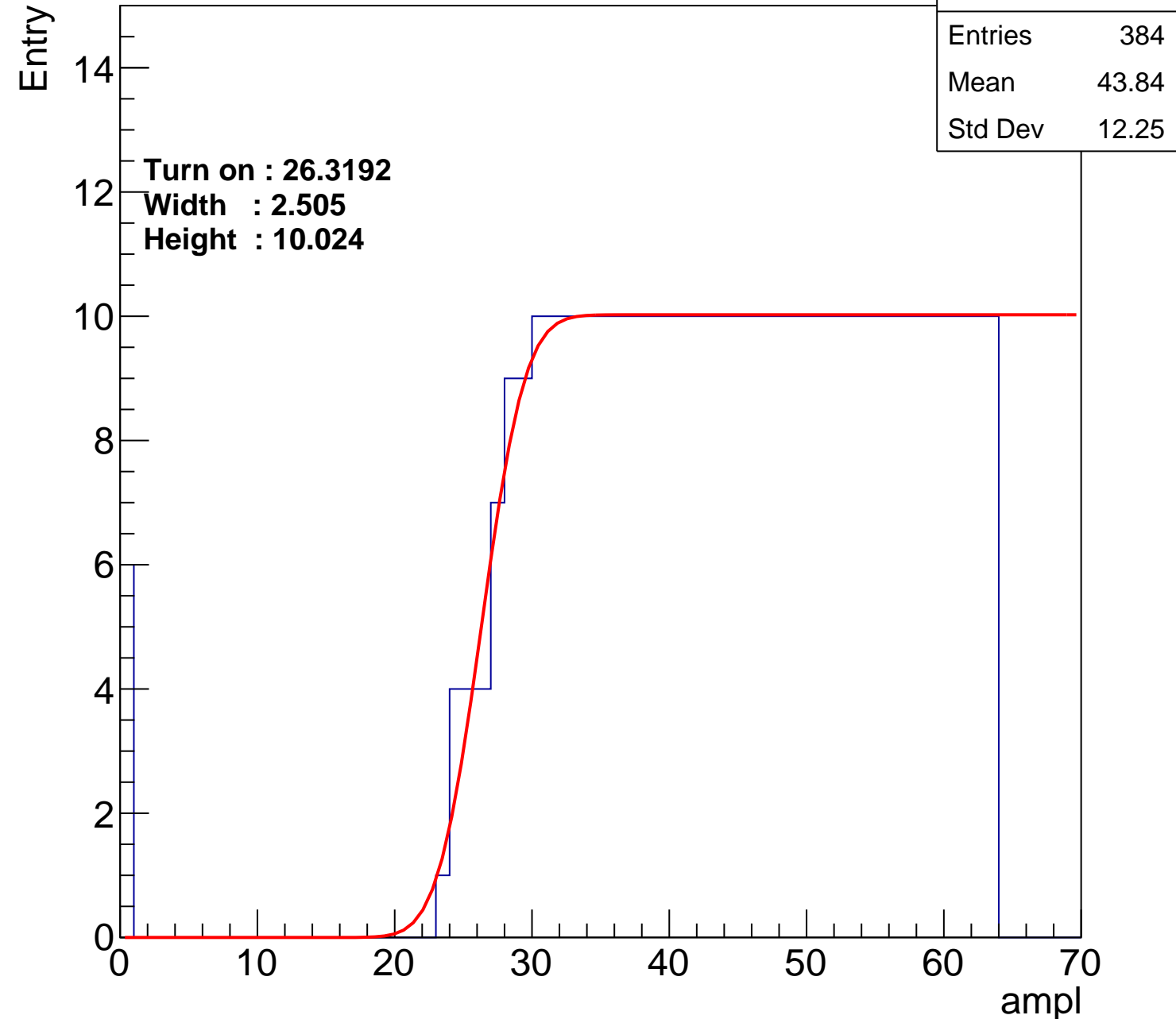
Width : 2.505

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch12

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	43.78
Std Dev	12.22

Turn on : 26.3561

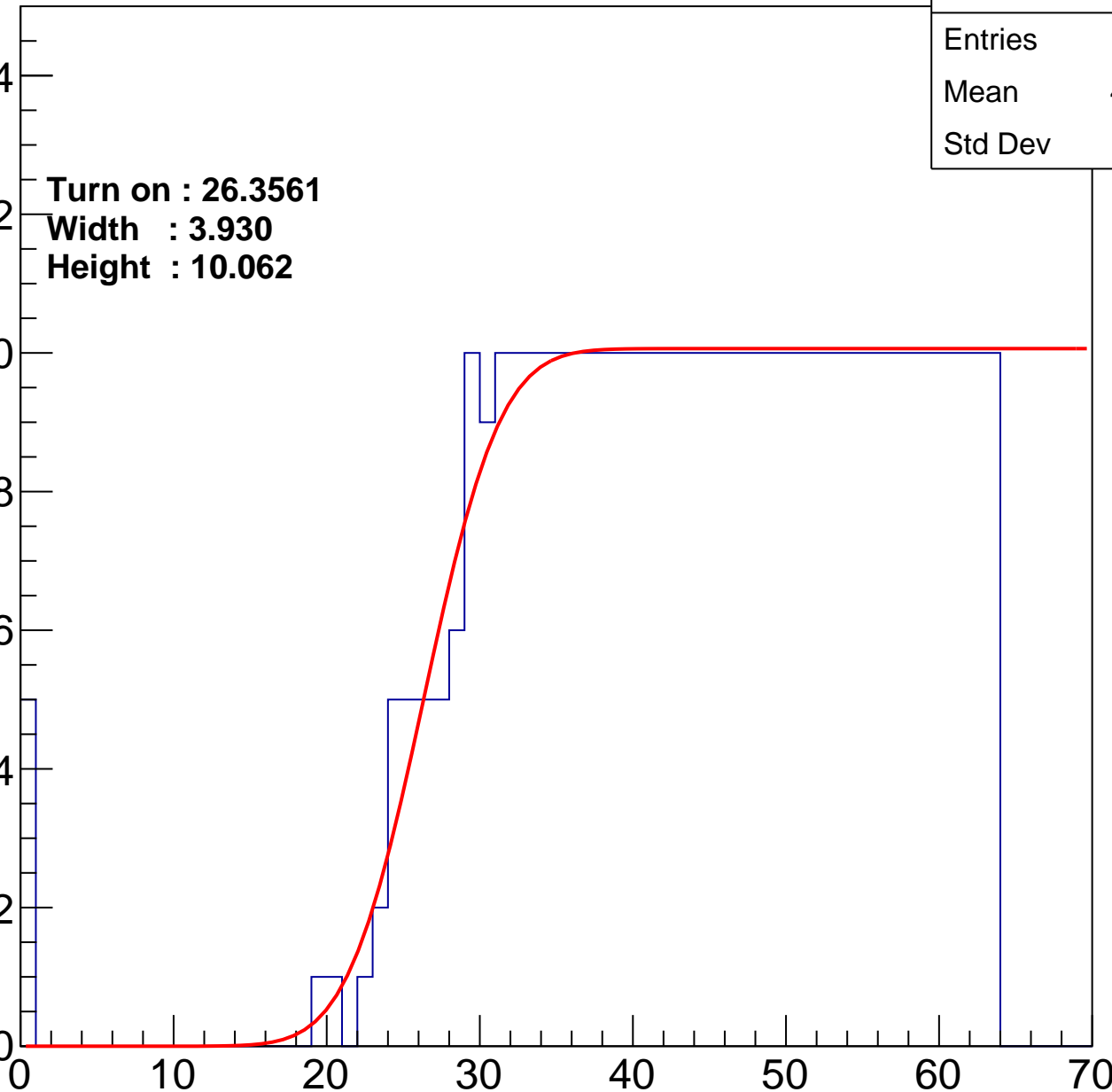
Width : 3.930

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch13

calib_packv5_042523_0143.root, FC#7, port C2

Entries	353
Mean	45.58
Std Dev	10.84

Turn on : 29.5925

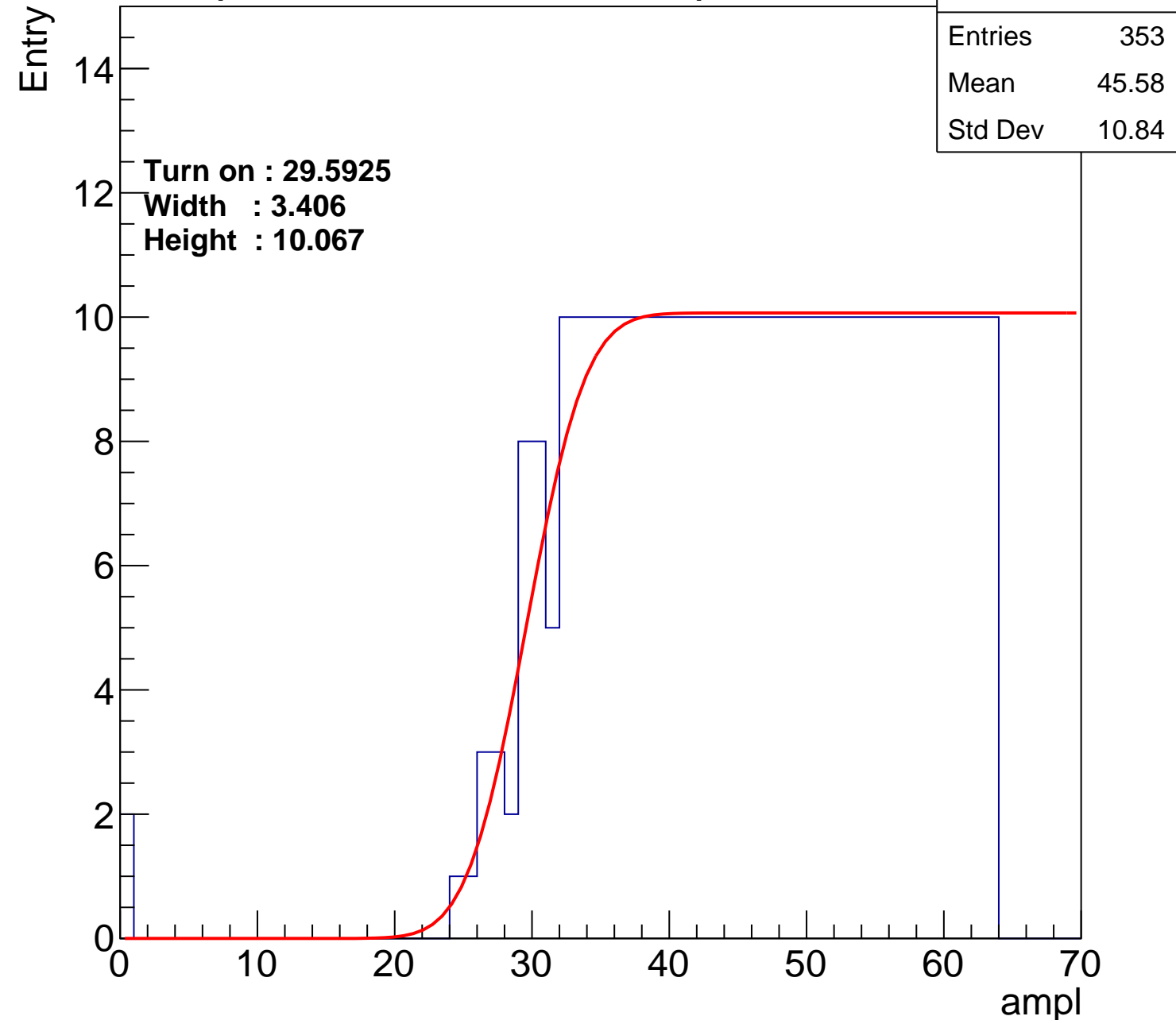
Width : 3.406

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch14

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.11
Std Dev	11.72

Turn on : 25.7548

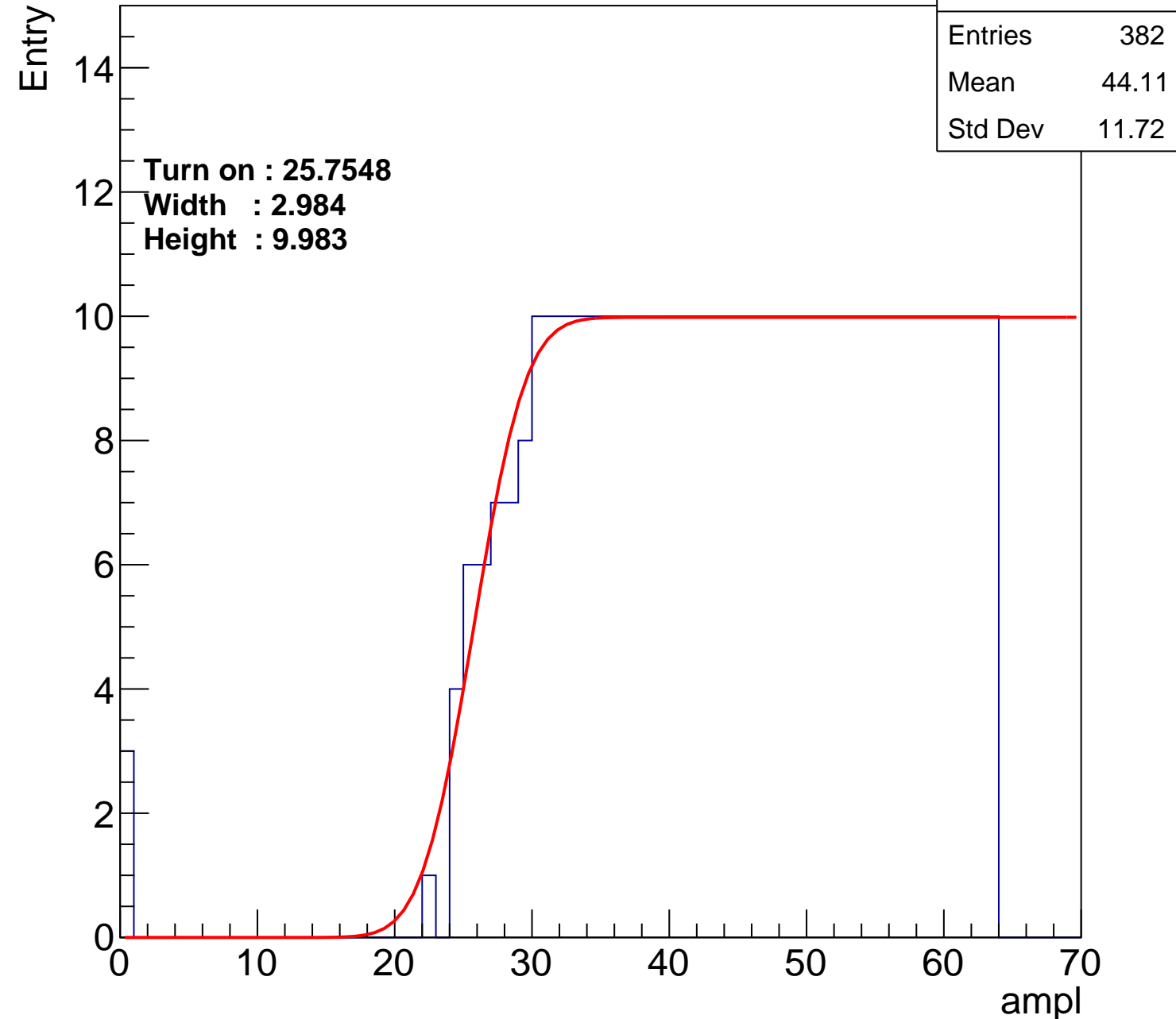
Width : 2.984

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch15

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.25
Std Dev	11.41

Turn on : 26.3204

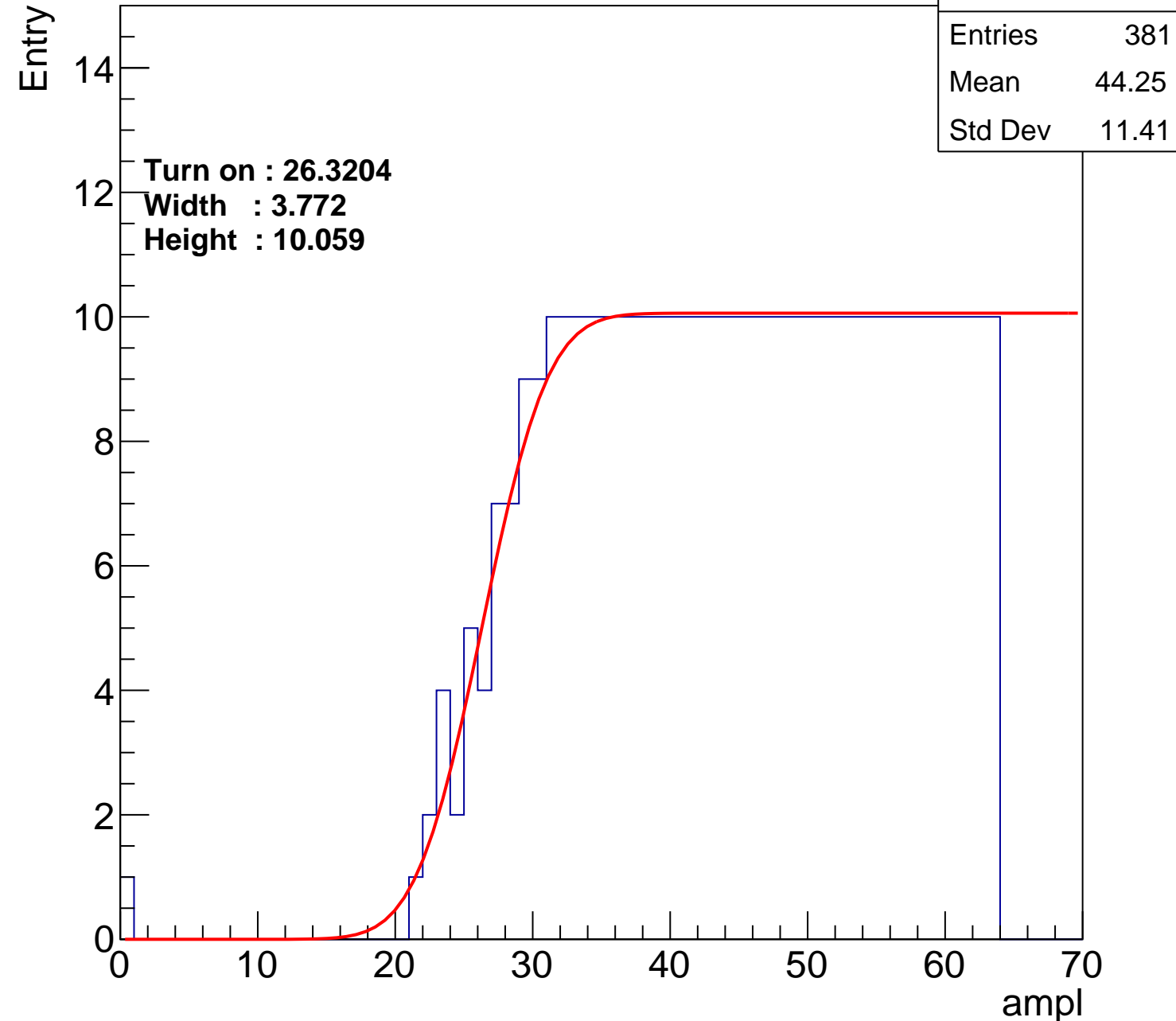
Width : 3.772

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch16

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.03
Std Dev	11.93

Turn on : 26.5501

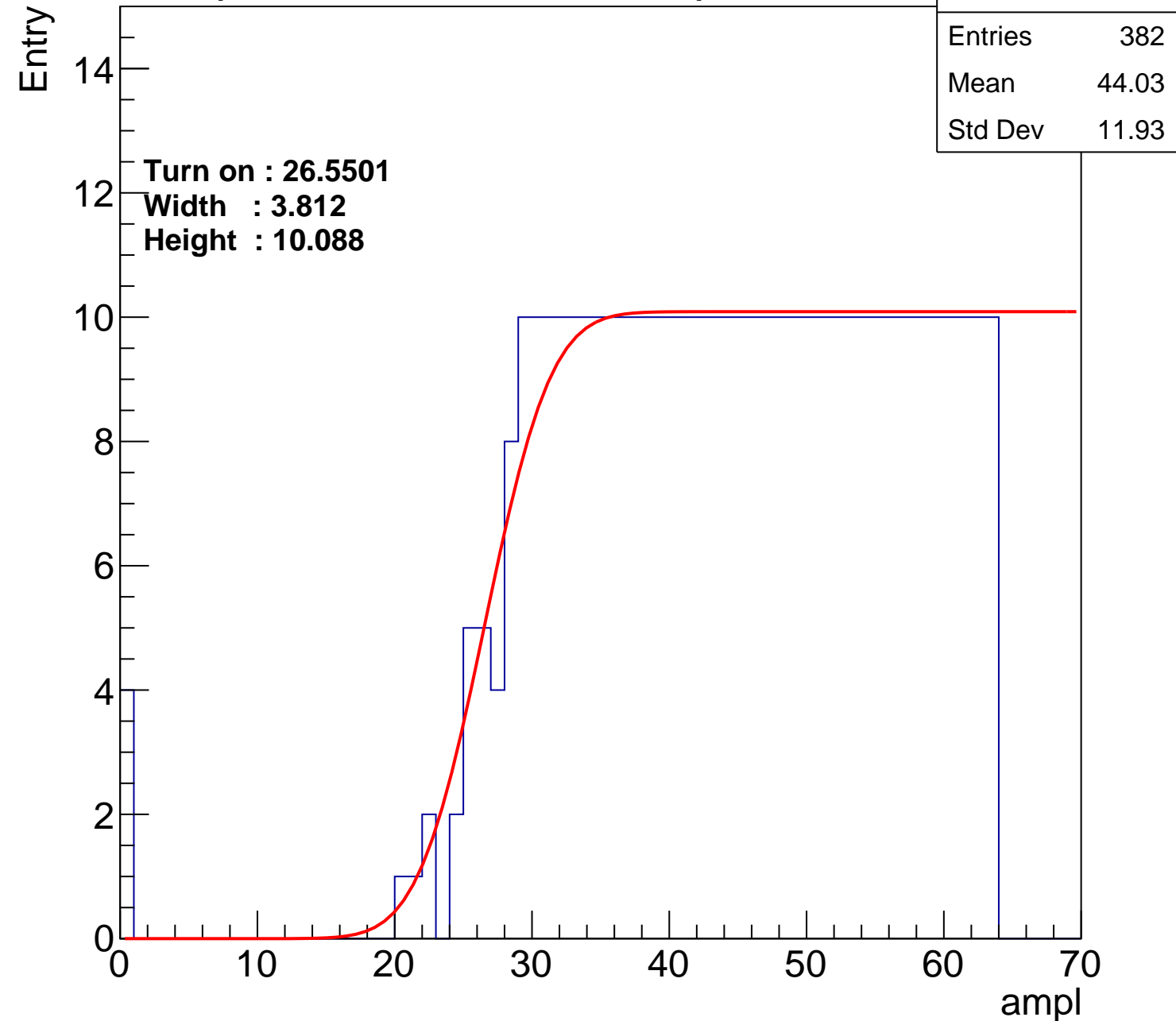
Width : 3.812

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch17

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.81
Std Dev	11.28

Turn on : 27.9641

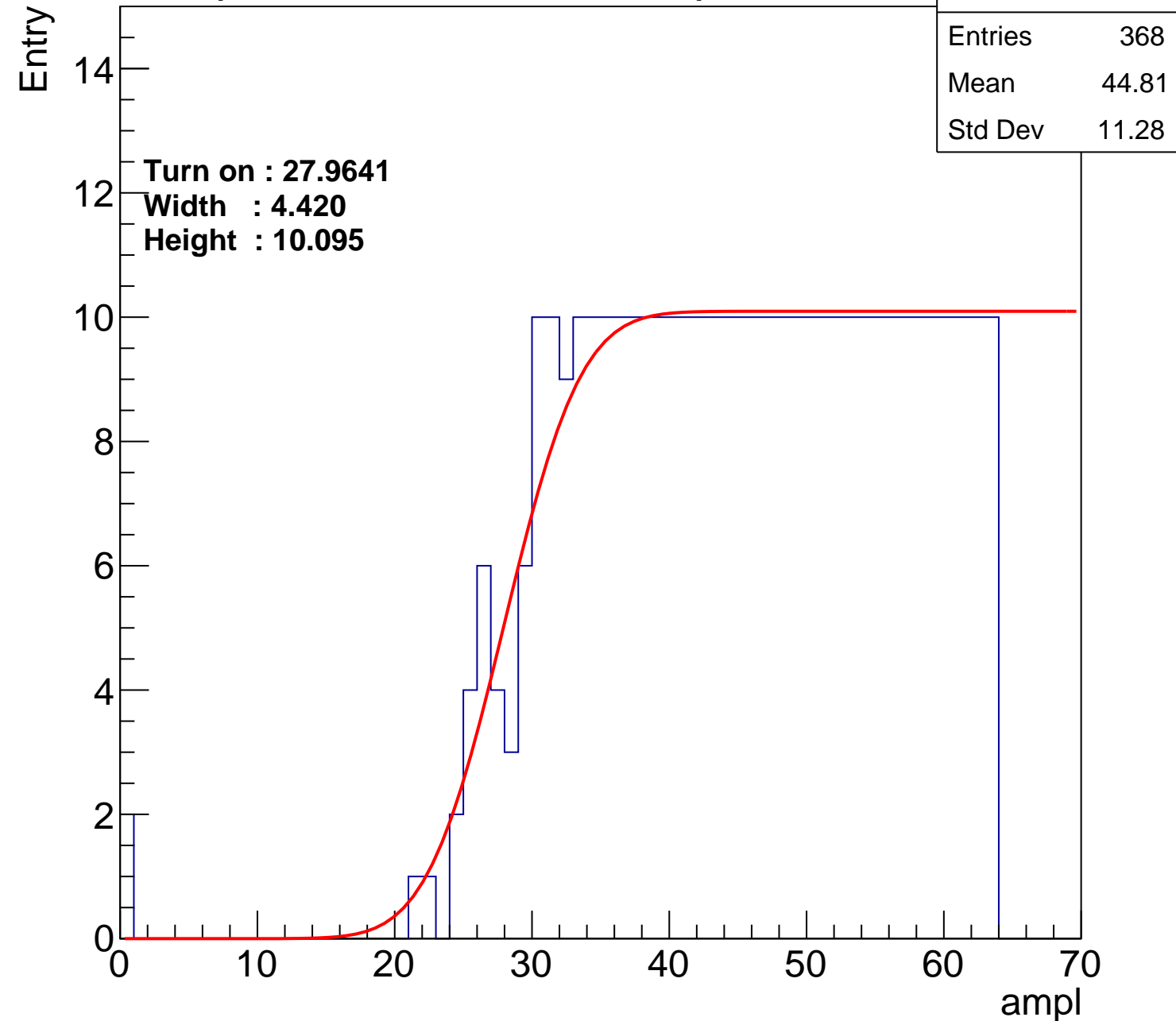
Width : 4.420

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch18

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.83
Std Dev	11.19

Turn on : 27.3801

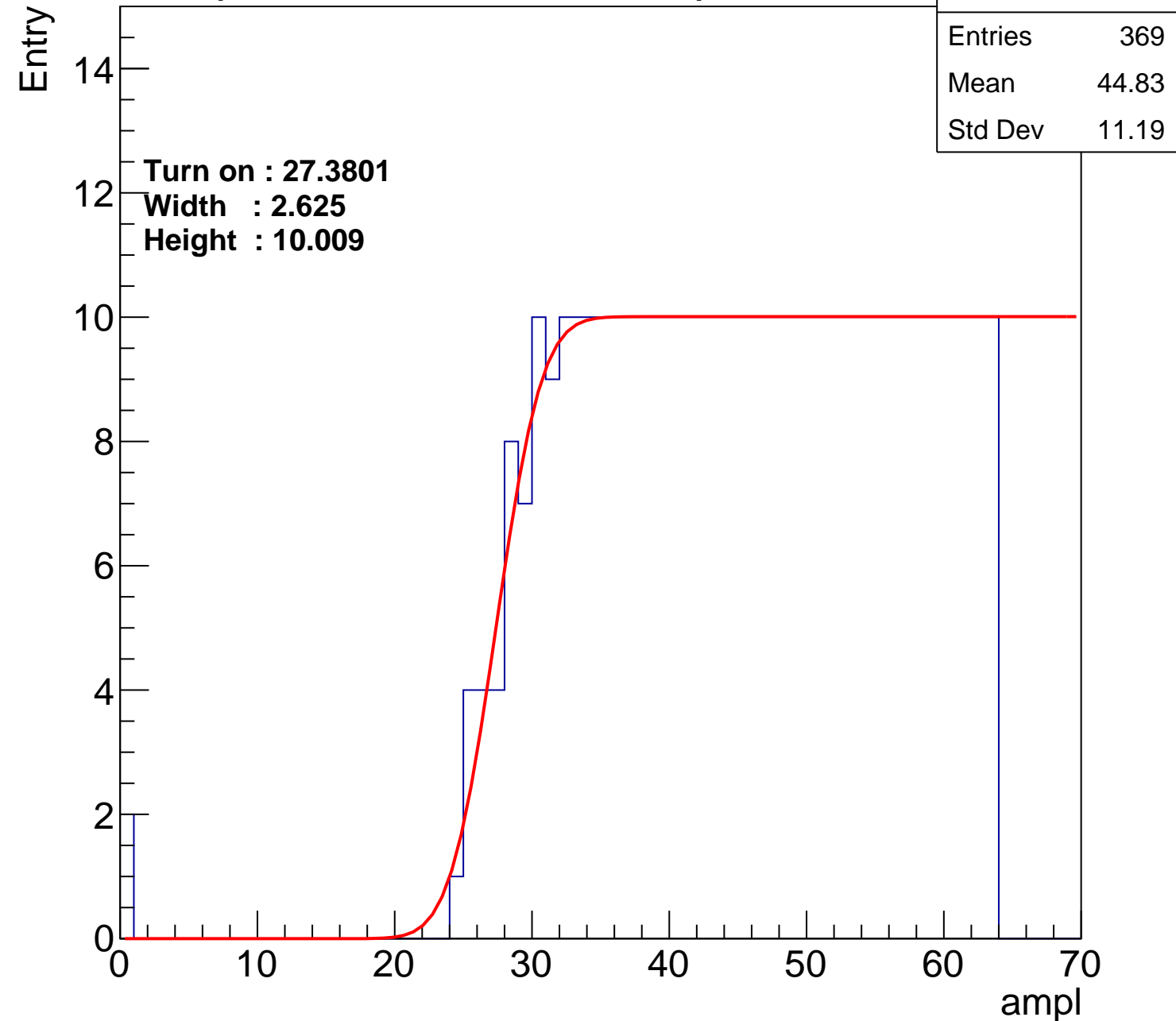
Width : 2.625

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch19

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.52
Std Dev	11.88

Turn on : 28.2659

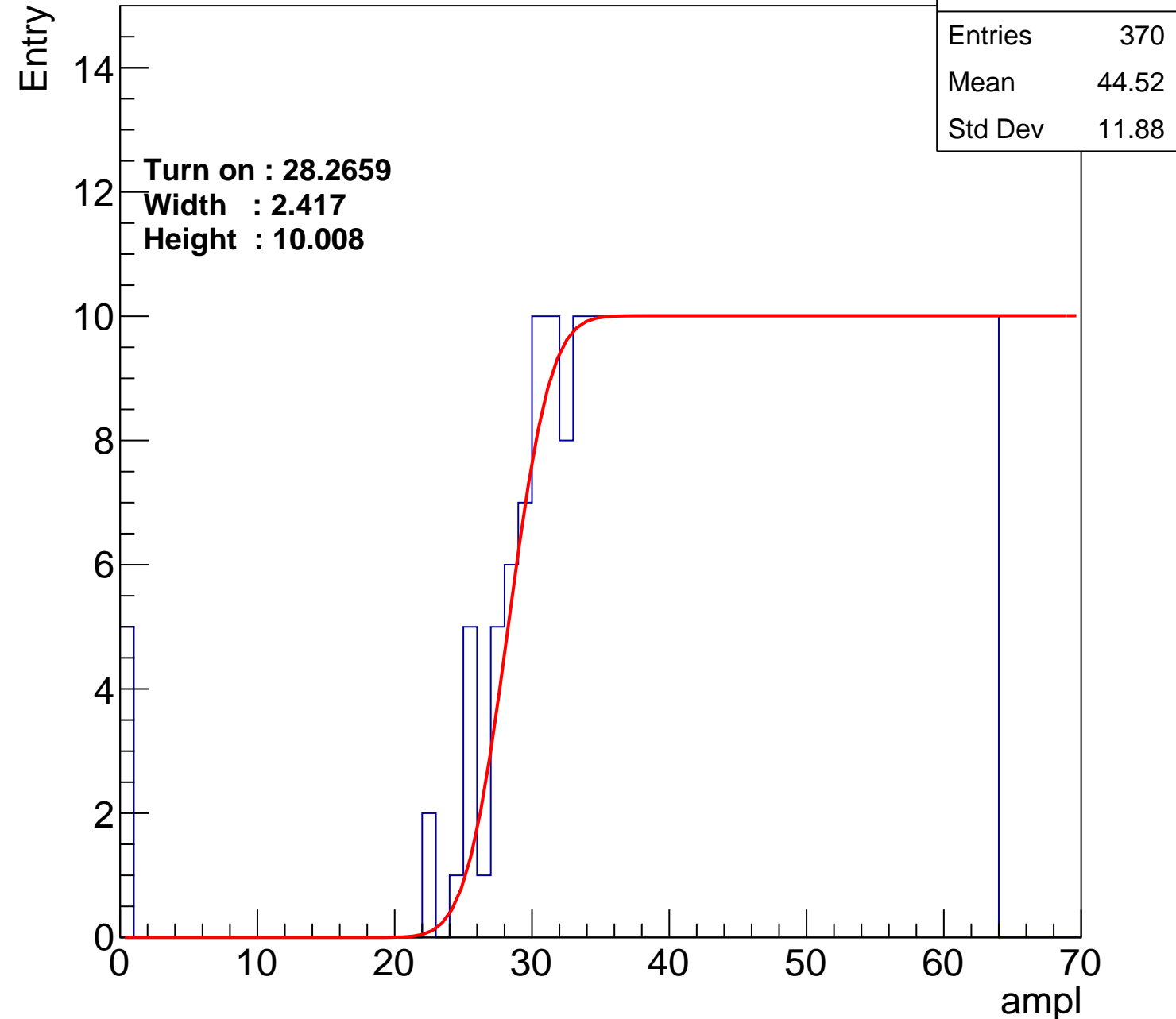
Width : 2.417

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch20

calib_packv5_042523_0143.root, FC#7, port C2

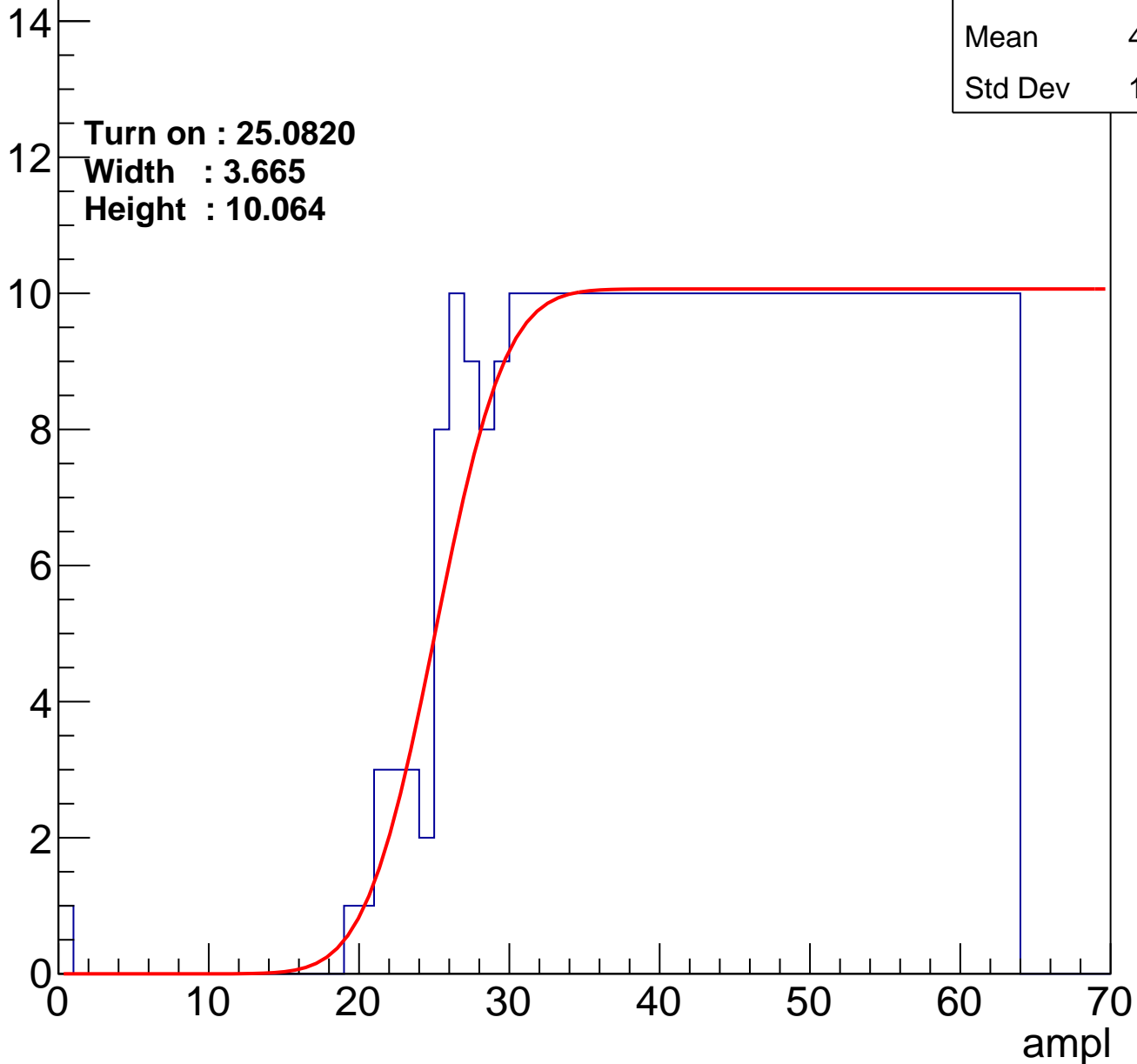
Entries	398
Mean	43.42
Std Dev	11.84

Turn on : 25.0820

Width : 3.665

Height : 10.064

Entry



B1L103S, U26-ch21

calib_packv5_042523_0143.root, FC#7, port C2

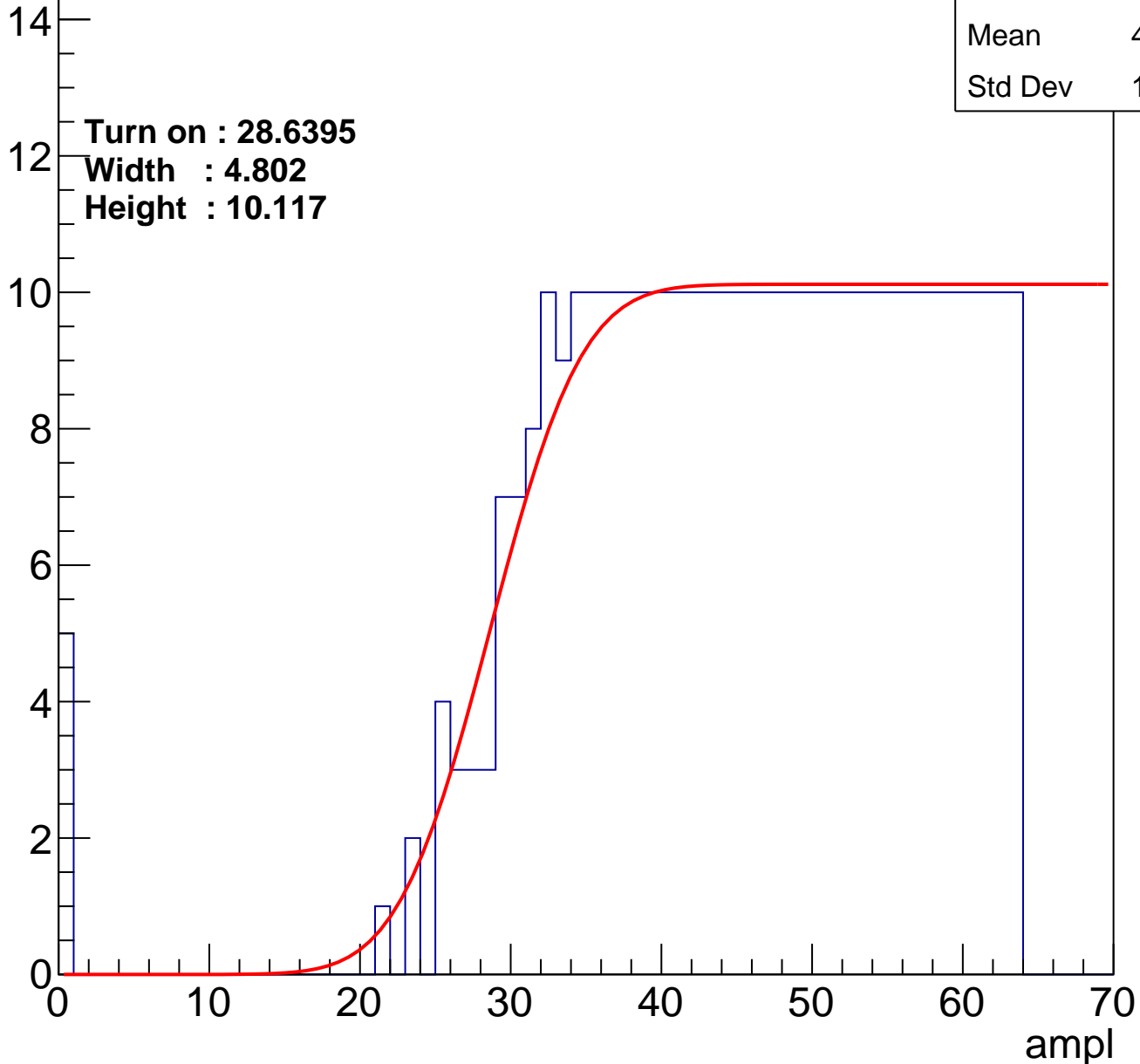
Entries	362
Mean	44.86
Std Dev	11.78

Turn on : 28.6395

Width : 4.802

Height : 10.117

Entry



B1L103S, U26-ch22

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.71
Std Dev	11.98

Turn on : 25.7326

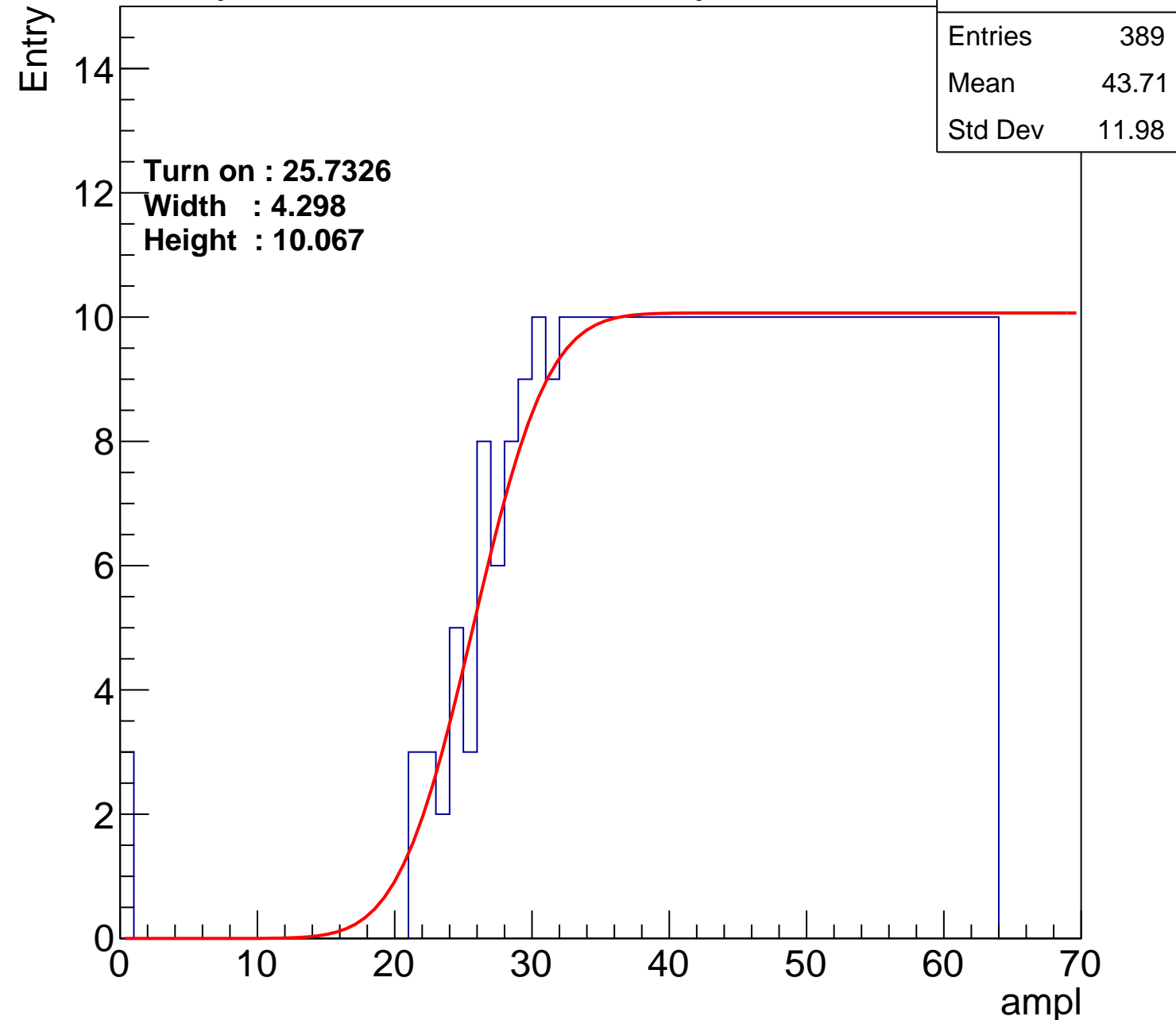
Width : 4.298

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch23

calib_packv5_042523_0143.root, FC#7, port C2

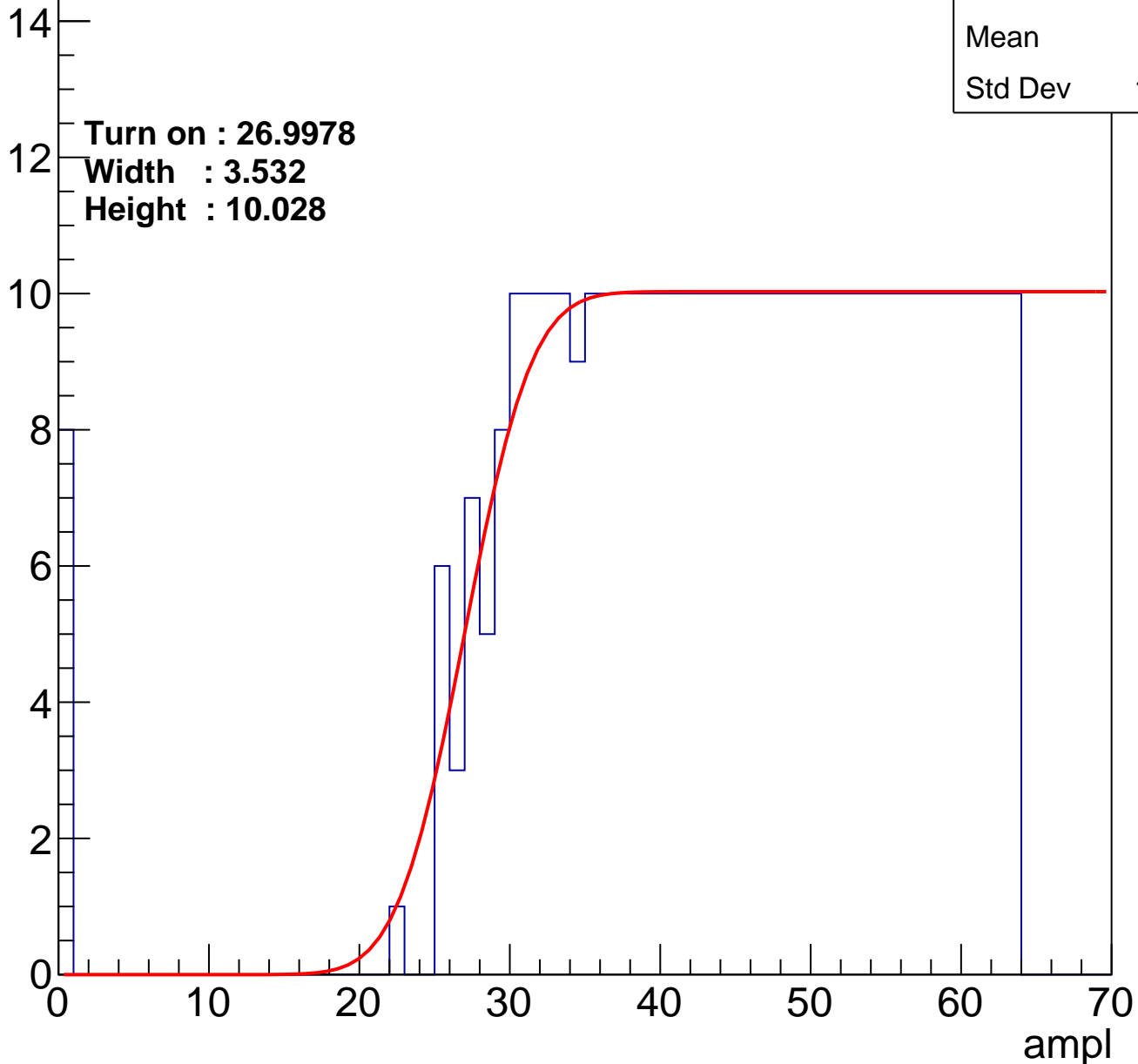
Entries	377
Mean	44
Std Dev	12.51

Turn on : 26.9978

Width : 3.532

Height : 10.028

Entry



B1L103S, U26-ch24

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.32
Std Dev	12.25

Turn on : 25.0810

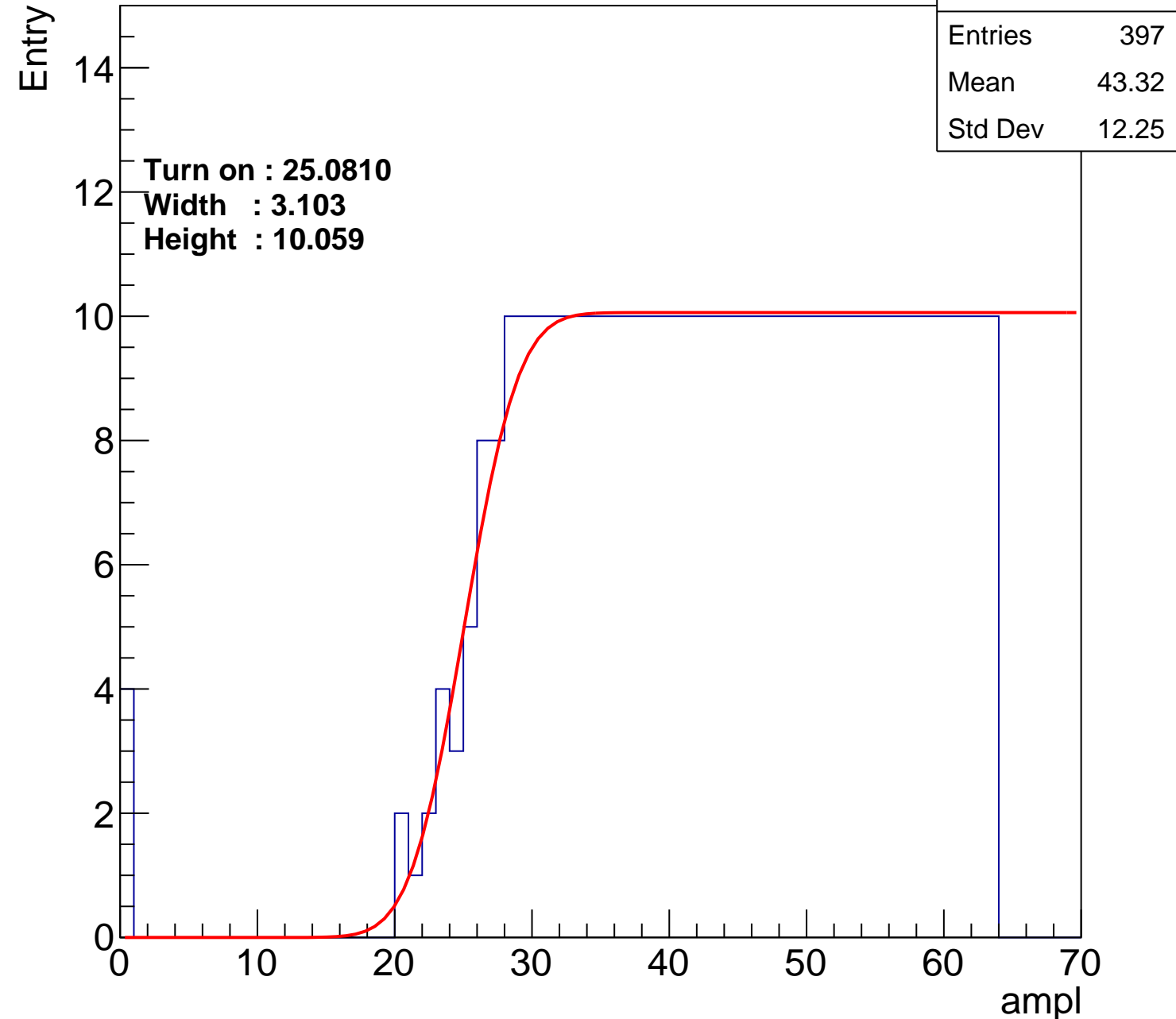
Width : 3.103

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch25

calib_packv5_042523_0143.root, FC#7, port C2

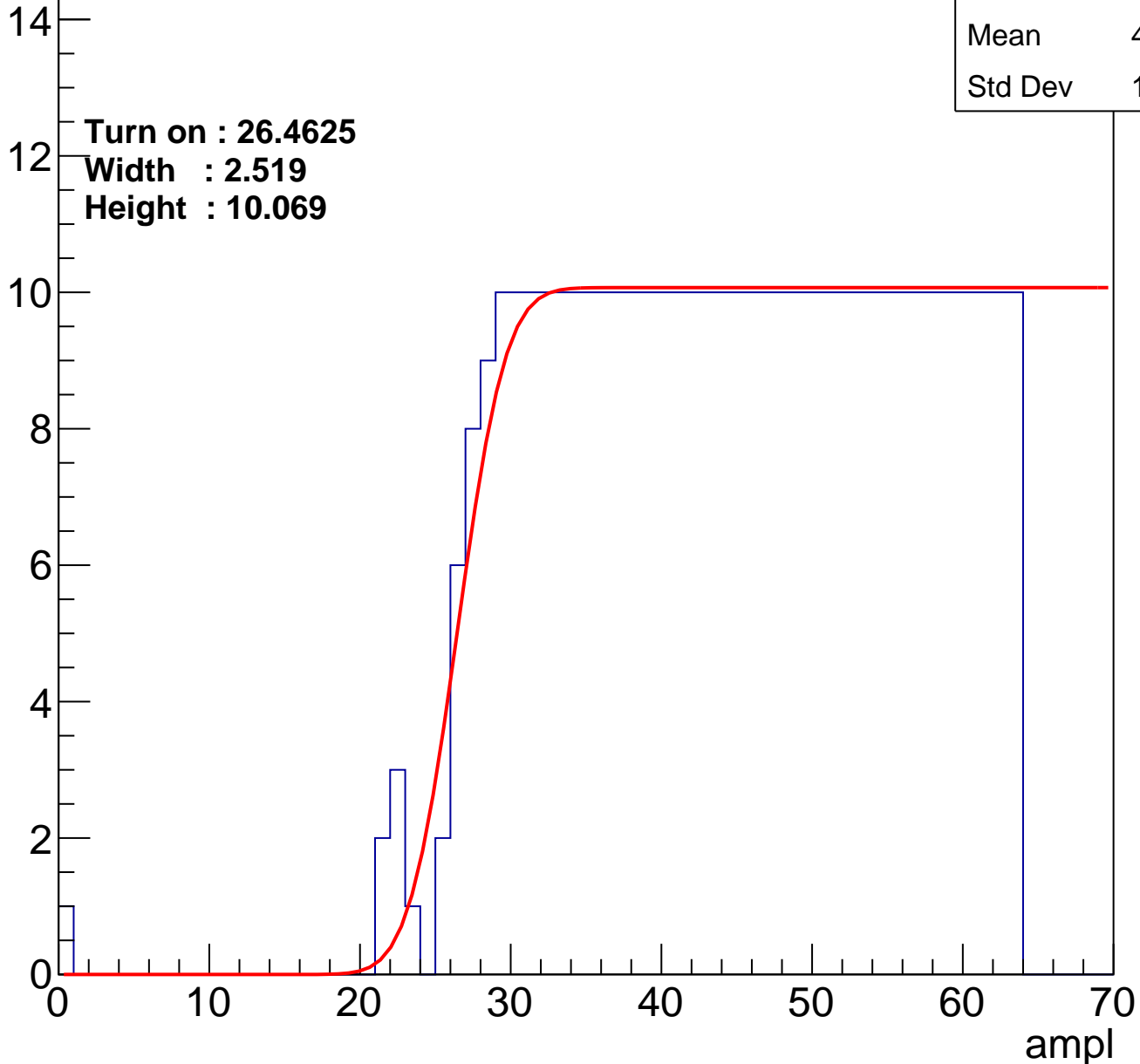
Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 26.4625

Width : 2.519

Height : 10.069

Entry



B1L103S, U26-ch26

calib_packv5_042523_0143.root, FC#7, port C2

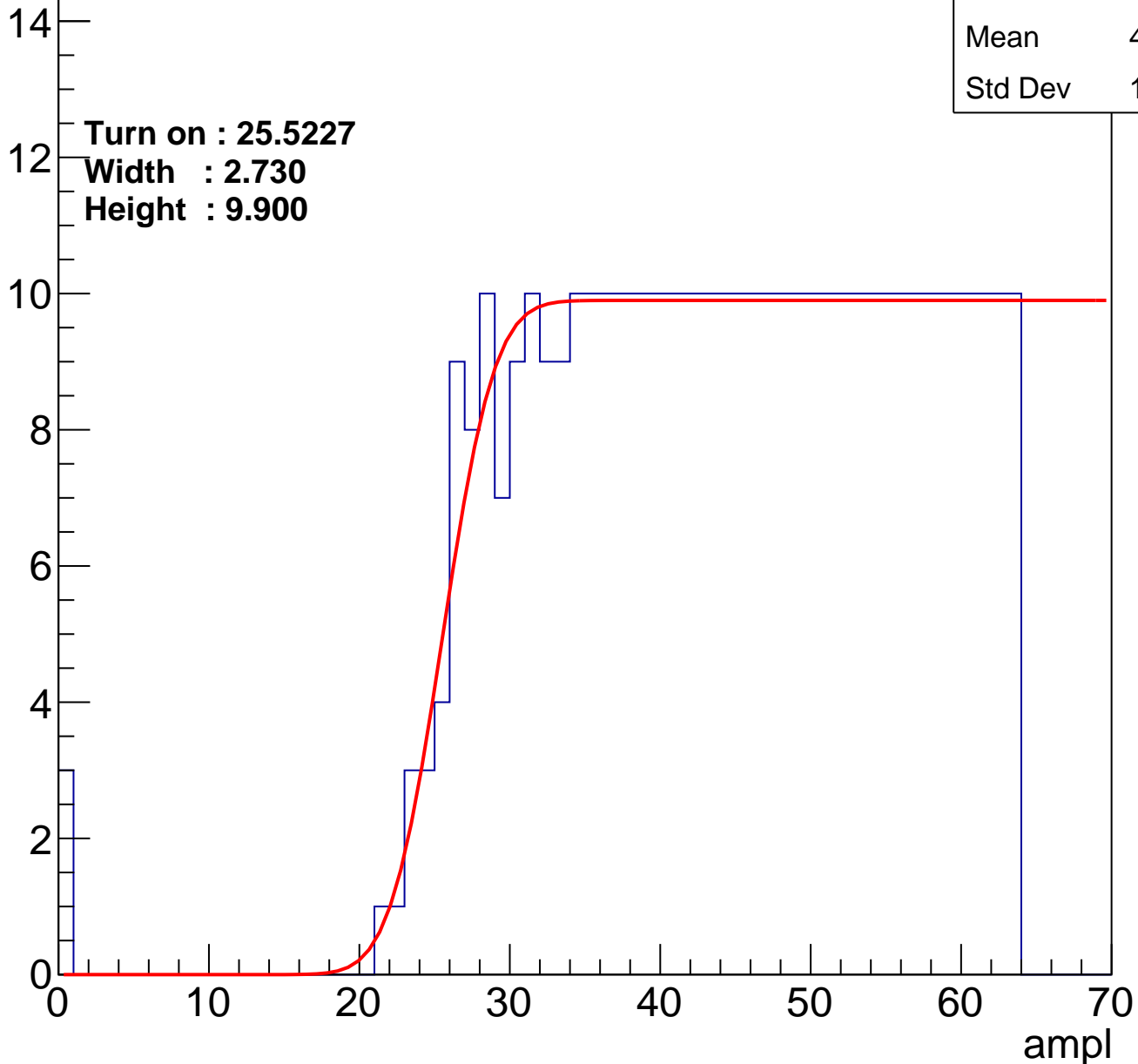
Entries	386
Mean	43.87
Std Dev	11.89

Turn on : 25.5227

Width : 2.730

Height : 9.900

Entry



B1L103S, U26-ch27

calib_packv5_042523_0143.root, FC#7, port C2

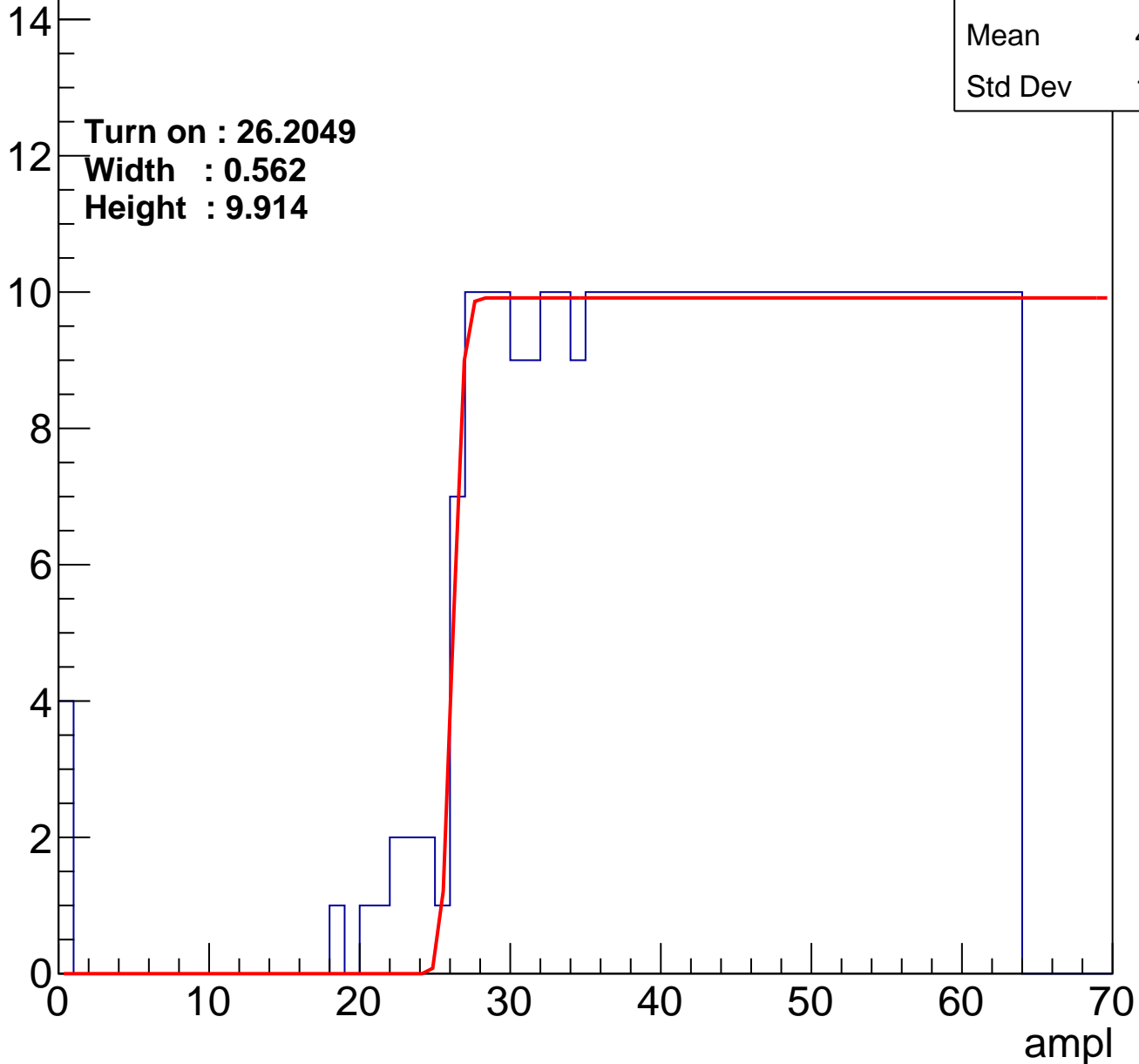
Entries	388
Mean	43.71
Std Dev	12.11

Turn on : 26.2049

Width : 0.562

Height : 9.914

Entry



B1L103S, U26-ch28

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.17
Std Dev	11.95

Turn on : 27.5627

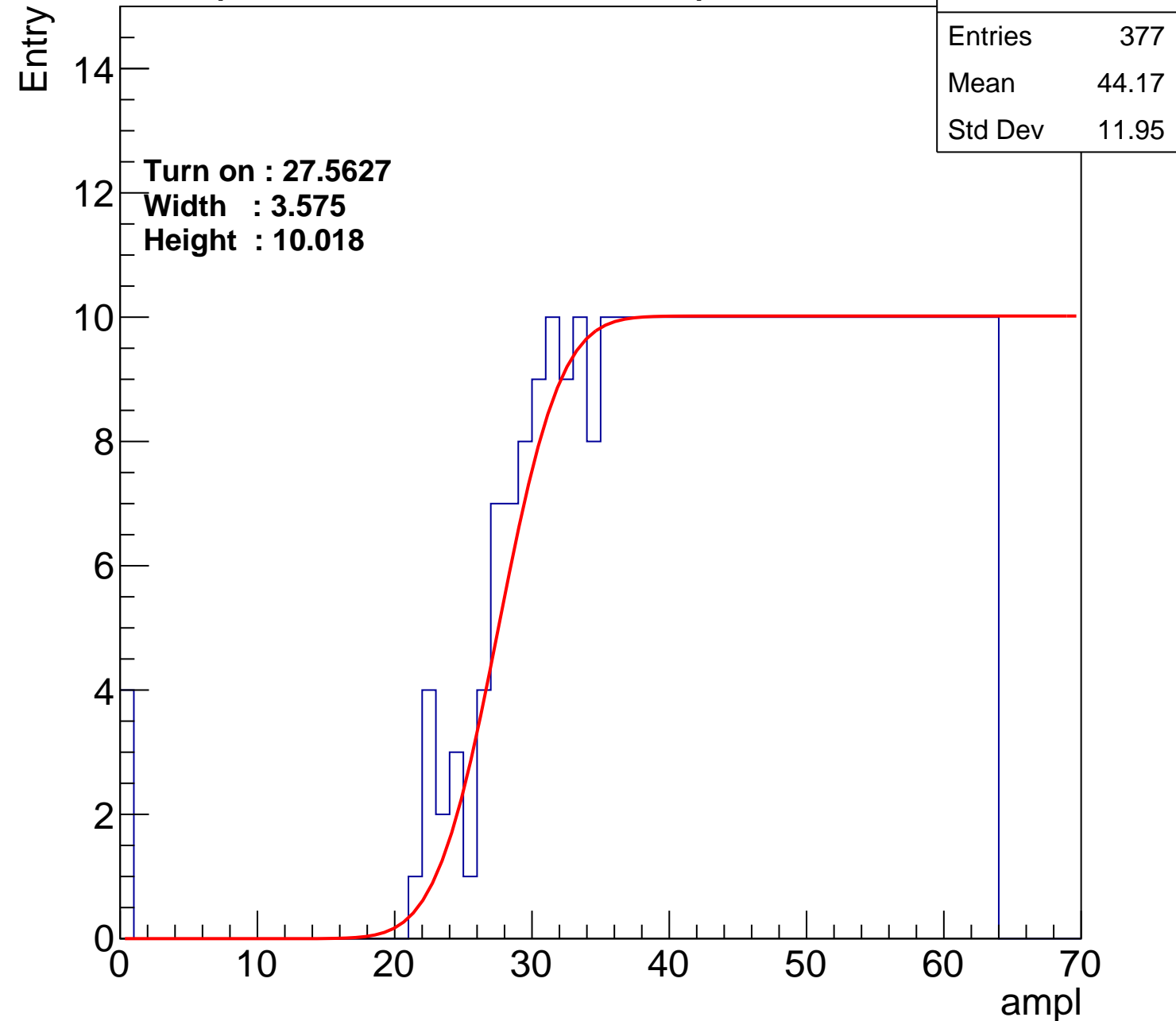
Width : 3.575

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch29

calib_packv5_042523_0143.root, FC#7, port C2

Entries	412
Mean	42.63
Std Dev	12.48

Turn on : 23.2817

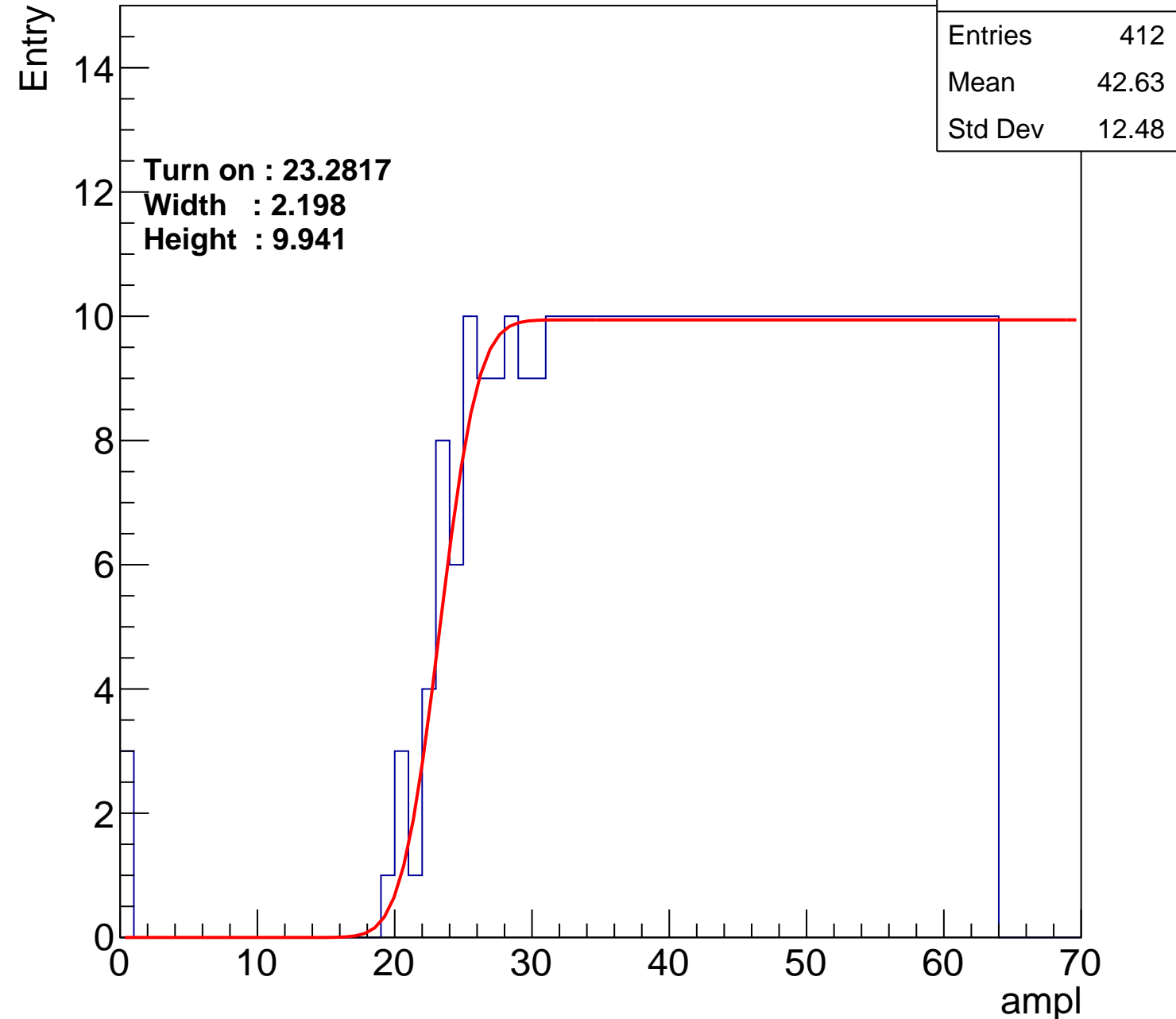
Width : 2.198

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch30

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.17
Std Dev	11.96

Turn on : 26.9540

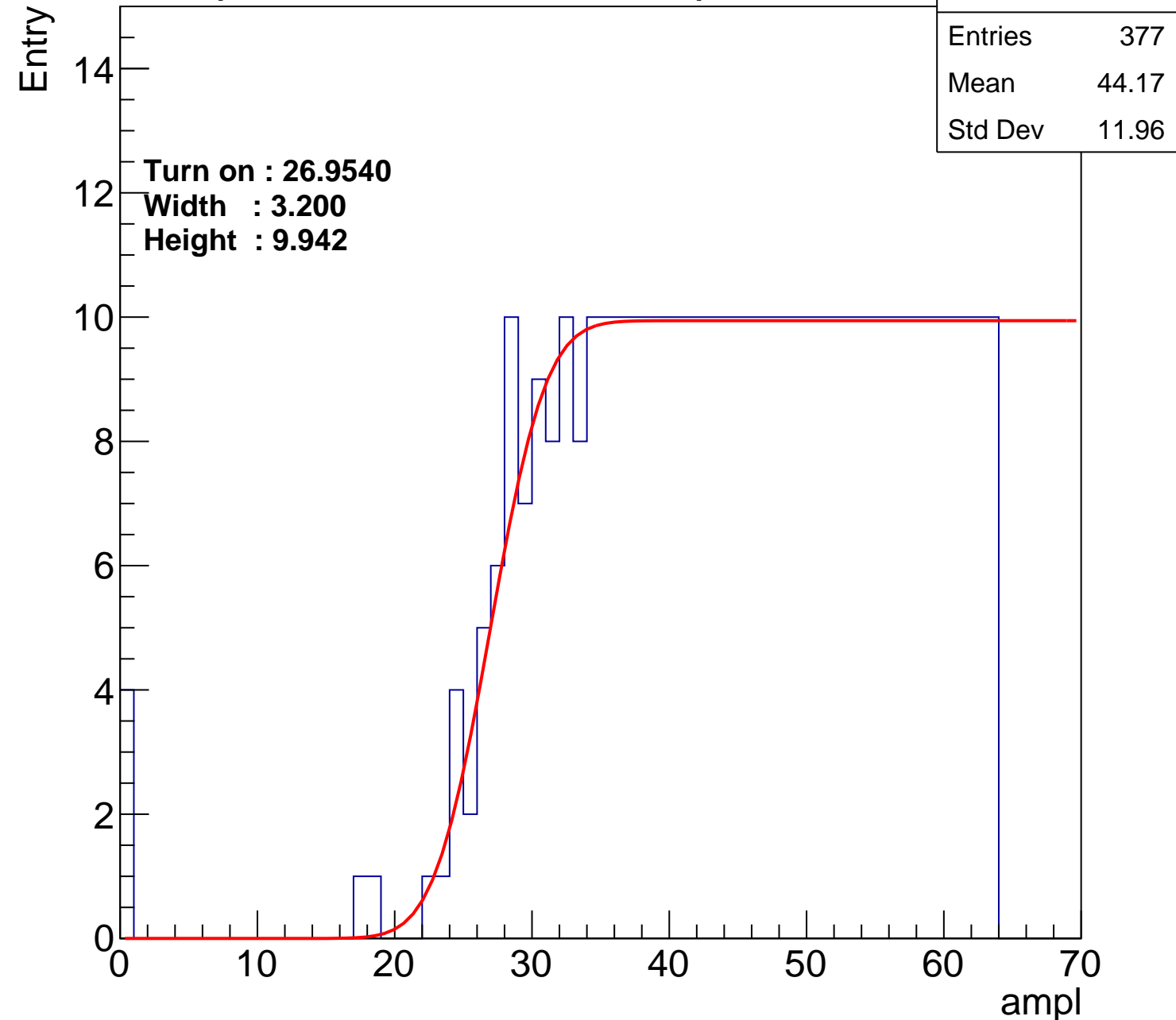
Width : 3.200

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch31

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 28.2887

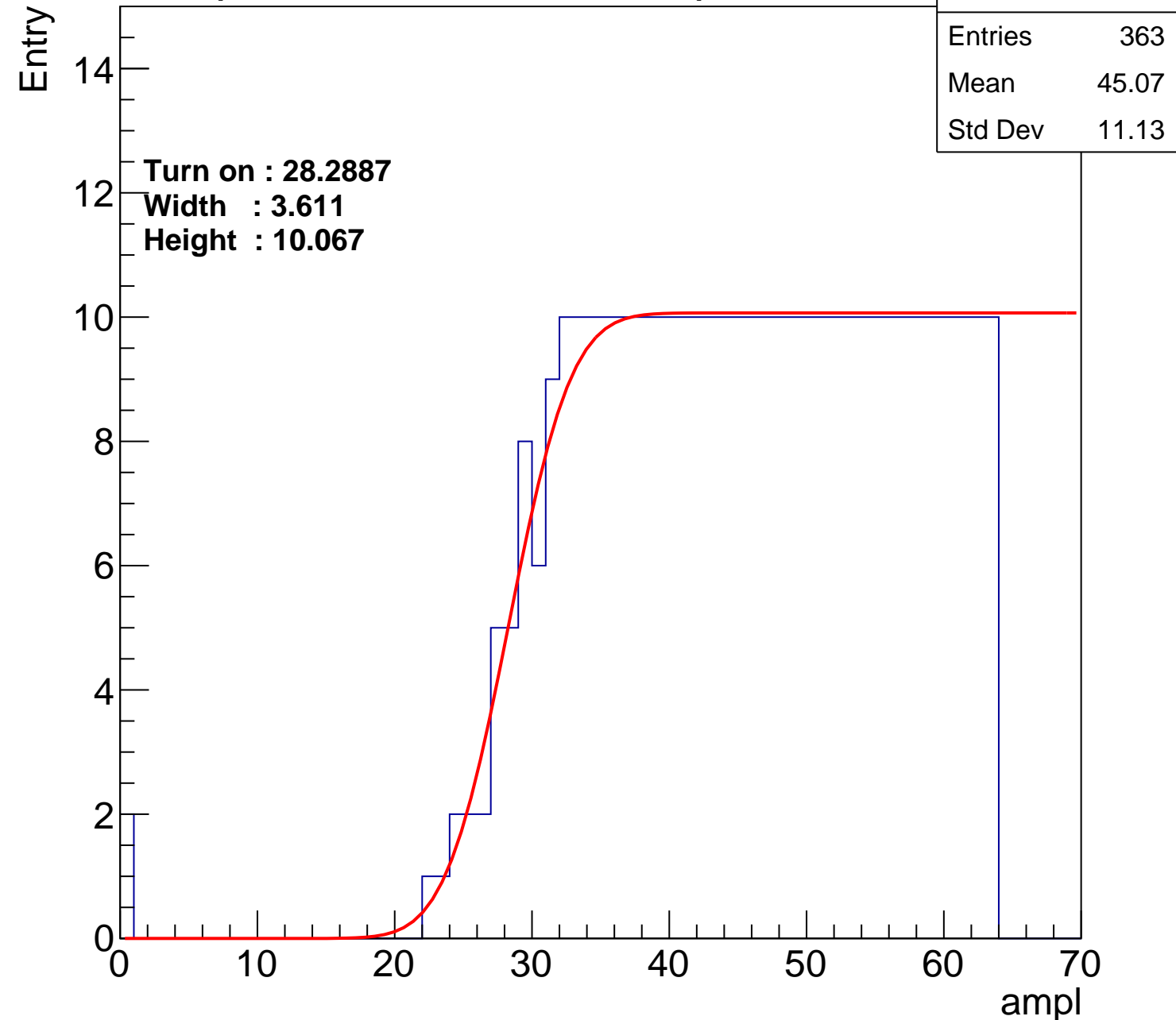
Width : 3.611

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	416
Mean	42.45
Std Dev	12.57

Turn on : 22.6783

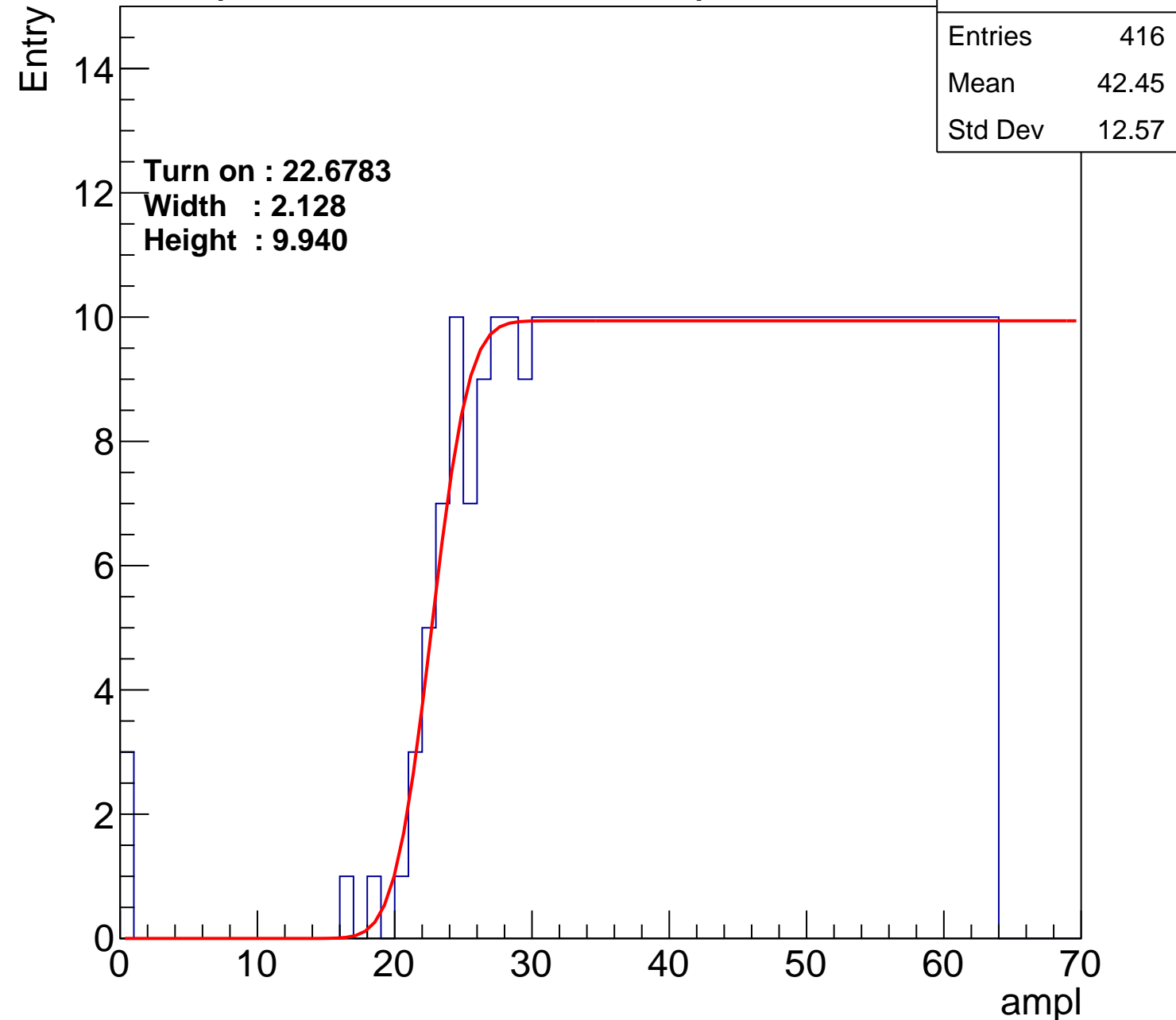
Width : 2.128

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch33

calib_packv5_042523_0143.root, FC#7, port C2

Entries	352
Mean	45.65
Std Dev	10.8

Turn on : 29.3894

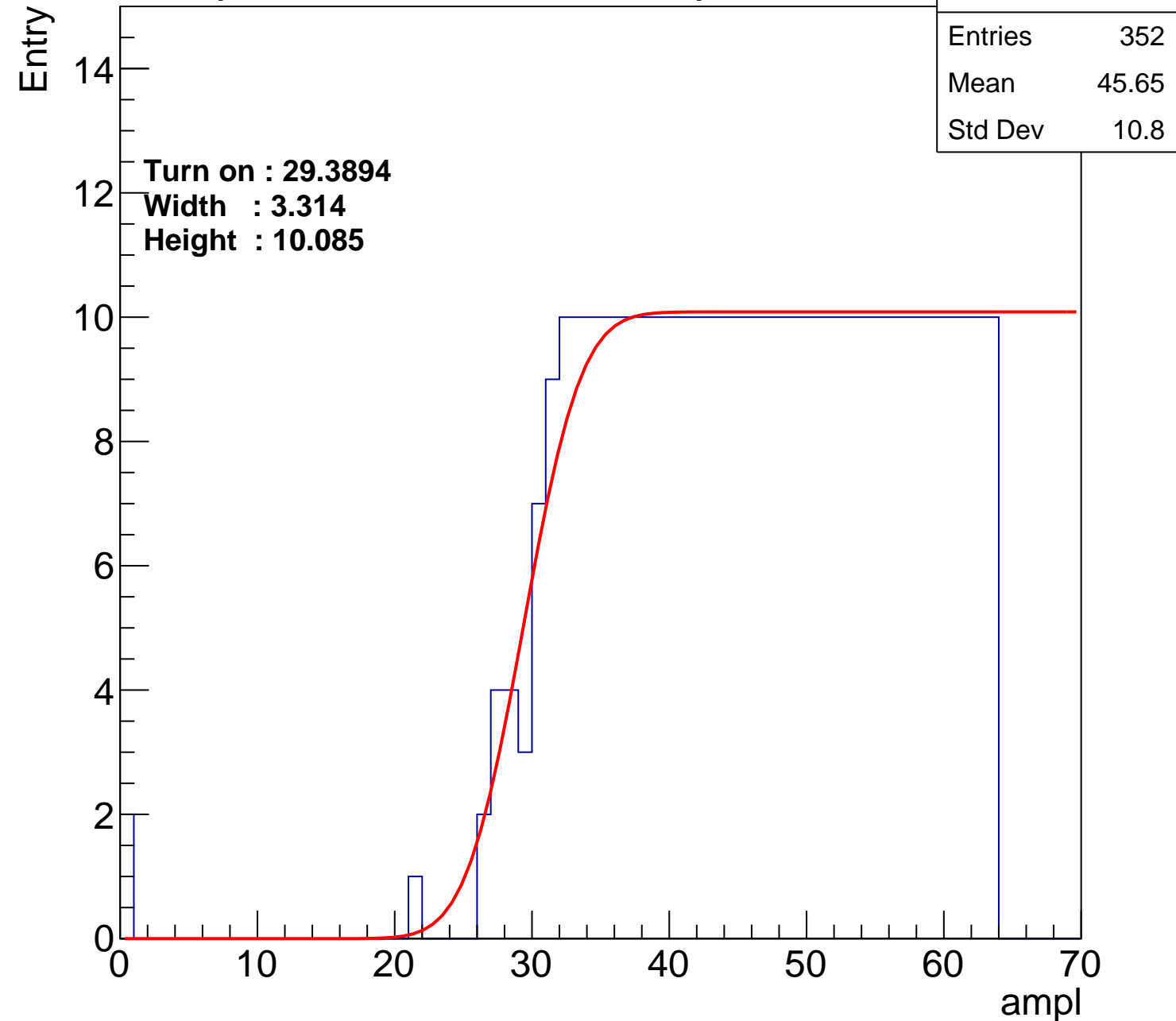
Width : 3.314

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch34

calib_packv5_042523_0143.root, FC#7, port C2

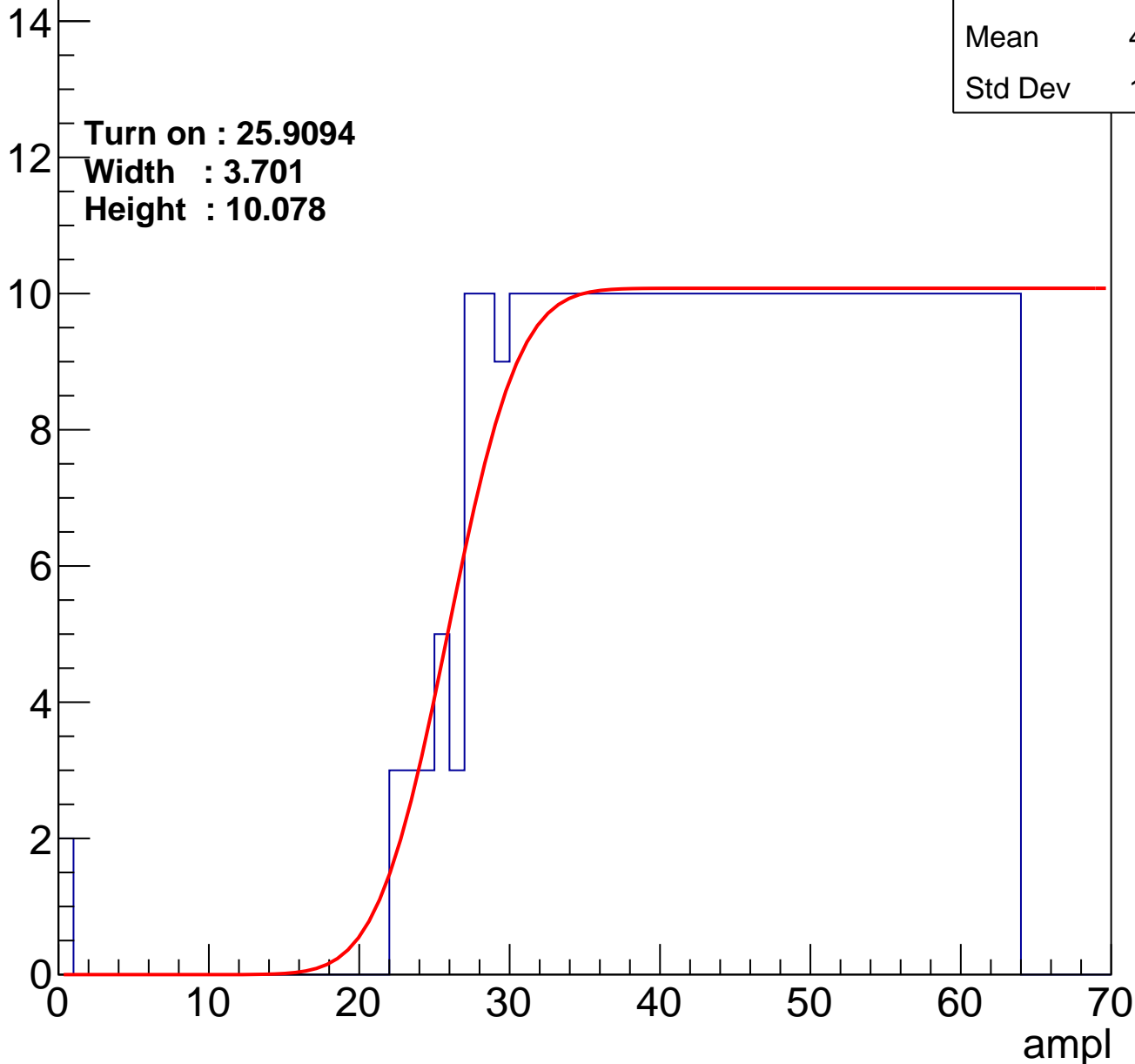
Entries	388
Mean	43.89
Std Dev	11.68

Turn on : 25.9094

Width : 3.701

Height : 10.078

Entry



B1L103S, U26-ch35

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.05
Std Dev	11.65

Turn on : 28.9181

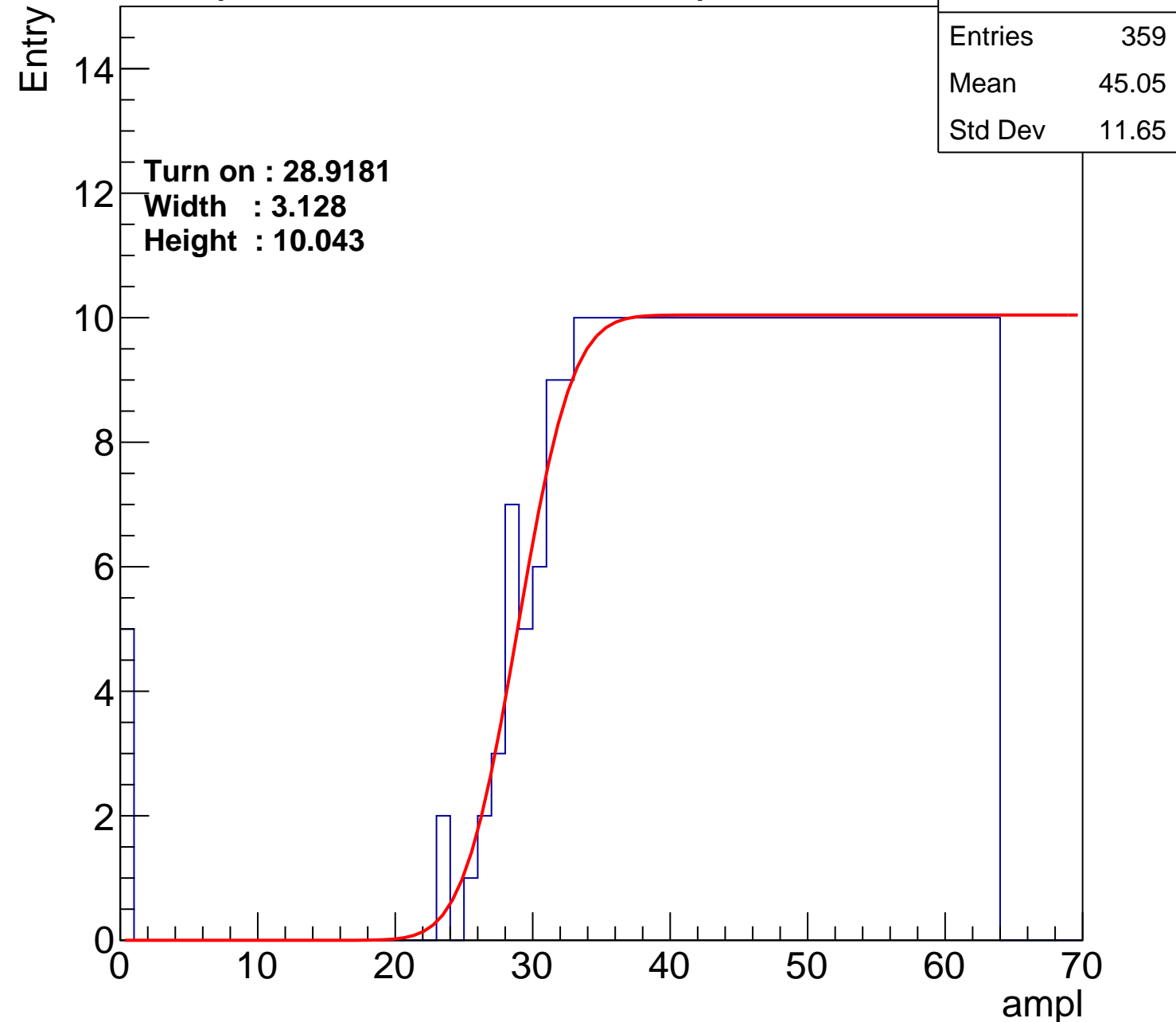
Width : 3.128

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch36

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.45
Std Dev	10.97

Turn on : 29.1202

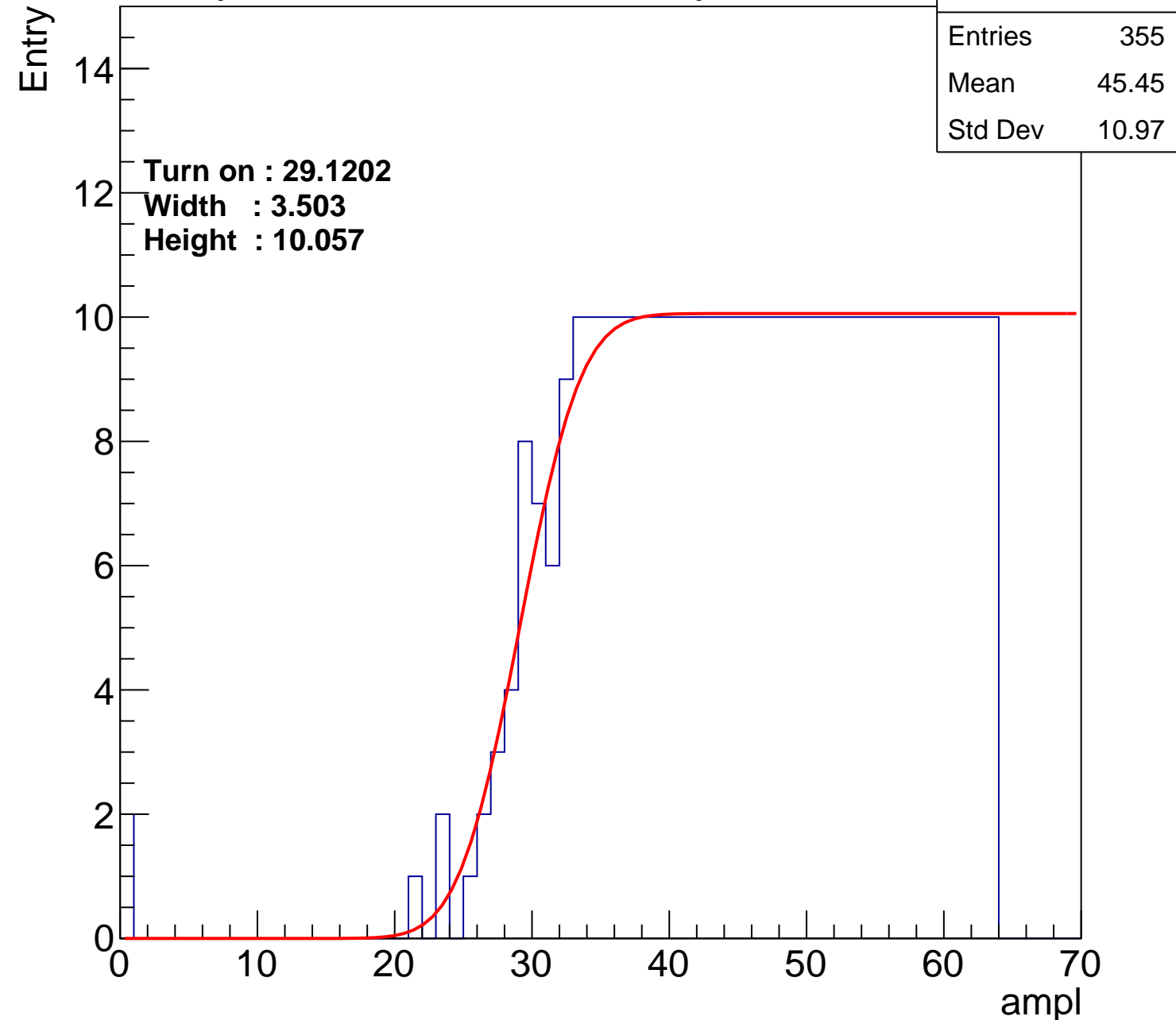
Width : 3.503

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch37

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.83
Std Dev	11.53

Turn on : 27.7180

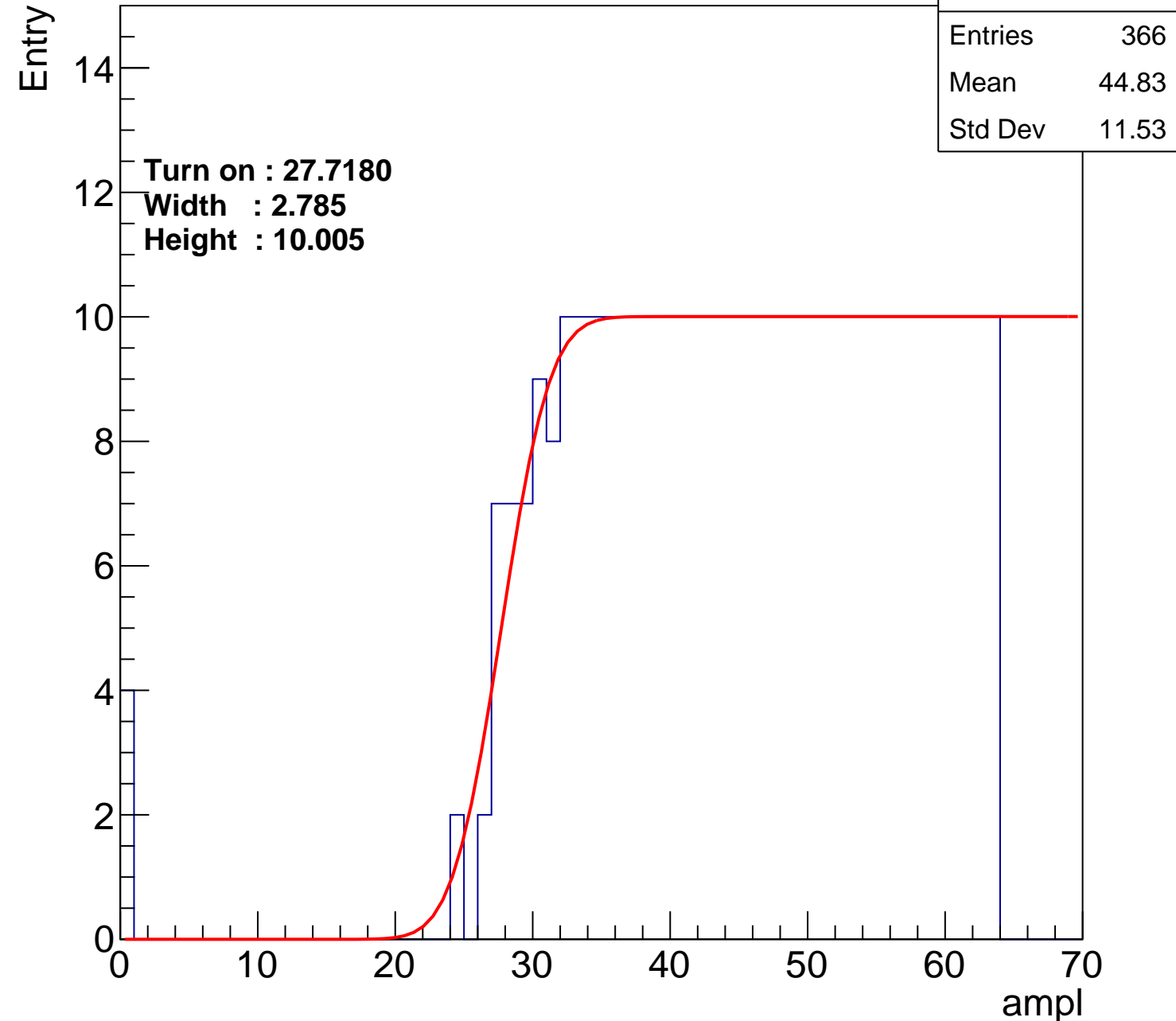
Width : 2.785

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch38

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.63
Std Dev	11.21

Turn on : 28.2024

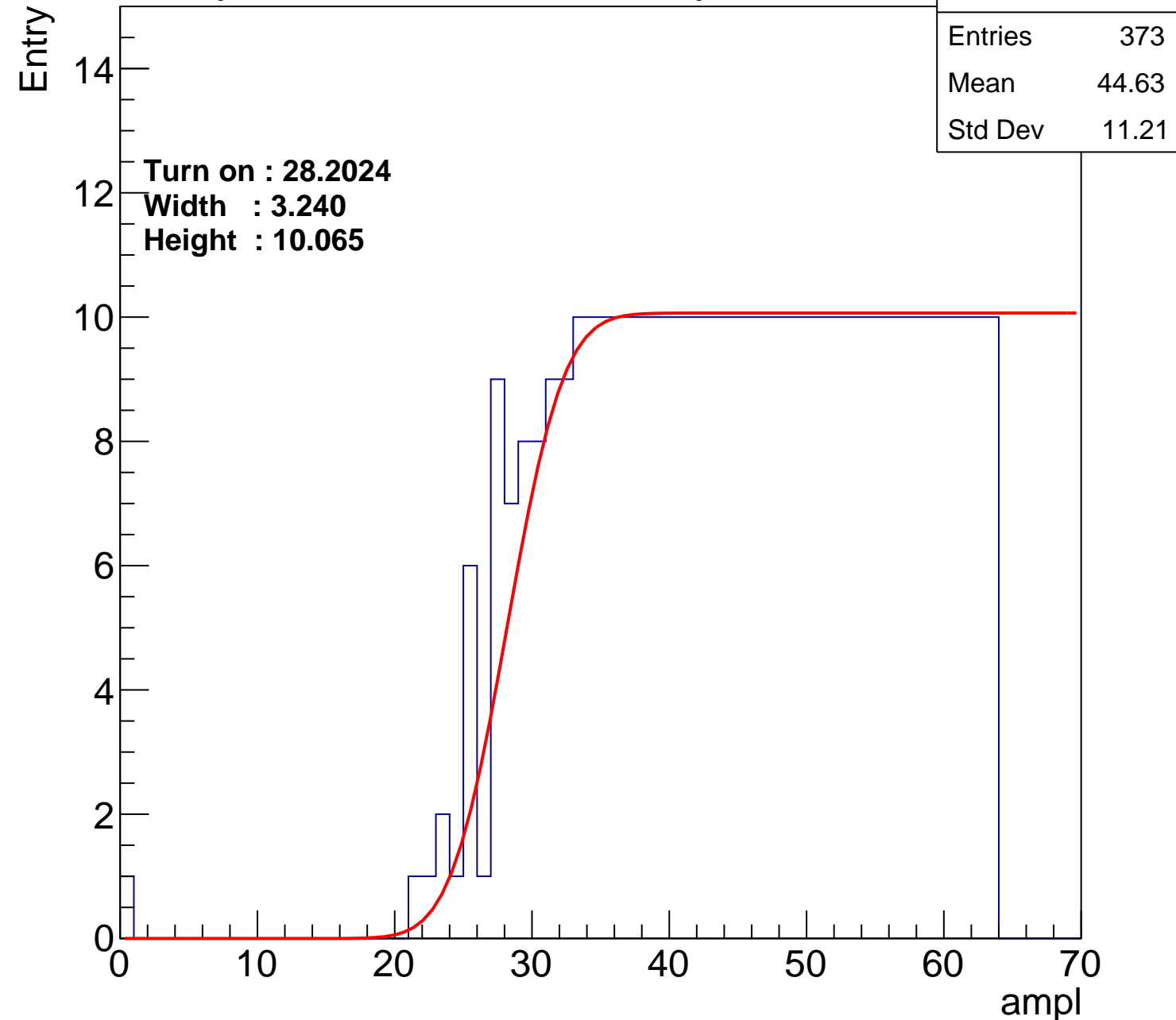
Width : 3.240

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch39

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.64
Std Dev	11.53

Turn on : 27.5466

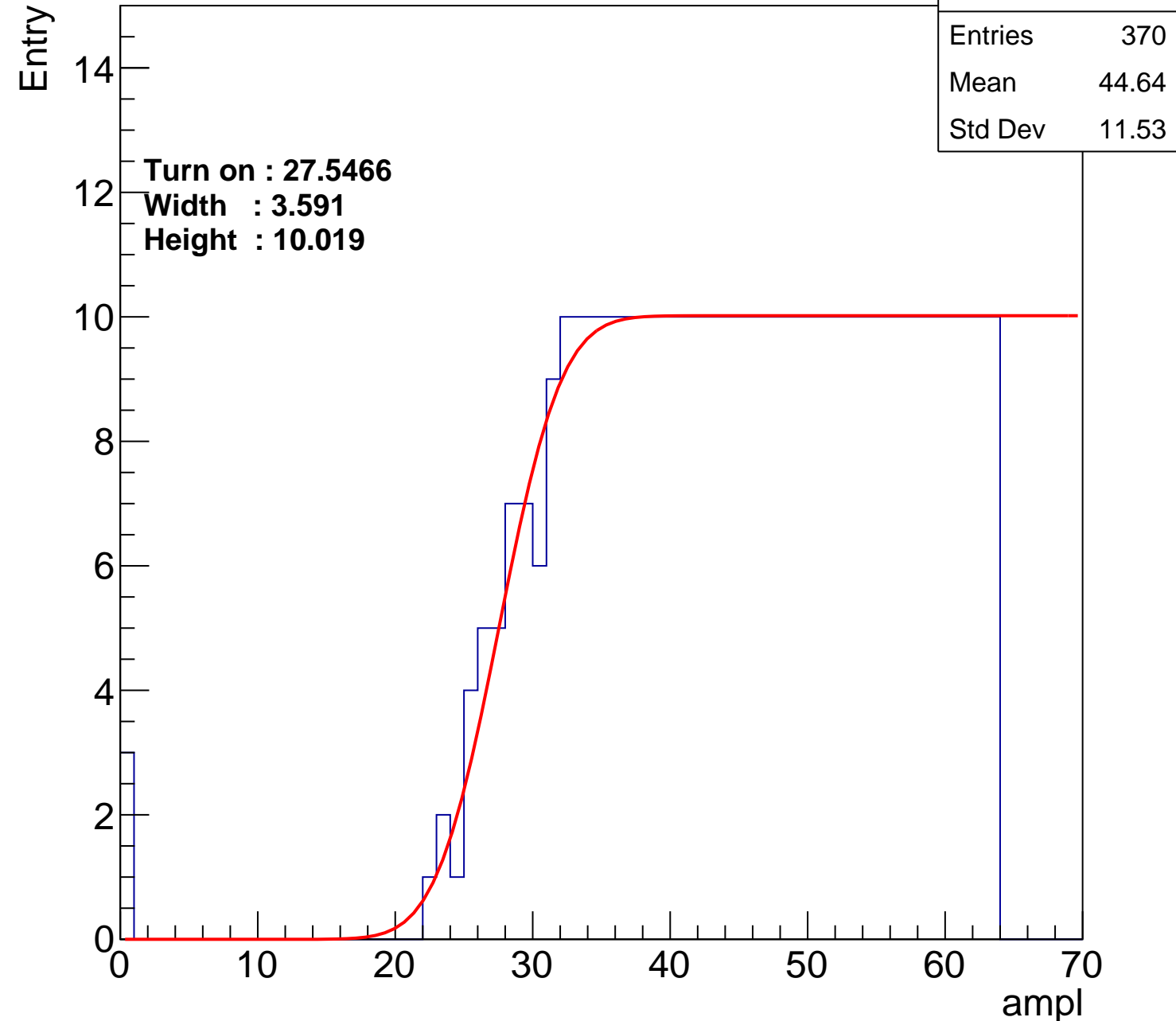
Width : 3.591

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch40

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.9
Std Dev	11.84

Turn on : 25.4071

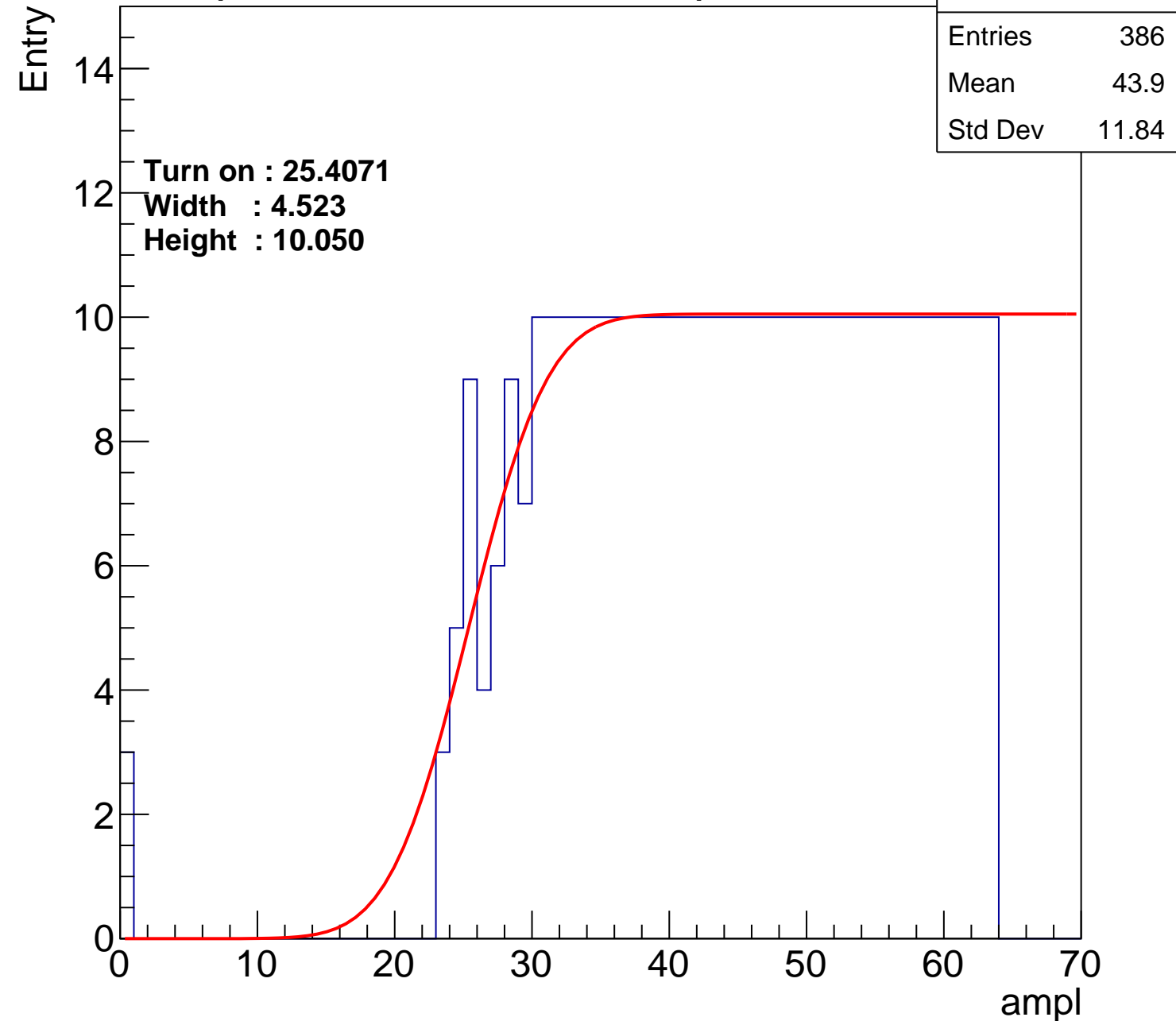
Width : 4.523

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch41

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.44
Std Dev	11.58

Turn on : 27.2371

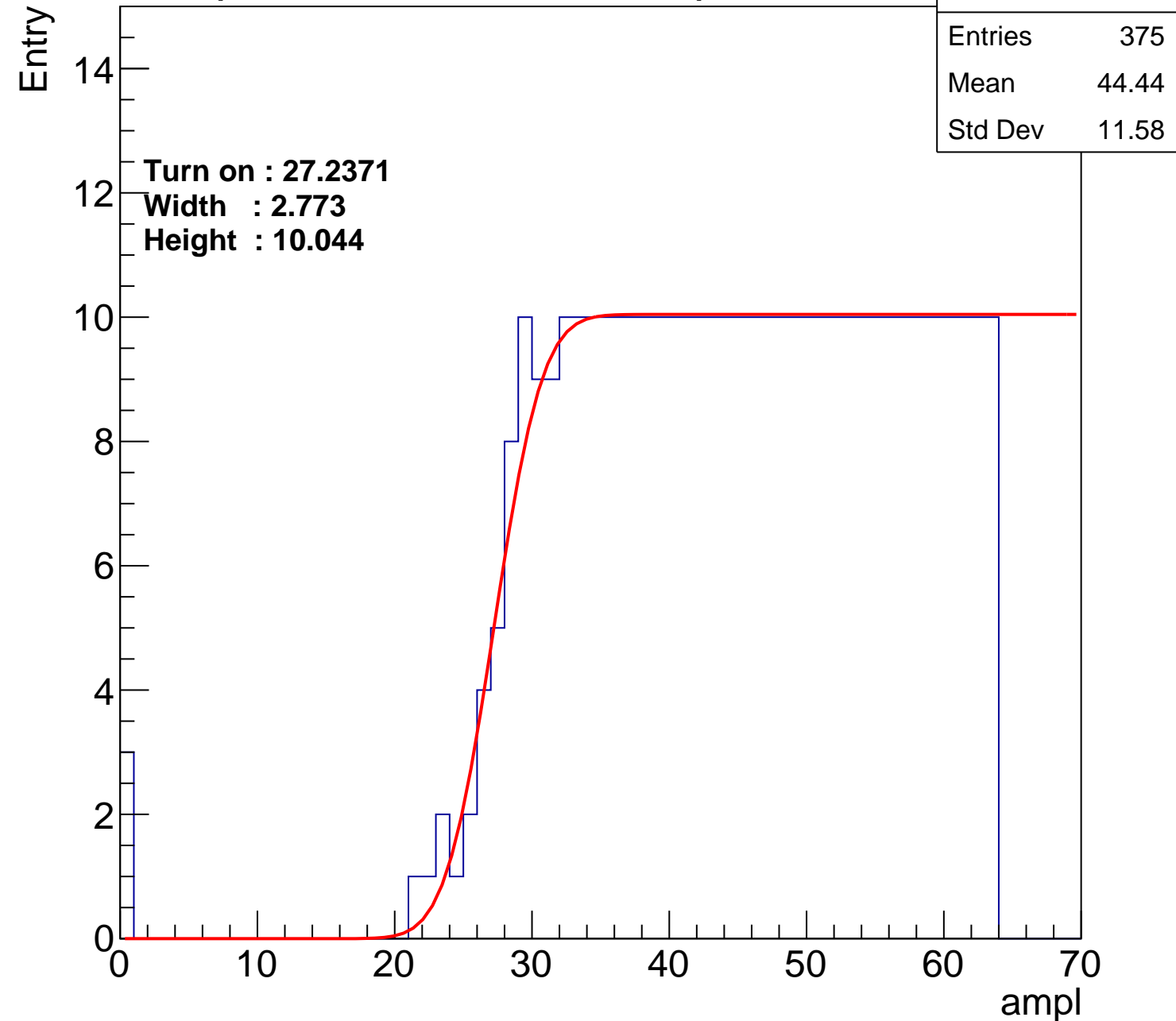
Width : 2.773

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch42

calib_packv5_042523_0143.root, FC#7, port C2

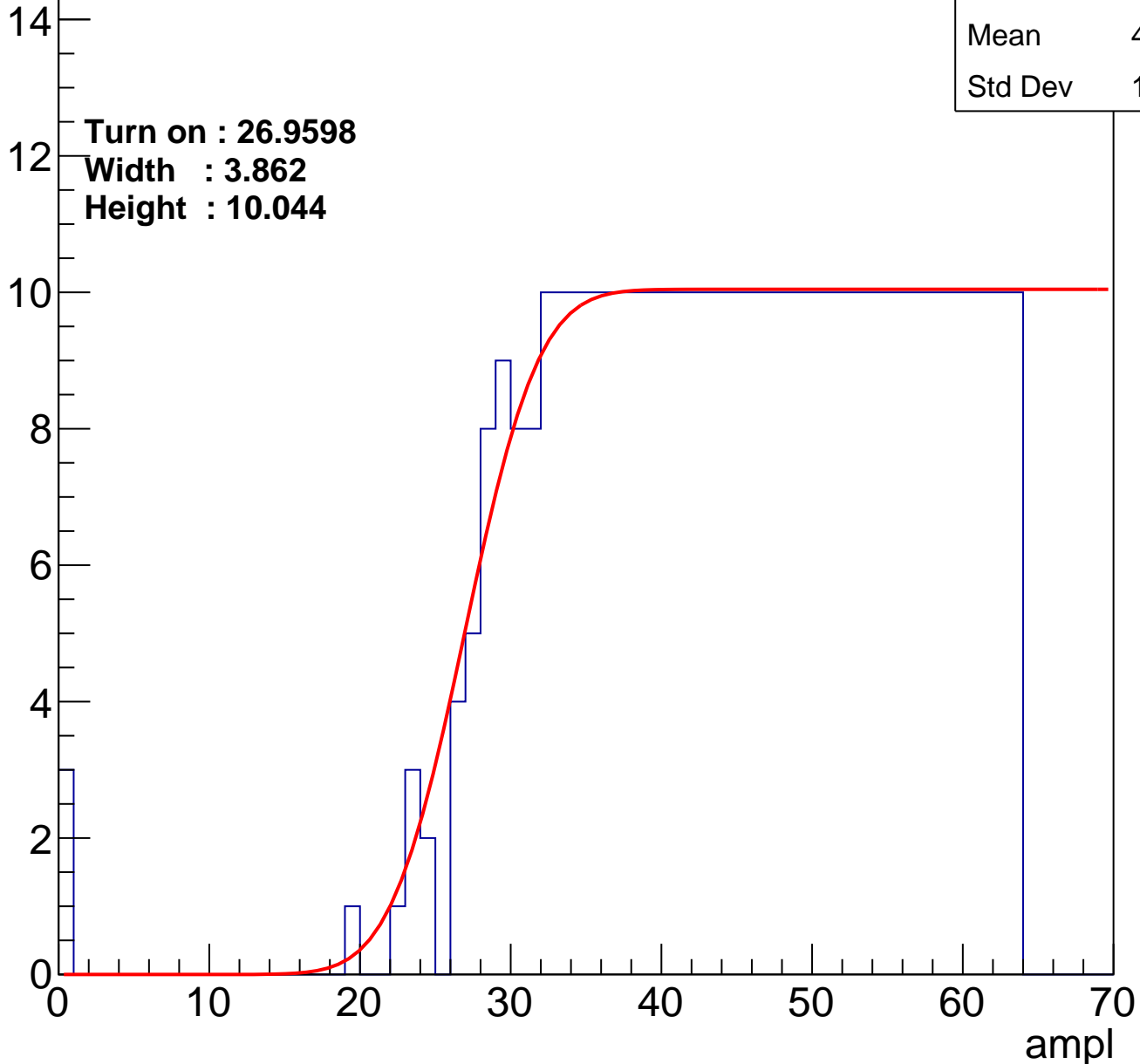
Entries	372
Mean	44.54
Std Dev	11.58

Turn on : 26.9598

Width : 3.862

Height : 10.044

Entry



B1L103S, U26-ch43

calib_packv5_042523_0143.root, FC#7, port C2

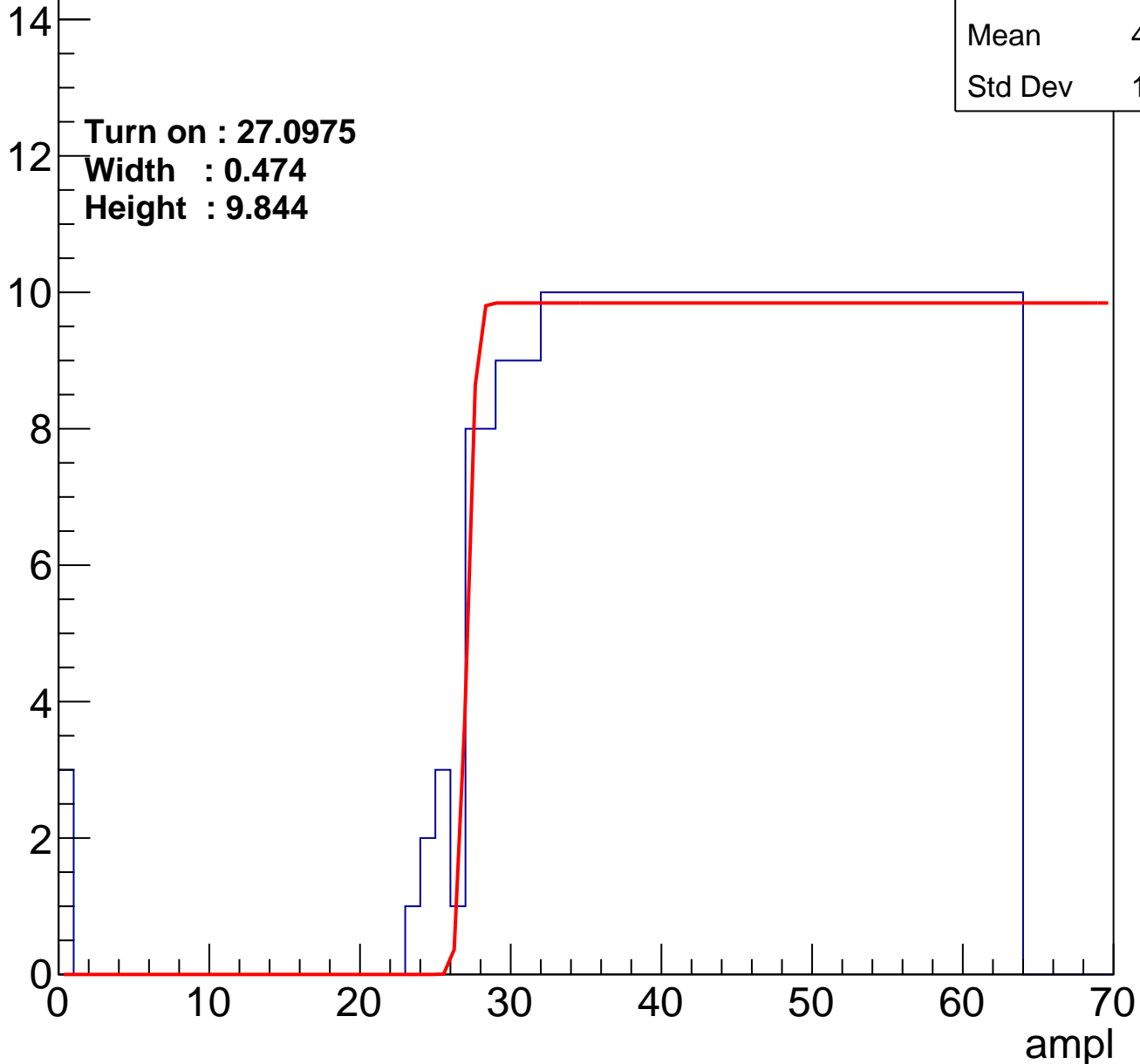
Entries	373
Mean	44.56
Std Dev	11.49

Turn on : 27.0975

Width : 0.474

Height : 9.844

Entry



B1L103S, U26-ch44

calib_packv5_042523_0143.root, FC#7, port C2

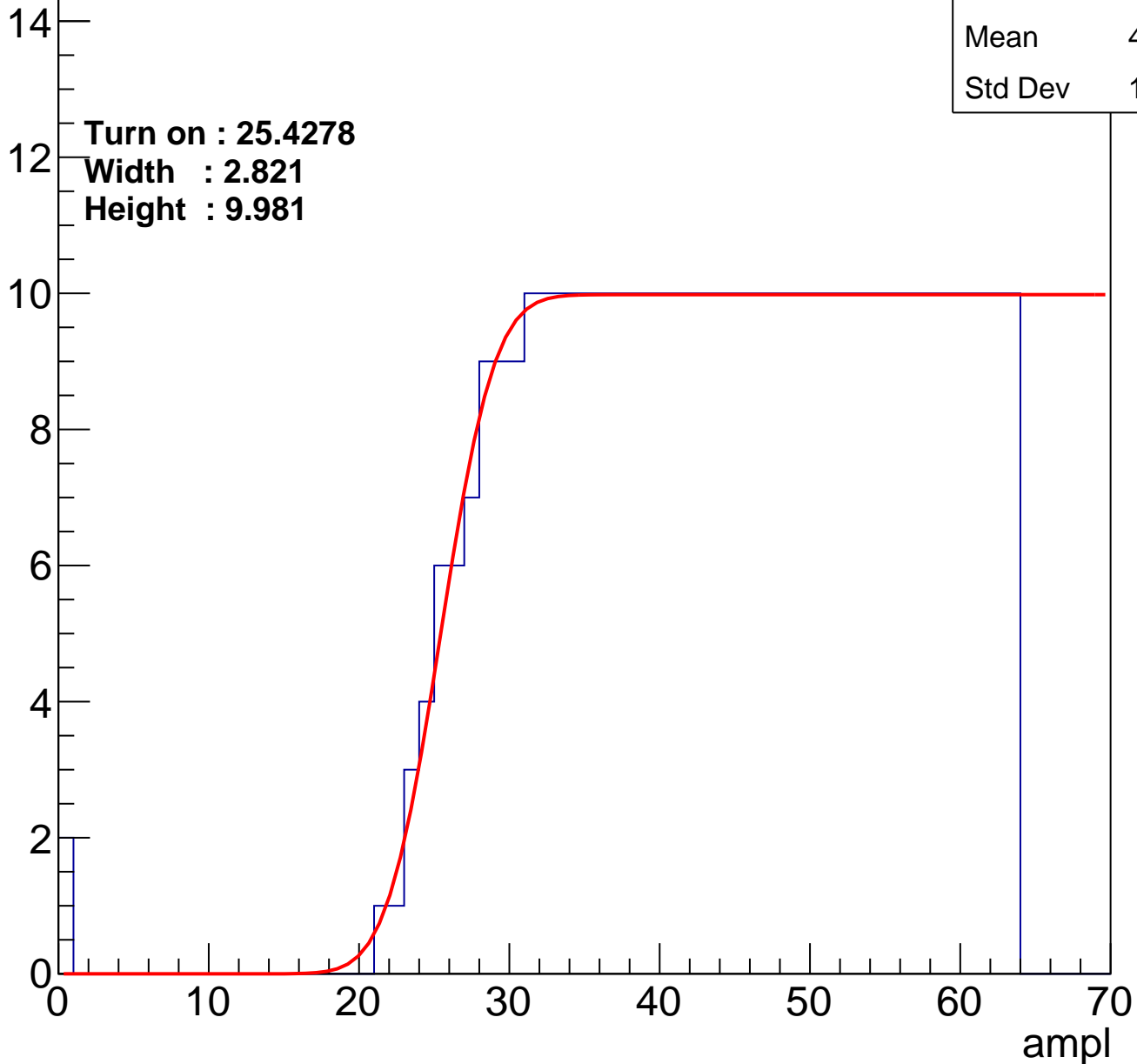
Entries	387
Mean	43.92
Std Dev	11.69

Turn on : 25.4278

Width : 2.821

Height : 9.981

Entry



B1L103S, U26-ch45

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.62
Std Dev	11.51

Turn on : 27.6614

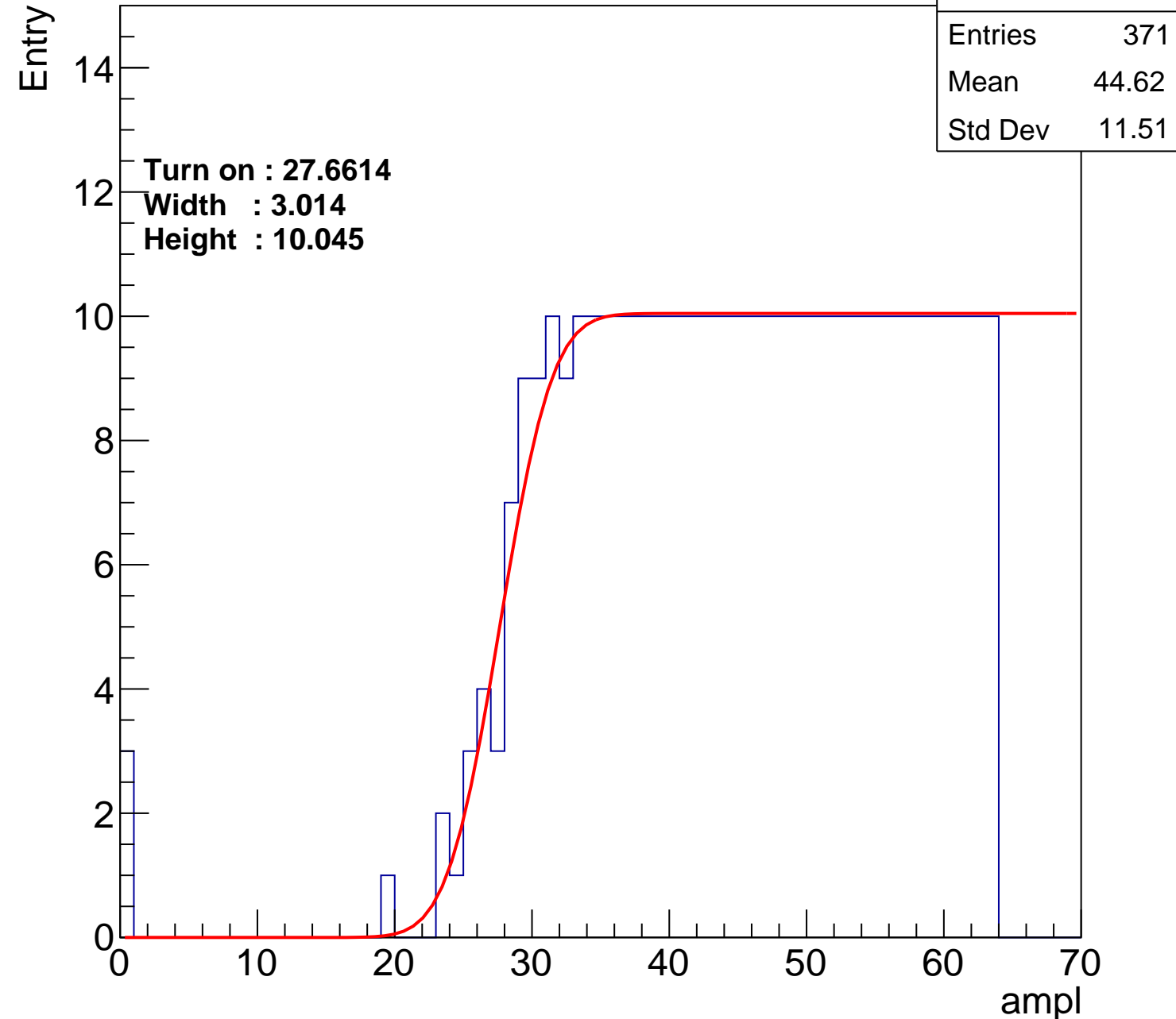
Width : 3.014

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch46

calib_packv5_042523_0143.root, FC#7, port C2

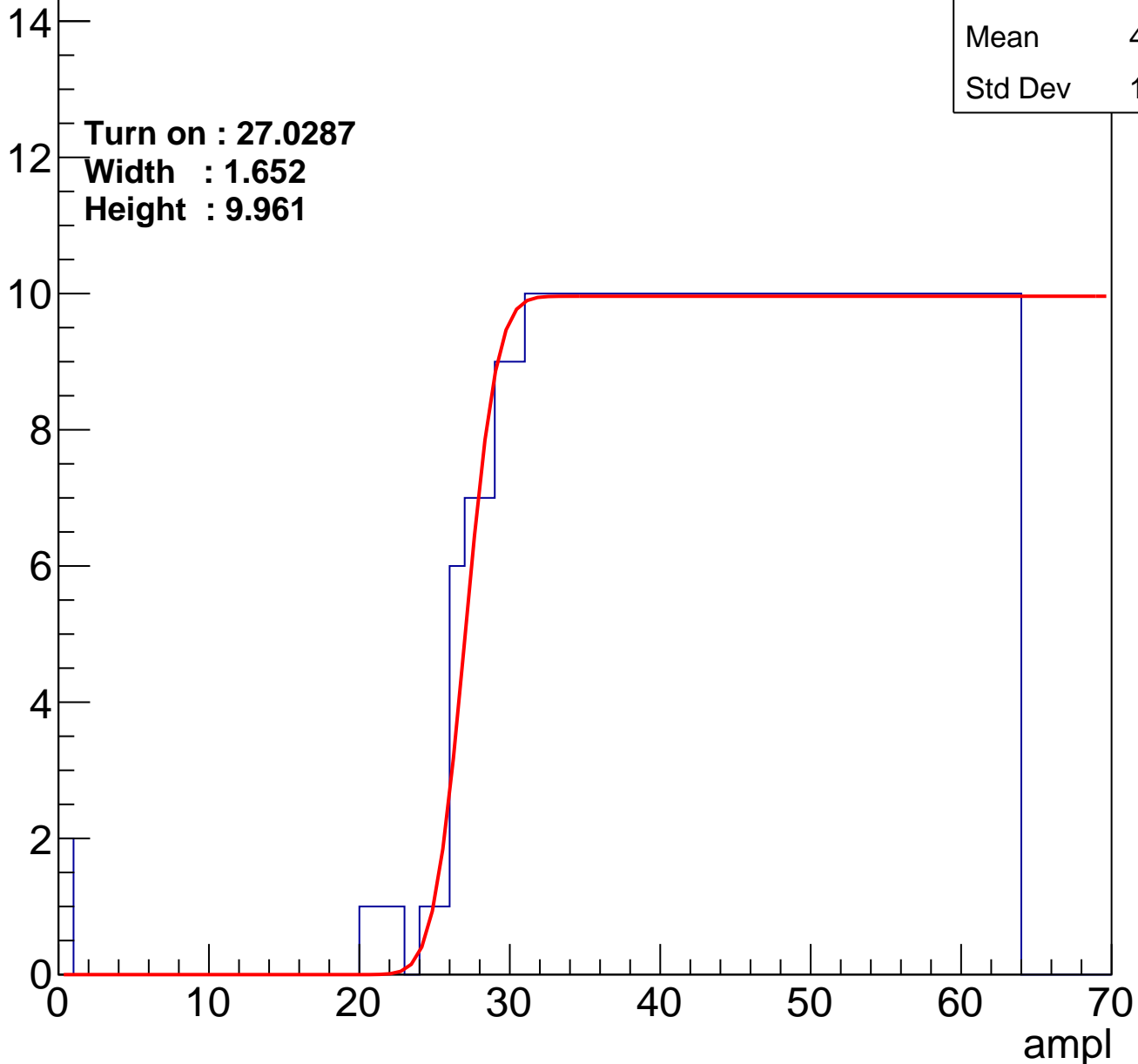
Entries	375
Mean	44.52
Std Dev	11.38

Turn on : 27.0287

Width : 1.652

Height : 9.961

Entry



B1L103S, U26-ch47

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.27
Std Dev	11.49

Turn on : 26.5315

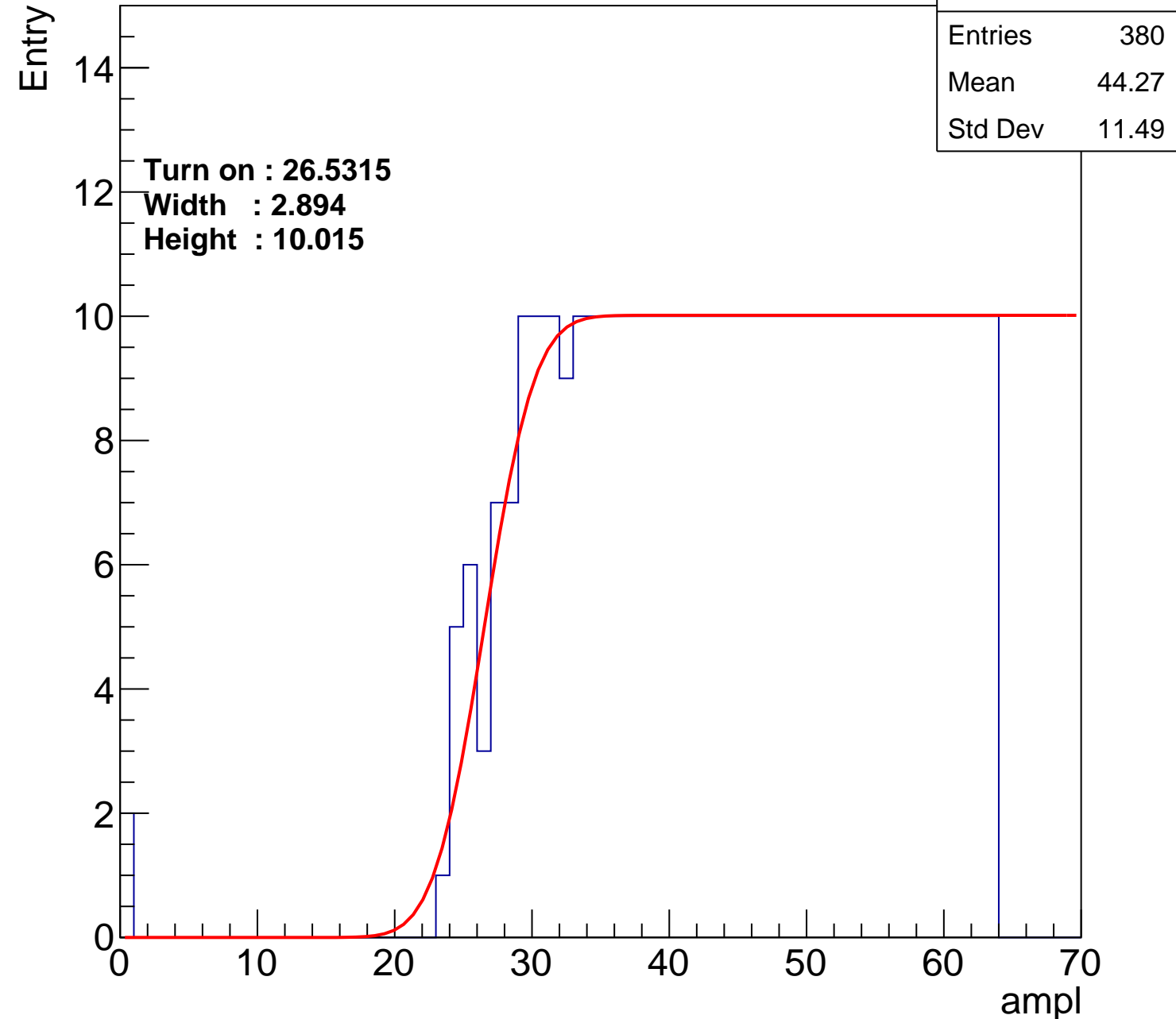
Width : 2.894

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.63
Std Dev	12.02

Turn on : 26.2053

Width : 2.340

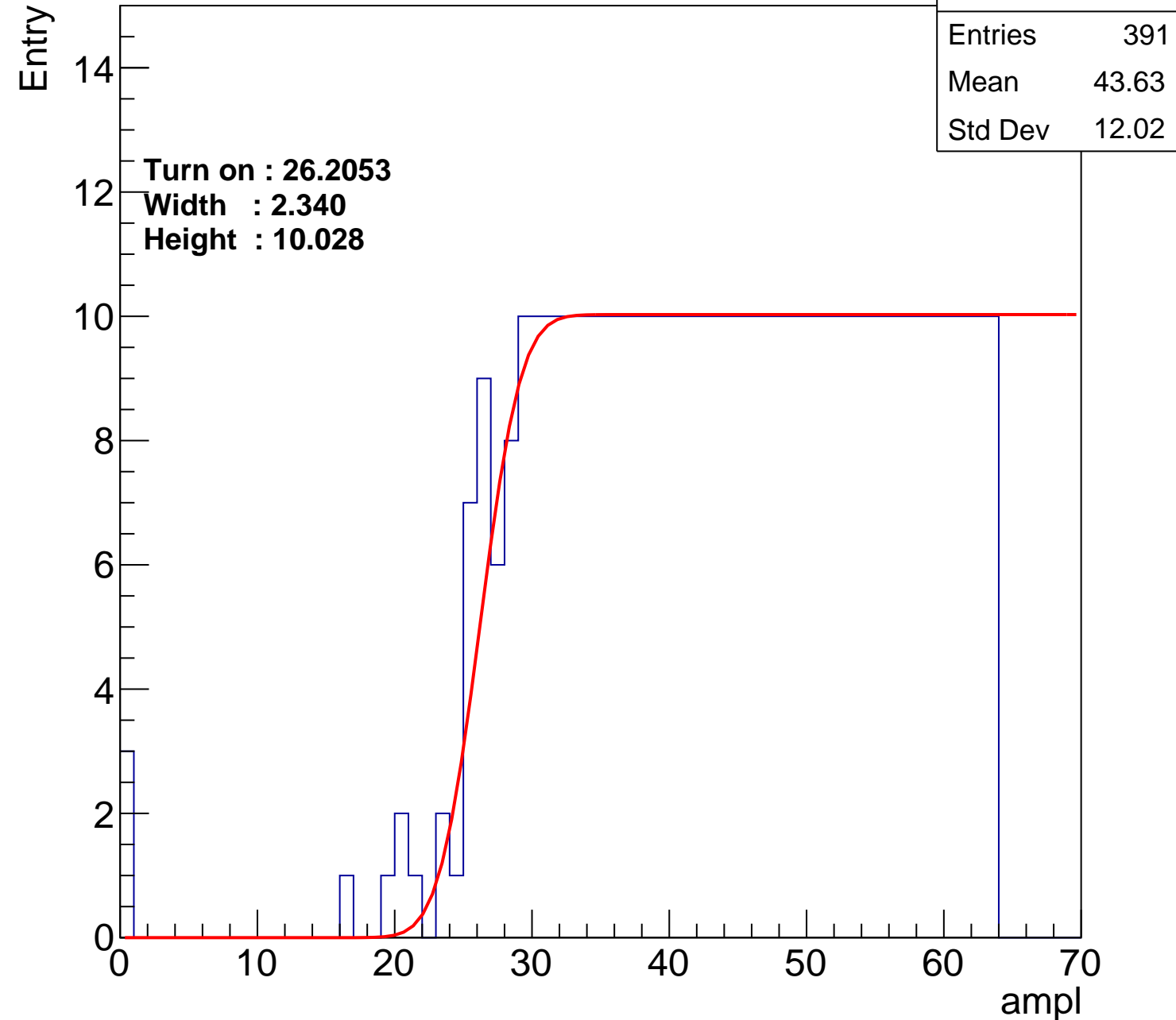
Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U26-ch49

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.08
Std Dev	11.6

Turn on : 25.9275

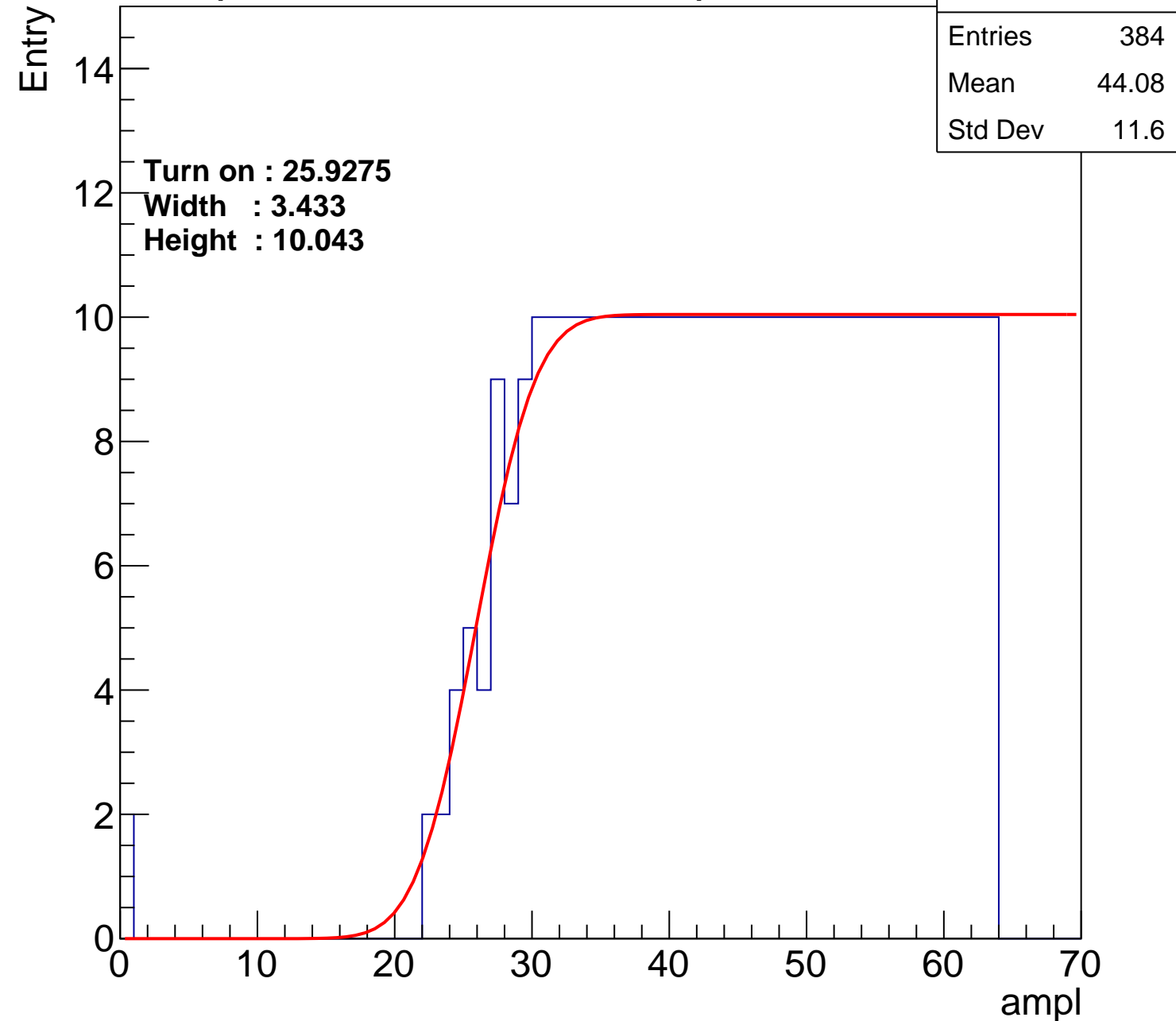
Width : 3.433

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch50

calib_packv5_042523_0143.root, FC#7, port C2

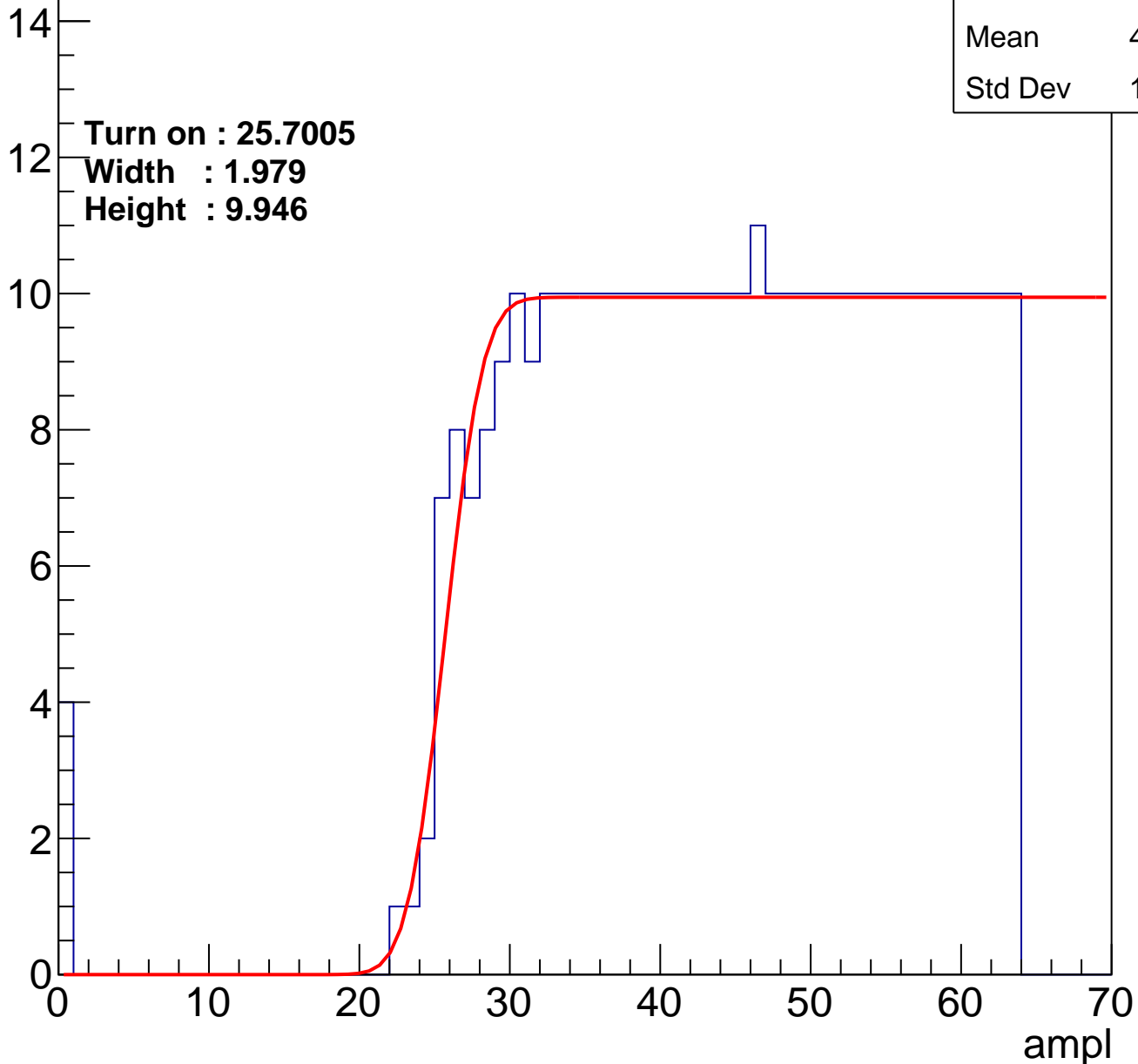
Entries	387
Mean	43.86
Std Dev	11.96

Turn on : 25.7005

Width : 1.979

Height : 9.946

Entry



B1L103S, U26-ch51

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 25.9471

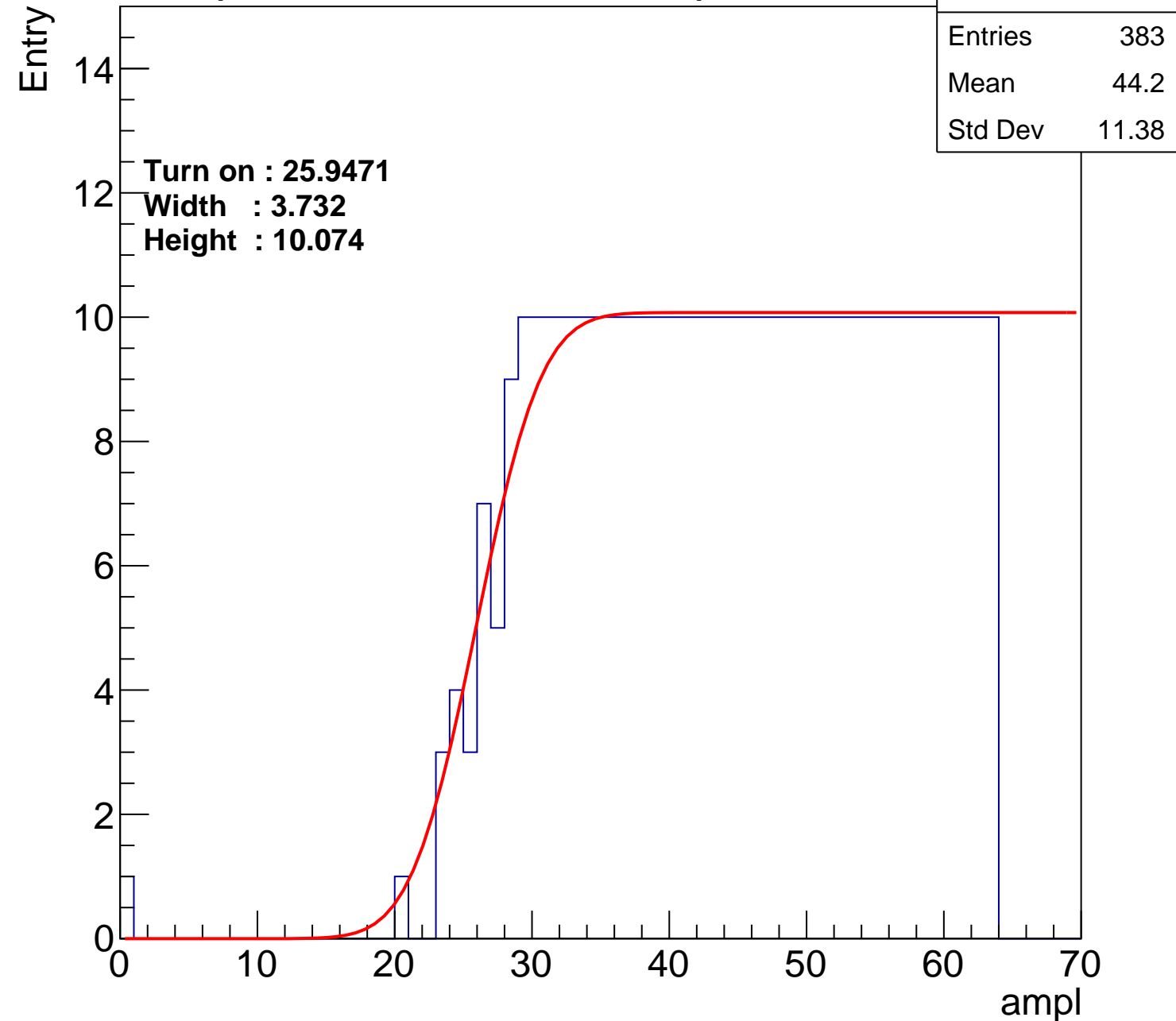
Width : 3.732

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch52

calib_packv5_042523_0143.root, FC#7, port C2

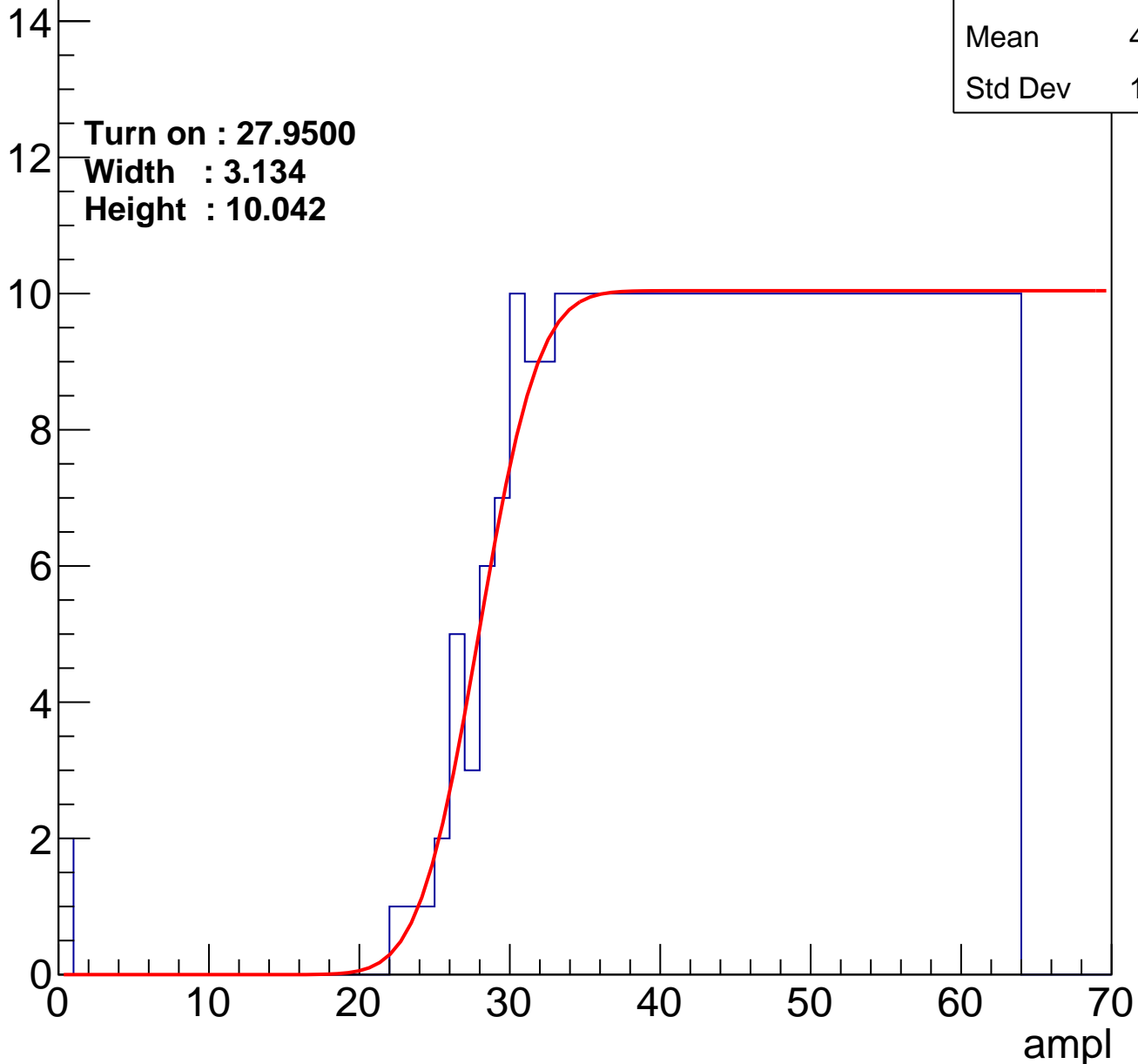
Entries	366
Mean	44.94
Std Dev	11.18

Turn on : 27.9500

Width : 3.134

Height : 10.042

Entry



B1L103S, U26-ch53

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.23
Std Dev	11.24

Turn on : 28.9464

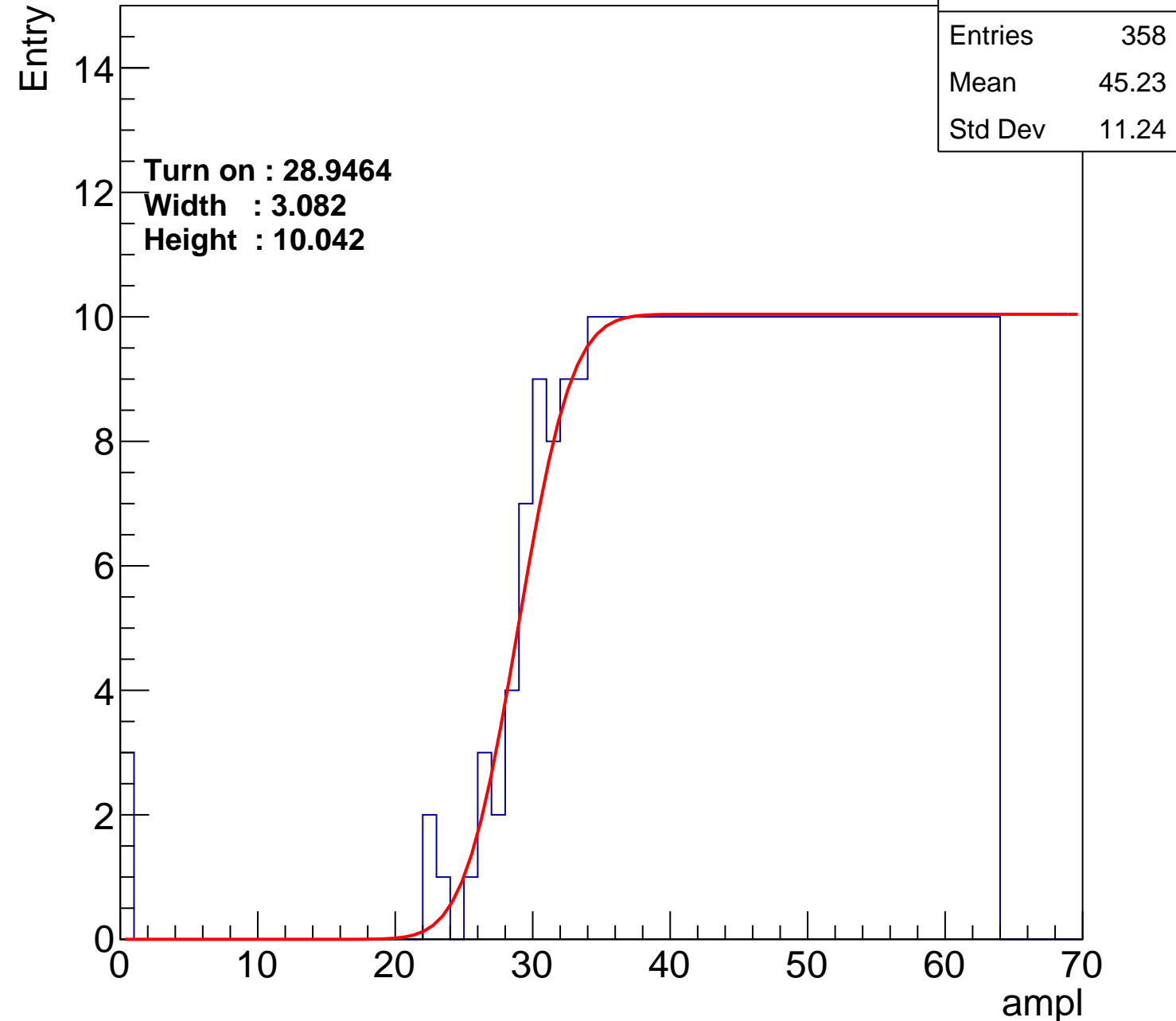
Width : 3.082

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.25
Std Dev	11.45

Turn on : 26.4169

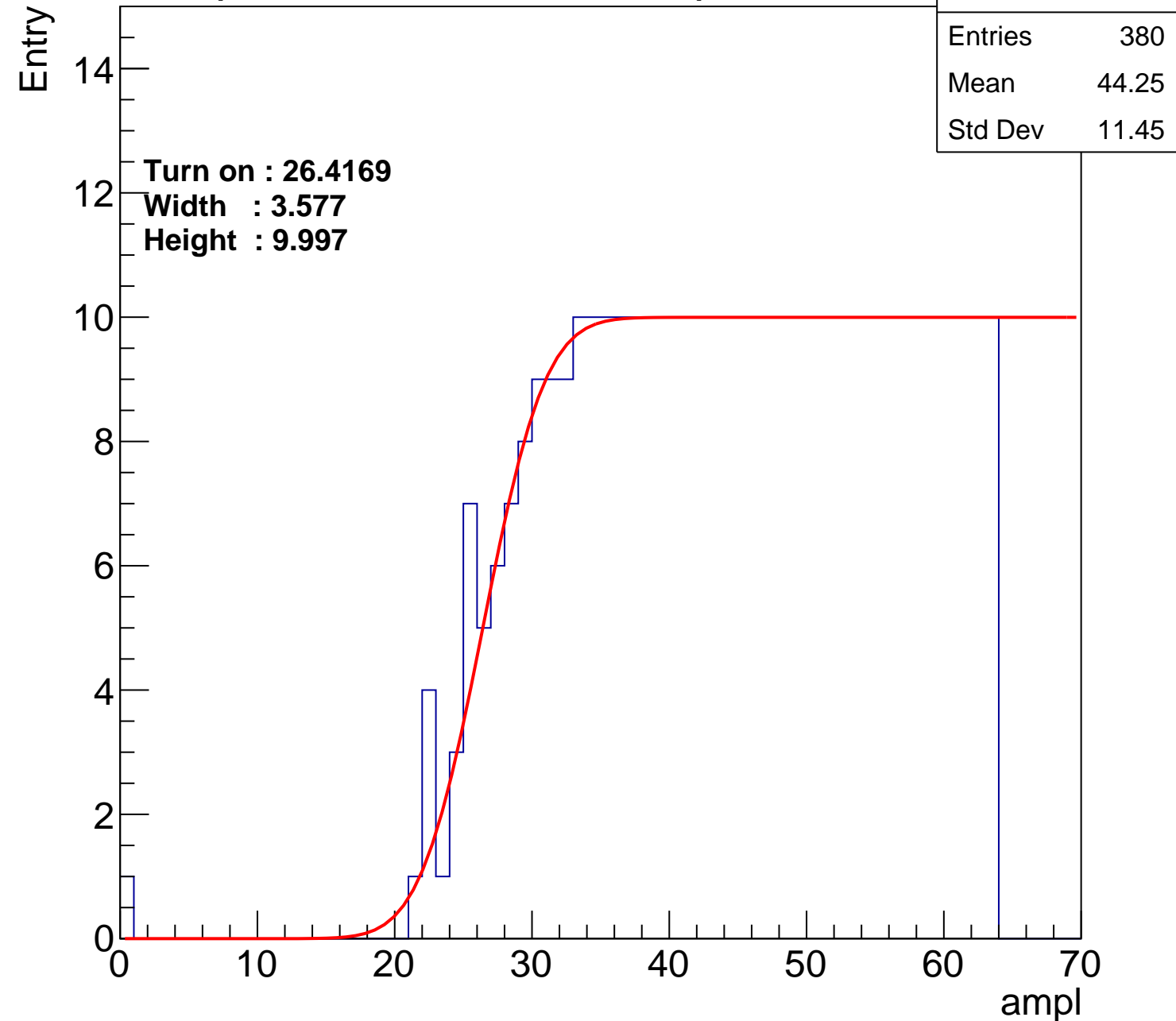
Width : 3.577

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch55

calib_packv5_042523_0143.root, FC#7, port C2

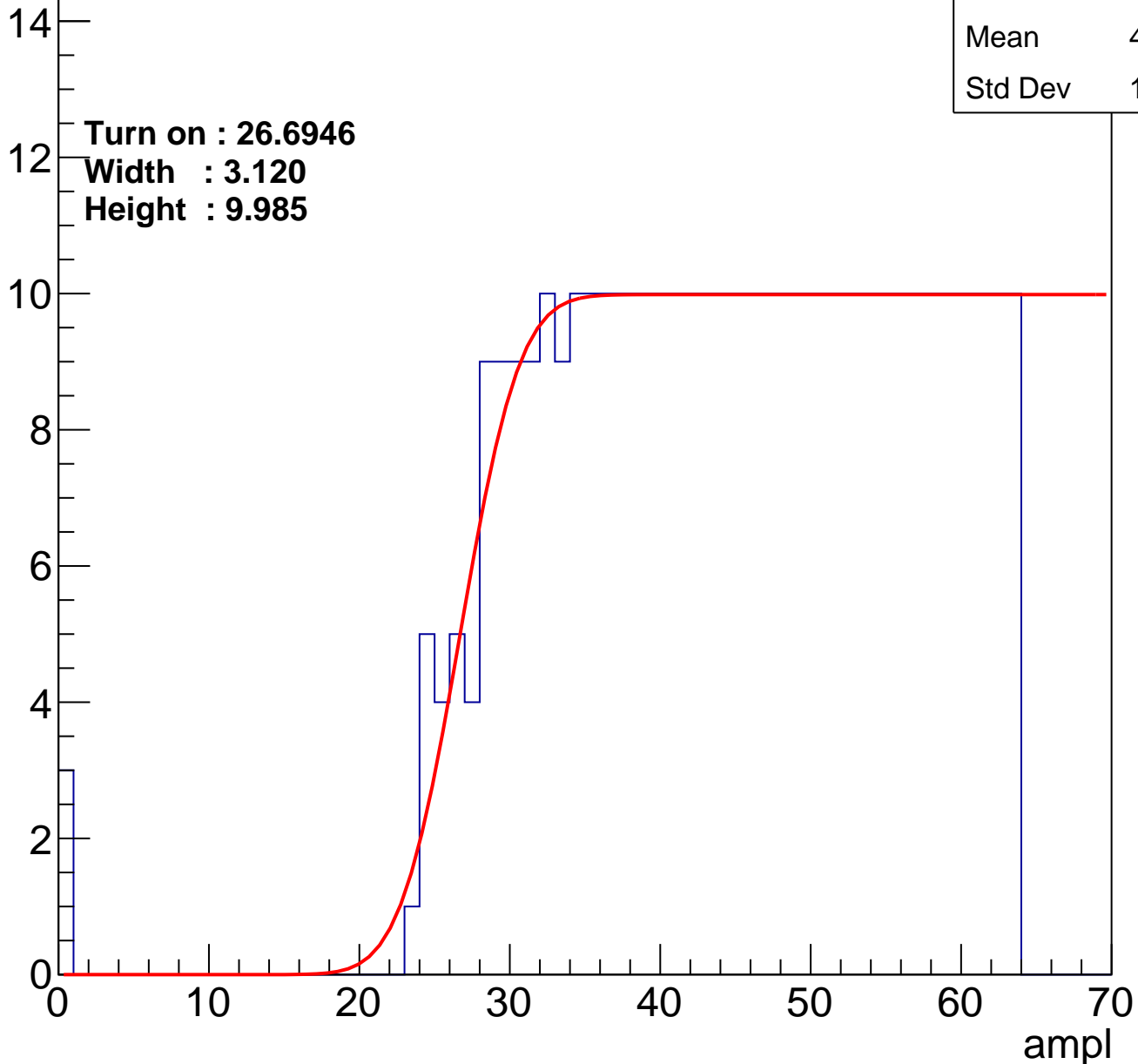
Entries	377
Mean	44.32
Std Dev	11.64

Turn on : 26.6946

Width : 3.120

Height : 9.985

Entry



B1L103S, U26-ch56

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.11
Std Dev	11.25

Turn on : 28.0531

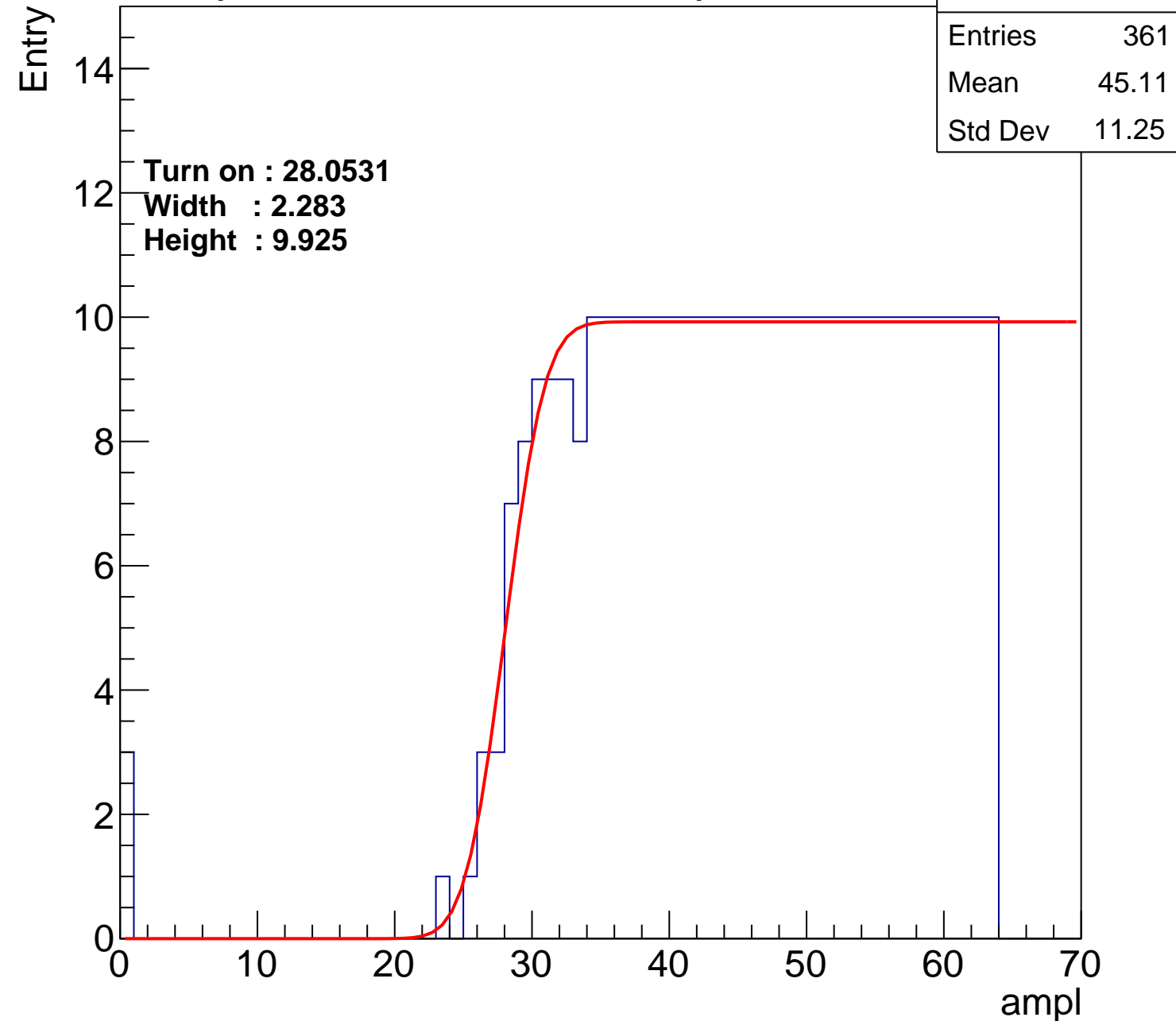
Width : 2.283

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch57

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 26.0531

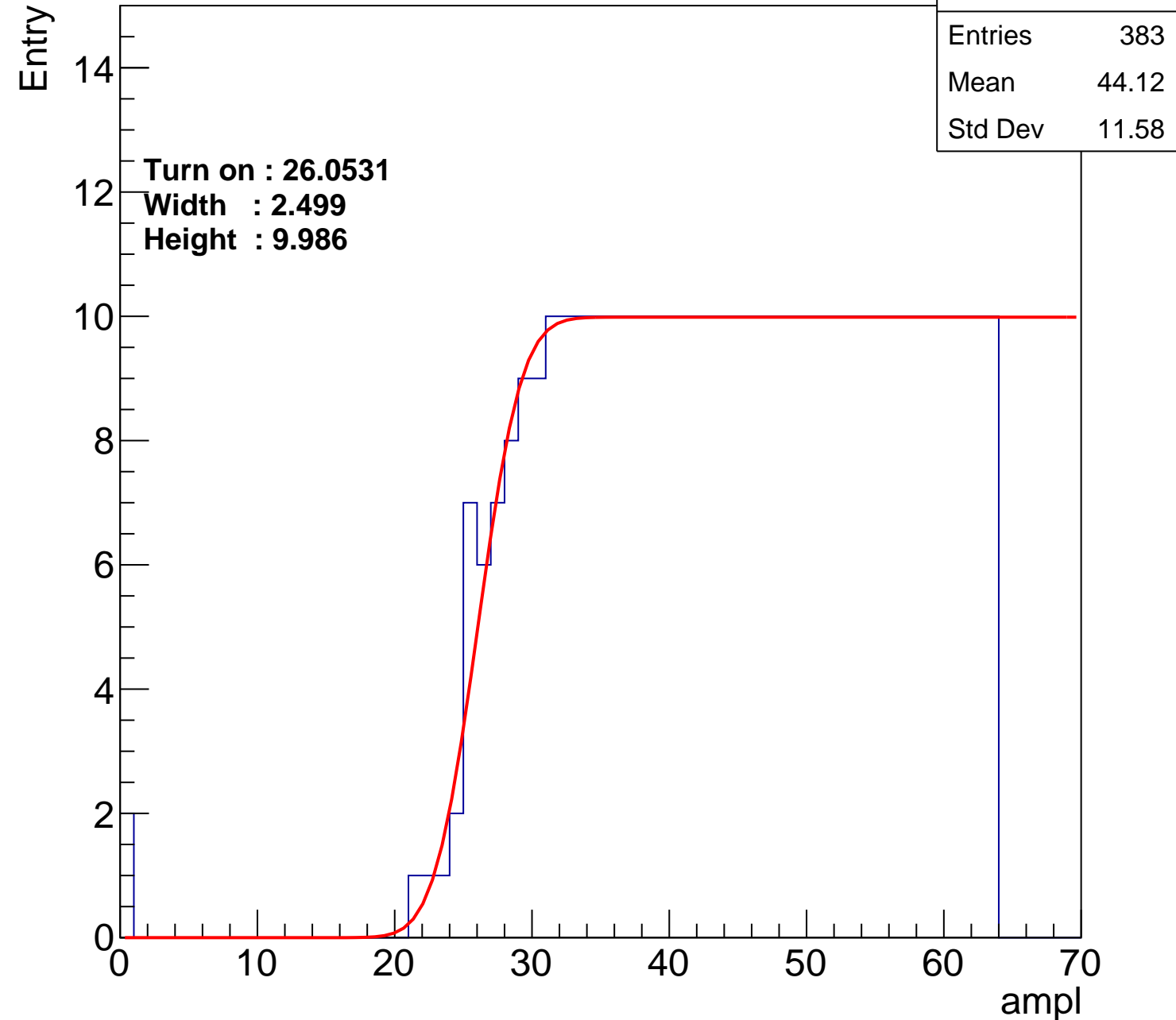
Width : 2.499

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch58

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.5
Std Dev	11.78

Turn on : 24.5116

Width : 2.479

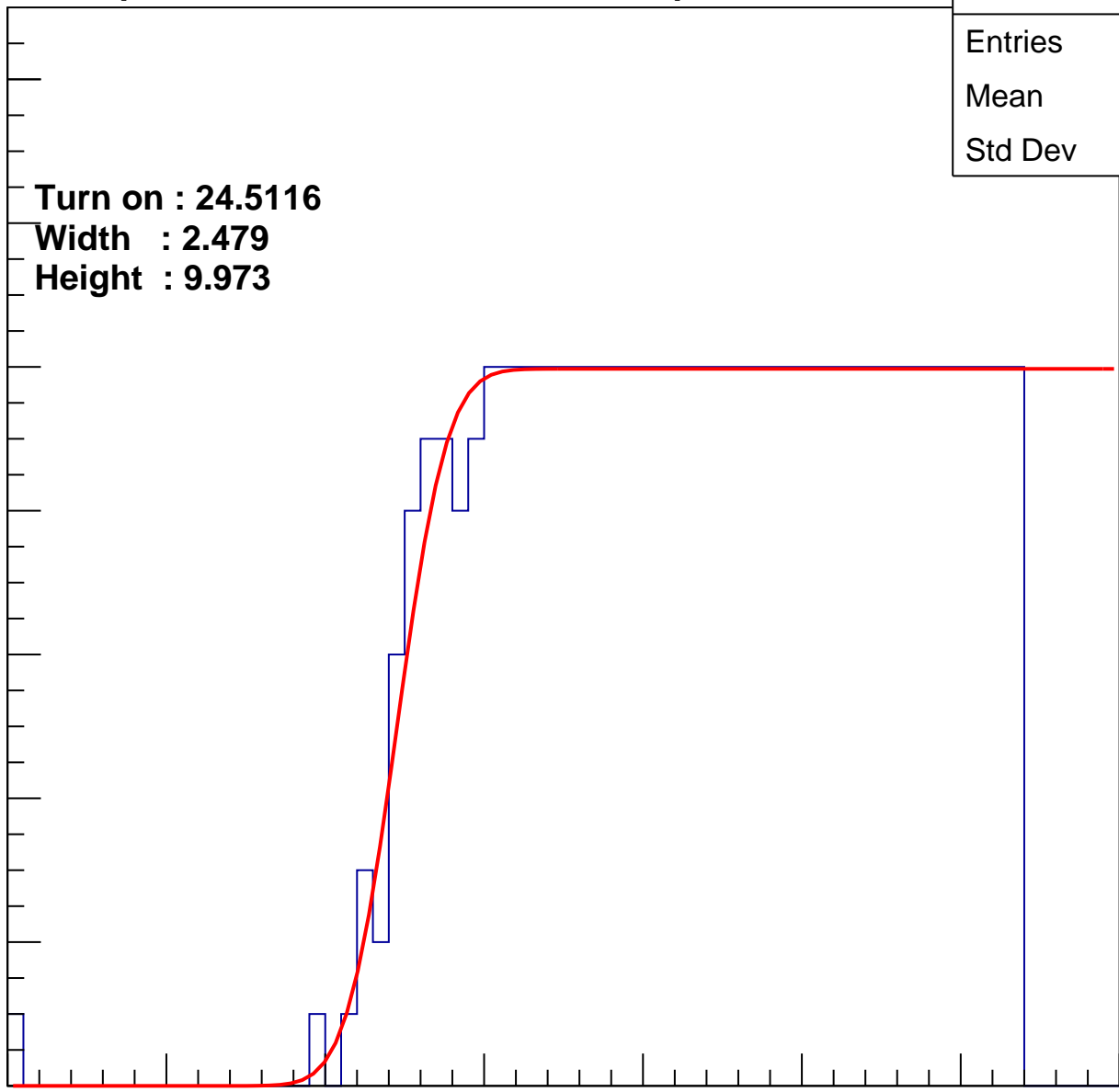
Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U26-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.12
Std Dev	11.31

Turn on : 29.0009

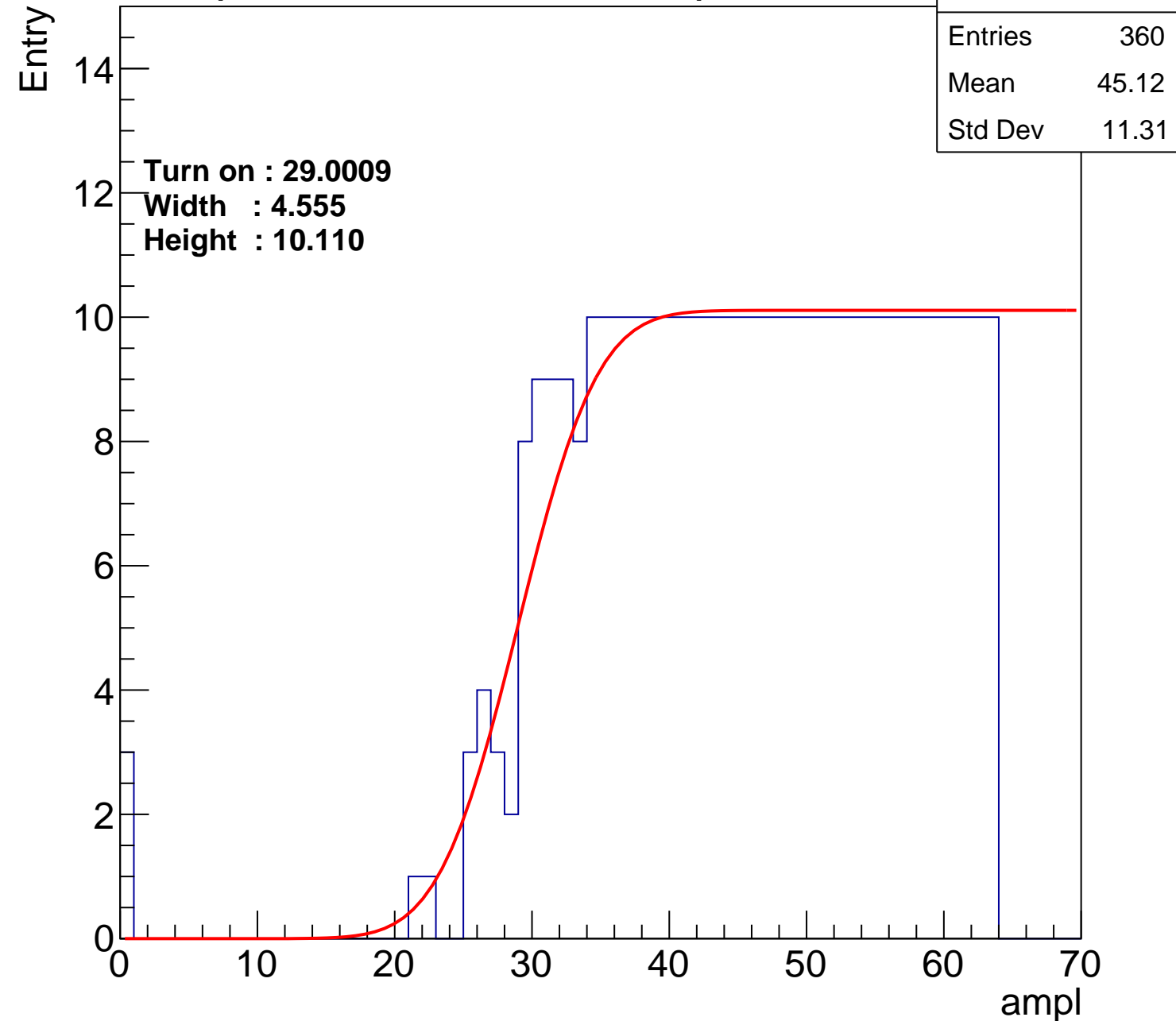
Width : 4.555

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch60

calib_packv5_042523_0143.root, FC#7, port C2

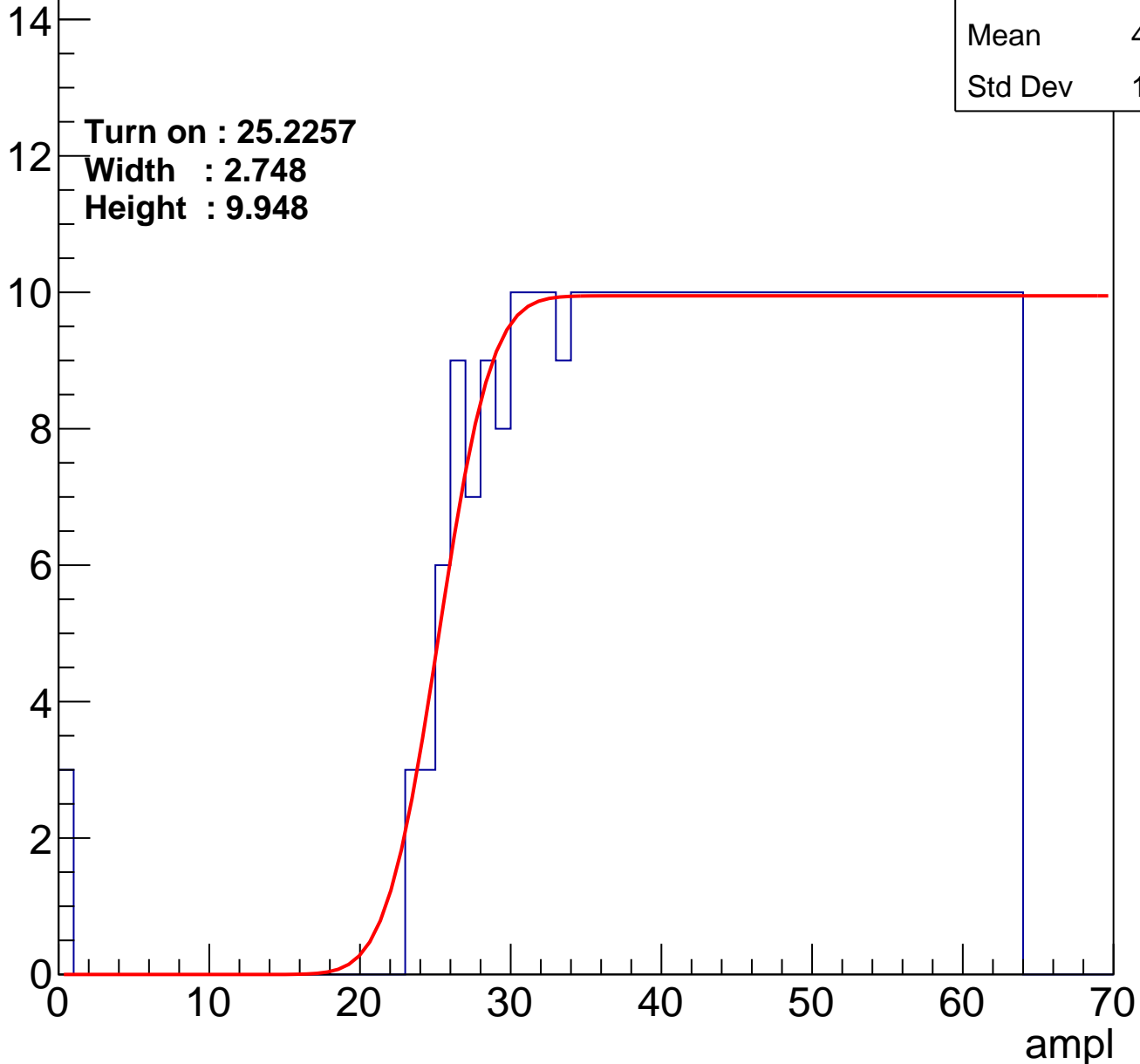
Entries	387
Mean	43.86
Std Dev	11.84

Turn on : 25.2257

Width : 2.748

Height : 9.948

Entry



B1L103S, U26-ch61

calib_packv5_042523_0143.root, FC#7, port C2

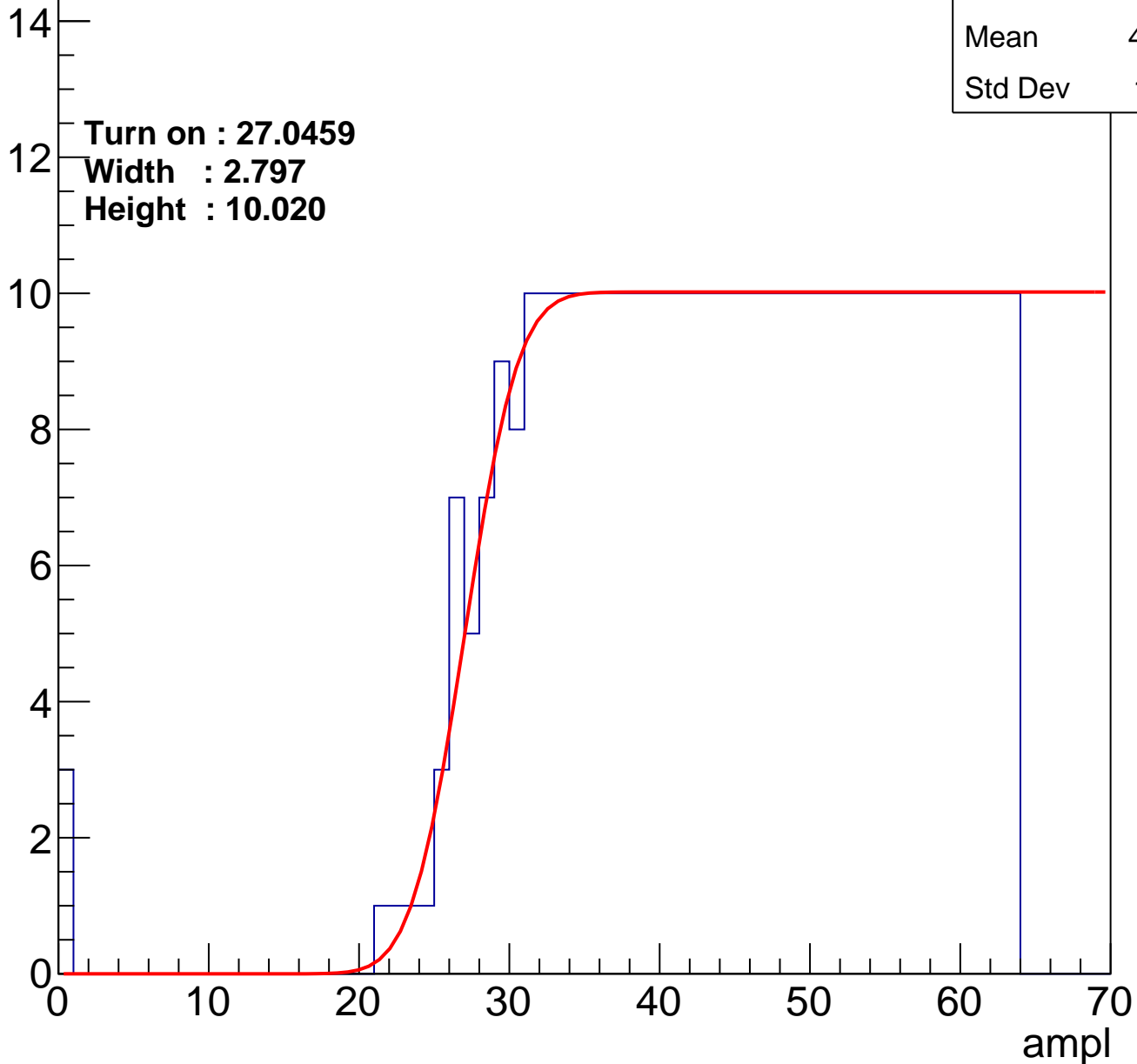
Entry

Entries	376
Mean	44.39
Std Dev	11.61

Turn on : 27.0459

Width : 2.797

Height : 10.020



B1L103S, U26-ch62

calib_packv5_042523_0143.root, FC#7, port C2

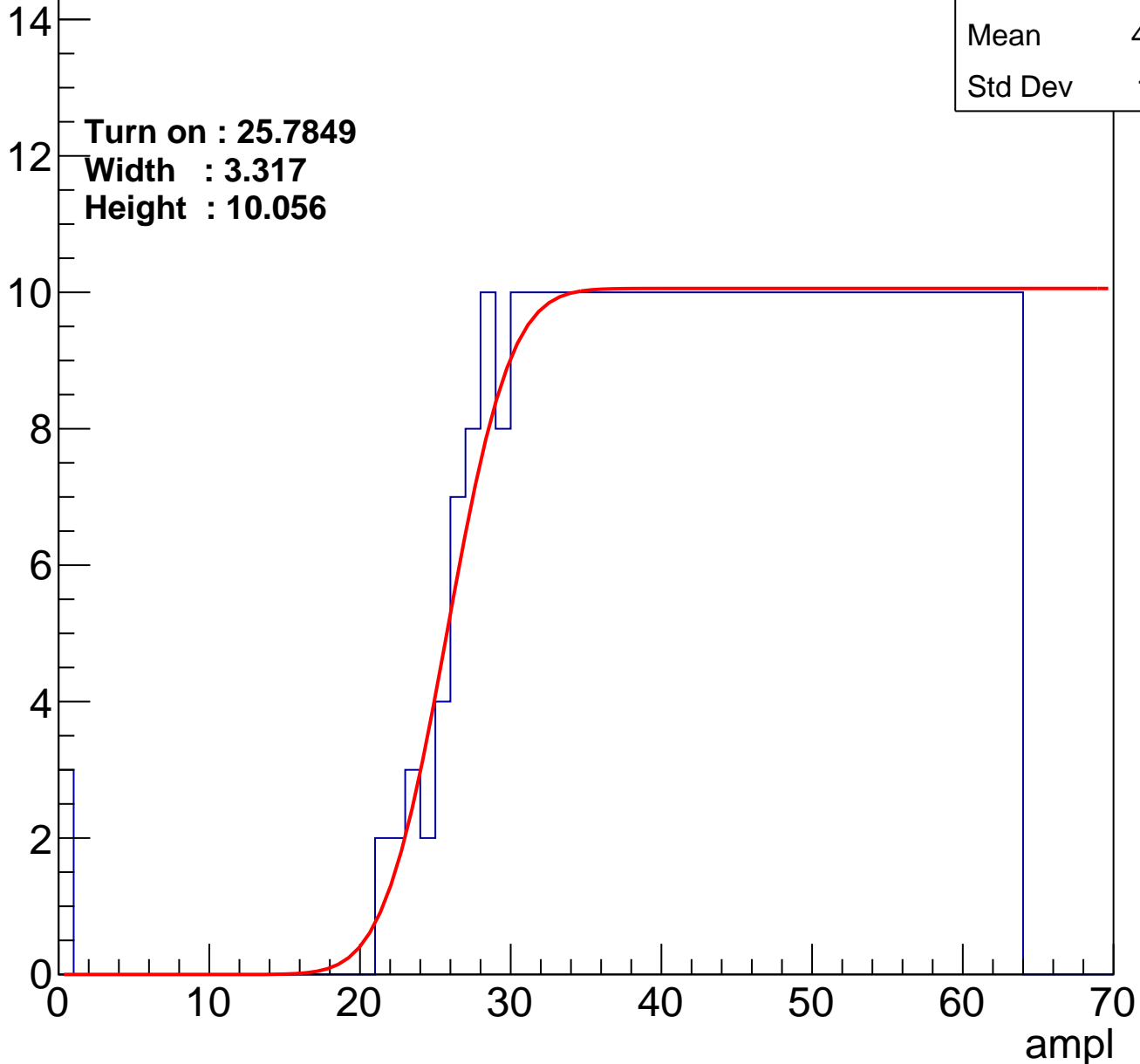
Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.7849

Width : 3.317

Height : 10.056

Entry



B1L103S, U26-ch63

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.72
Std Dev	11.08

Turn on : 26.4461

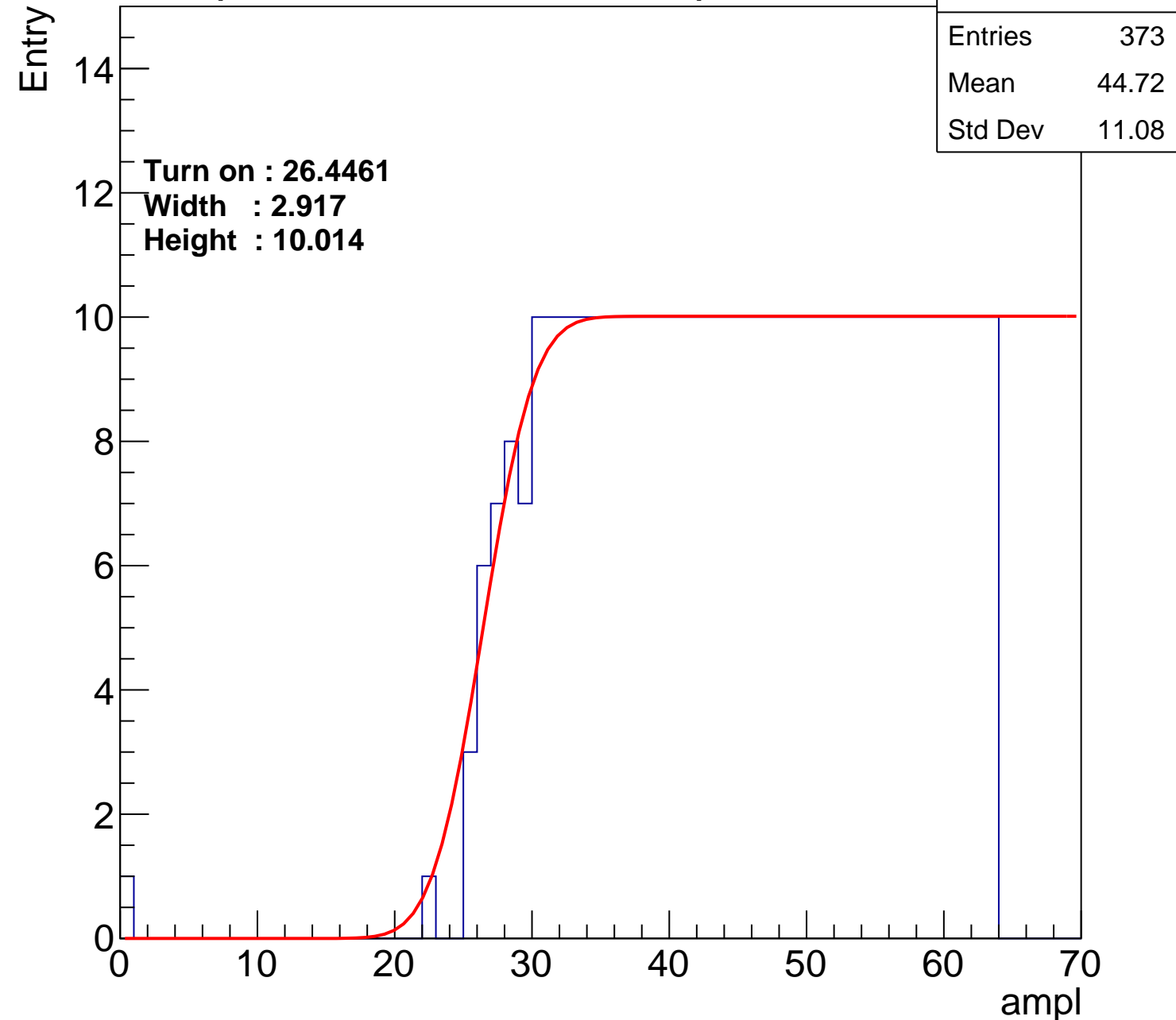
Width : 2.917

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch64

calib_packv5_042523_0143.root, FC#7, port C2

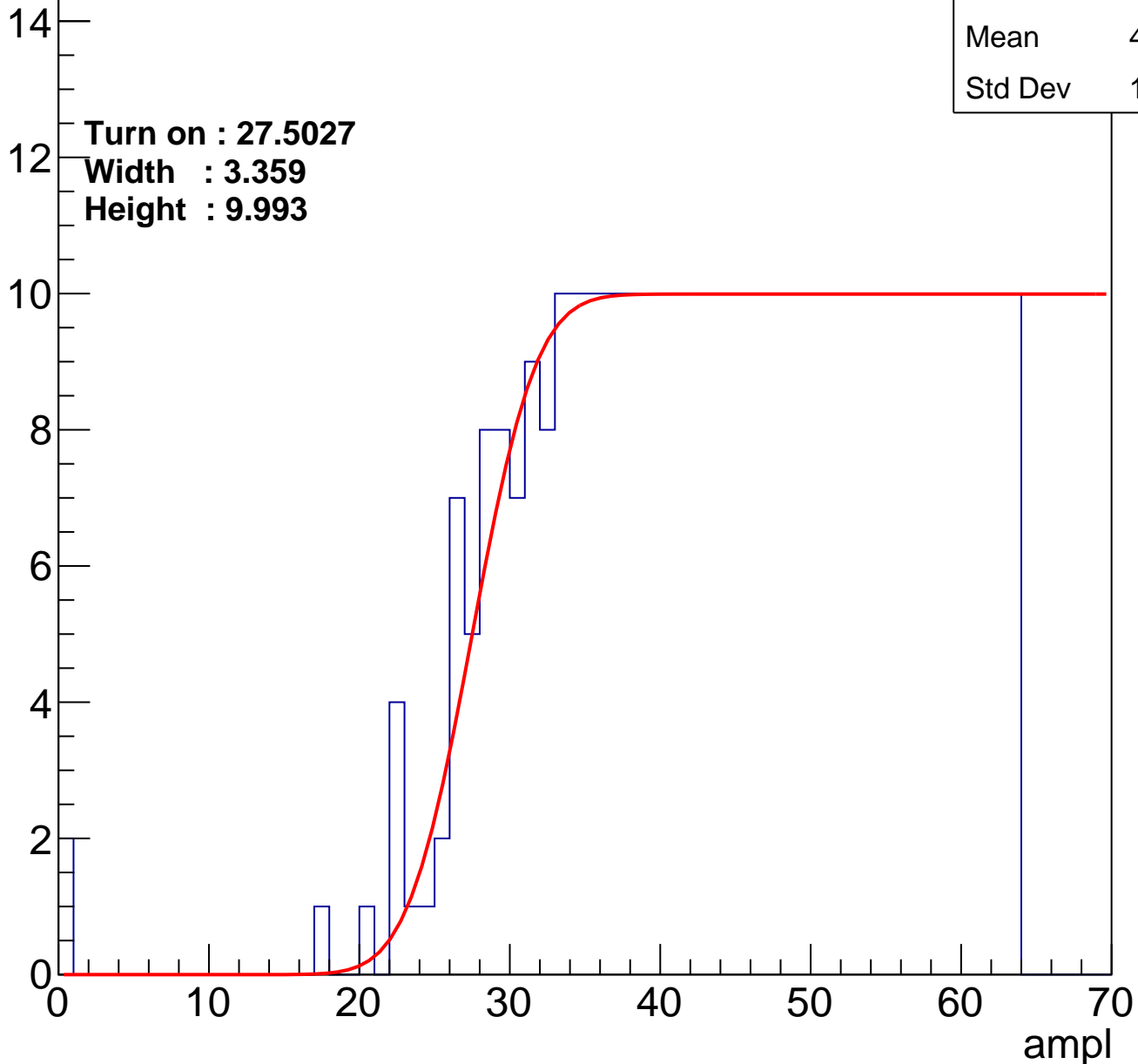
Entries	374
Mean	44.44
Std Dev	11.56

Turn on : 27.5027

Width : 3.359

Height : 9.993

Entry



B1L103S, U26-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	45.11
Std Dev	10.92

Turn on : 27.8187

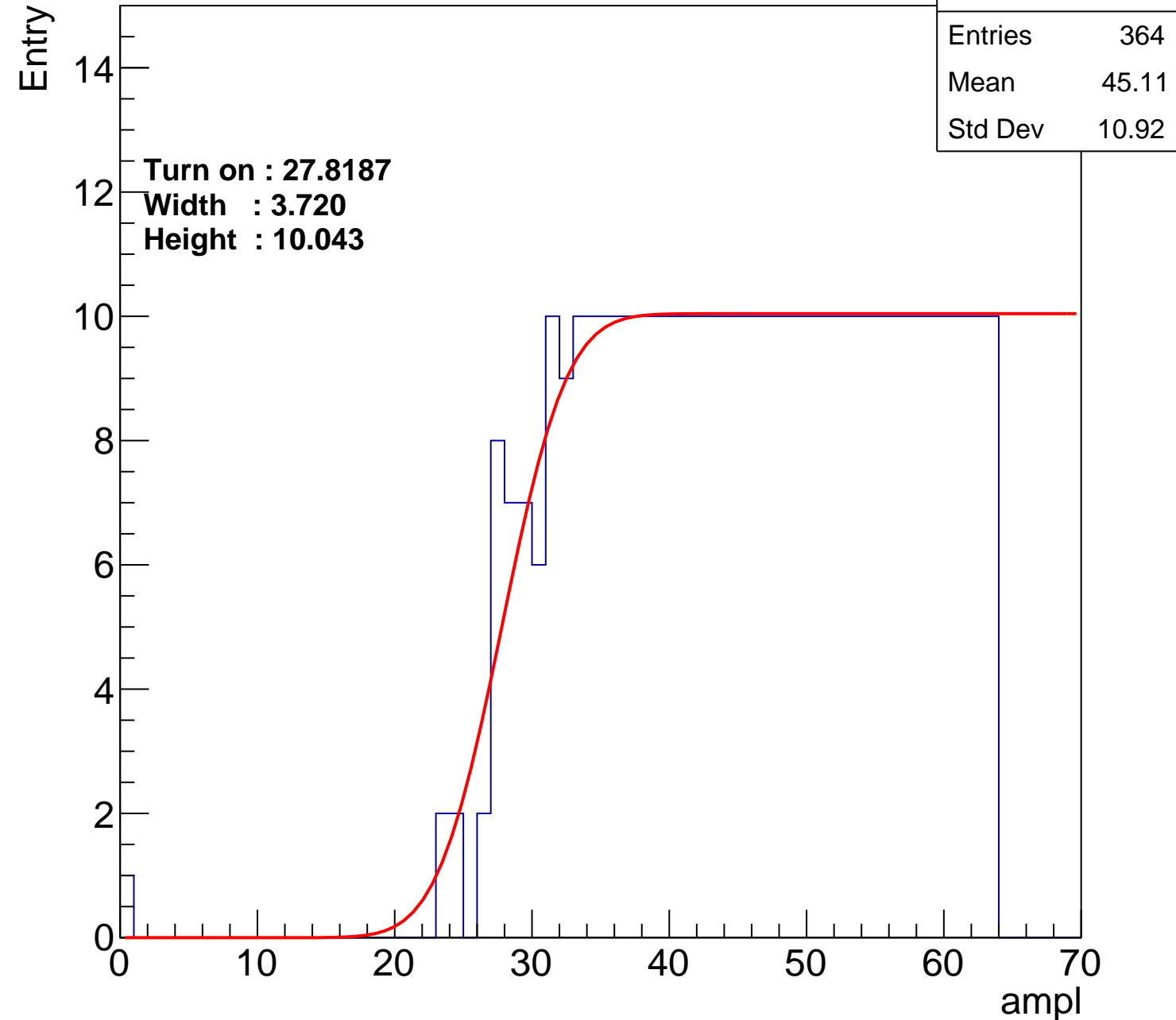
Width : 3.720

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch66

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.58
Std Dev	11.53

Turn on : 27.6631

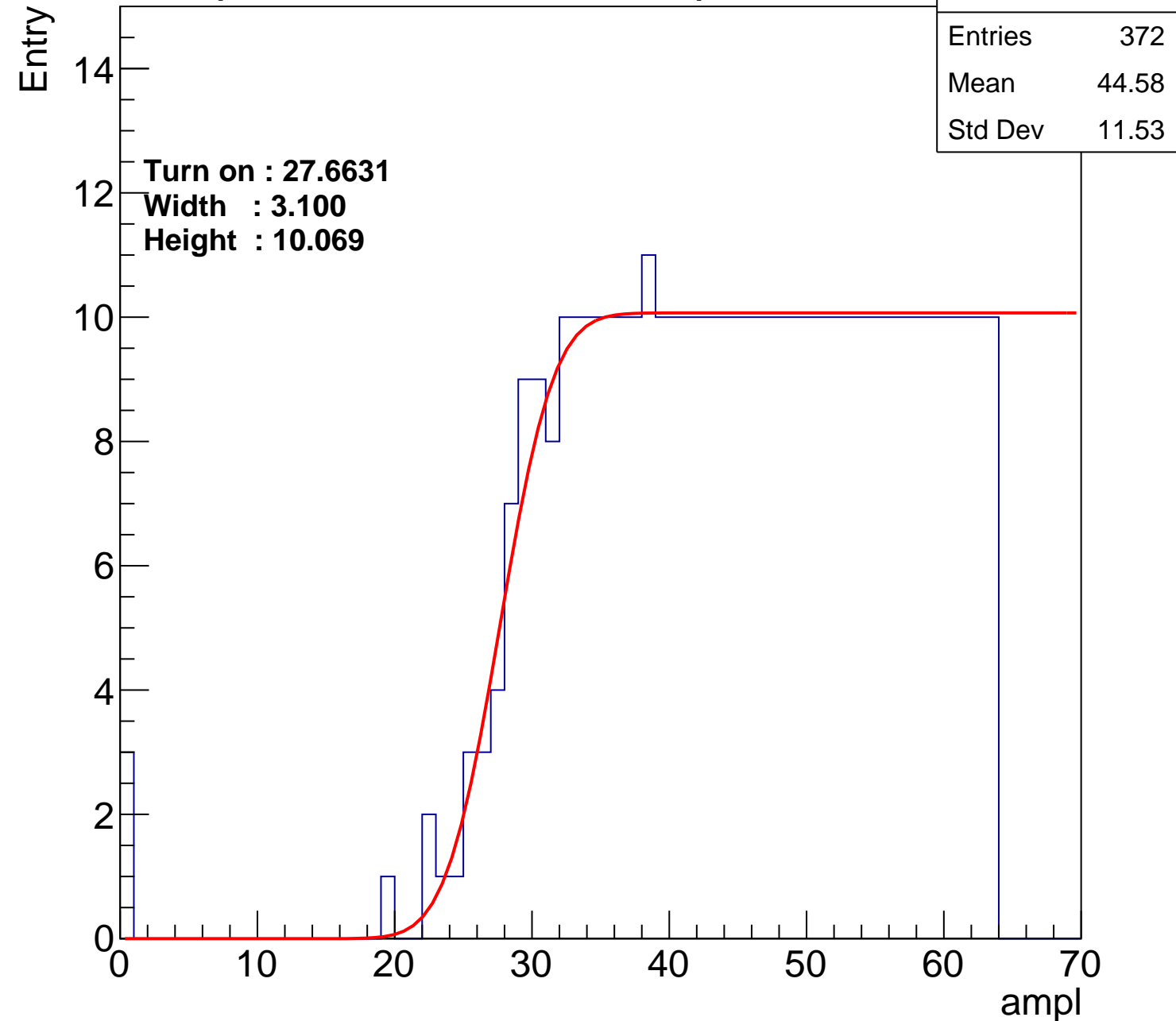
Width : 3.100

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch67

calib_packv5_042523_0143.root, FC#7, port C2

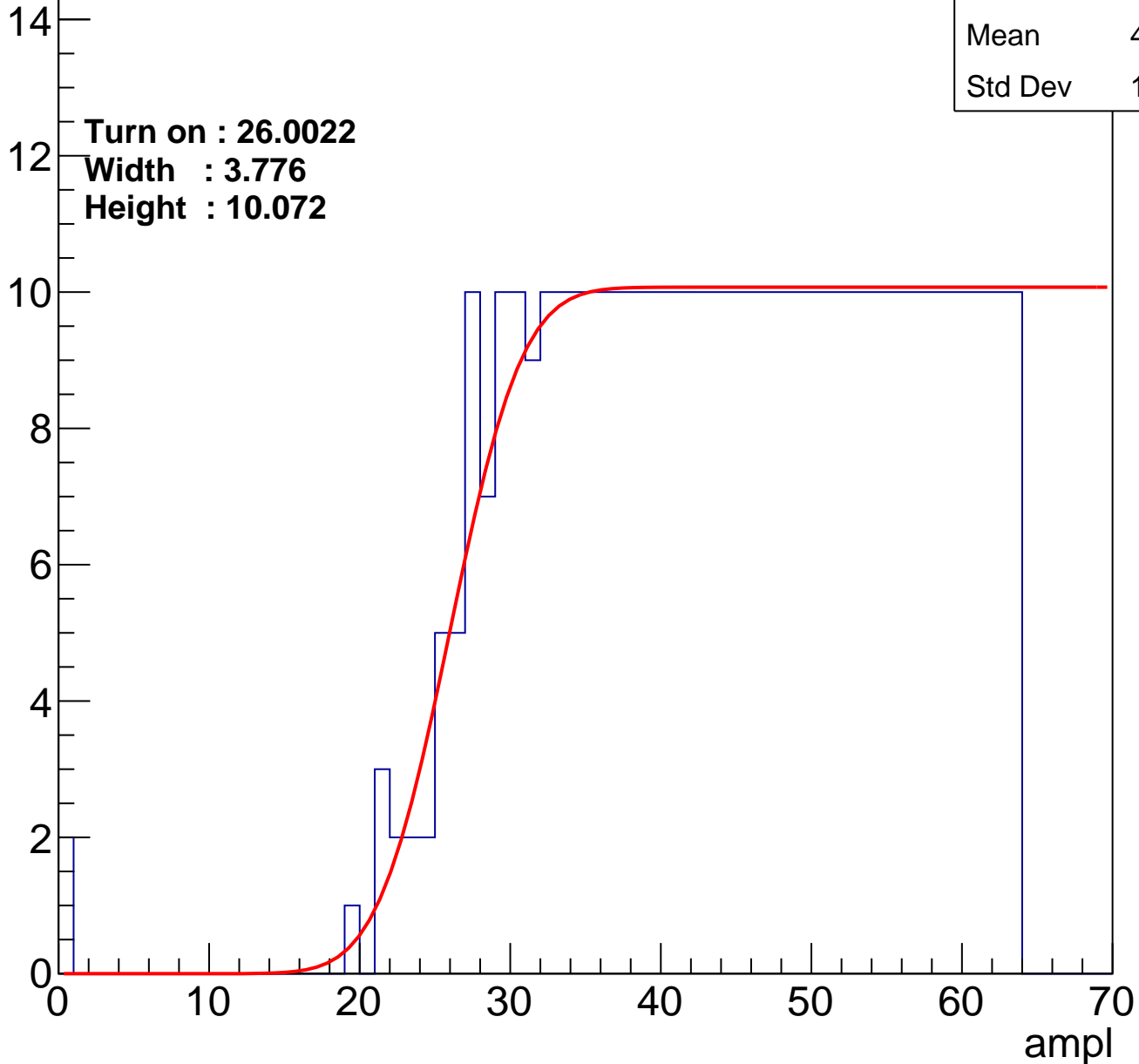
Entries	388
Mean	43.84
Std Dev	11.77

Turn on : 26.0022

Width : 3.776

Height : 10.072

Entry



B1L103S, U26-ch68

calib_packv5_042523_0143.root, FC#7, port C2

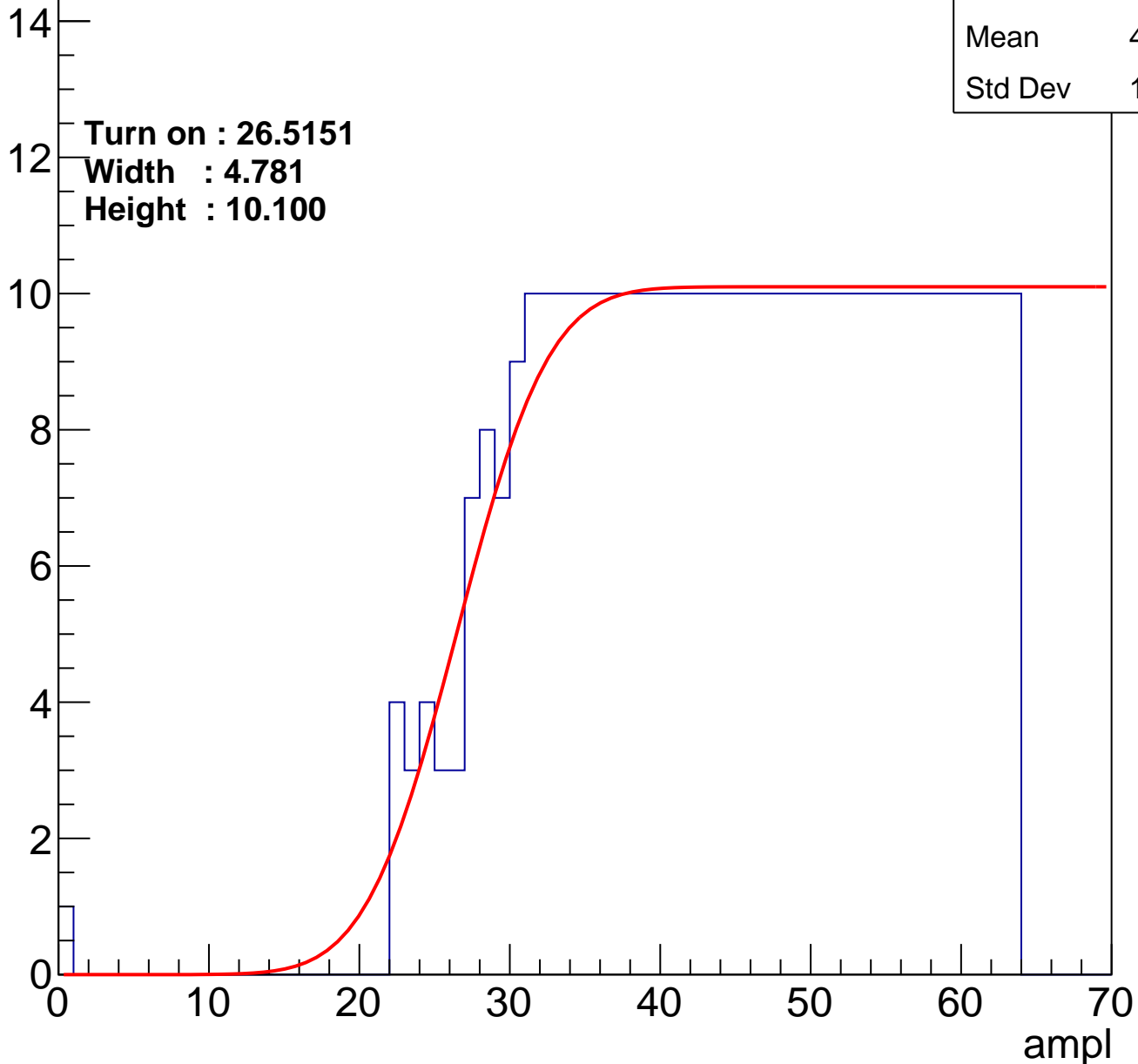
Entries	379
Mean	44.33
Std Dev	11.38

Turn on : 26.5151

Width : 4.781

Height : 10.100

Entry



B1L103S, U26-ch69

calib_packv5_042523_0143.root, FC#7, port C2

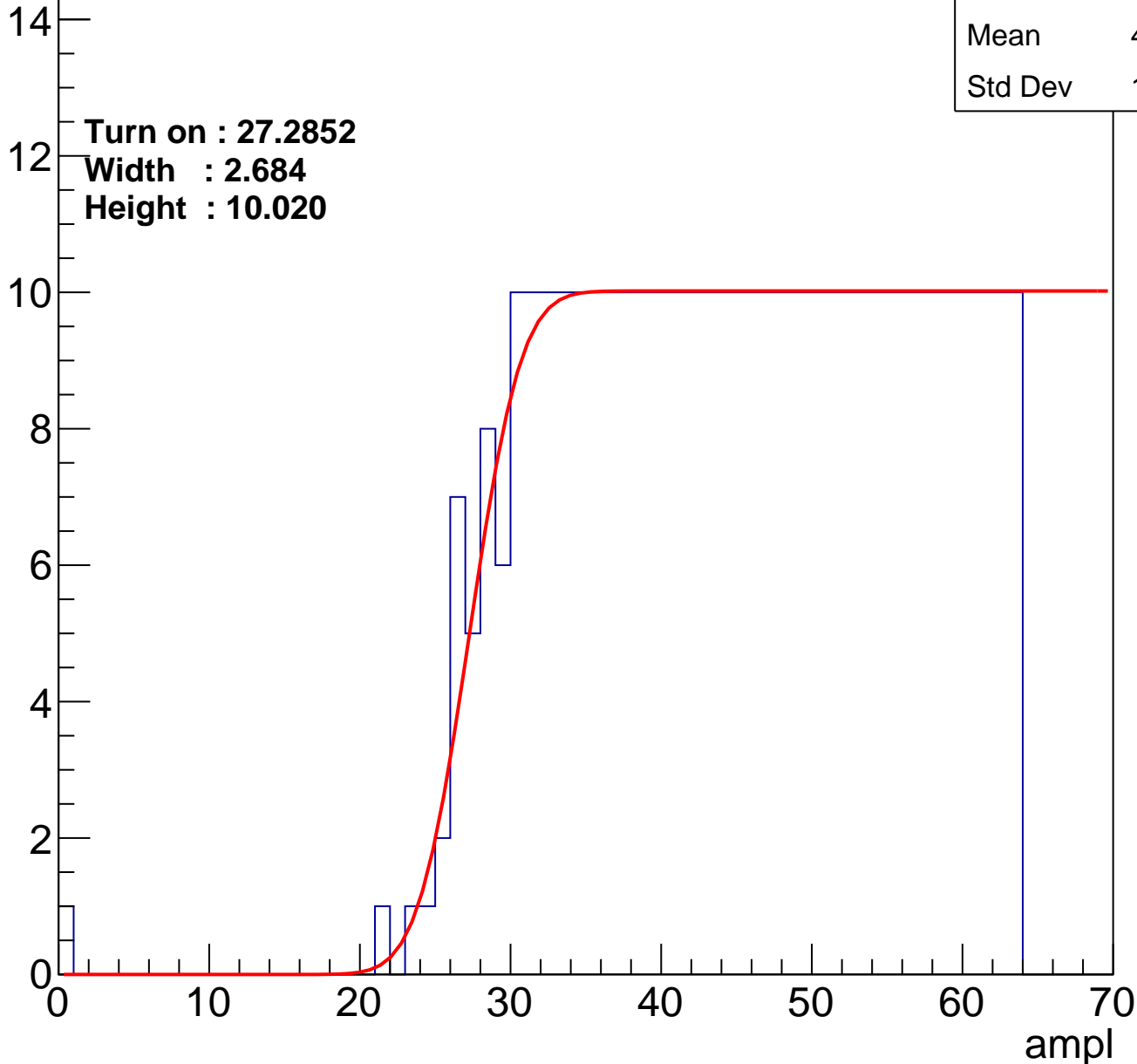
Entries	372
Mean	44.74
Std Dev	11.09

Turn on : 27.2852

Width : 2.684

Height : 10.020

Entry



B1L103S, U26-ch70

calib_packv5_042523_0143.root, FC#7, port C2

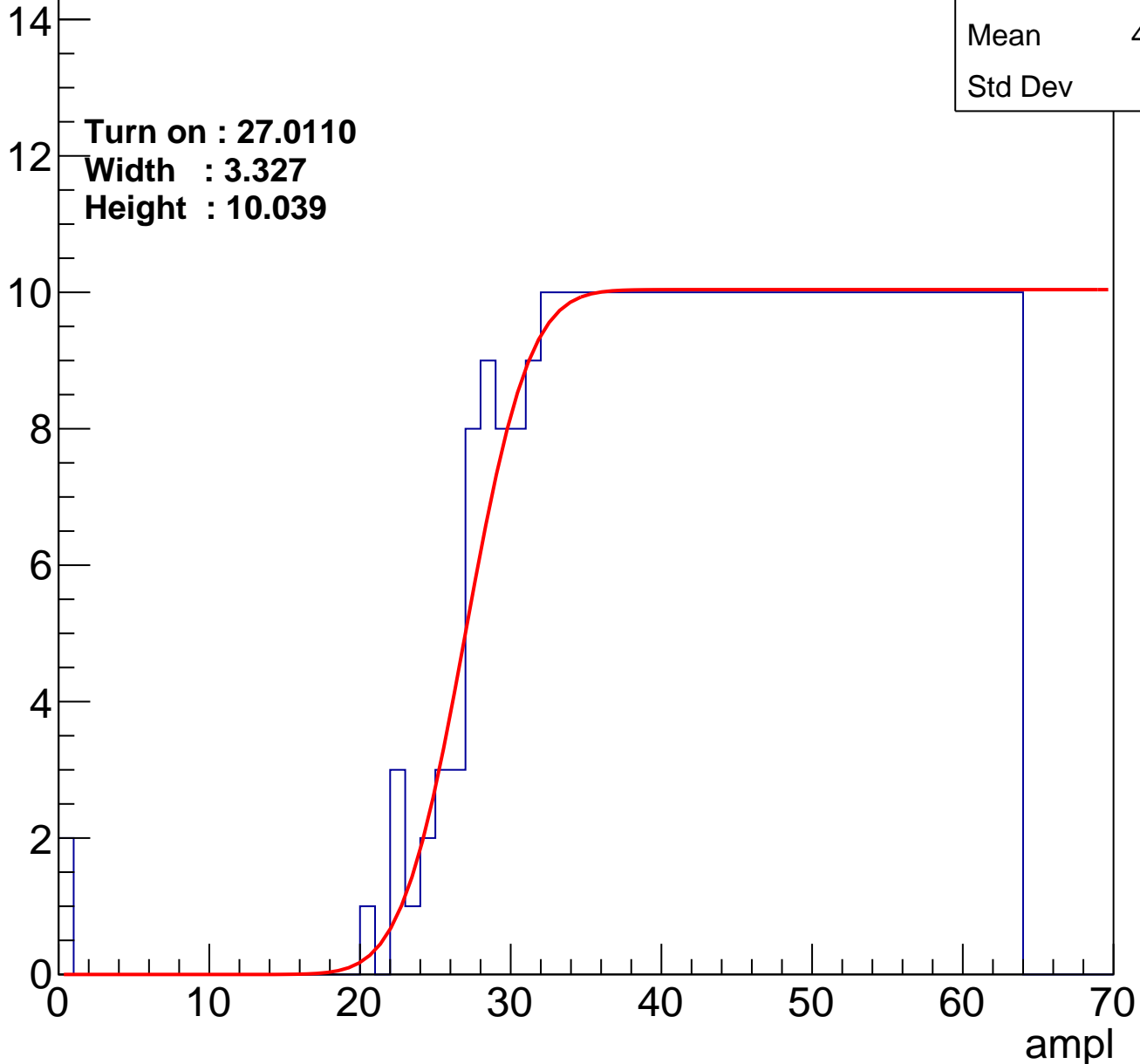
Entries	377
Mean	44.37
Std Dev	11.5

Turn on : 27.0110

Width : 3.327

Height : 10.039

Entry



B1L103S, U26-ch71

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.75
Std Dev	11.13

Turn on : 28.1823

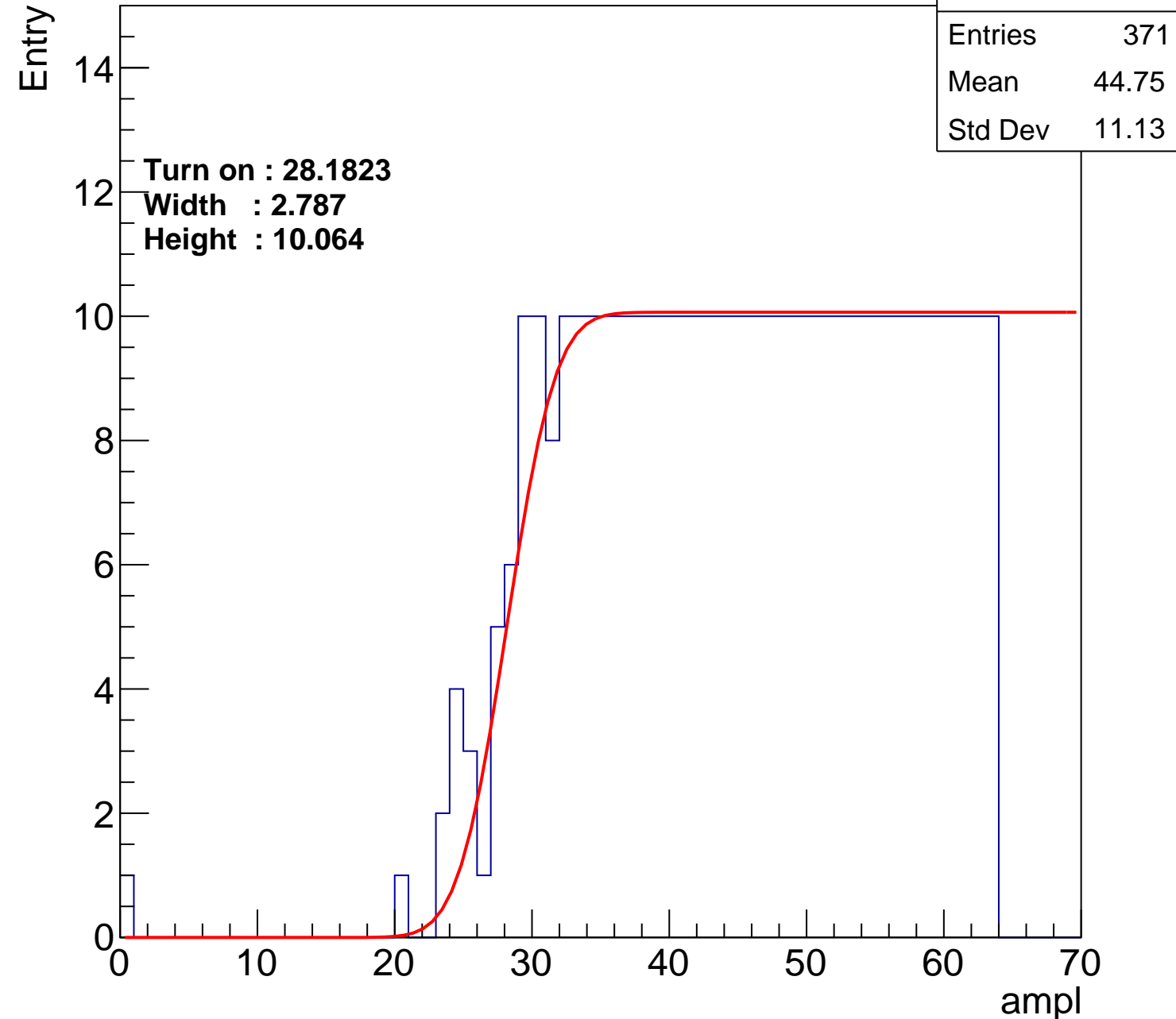
Width : 2.787

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch72

calib_packv5_042523_0143.root, FC#7, port C2

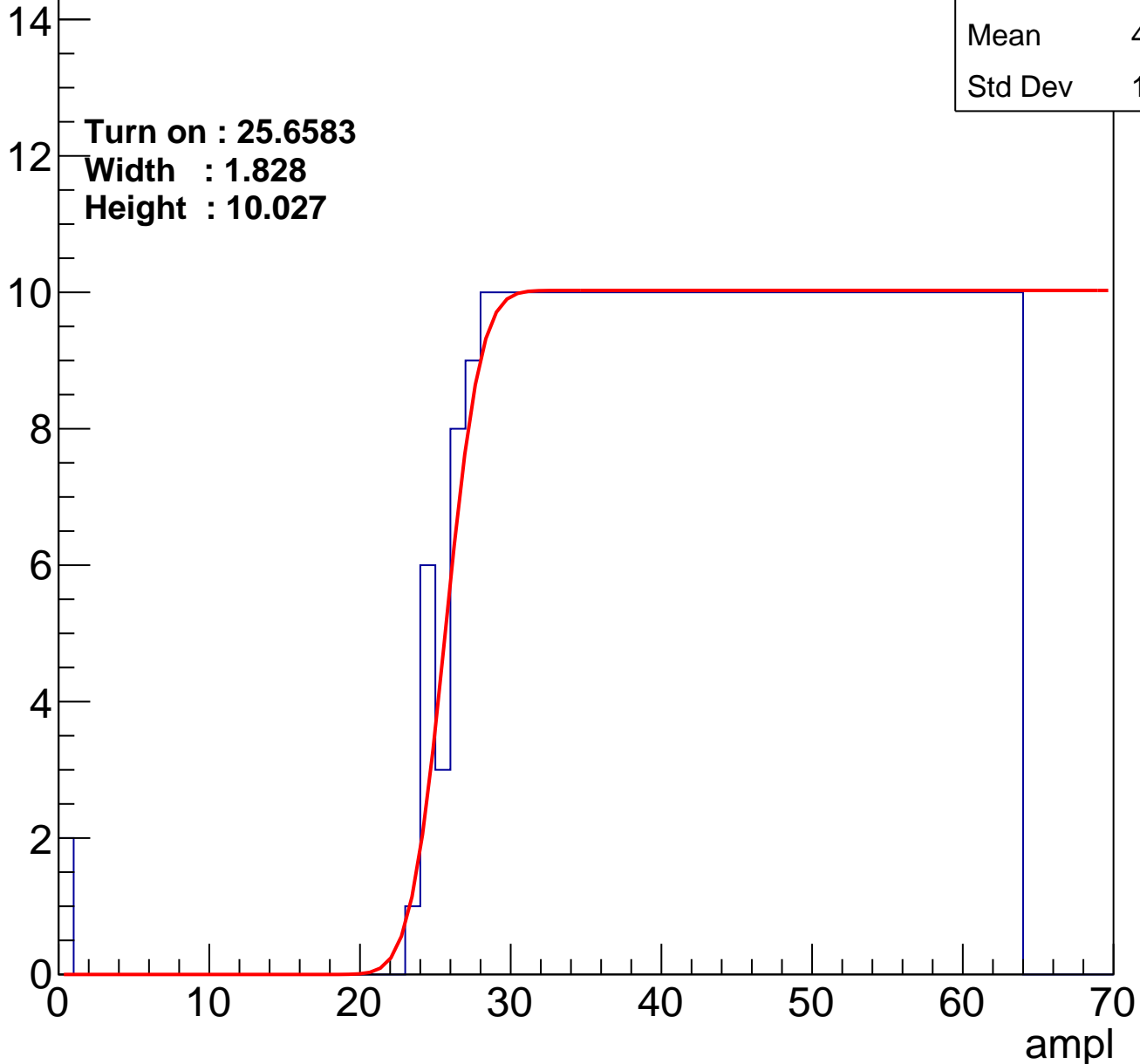
Entries	389
Mean	43.89
Std Dev	11.63

Turn on : 25.6583

Width : 1.828

Height : 10.027

Entry



B1L103S, U26-ch73

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.63
Std Dev	11.49

Turn on : 27.0421

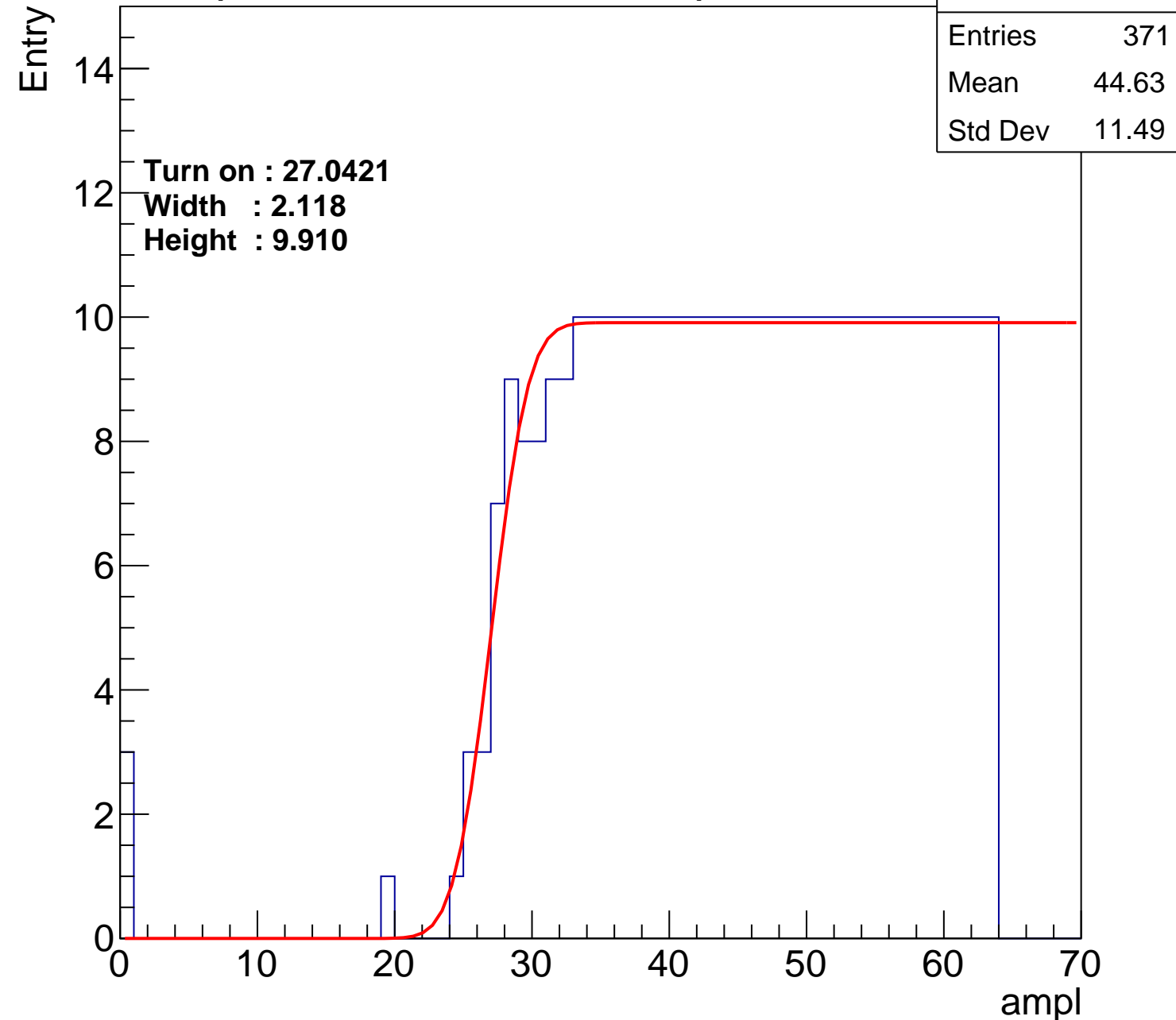
Width : 2.118

Height : 9.910

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch74

calib_packv5_042523_0143.root, FC#7, port C2

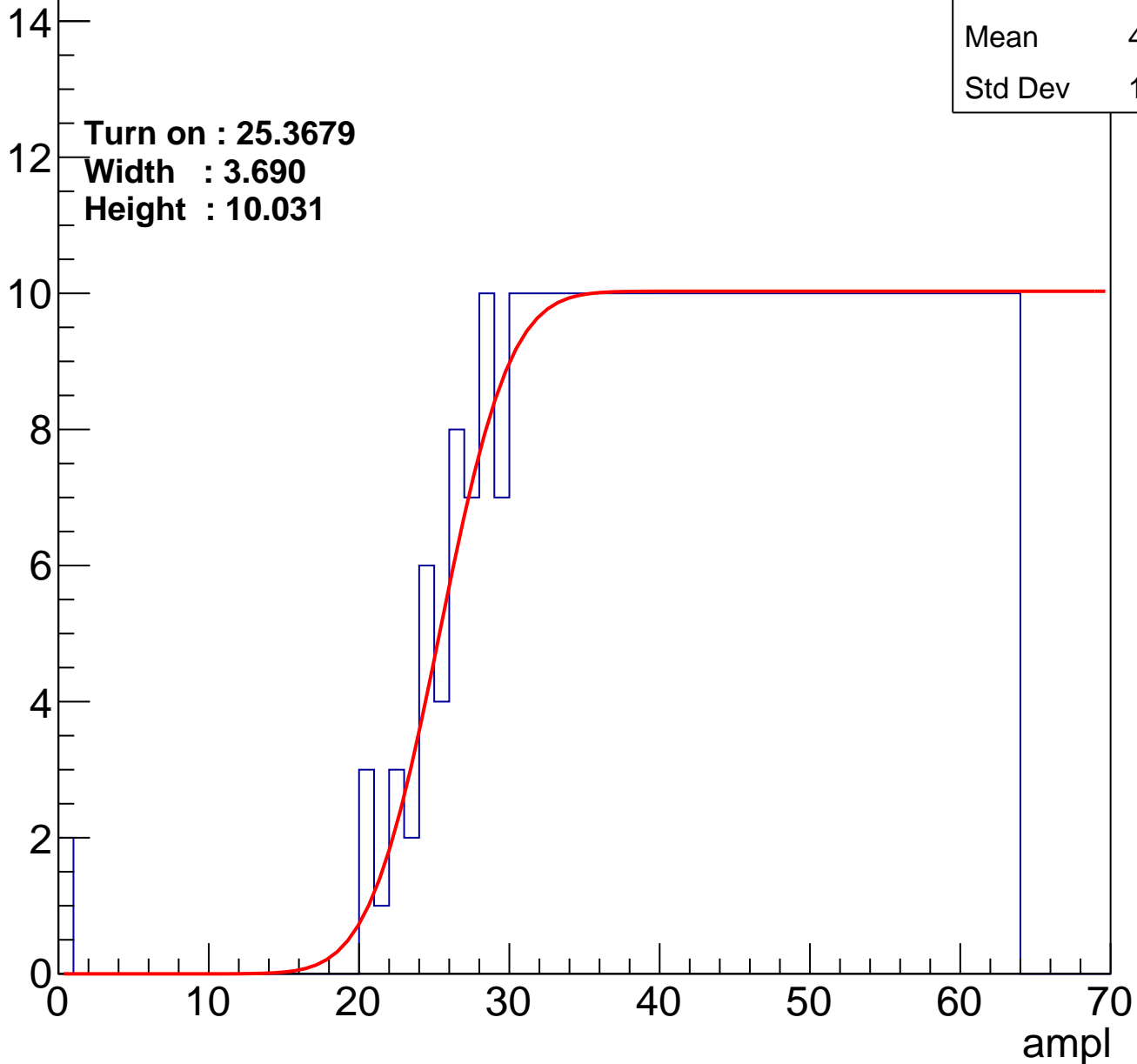
Entries	393
Mean	43.58
Std Dev	11.92

Turn on : 25.3679

Width : 3.690

Height : 10.031

Entry



B1L103S, U26-ch75

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.71
Std Dev	11.2

Turn on : 28.2950

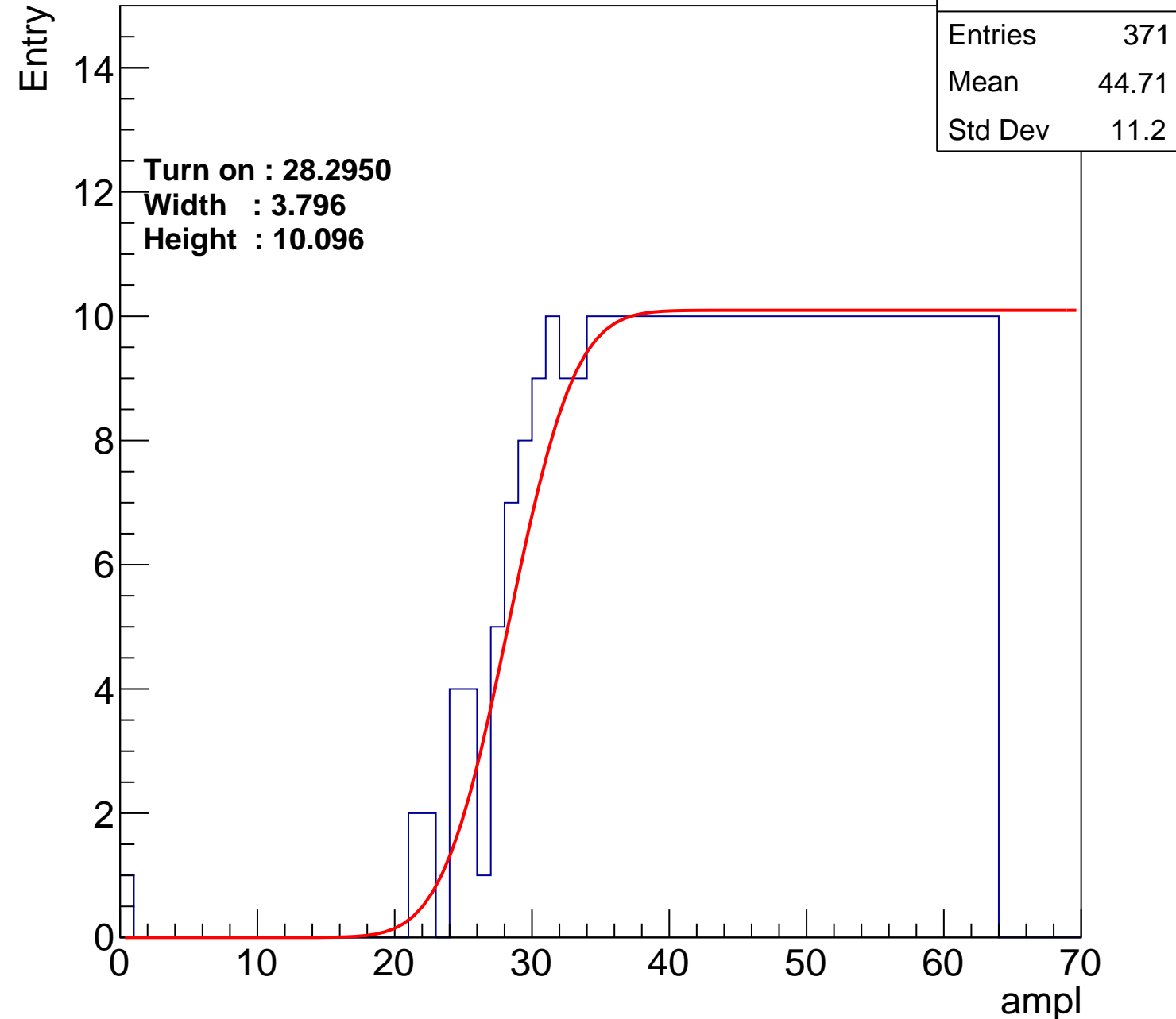
Width : 3.796

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch76

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.66
Std Dev	12.31

Turn on : 26.0581

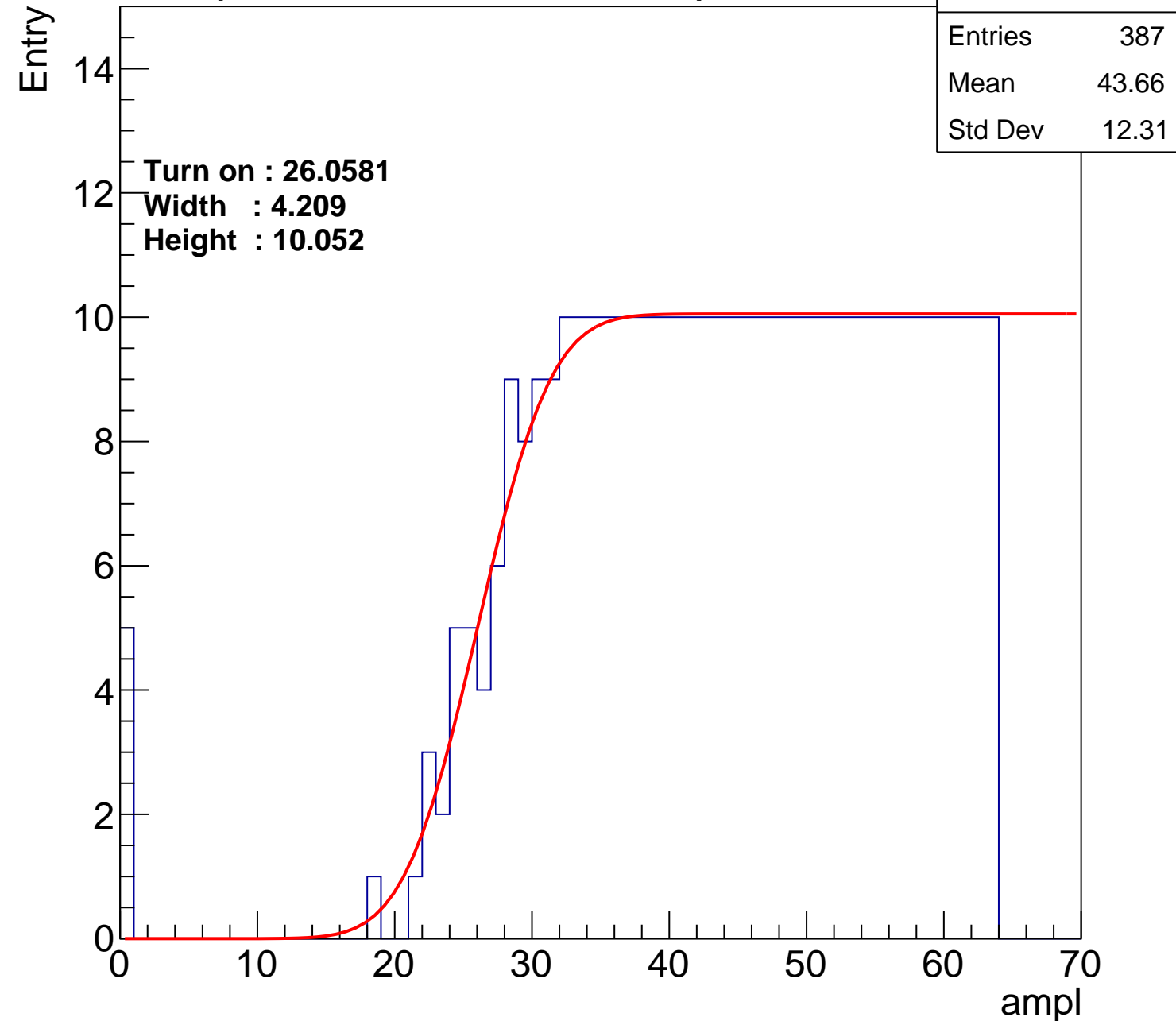
Width : 4.209

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch77

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.6412

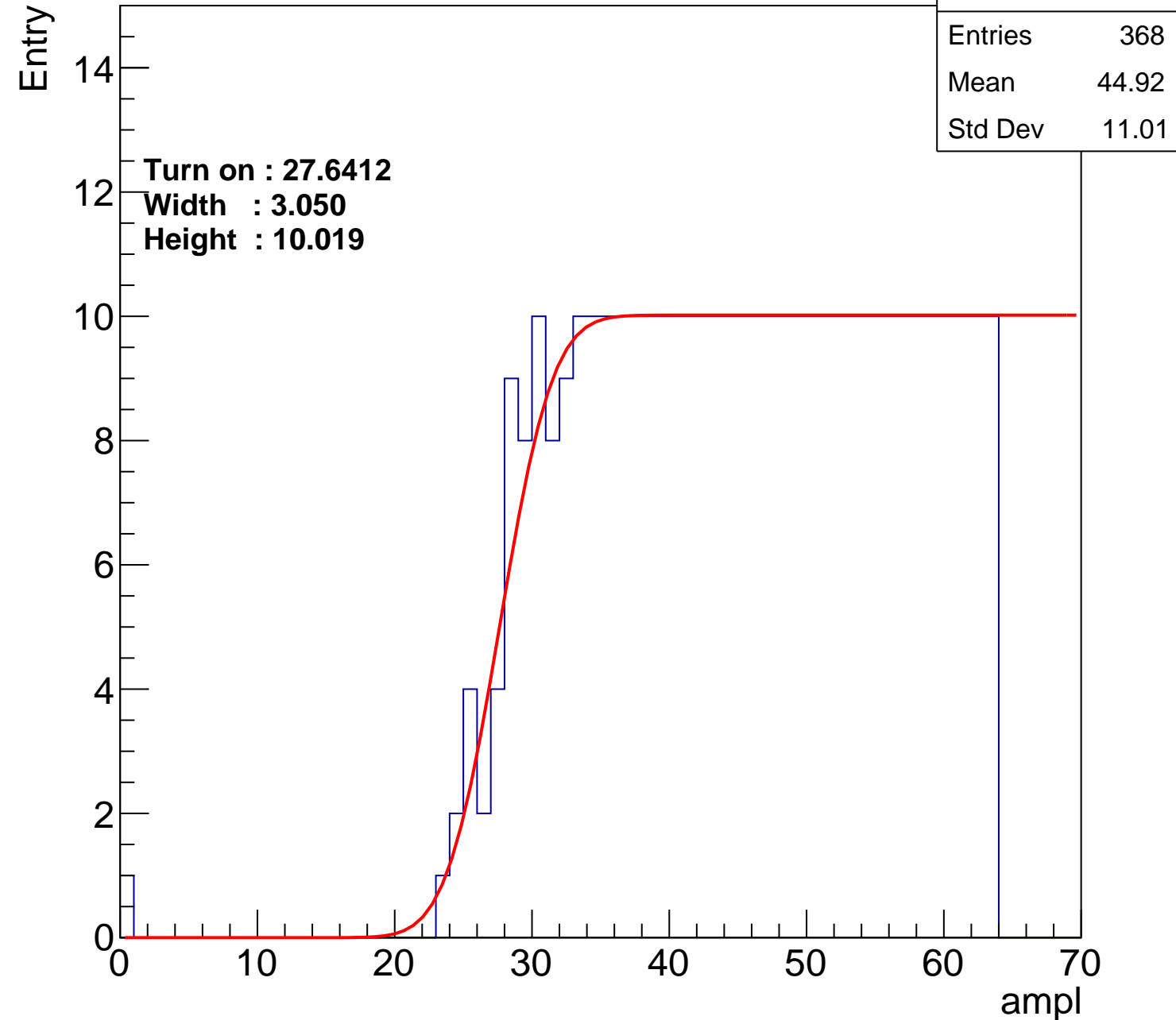
Width : 3.050

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch78

calib_packv5_042523_0143.root, FC#7, port C2

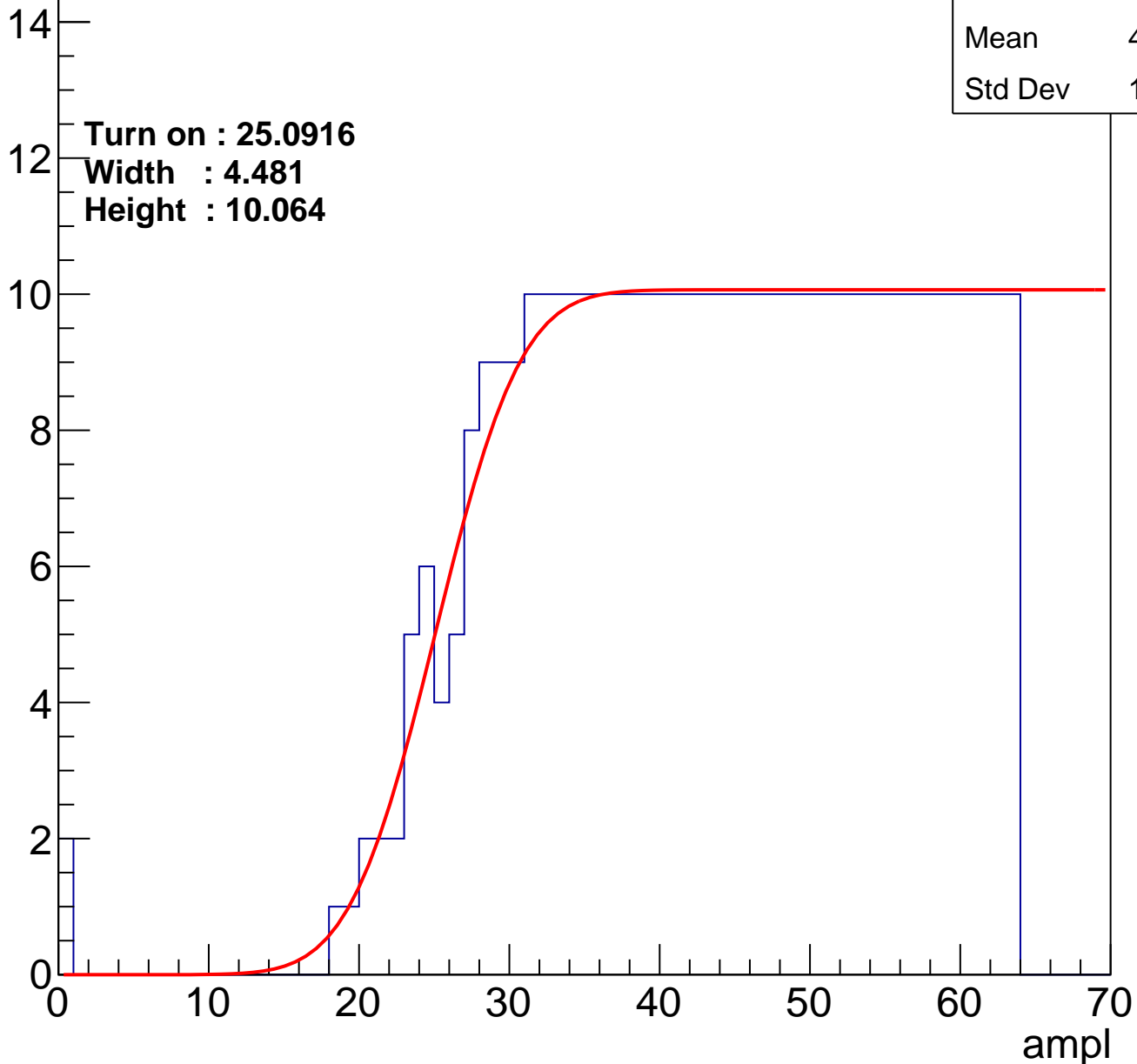
Entries	395
Mean	43.45
Std Dev	12.03

Turn on : 25.0916

Width : 4.481

Height : 10.064

Entry



B1L103S, U26-ch79

calib_packv5_042523_0143.root, FC#7, port C2

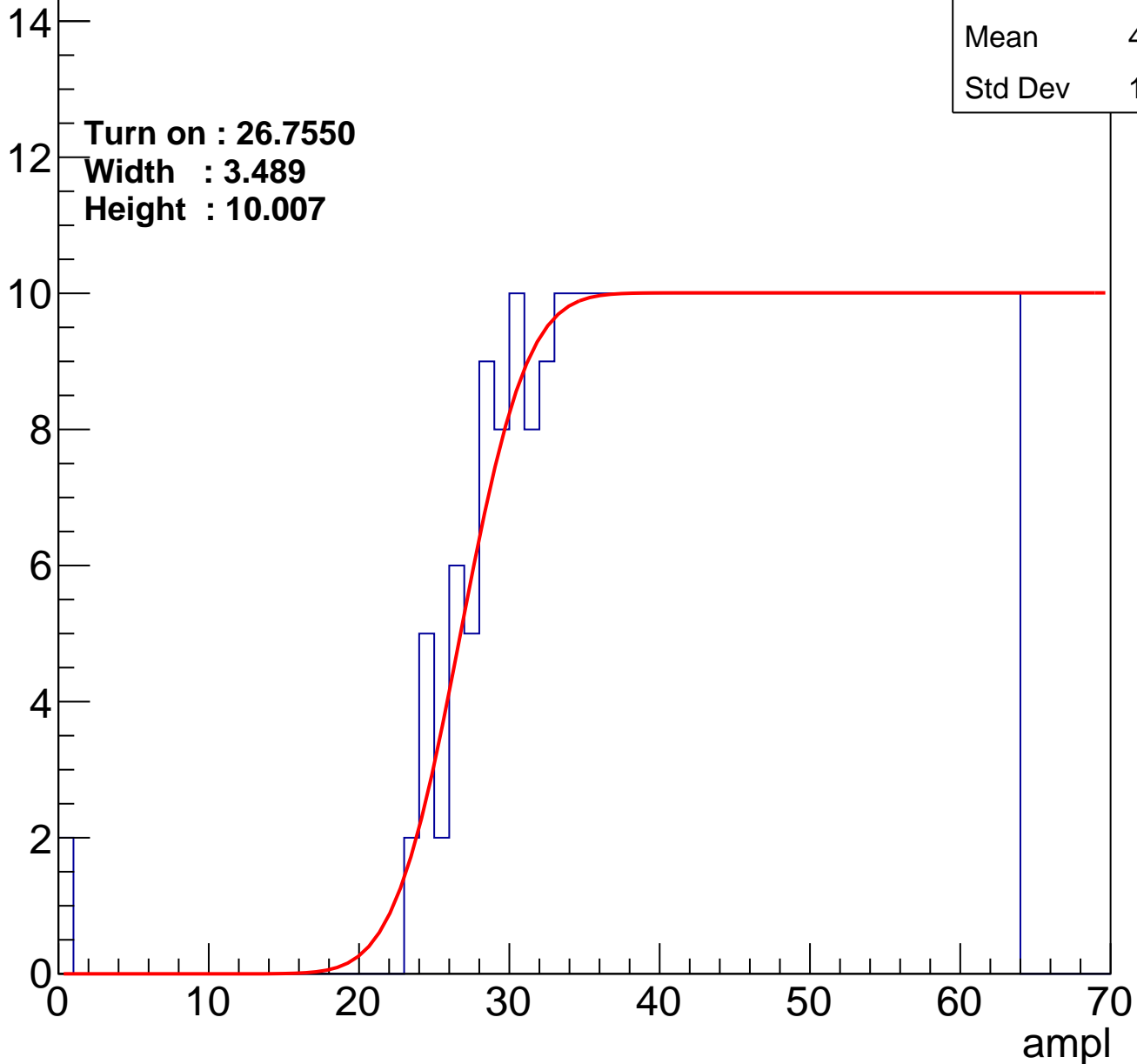
Entries	376
Mean	44.43
Std Dev	11.45

Turn on : 26.7550

Width : 3.489

Height : 10.007

Entry



B1L103S, U26-ch80

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.63
Std Dev	12.01

Turn on : 25.7371

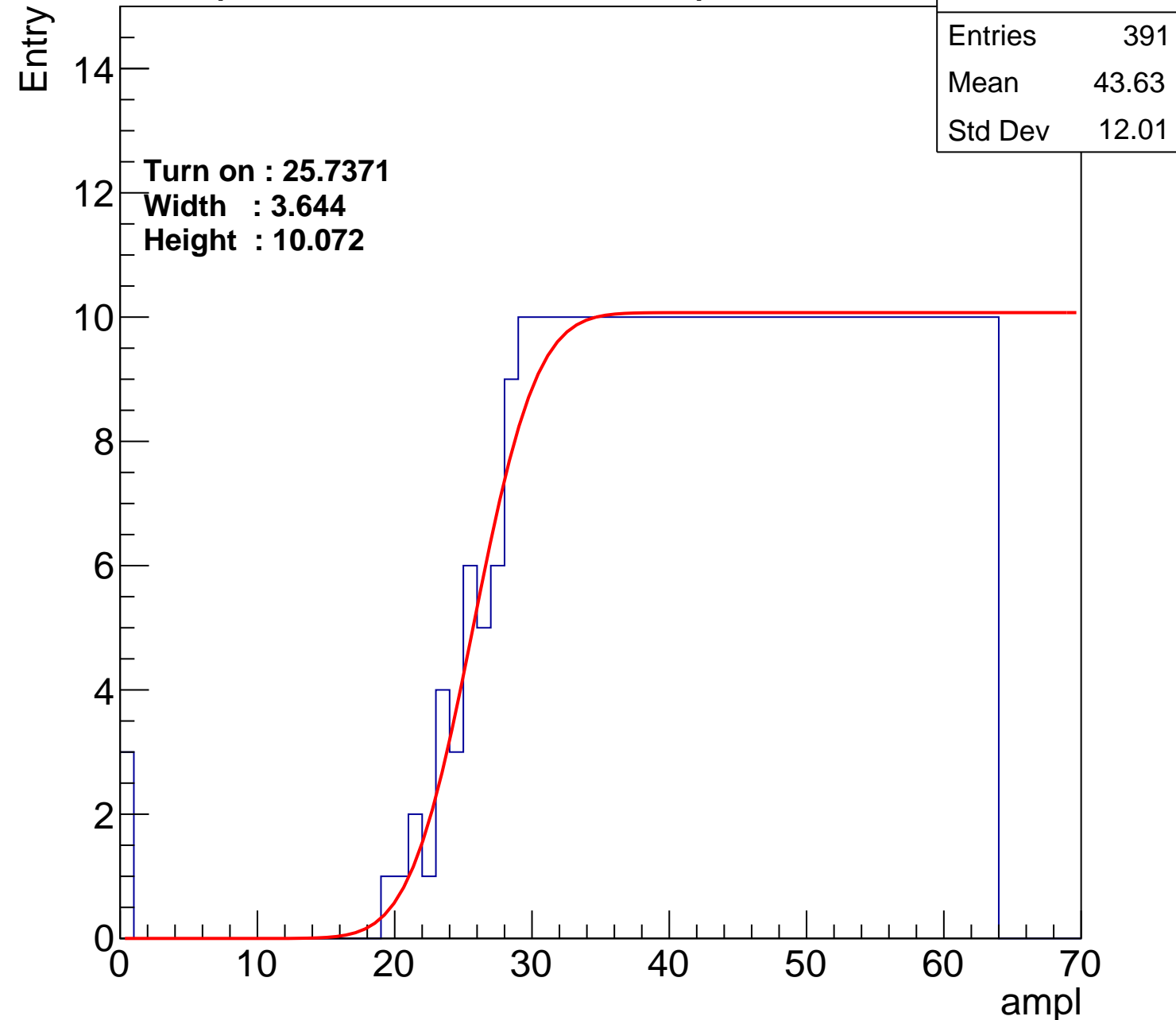
Width : 3.644

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch81

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.7
Std Dev	12.13

Turn on : 26.4681

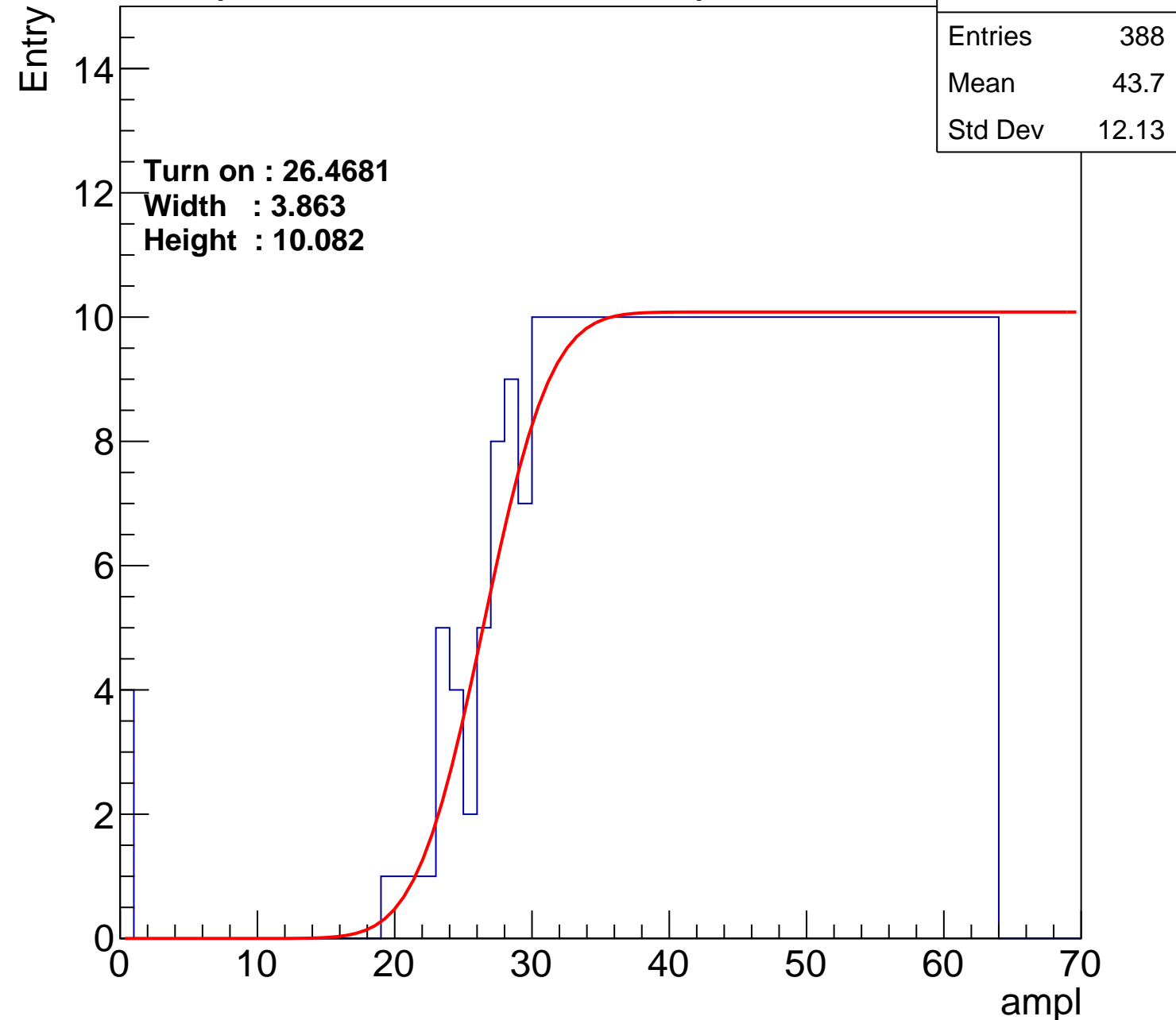
Width : 3.863

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch82

calib_packv5_042523_0143.root, FC#7, port C2

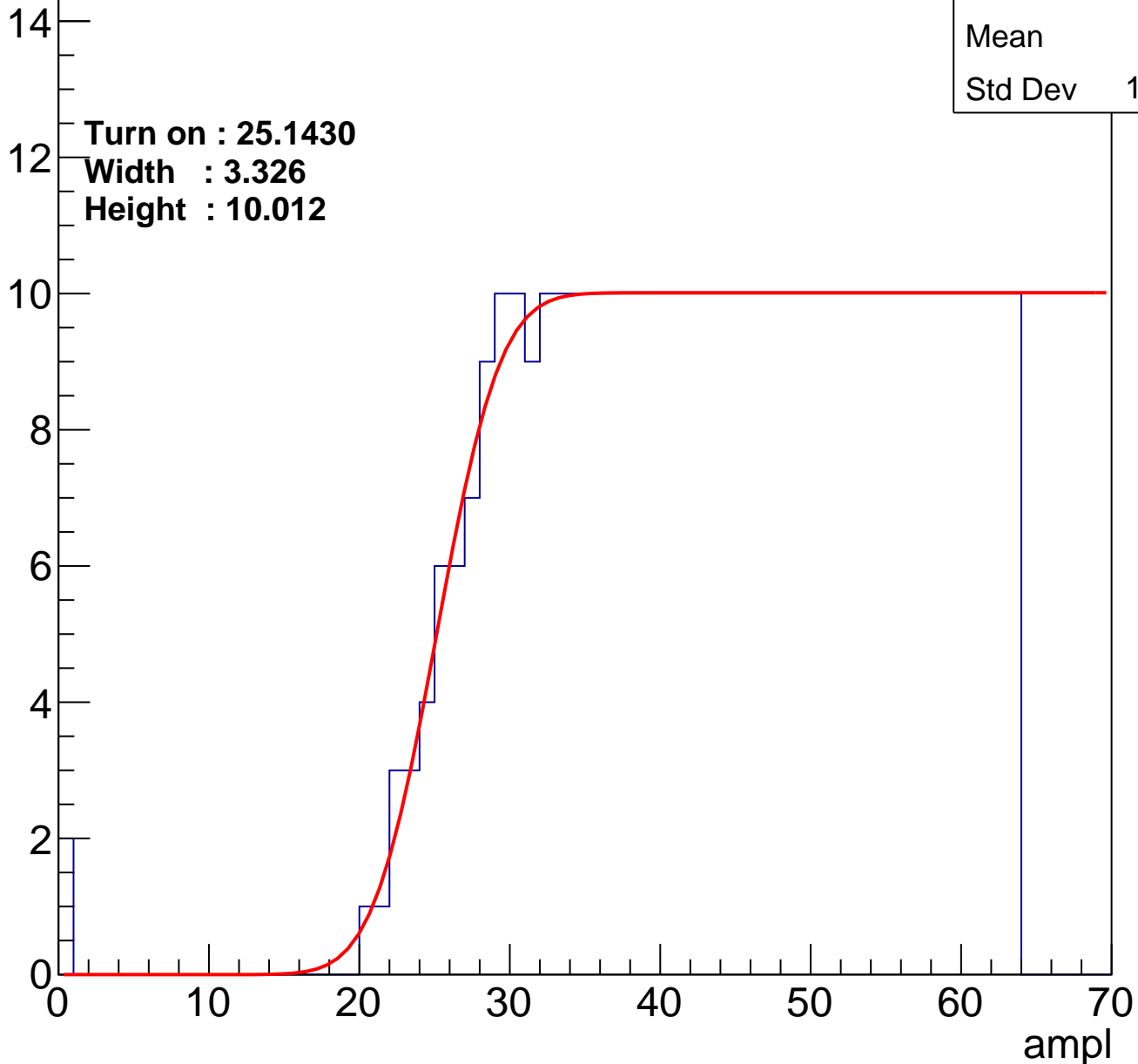
Entries	391
Mean	43.7
Std Dev	11.83

Turn on : 25.1430

Width : 3.326

Height : 10.012

Entry



B1L103S, U26-ch83

calib_packv5_042523_0143.root, FC#7, port C2

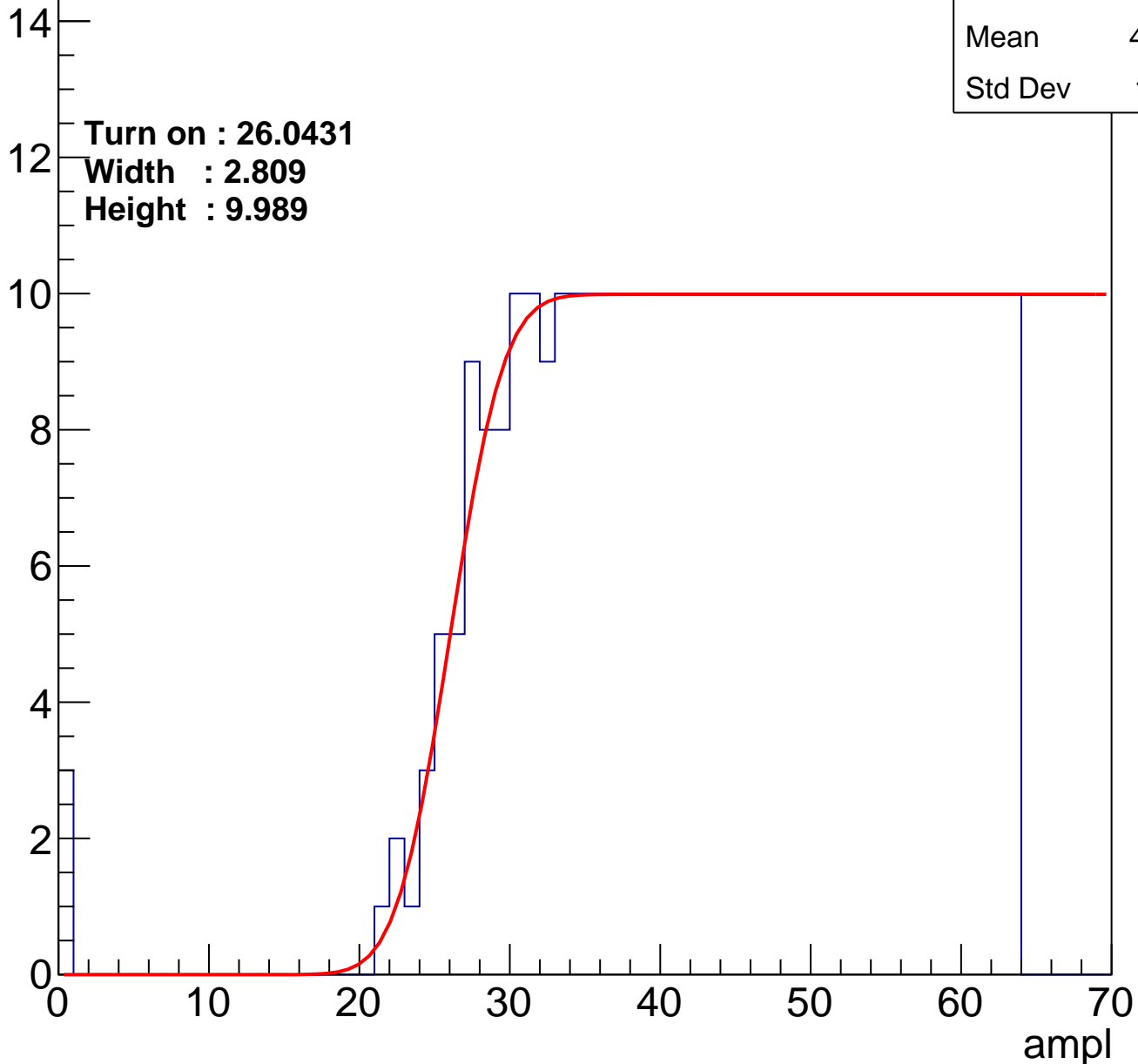
Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 26.0431

Width : 2.809

Height : 9.989

Entry



B1L103S, U26-ch84

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.14
Std Dev	11.26

Turn on : 29.1402

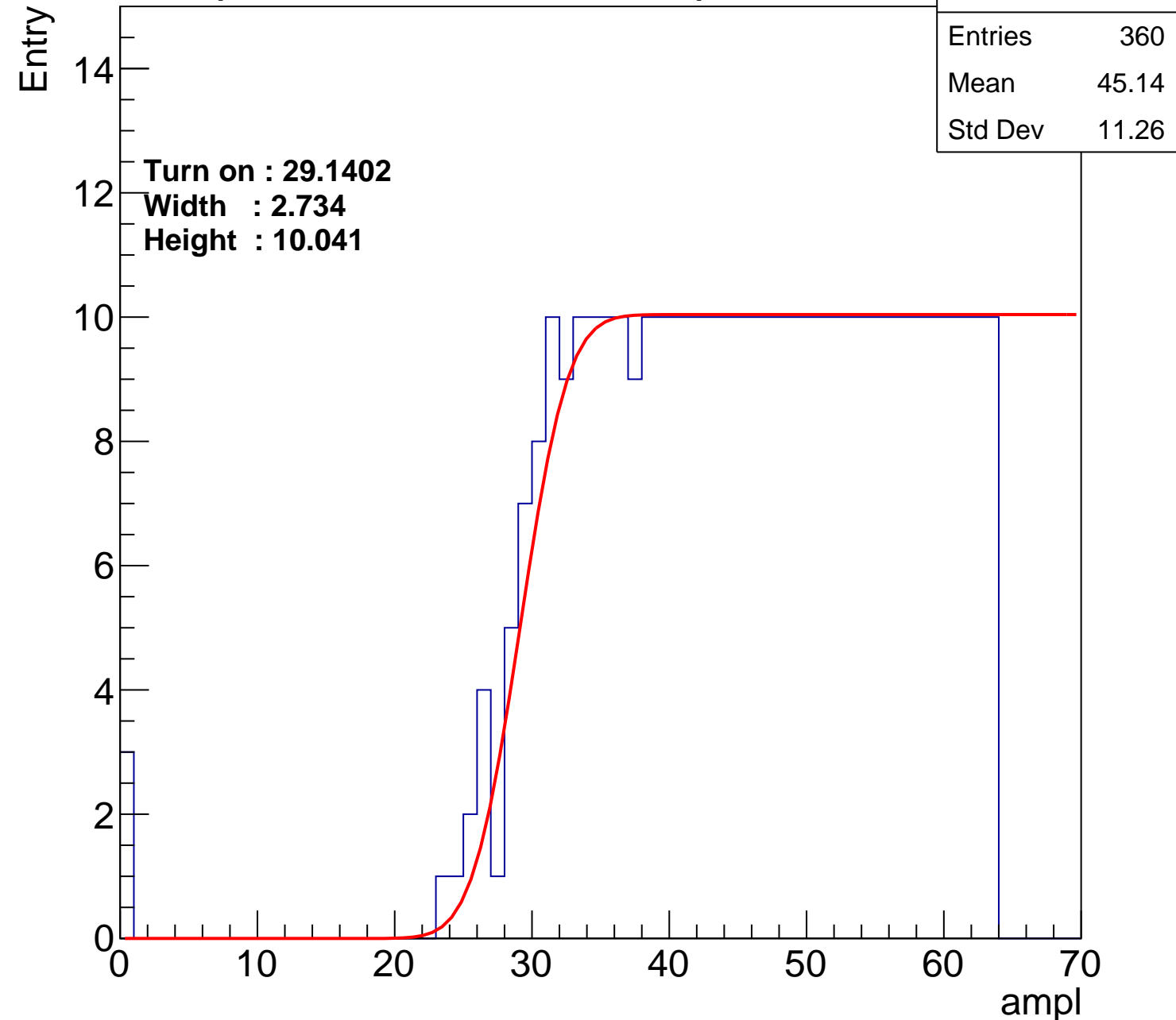
Width : 2.734

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch85

calib_packv5_042523_0143.root, FC#7, port C2

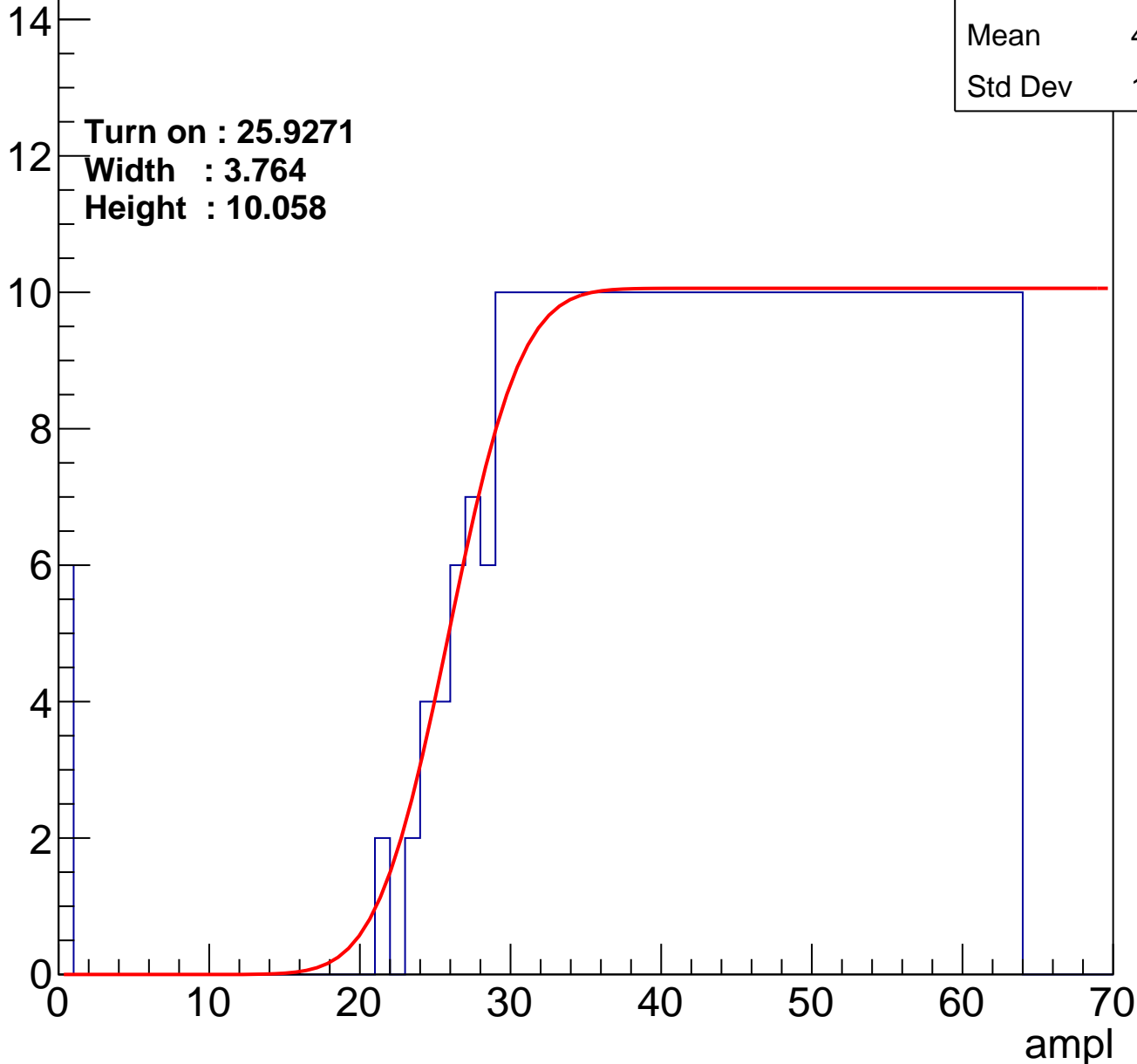
Entries	387
Mean	43.66
Std Dev	12.36

Turn on : 25.9271

Width : 3.764

Height : 10.058

Entry



B1L103S, U26-ch86

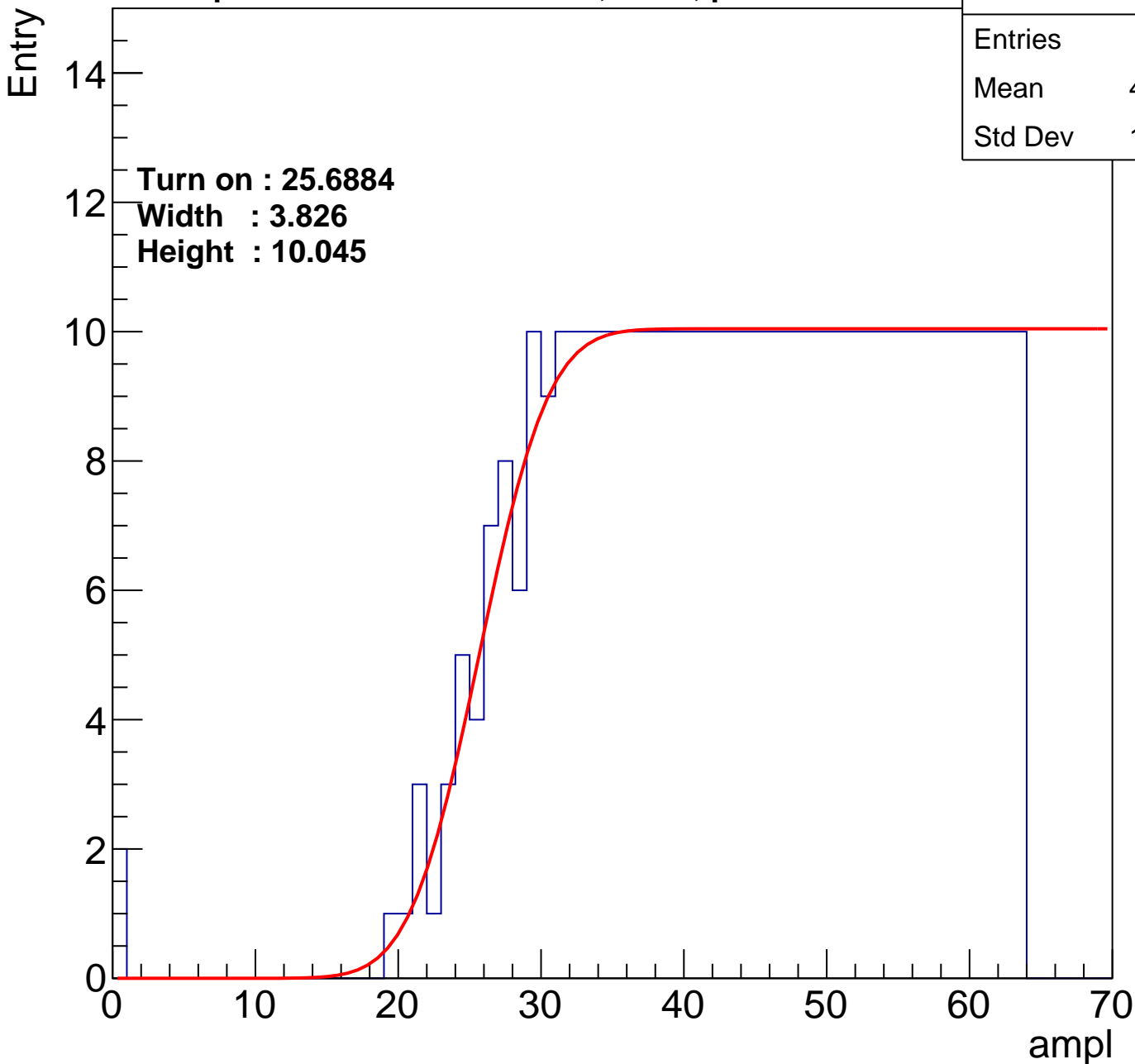
calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.72
Std Dev	11.86

Turn on : 25.6884

Width : 3.826

Height : 10.045



B1L103S, U26-ch87

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.65
Std Dev	11.14

Turn on : 26.2737

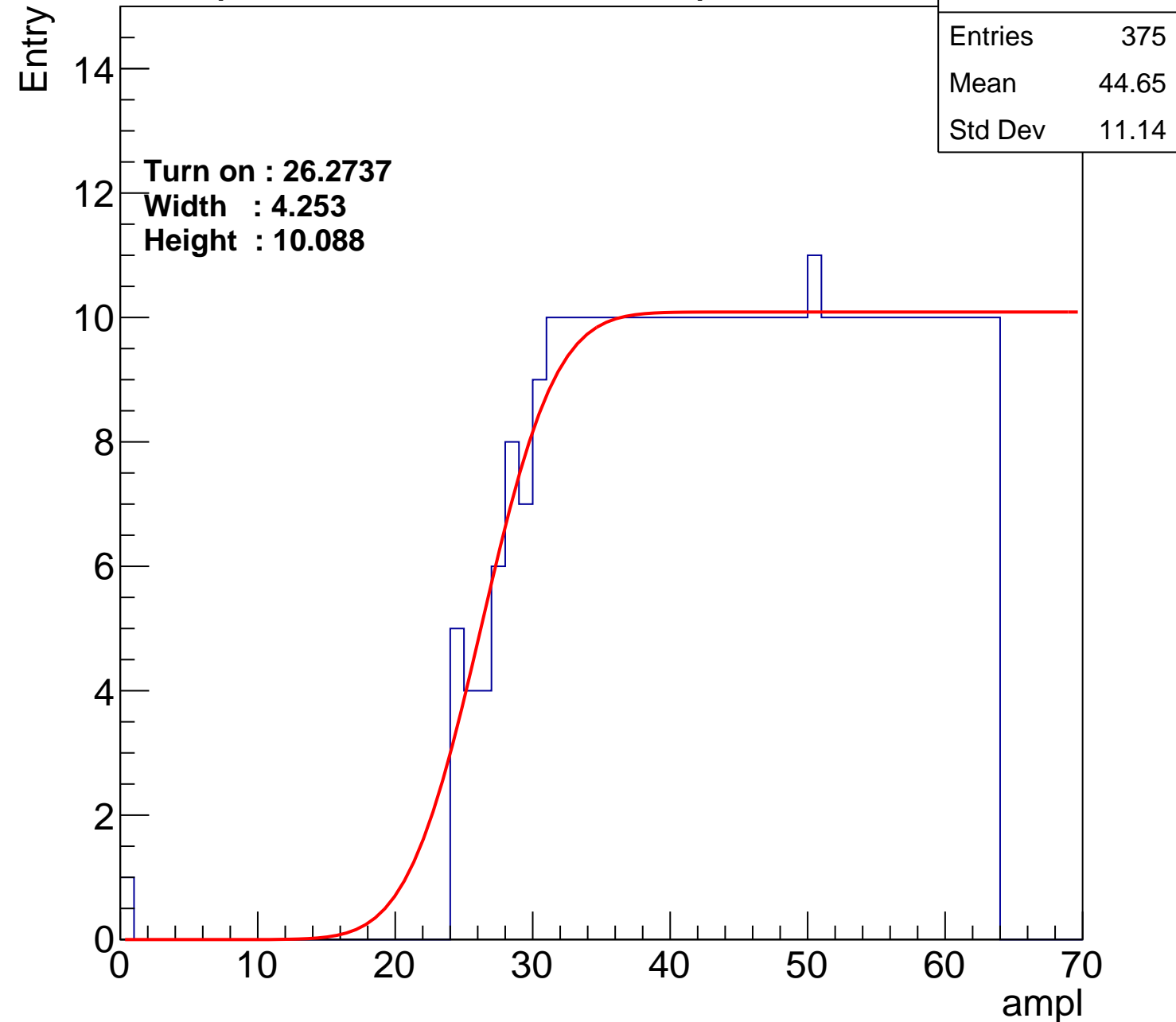
Width : 4.253

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch88

calib_packv5_042523_0143.root, FC#7, port C2

Entries	398
Mean	43.16
Std Dev	12.5

Turn on : 26.1384

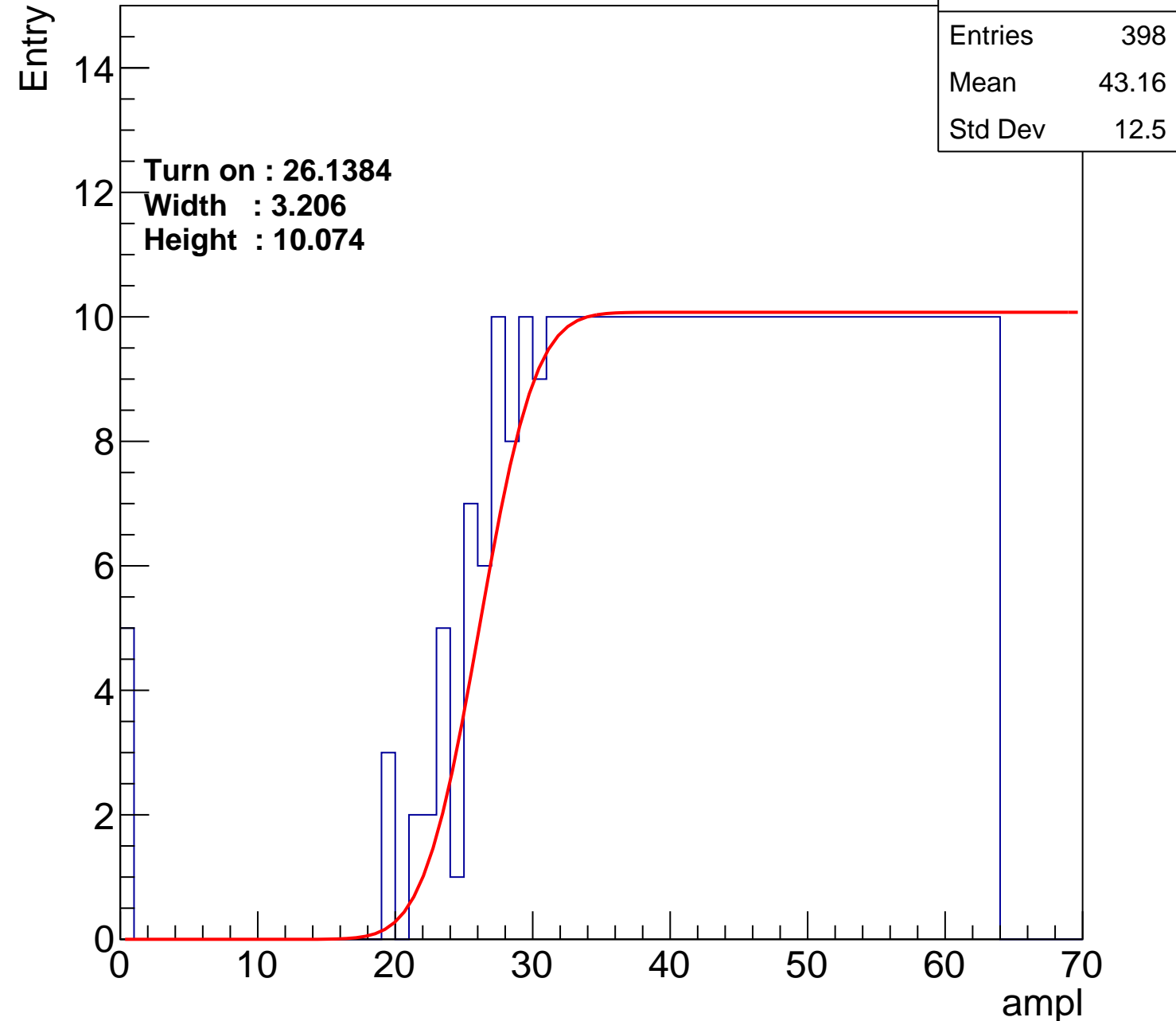
Width : 3.206

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch89

calib_packv5_042523_0143.root, FC#7, port C2

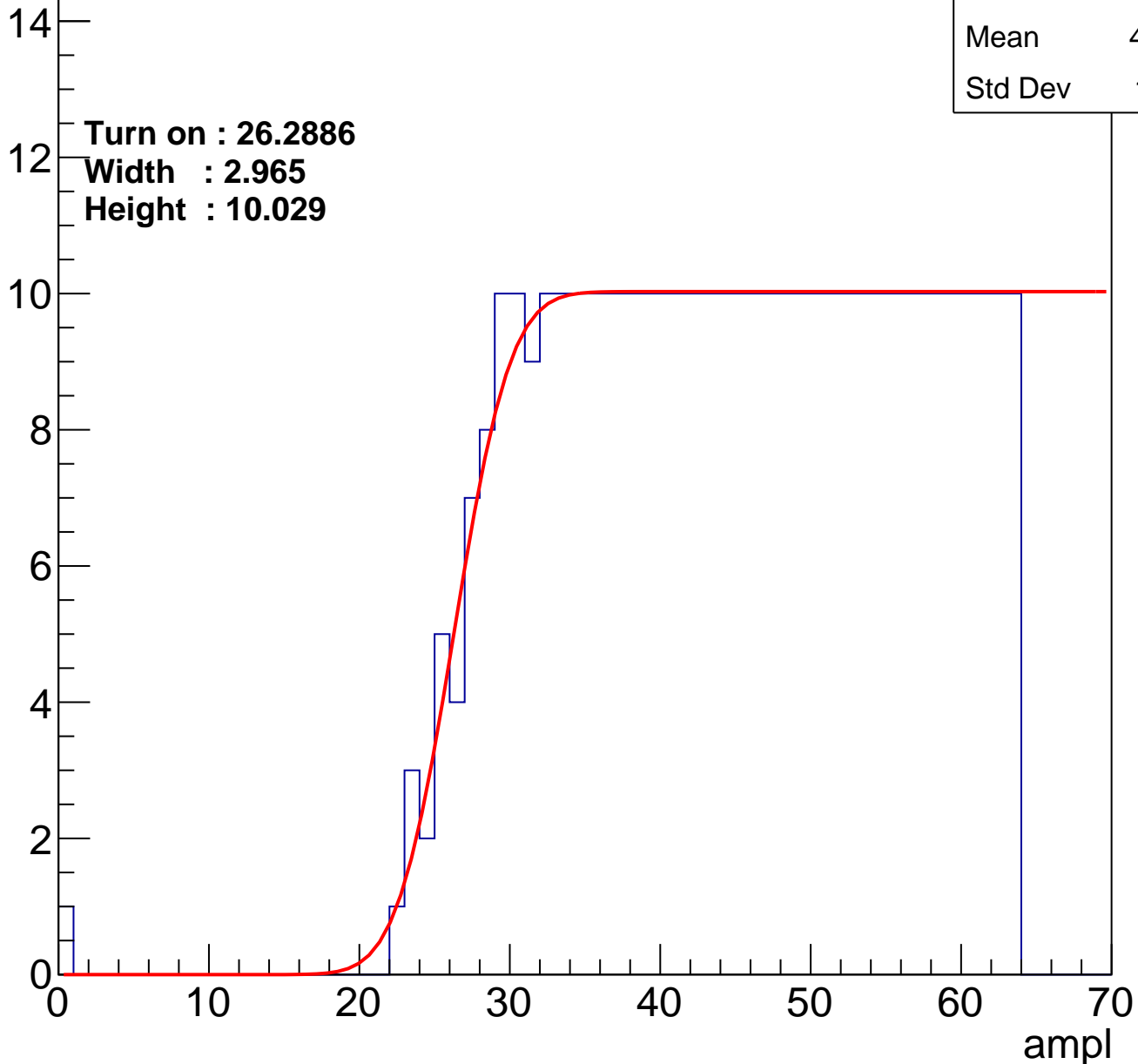
Entries	380
Mean	44.34
Std Dev	11.31

Turn on : 26.2886

Width : 2.965

Height : 10.029

Entry



B1L103S, U26-ch90

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.51
Std Dev	12.26

Turn on : 25.9861

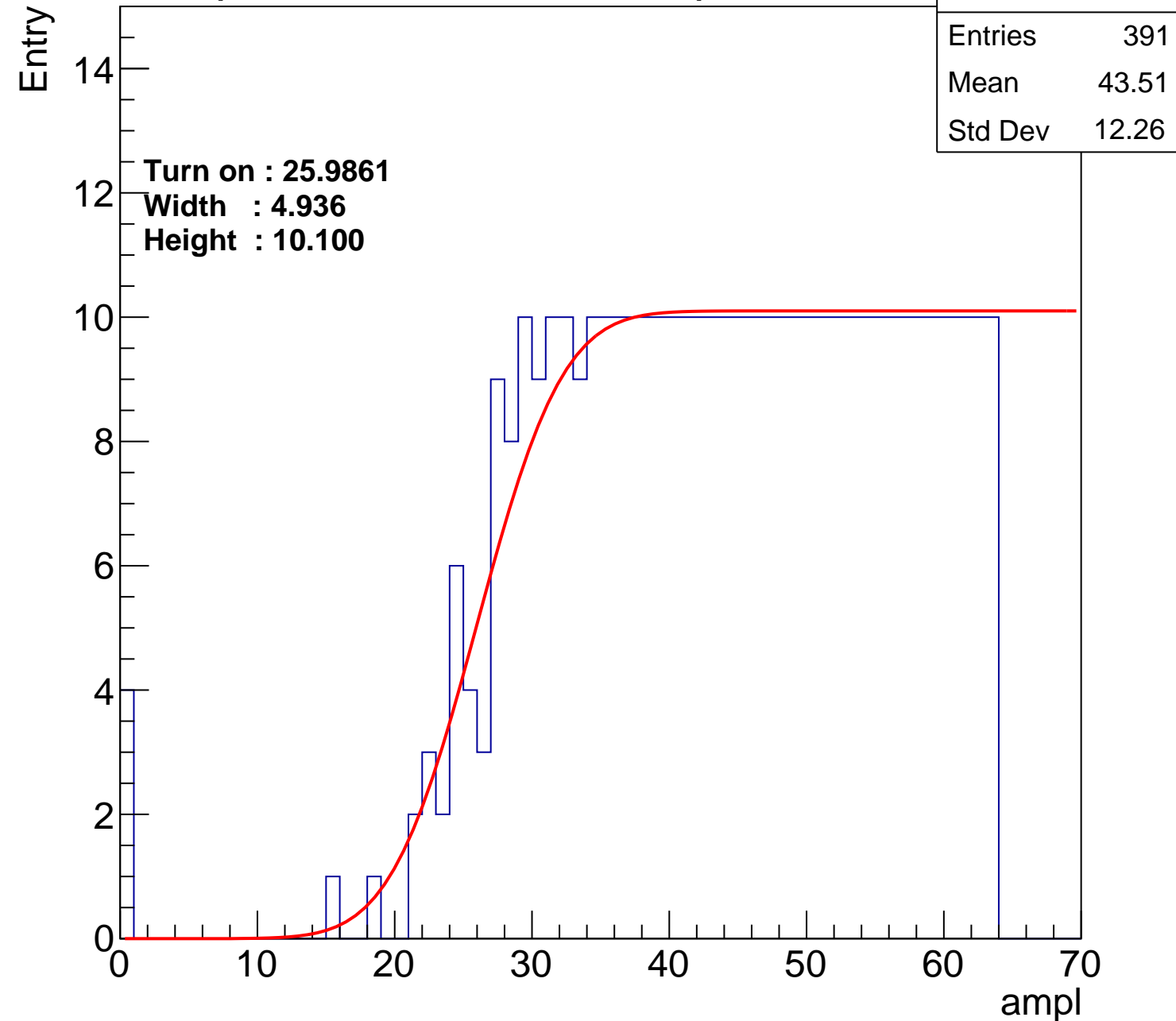
Width : 4.936

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch91

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.32
Std Dev	11.65

Turn on : 26.7843

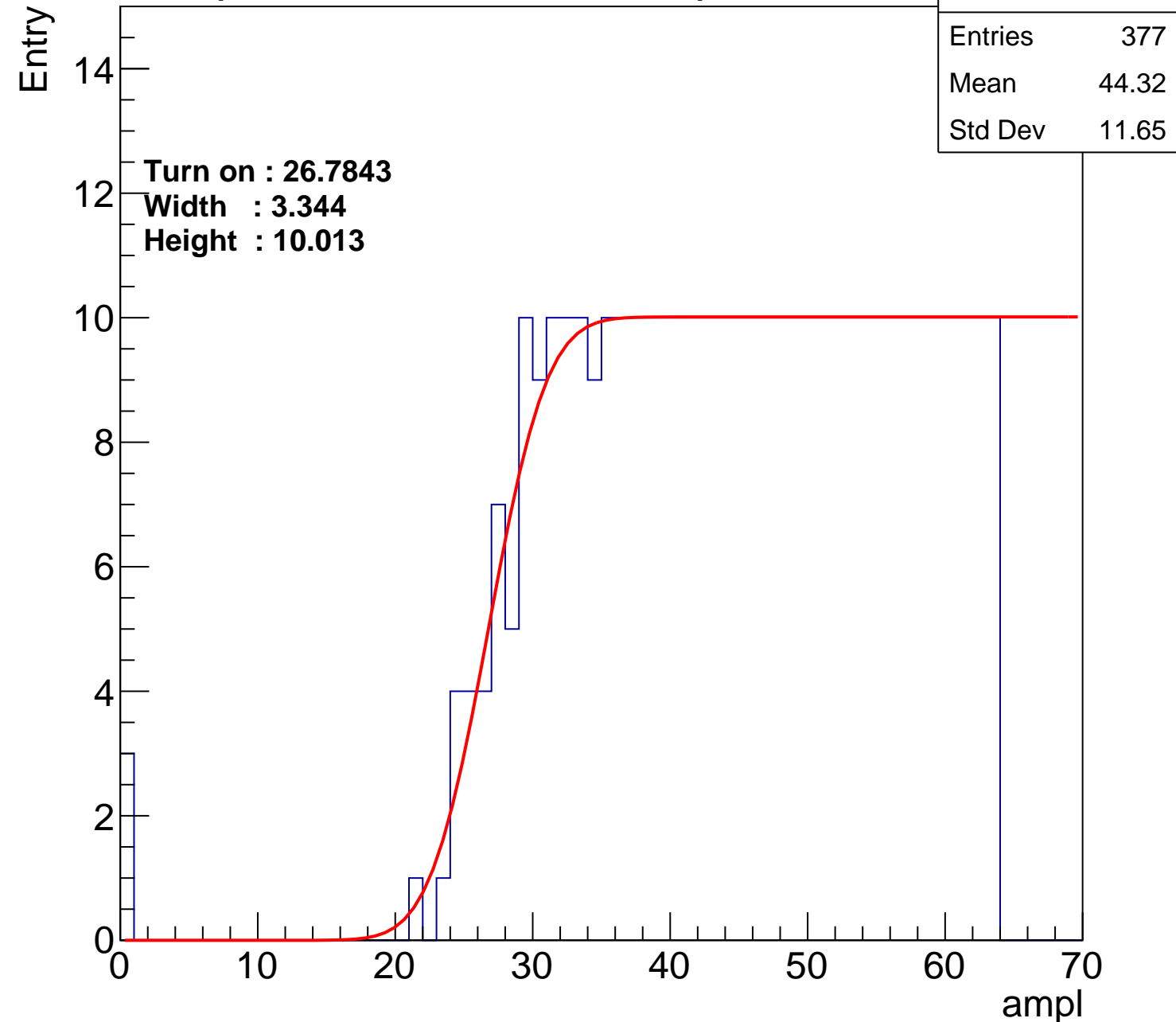
Width : 3.344

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch92

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.54
Std Dev	11.76

Turn on : 24.6882

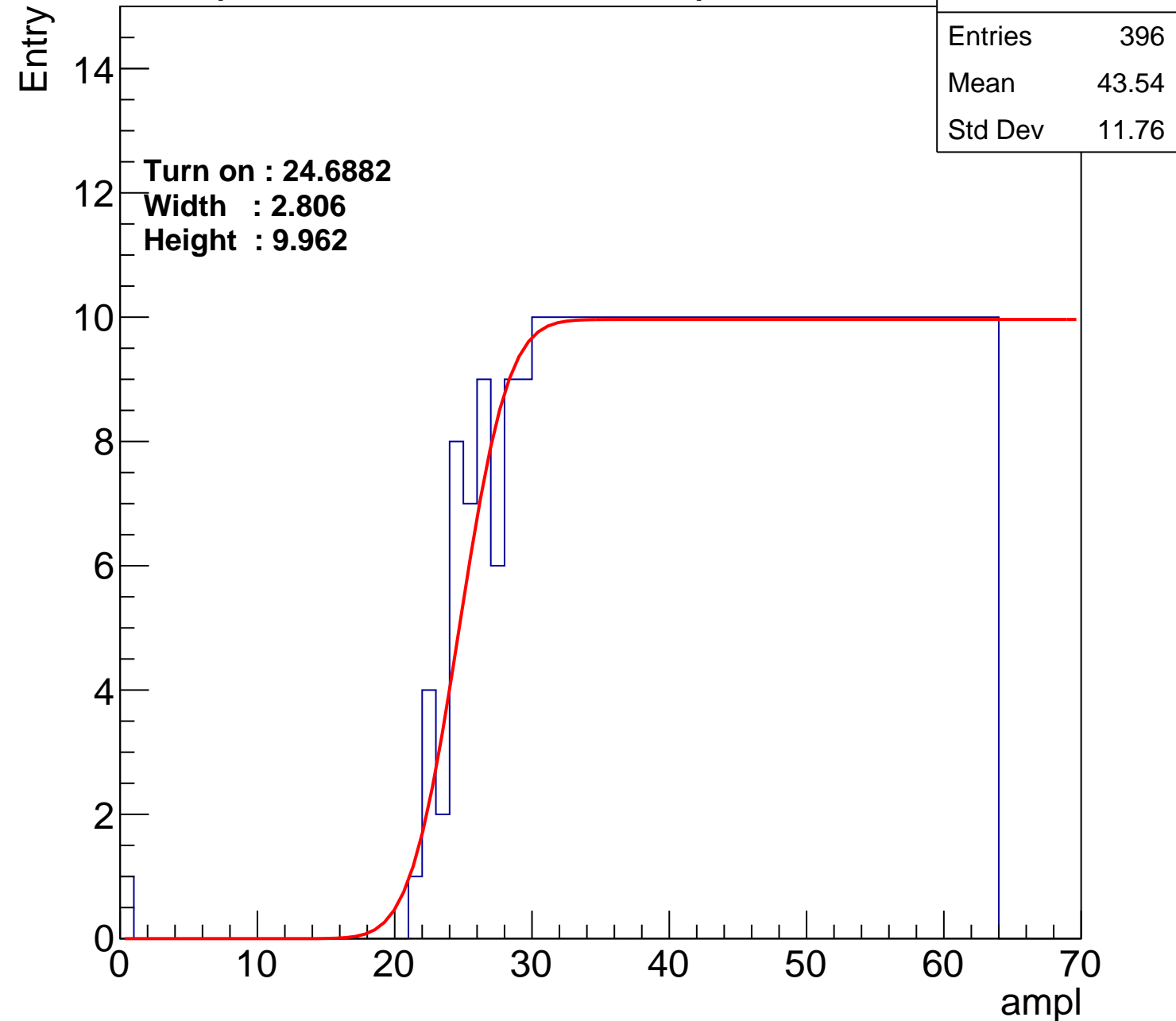
Width : 2.806

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch93

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 26.8589

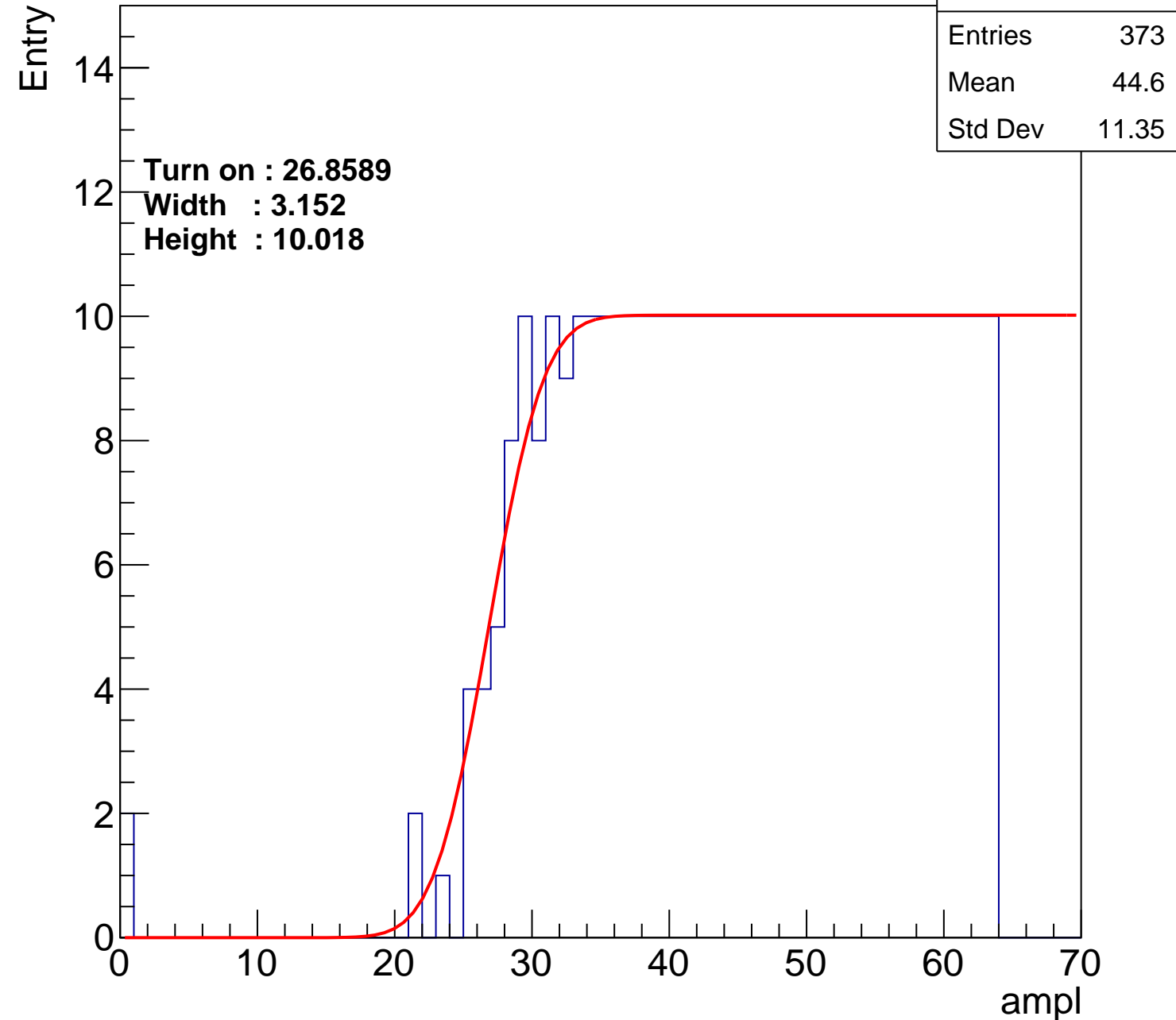
Width : 3.152

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch94

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	44.93
Std Dev	11.3

Turn on : 28.2342

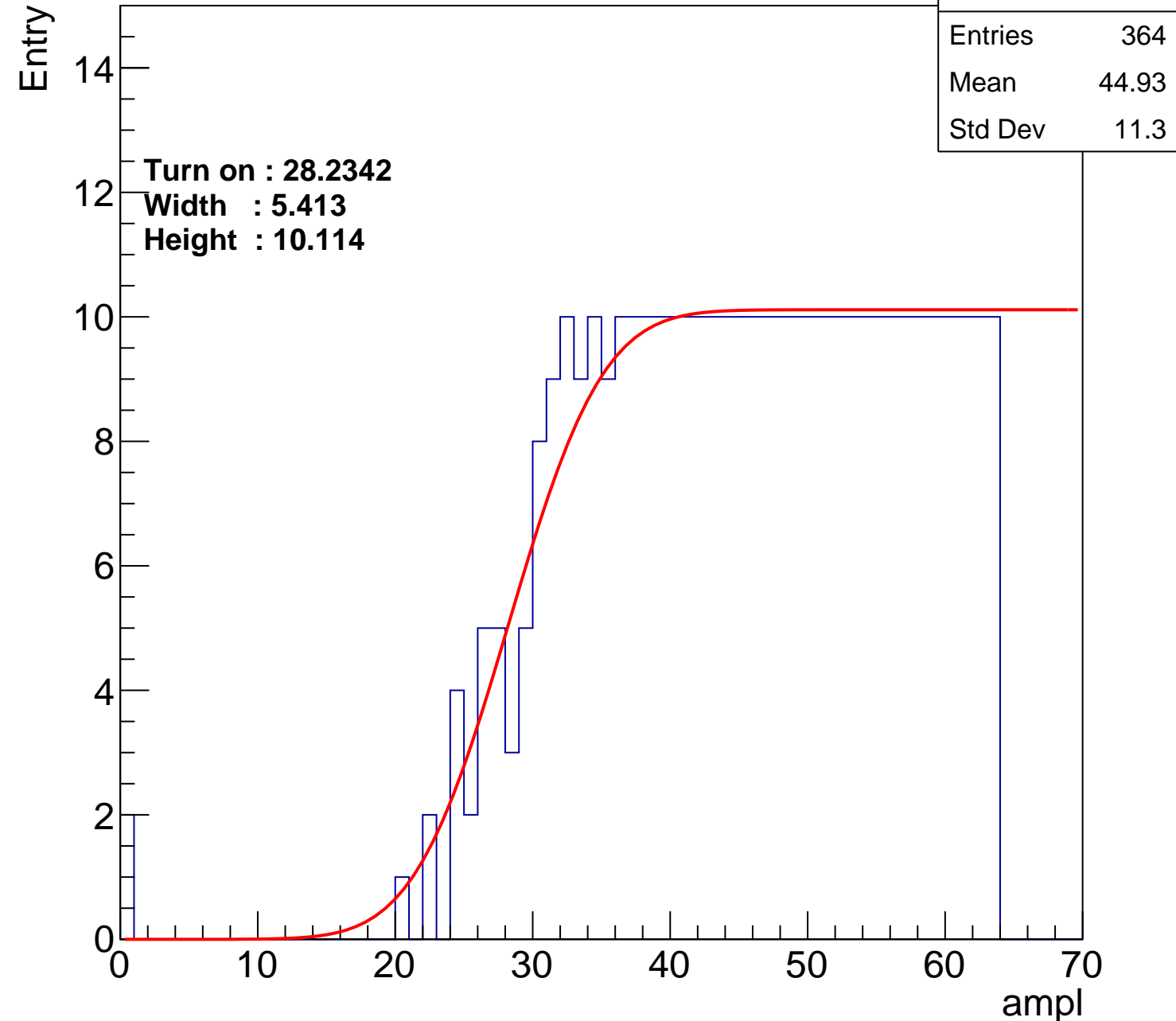
Width : 5.413

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch95

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.57
Std Dev	11.21

Turn on : 26.6944

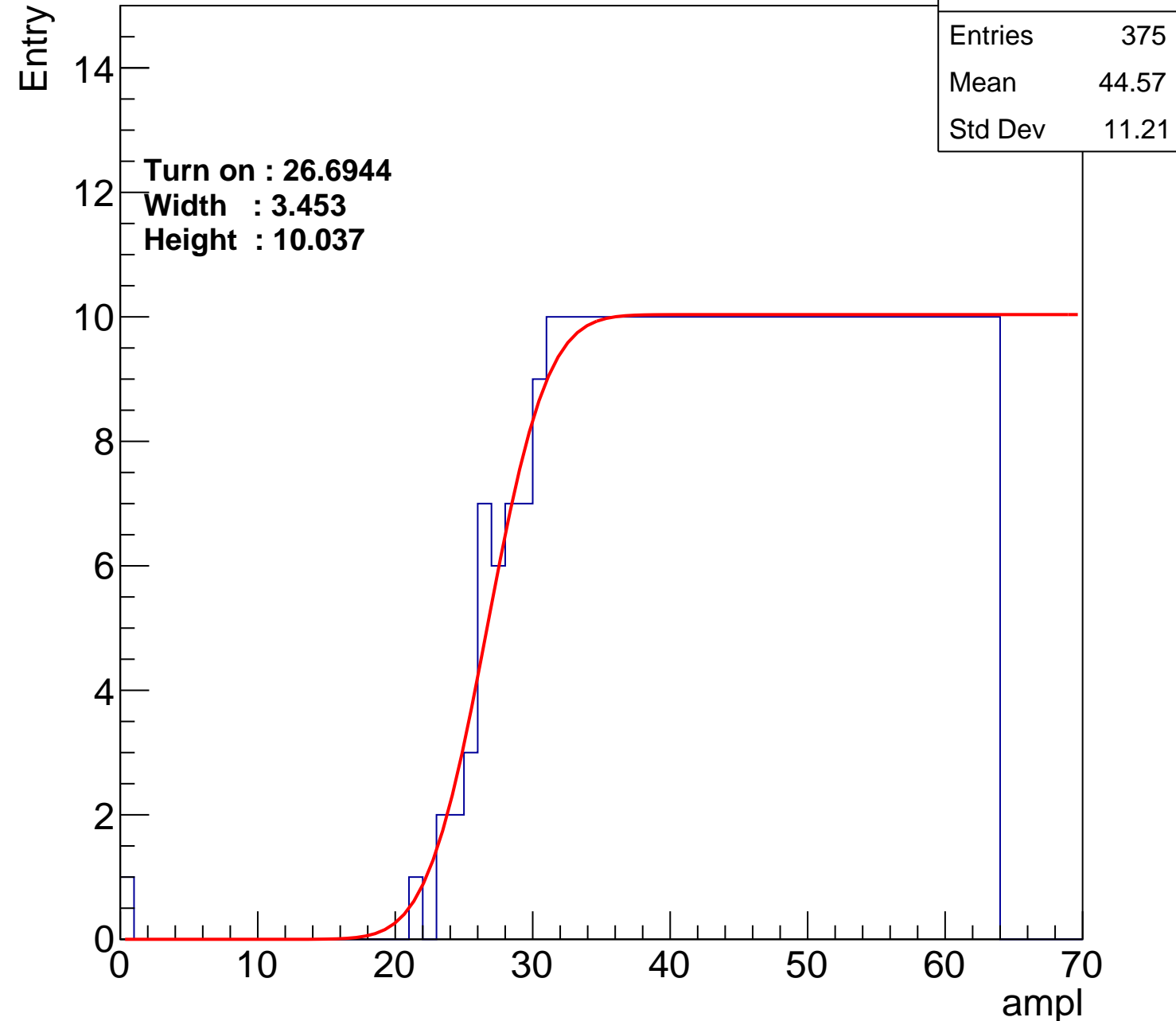
Width : 3.453

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch96

calib_packv5_042523_0143.root, FC#7, port C2

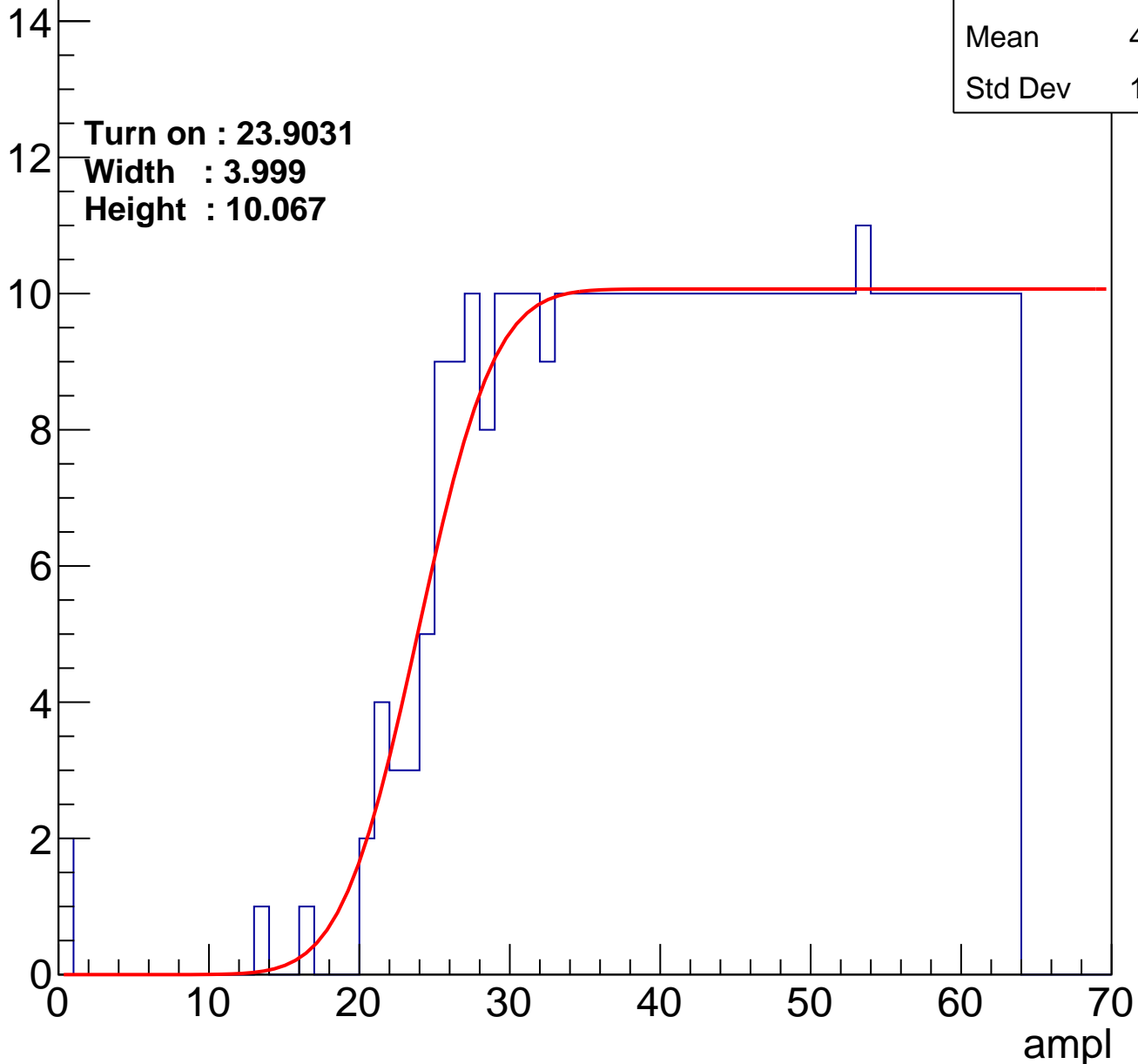
Entries	407
Mean	42.95
Std Dev	12.28

Turn on : 23.9031

Width : 3.999

Height : 10.067

Entry



B1L103S, U26-ch97

calib_packv5_042523_0143.root, FC#7, port C2

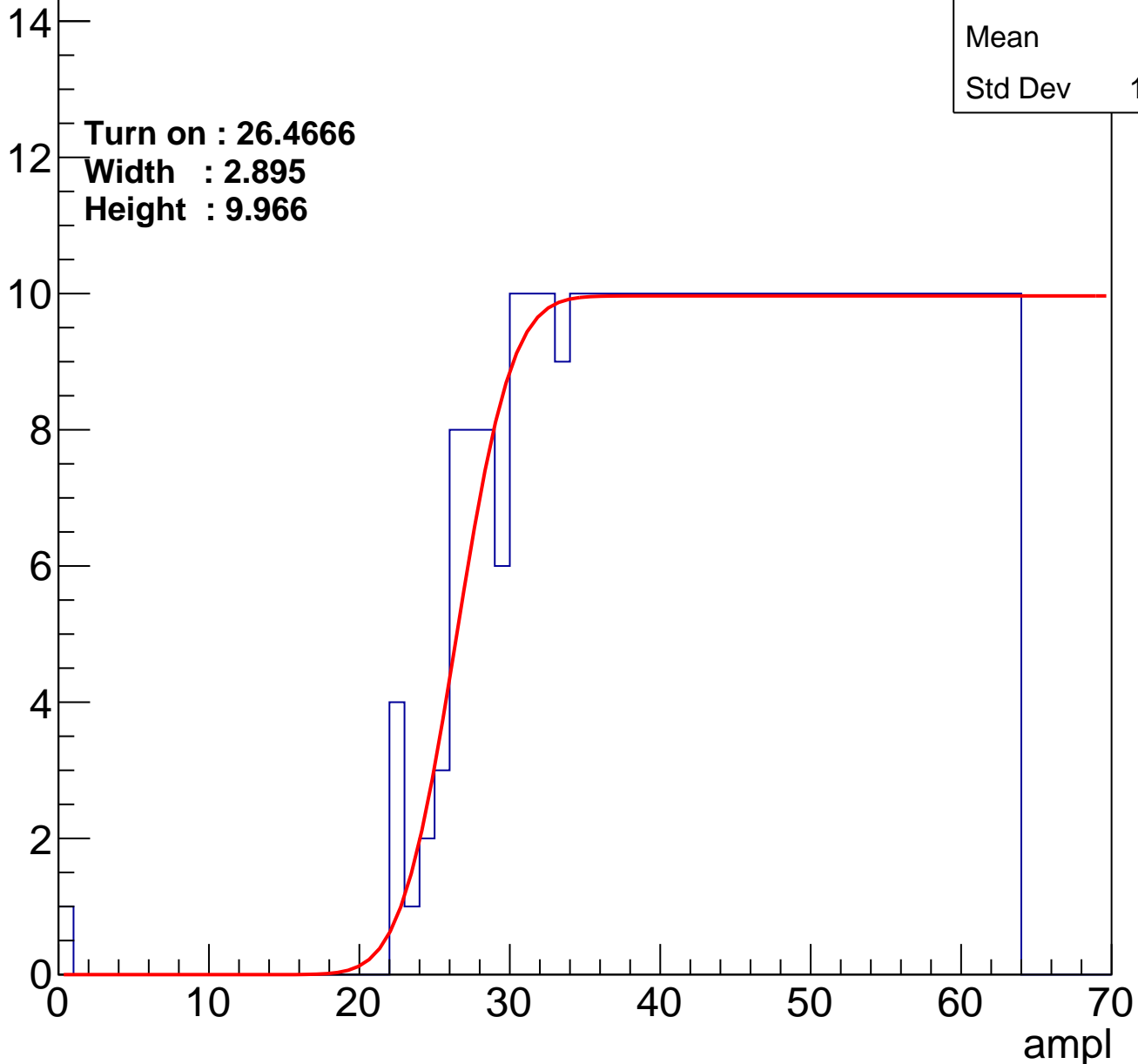
Entries	380
Mean	44.3
Std Dev	11.38

Turn on : 26.4666

Width : 2.895

Height : 9.966

Entry



B1L103S, U26-ch98

calib_packv5_042523_0143.root, FC#7, port C2

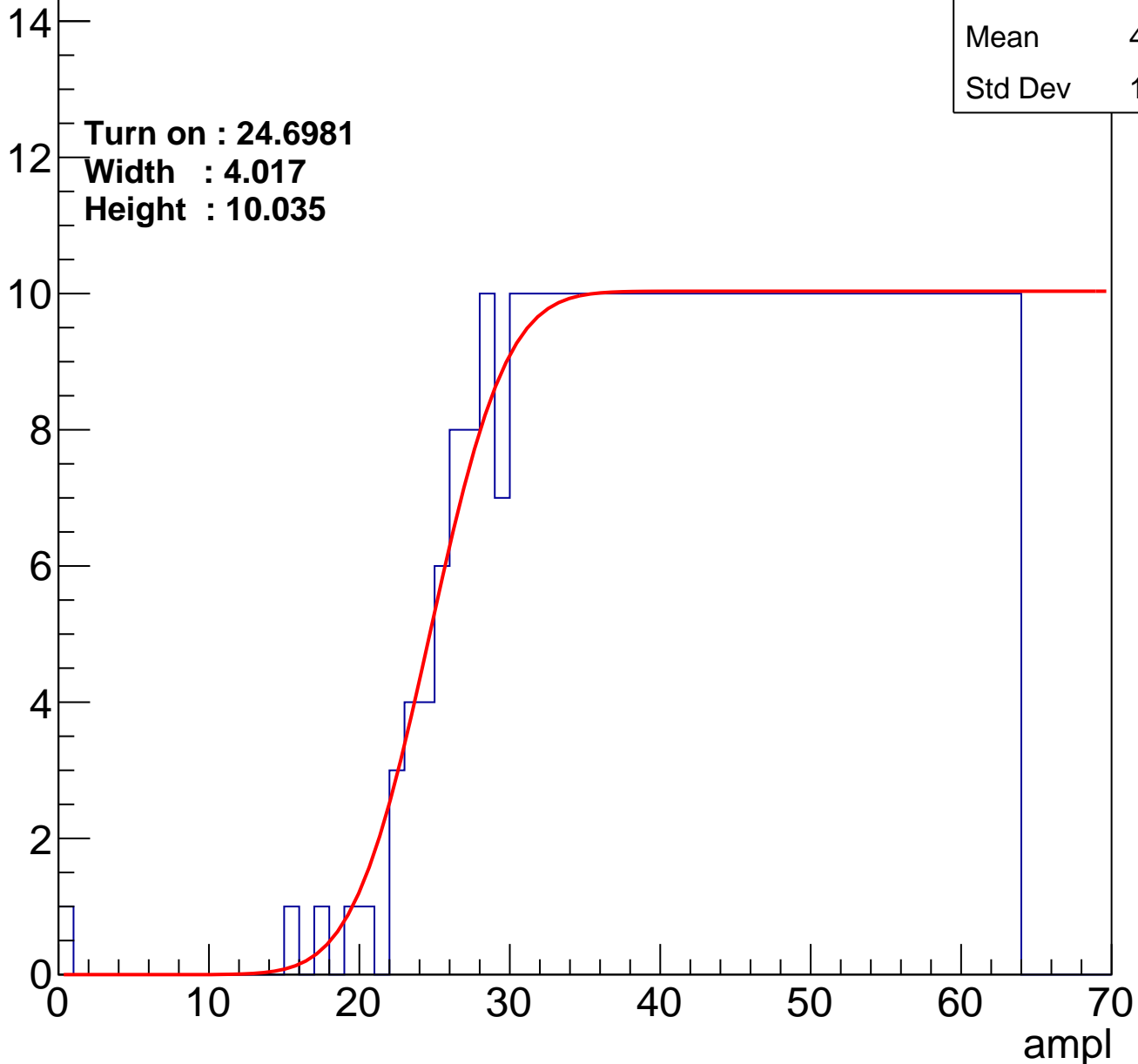
Entries	395
Mean	43.52
Std Dev	11.85

Turn on : 24.6981

Width : 4.017

Height : 10.035

Entry



B1L103S, U26-ch99

calib_packv5_042523_0143.root, FC#7, port C2

Entries	362
Mean	45.15
Std Dev	11.06

Turn on : 28.2443

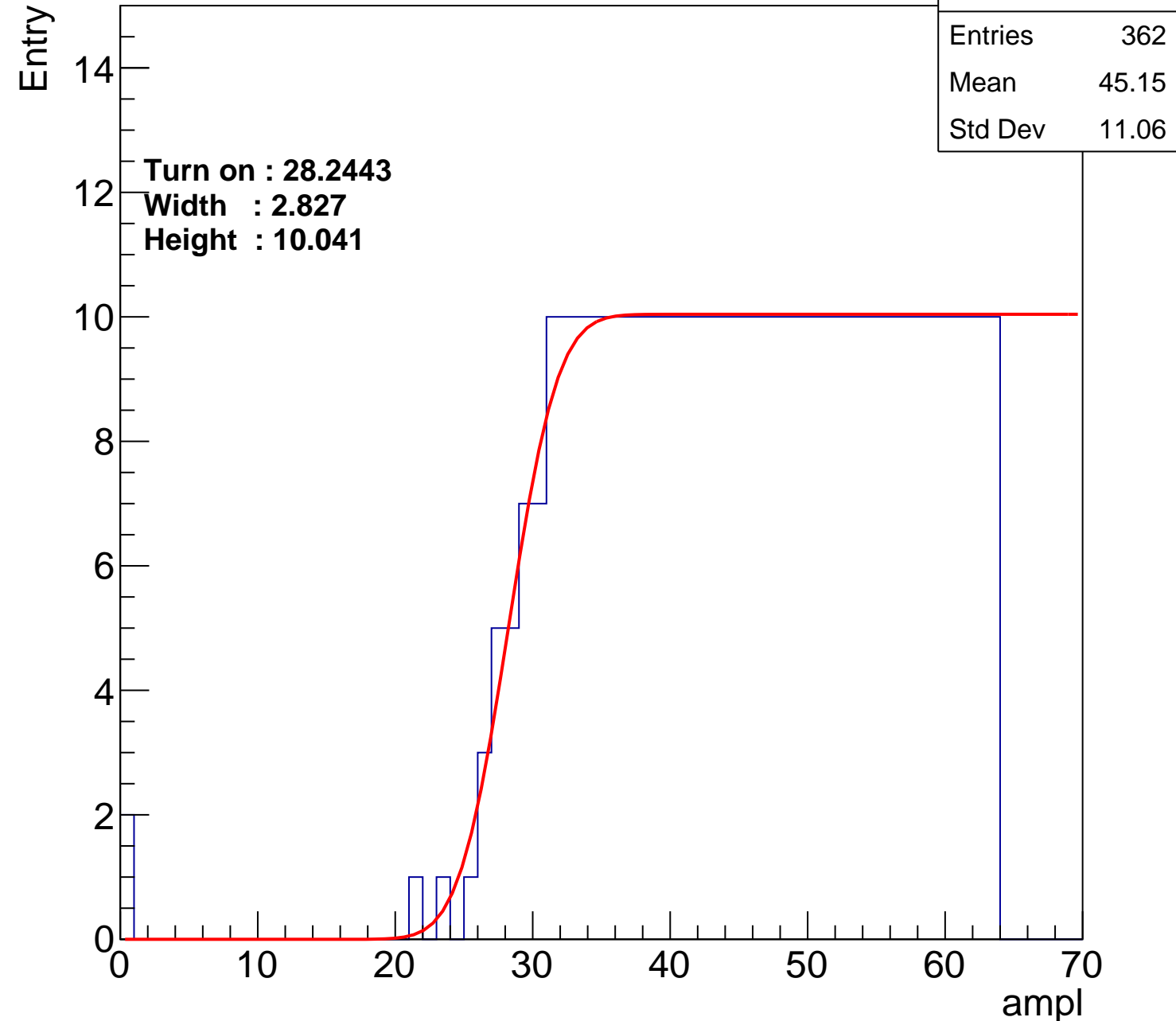
Width : 2.827

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch100

calib_packv5_042523_0143.root, FC#7, port C2

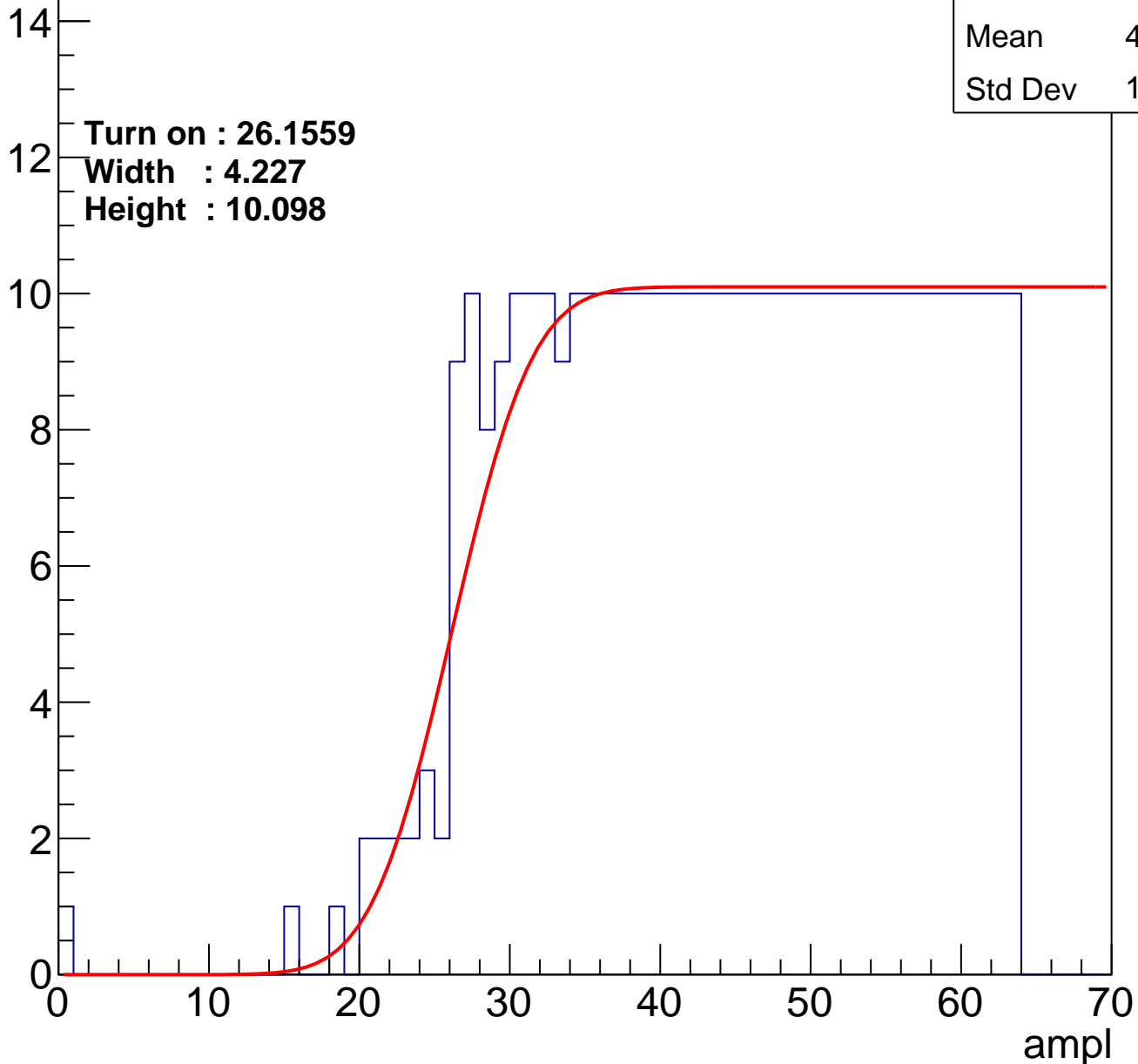
Entries	391
Mean	43.72
Std Dev	11.75

Turn on : 26.1559

Width : 4.227

Height : 10.098

Entry



B1L103S, U26-ch101

calib_packv5_042523_0143.root, FC#7, port C2

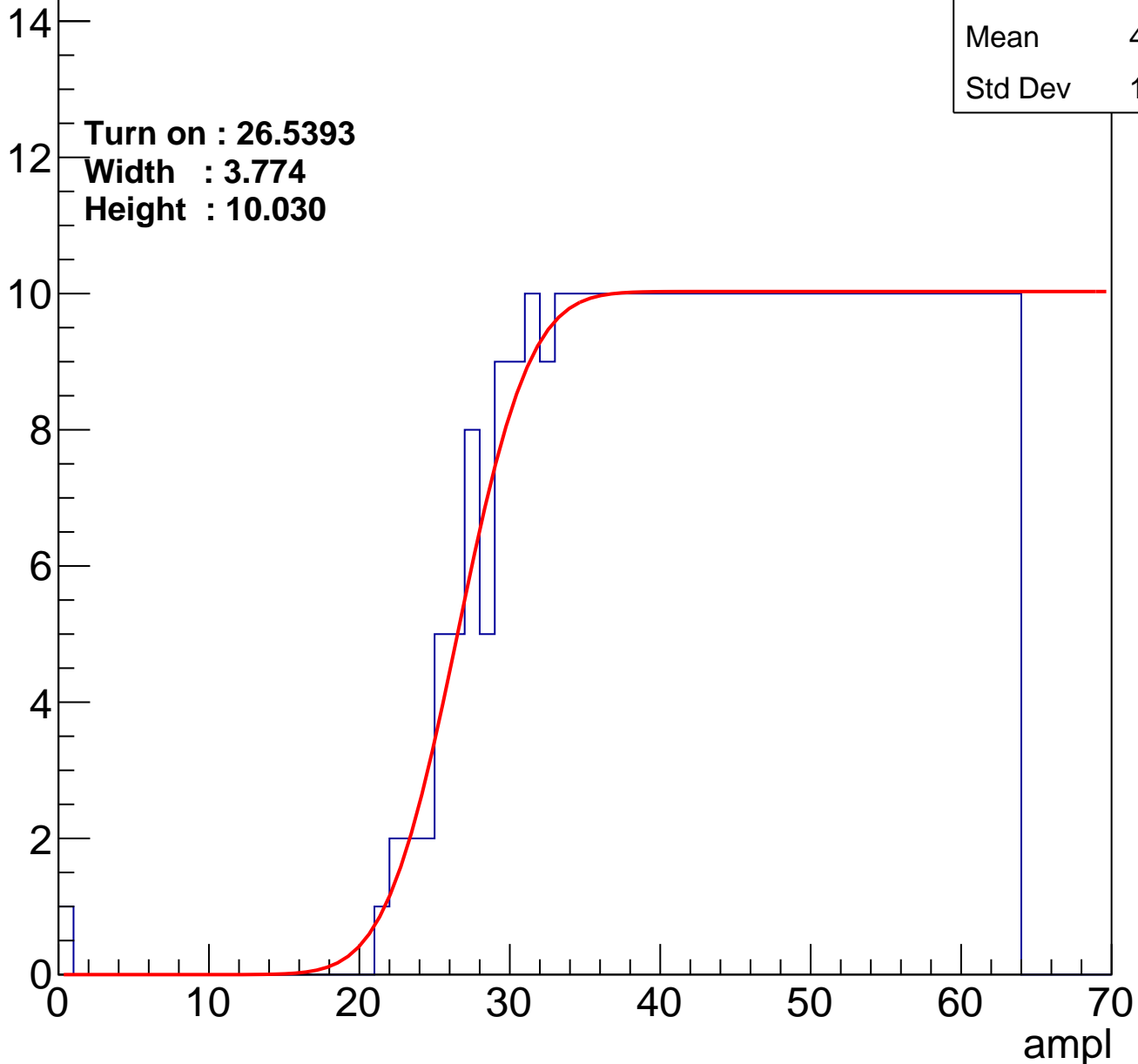
Entries	378
Mean	44.39
Std Dev	11.34

Turn on : 26.5393

Width : 3.774

Height : 10.030

Entry



B1L103S, U26-ch102

calib_packv5_042523_0143.root, FC#7, port C2

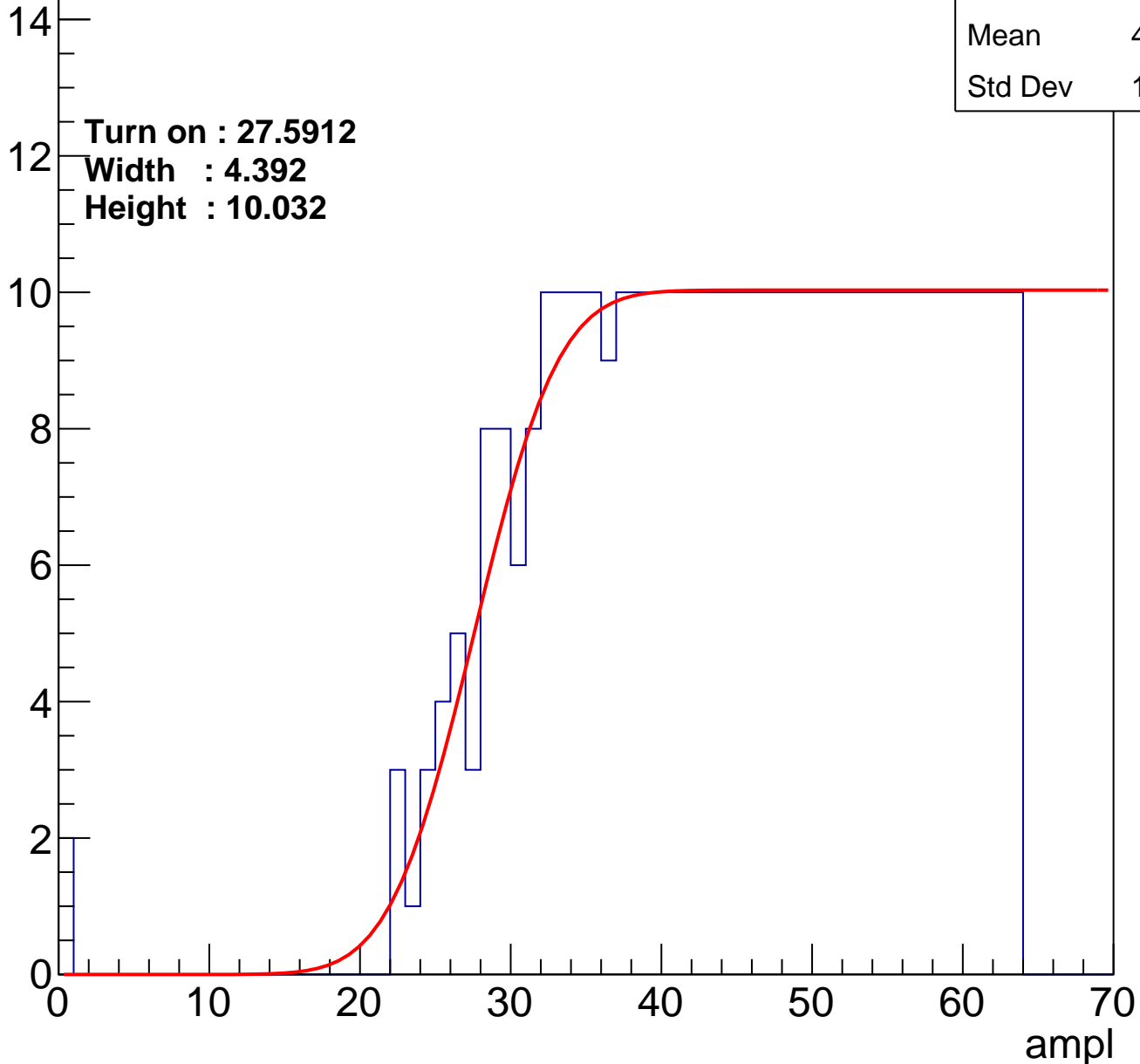
Entries	370
Mean	44.65
Std Dev	11.42

Turn on : 27.5912

Width : 4.392

Height : 10.032

Entry



B1L103S, U26-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.6
Std Dev	11.36

Turn on : 27.4948

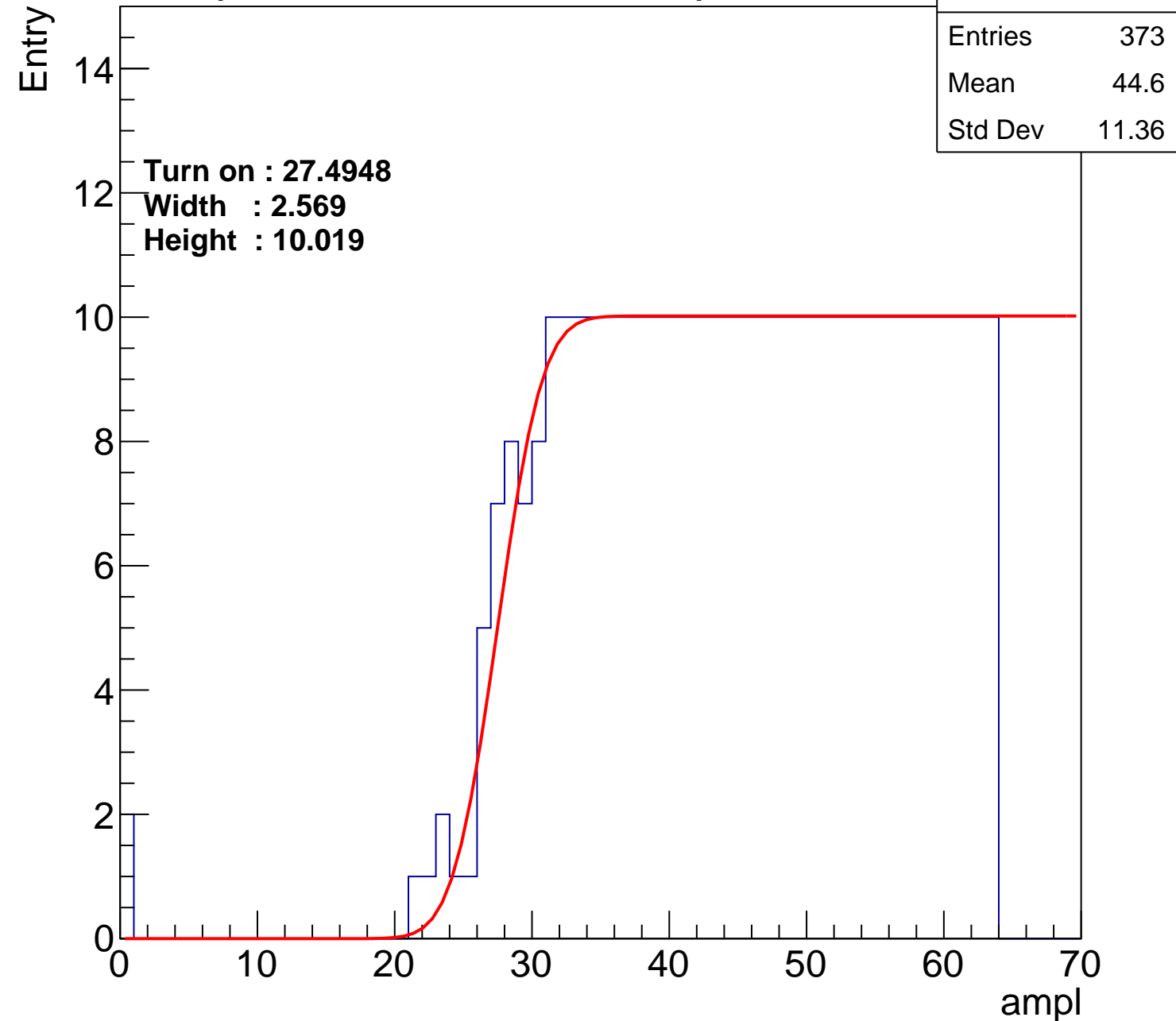
Width : 2.569

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch104

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.97
Std Dev	11.96

Turn on : 27.0926

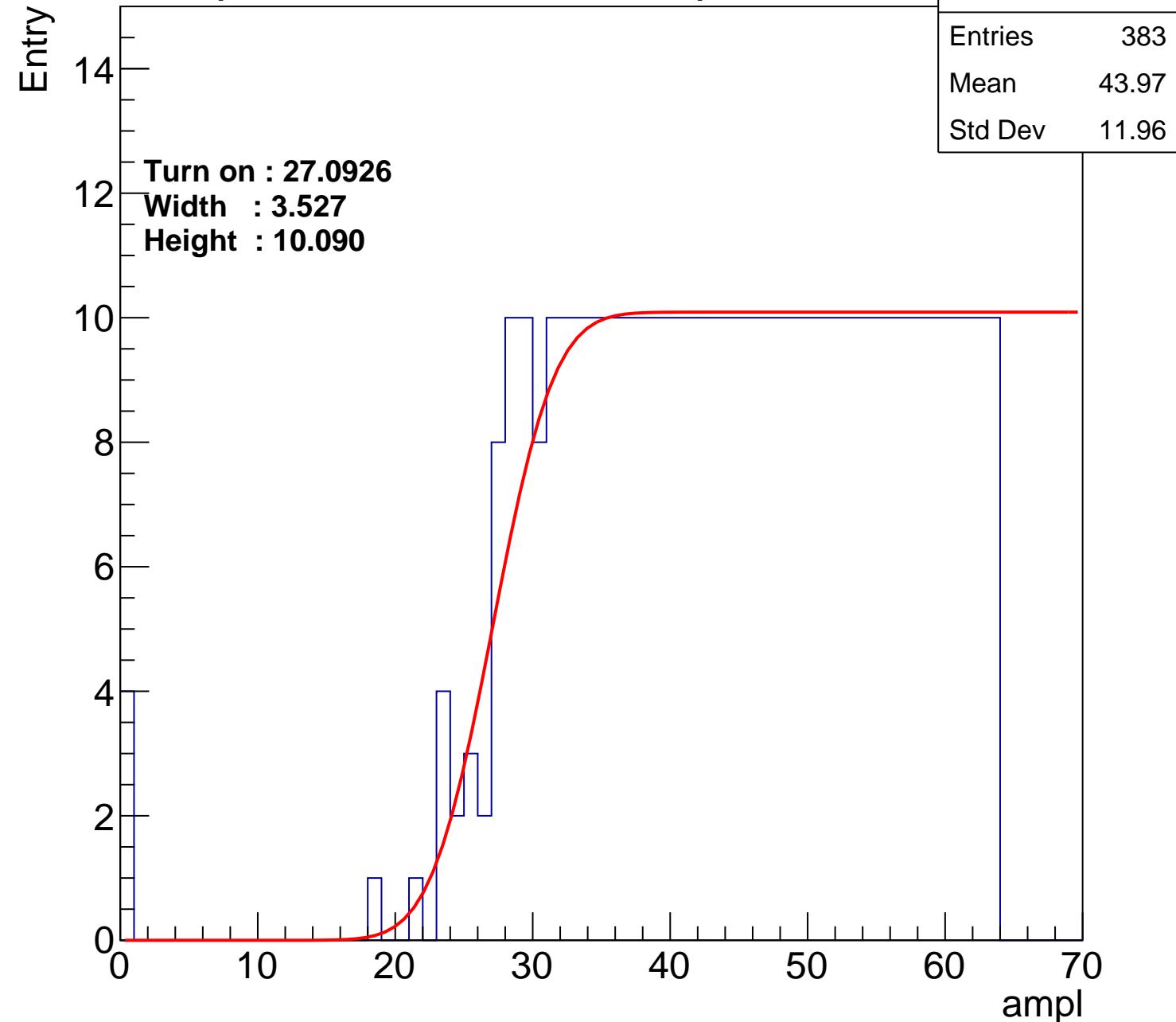
Width : 3.527

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch105

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.94
Std Dev	11.89

Turn on : 25.8800

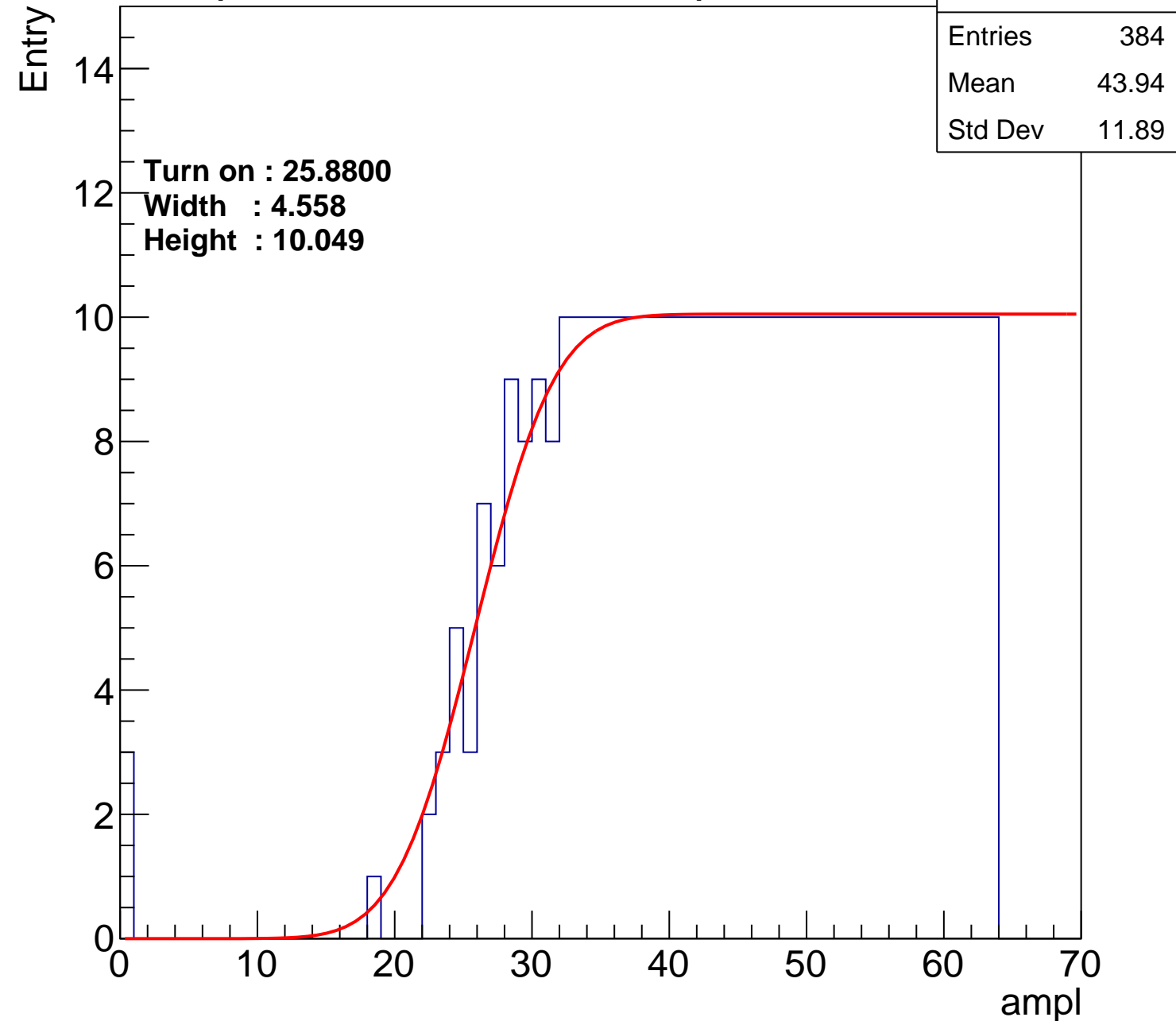
Width : 4.558

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch106

calib_packv5_042523_0143.root, FC#7, port C2

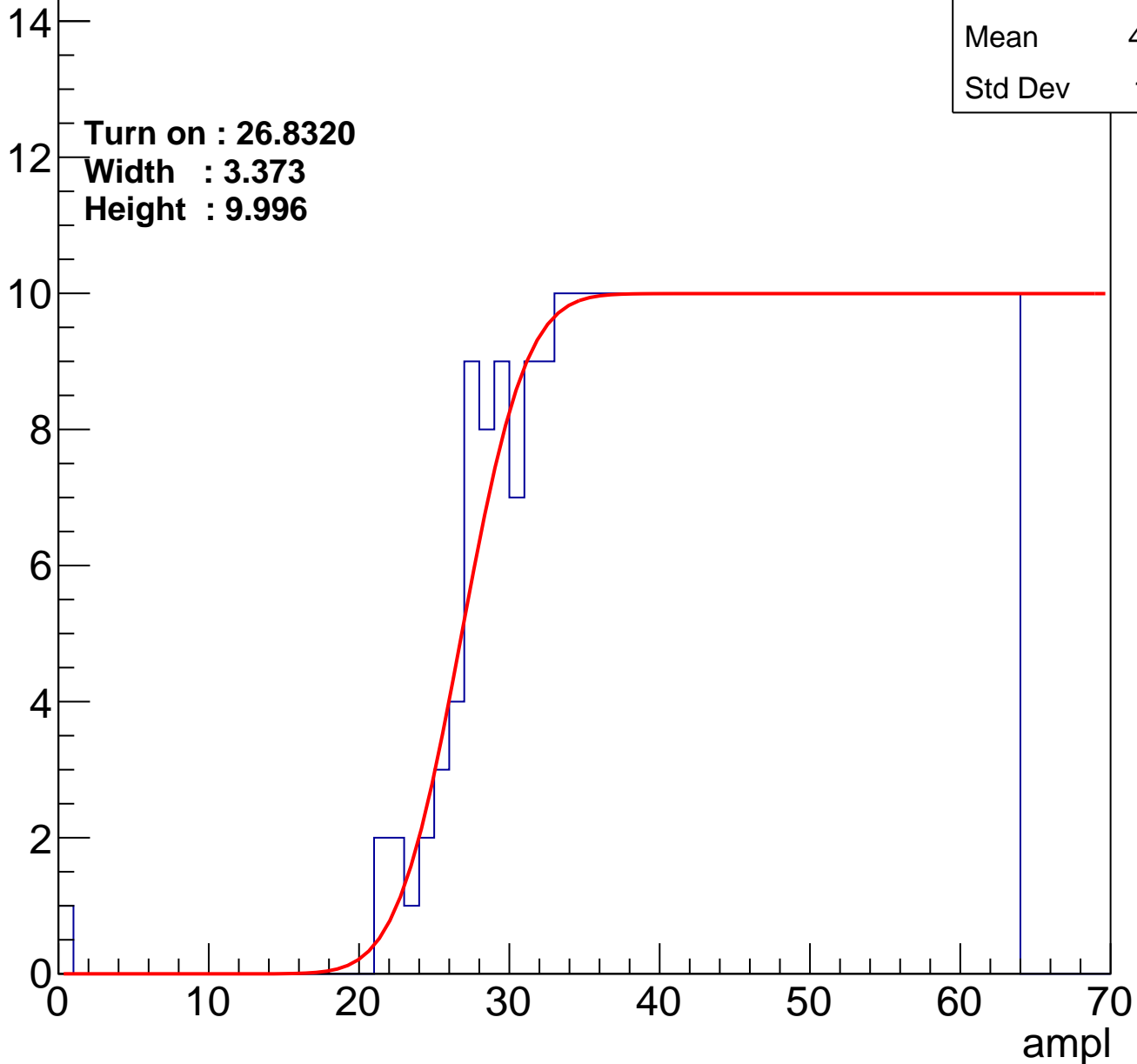
Entries	376
Mean	44.47
Std Dev	11.31

Turn on : 26.8320

Width : 3.373

Height : 9.996

Entry



B1L103S, U26-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.88
Std Dev	11.08

Turn on : 27.2427

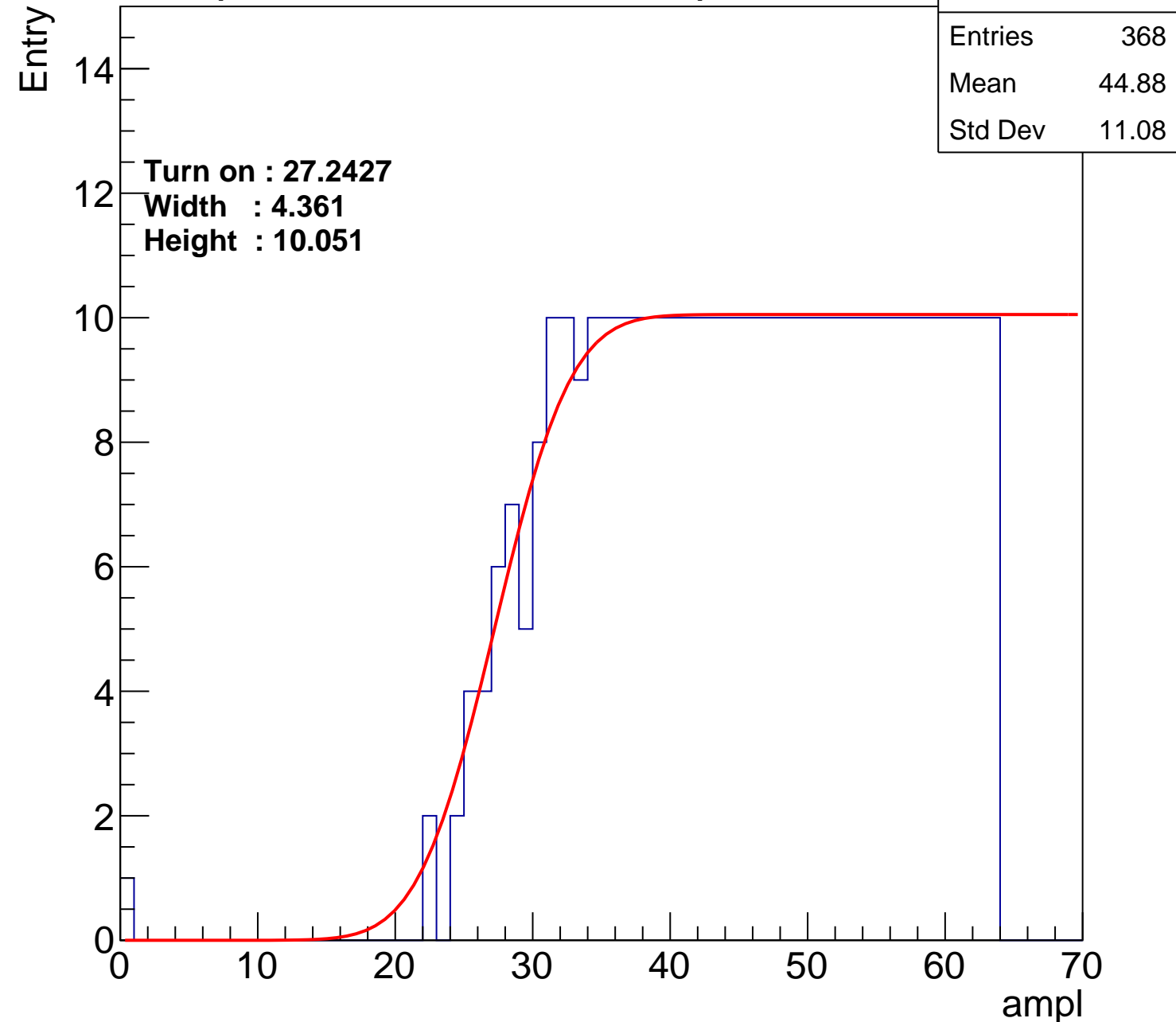
Width : 4.361

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch108

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.65
Std Dev	11.9

Turn on : 25.2237

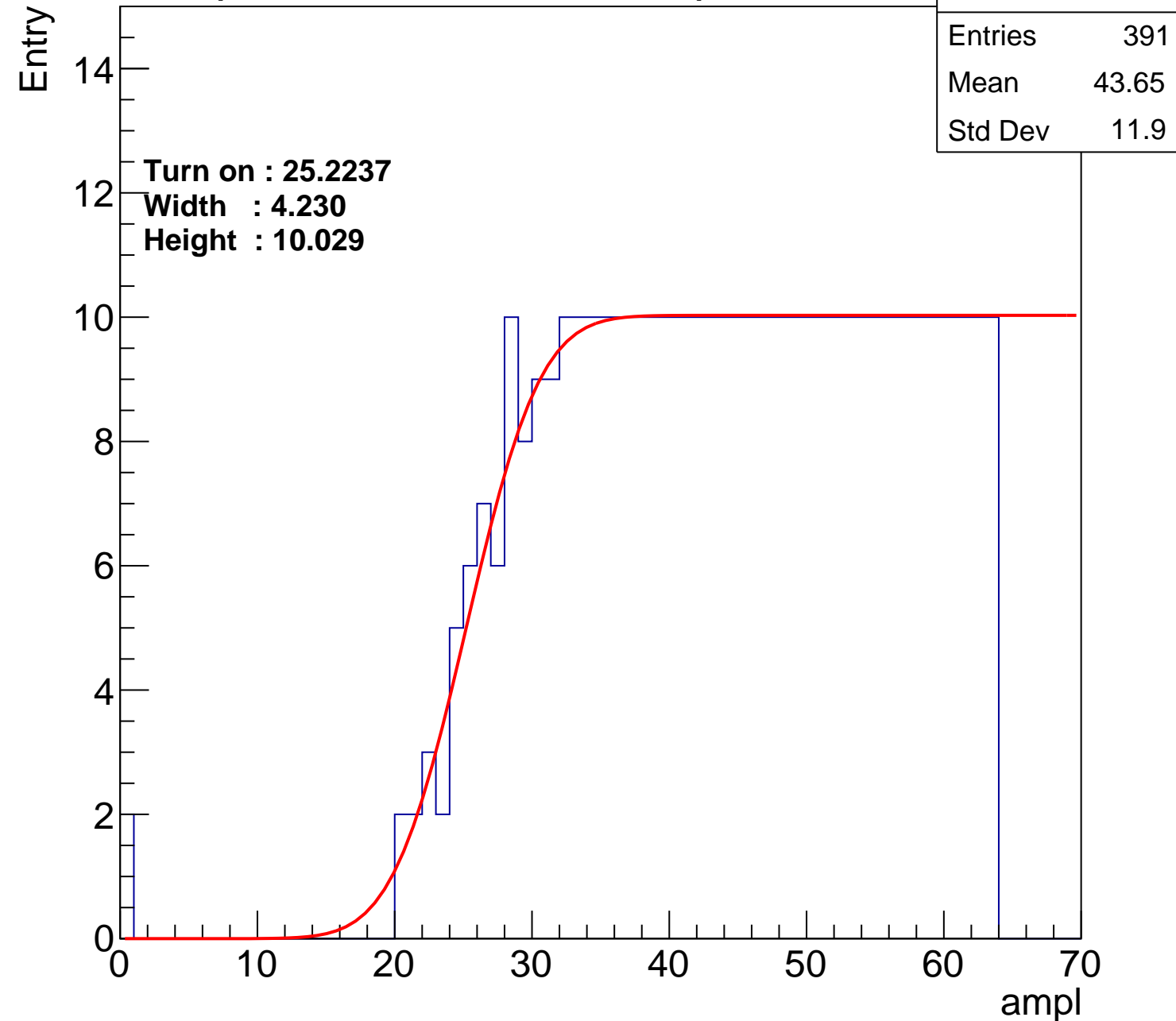
Width : 4.230

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch109

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.4
Std Dev	11.82

Turn on : 27.4002

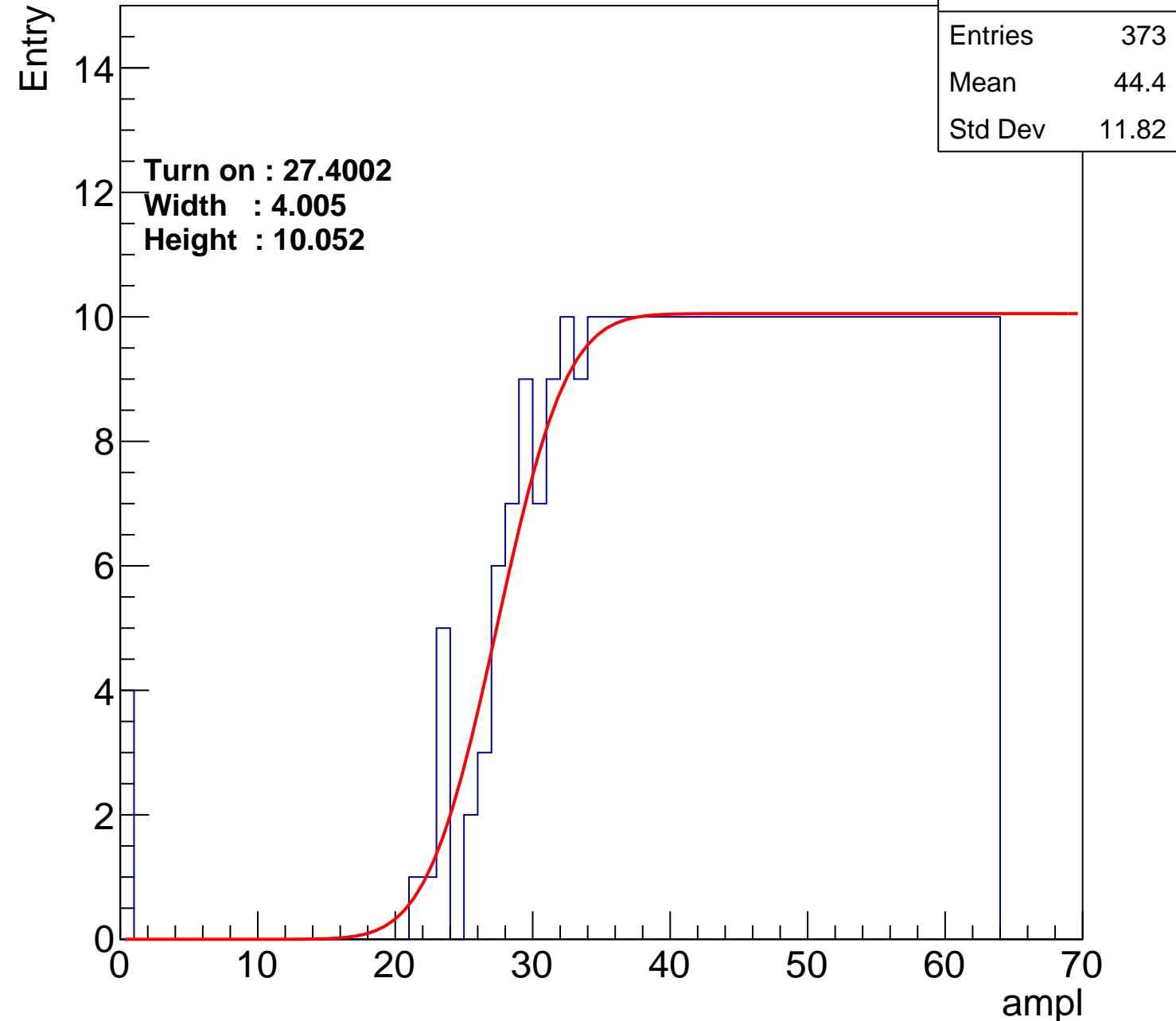
Width : 4.005

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch110

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.64
Std Dev	11.51

Turn on : 27.4218

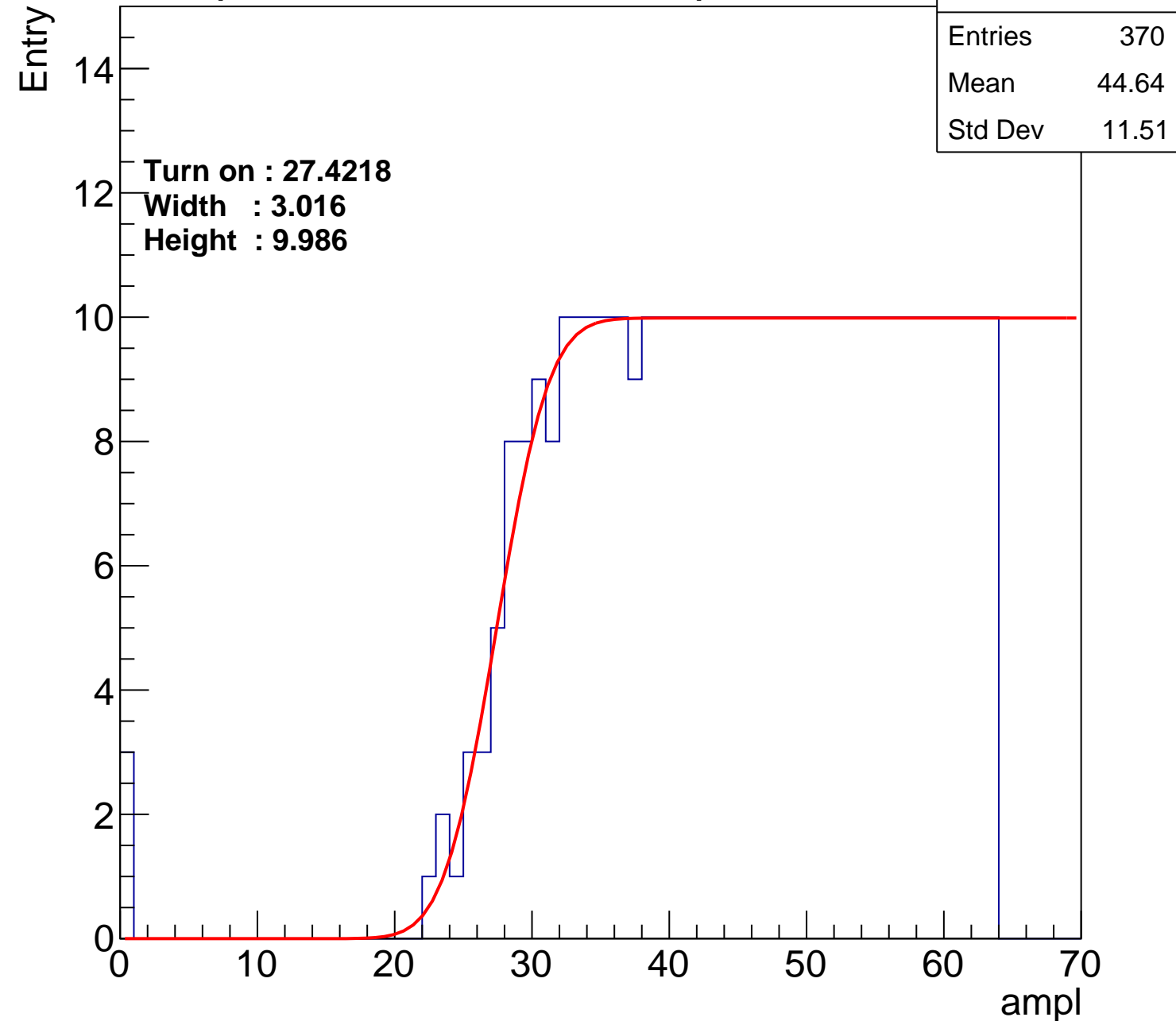
Width : 3.016

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch111

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.94
Std Dev	11.58

Turn on : 25.3667

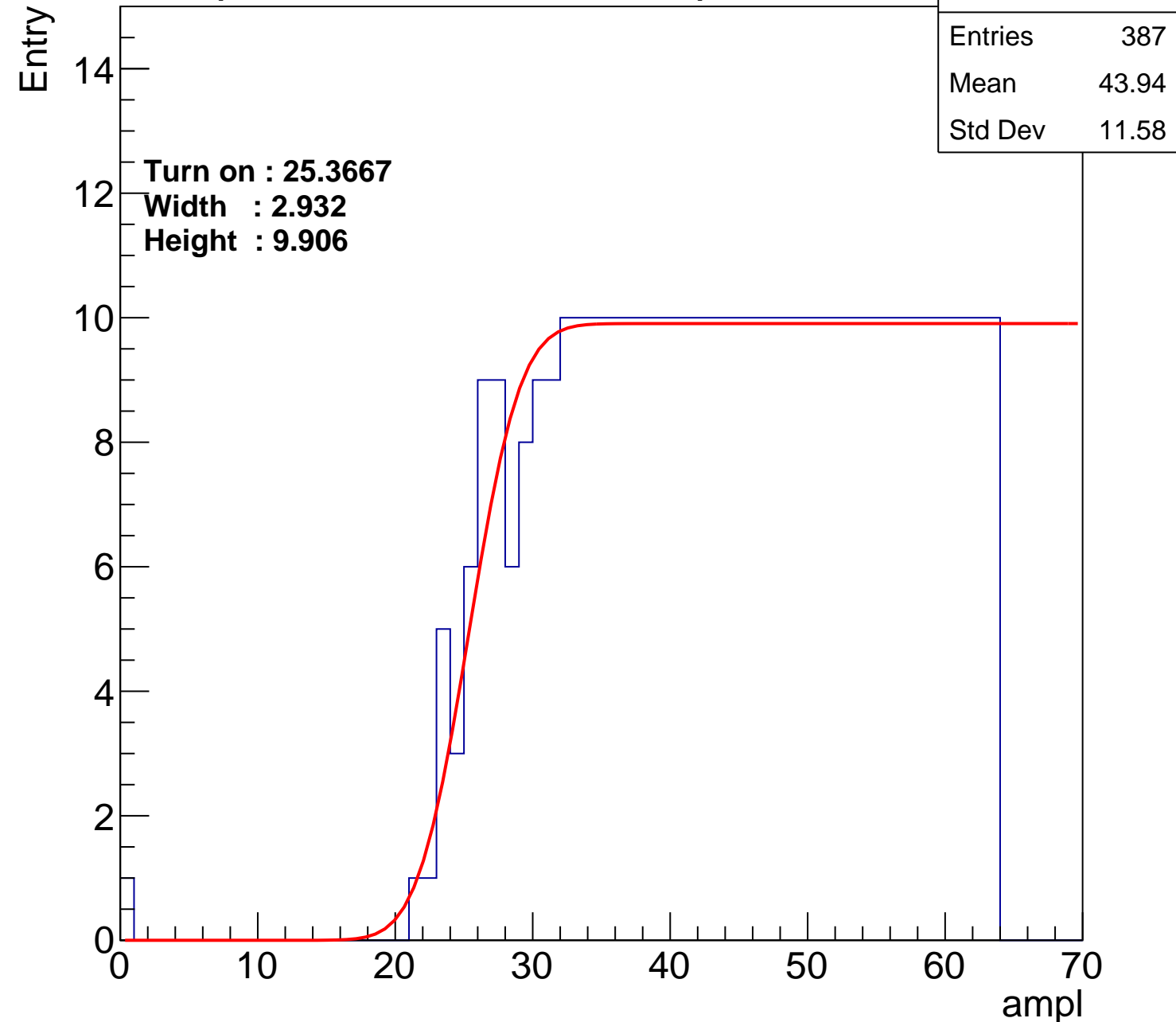
Width : 2.932

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.36
Std Dev	12.04

Turn on : 25.5154

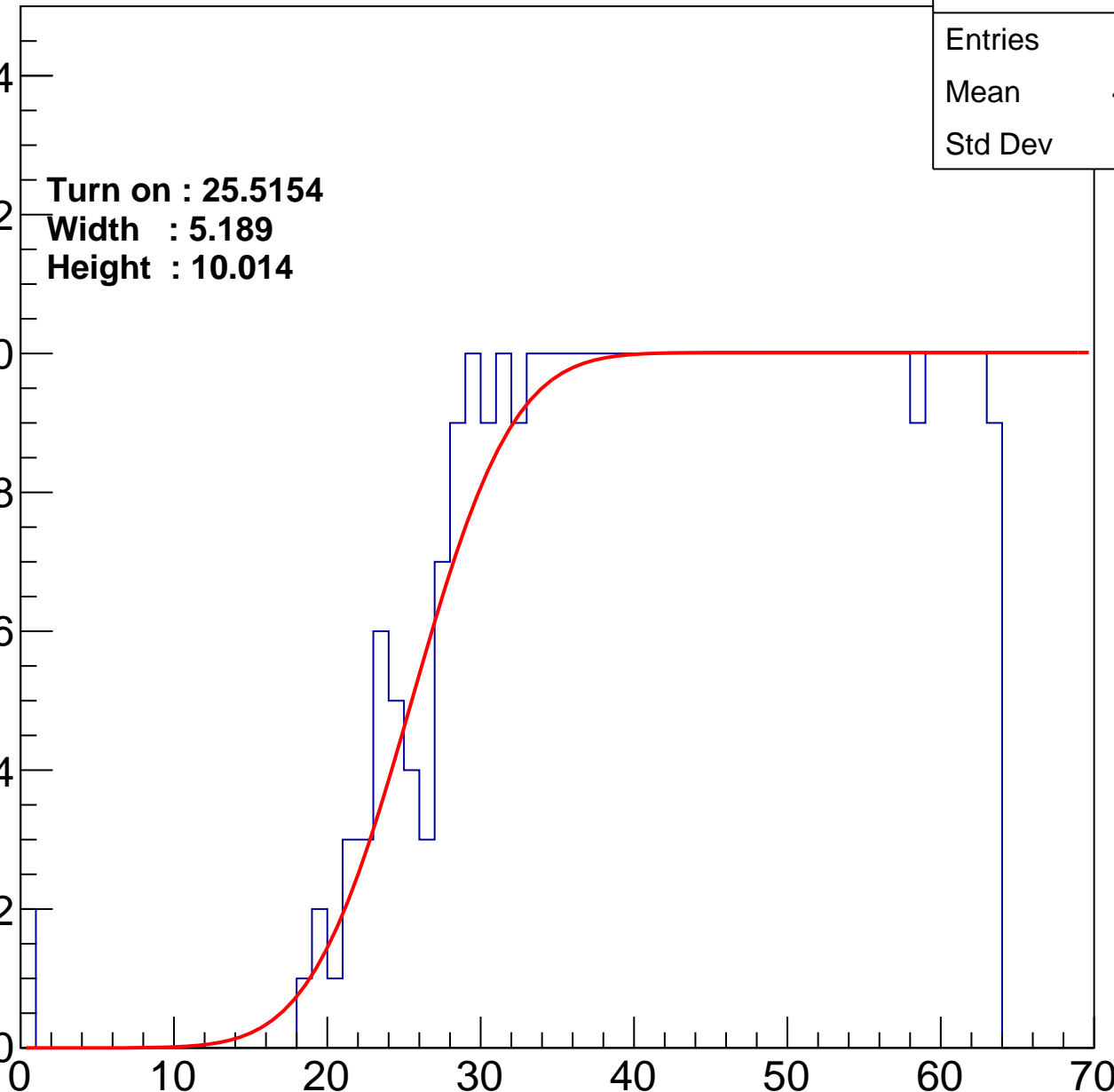
Width : 5.189

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch113

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.27
Std Dev	11.79

Turn on : 28.0224

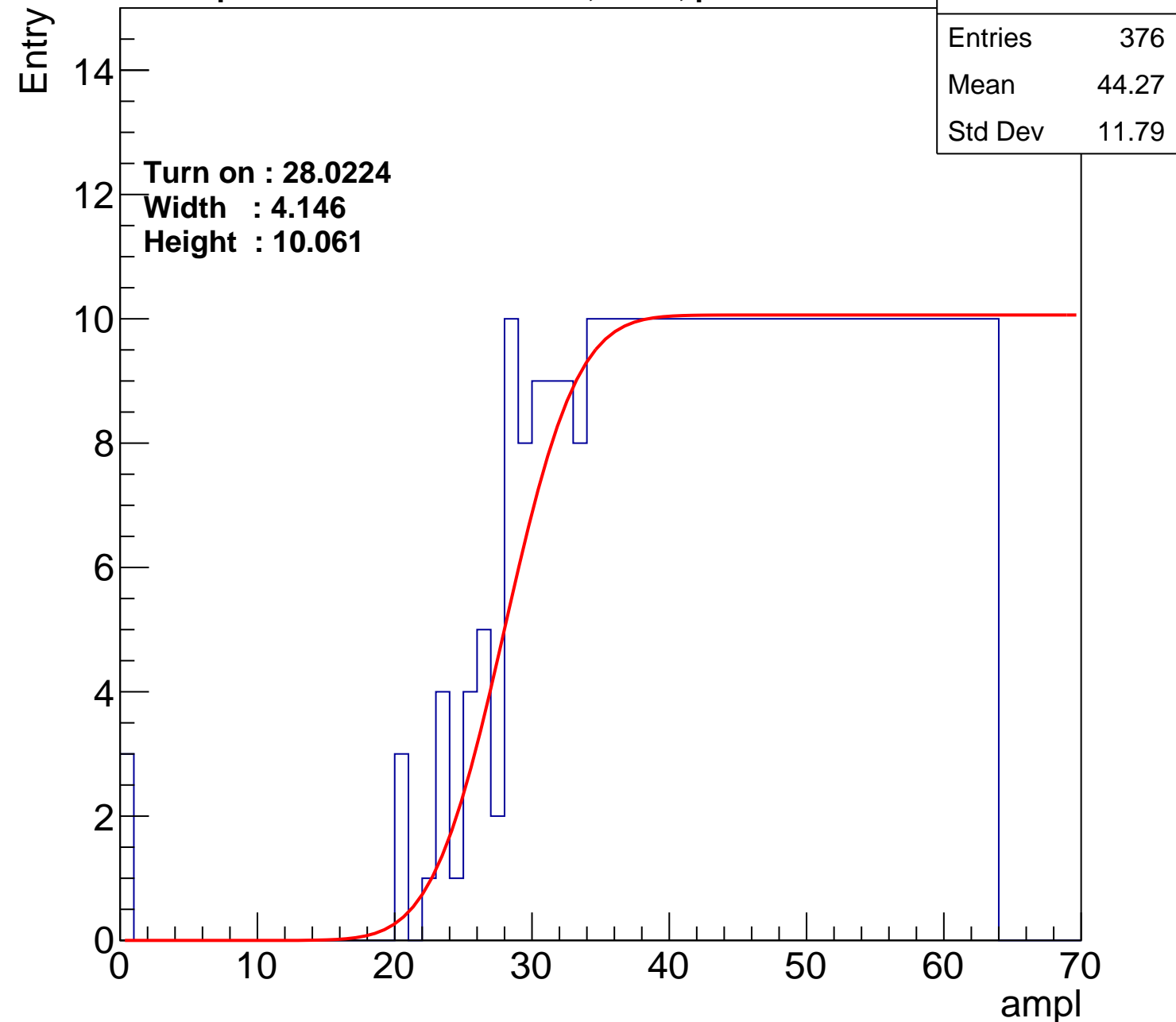
Width : 4.146

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch114

calib_packv5_042523_0143.root, FC#7, port C2

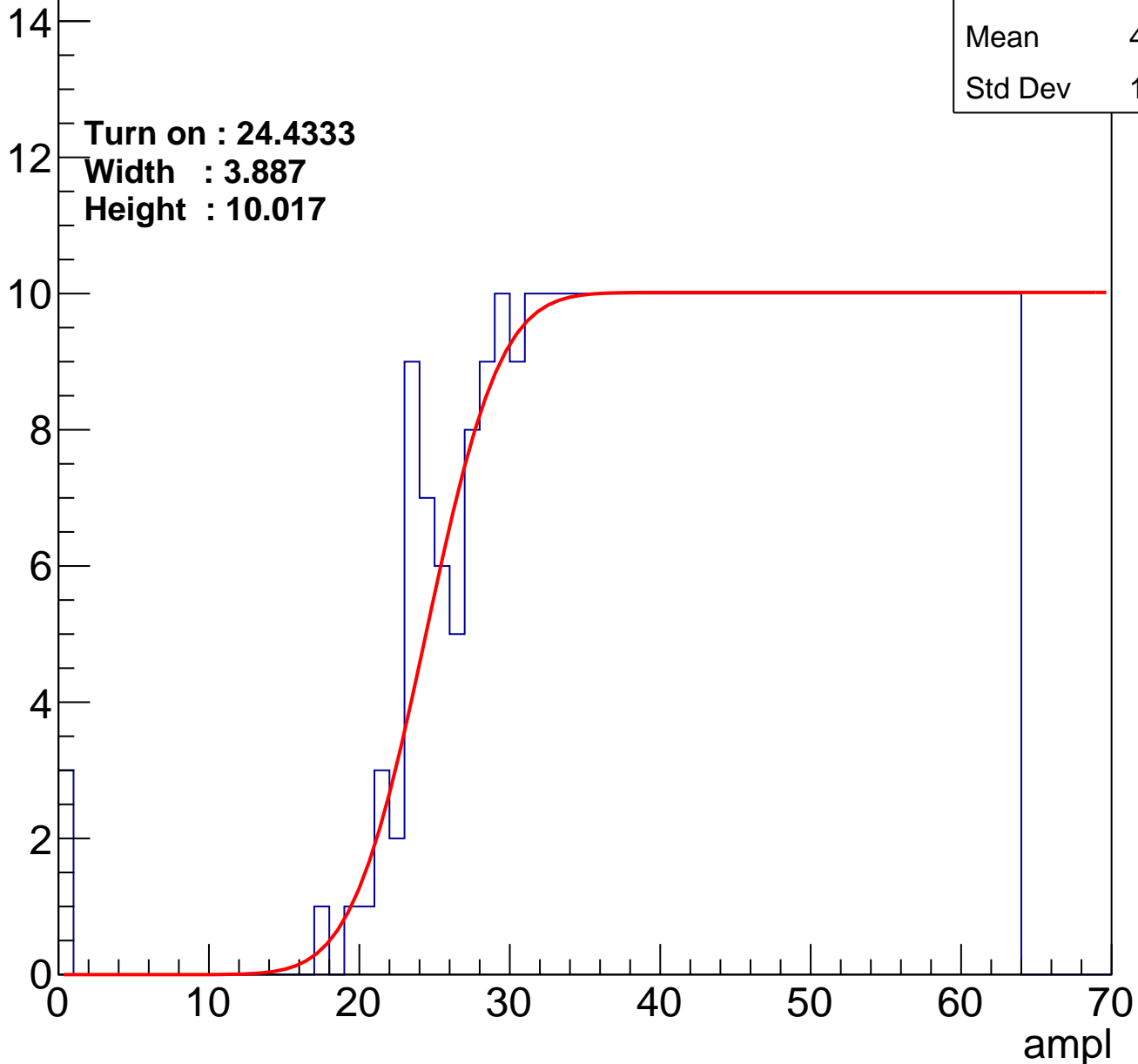
Entries	404
Mean	42.96
Std Dev	12.38

Turn on : 24.4333

Width : 3.887

Height : 10.017

Entry



B1L103S, U26-ch115

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.65
Std Dev	12.22

Turn on : 26.1858

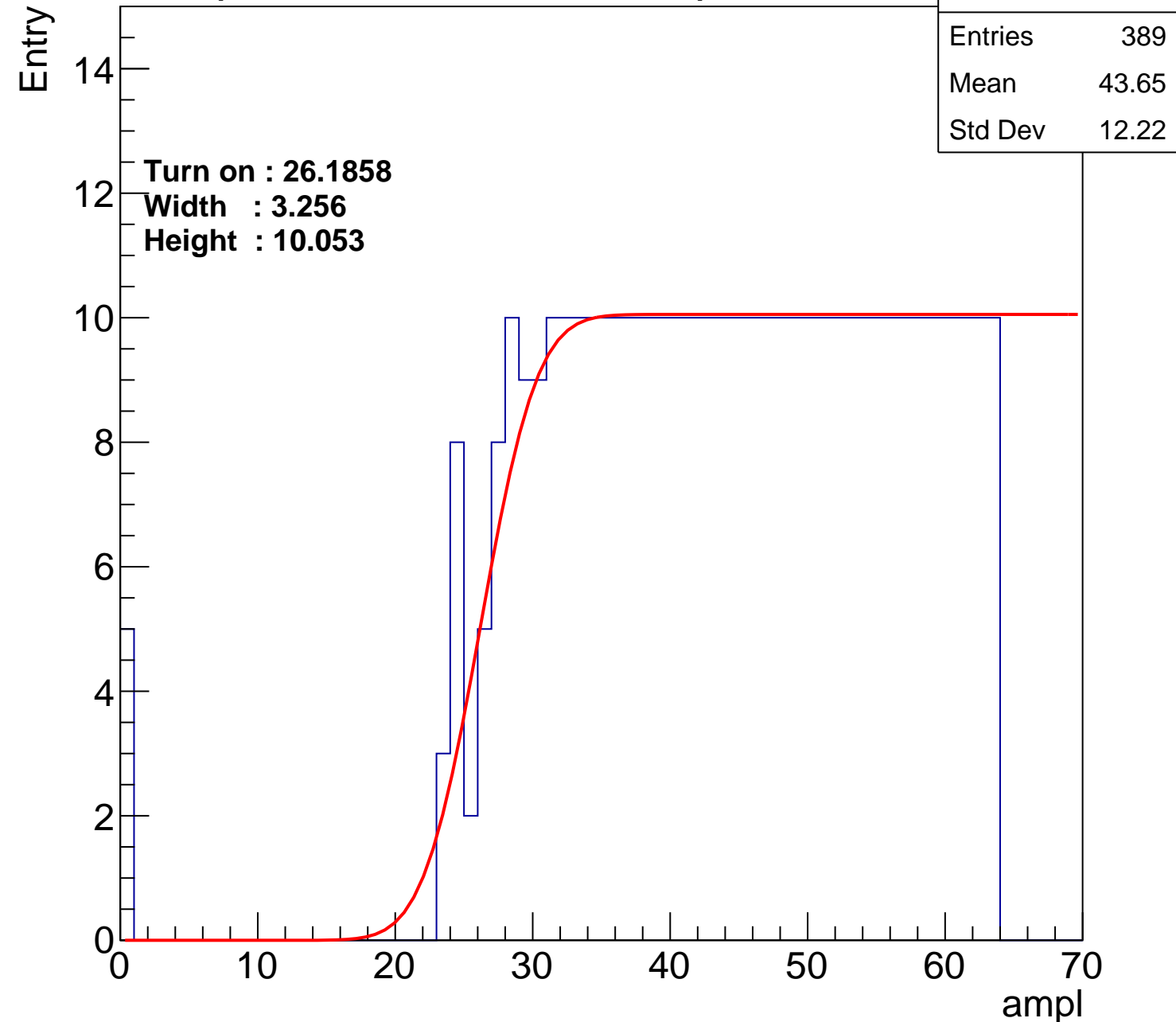
Width : 3.256

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch116

calib_packv5_042523_0143.root, FC#7, port C2

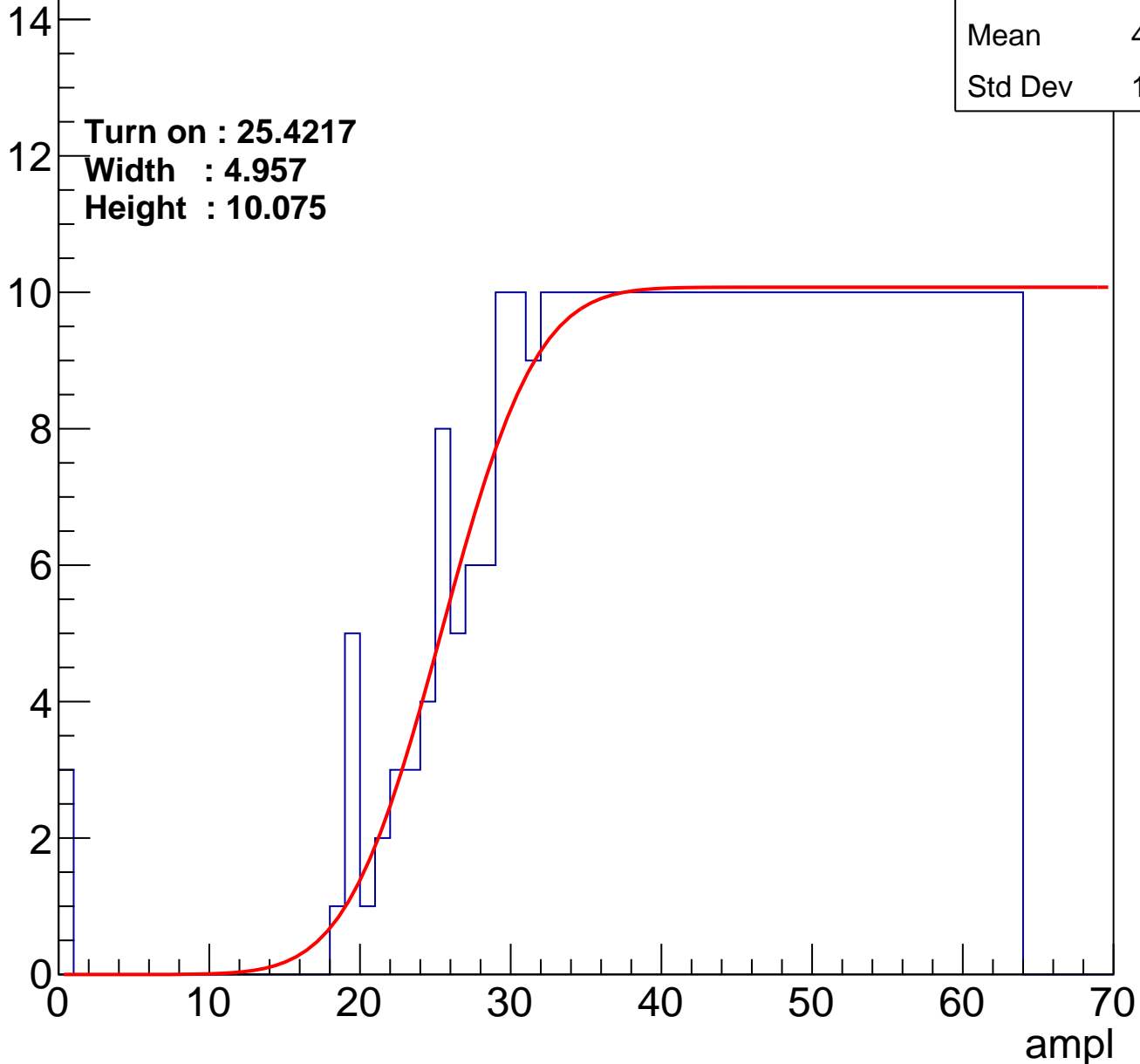
Entries	396
Mean	43.27
Std Dev	12.32

Turn on : 25.4217

Width : 4.957

Height : 10.075

Entry



B1L103S, U26-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.09
Std Dev	11.86

Turn on : 26.2804

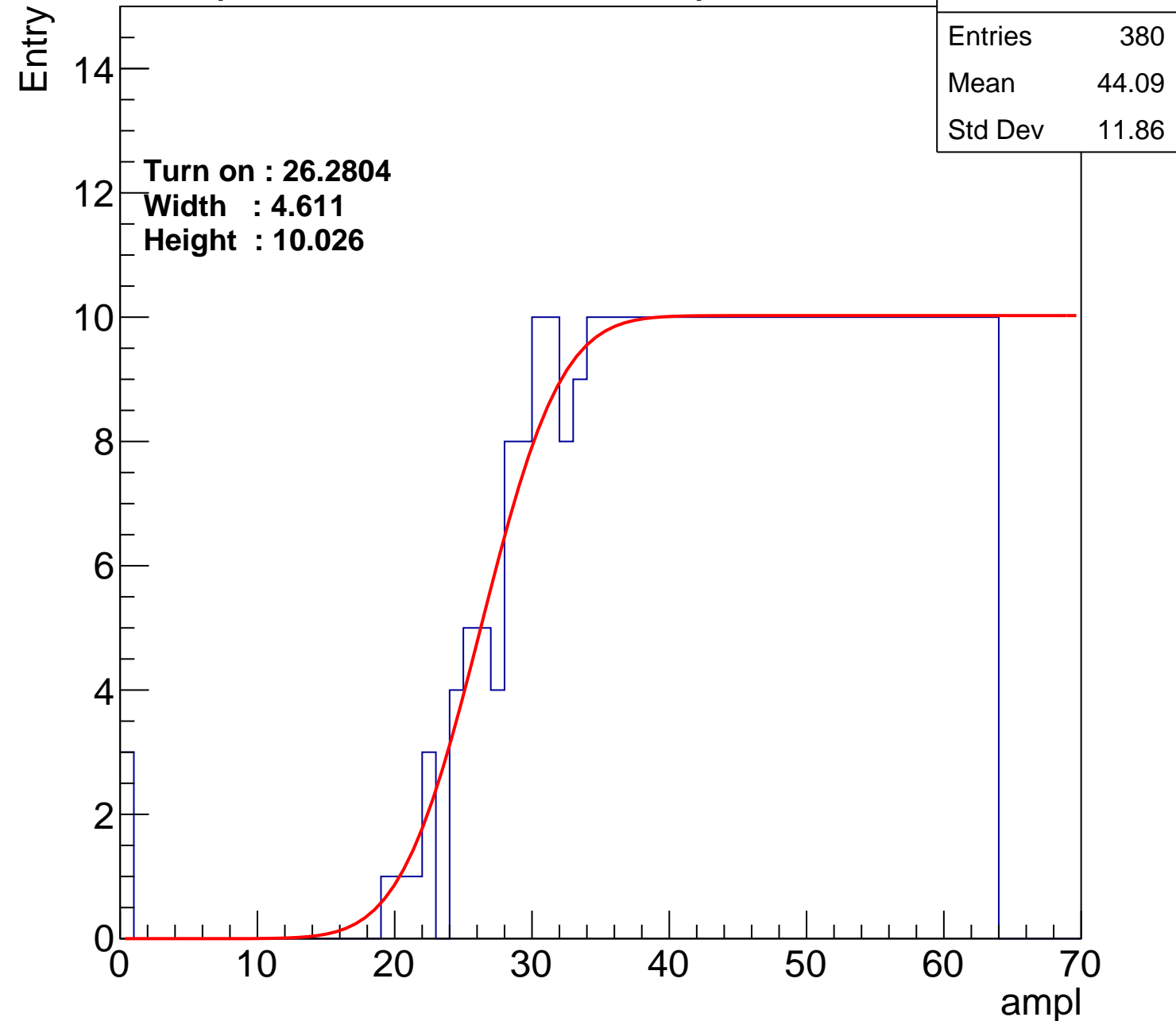
Width : 4.611

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.32
Std Dev	11.73

Turn on : 26.5826

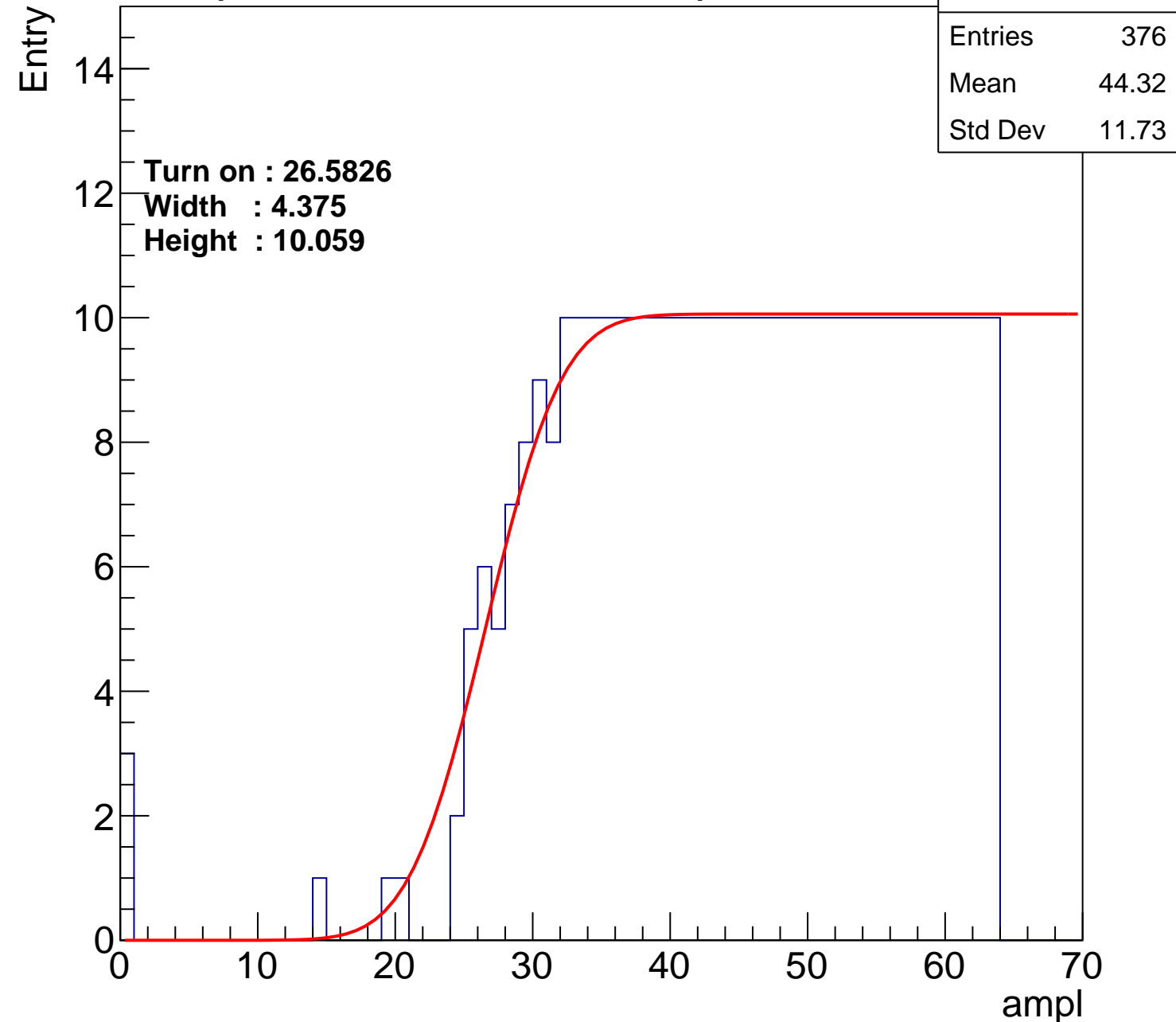
Width : 4.375

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch119

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.12
Std Dev	11.82

Turn on : 26.8925

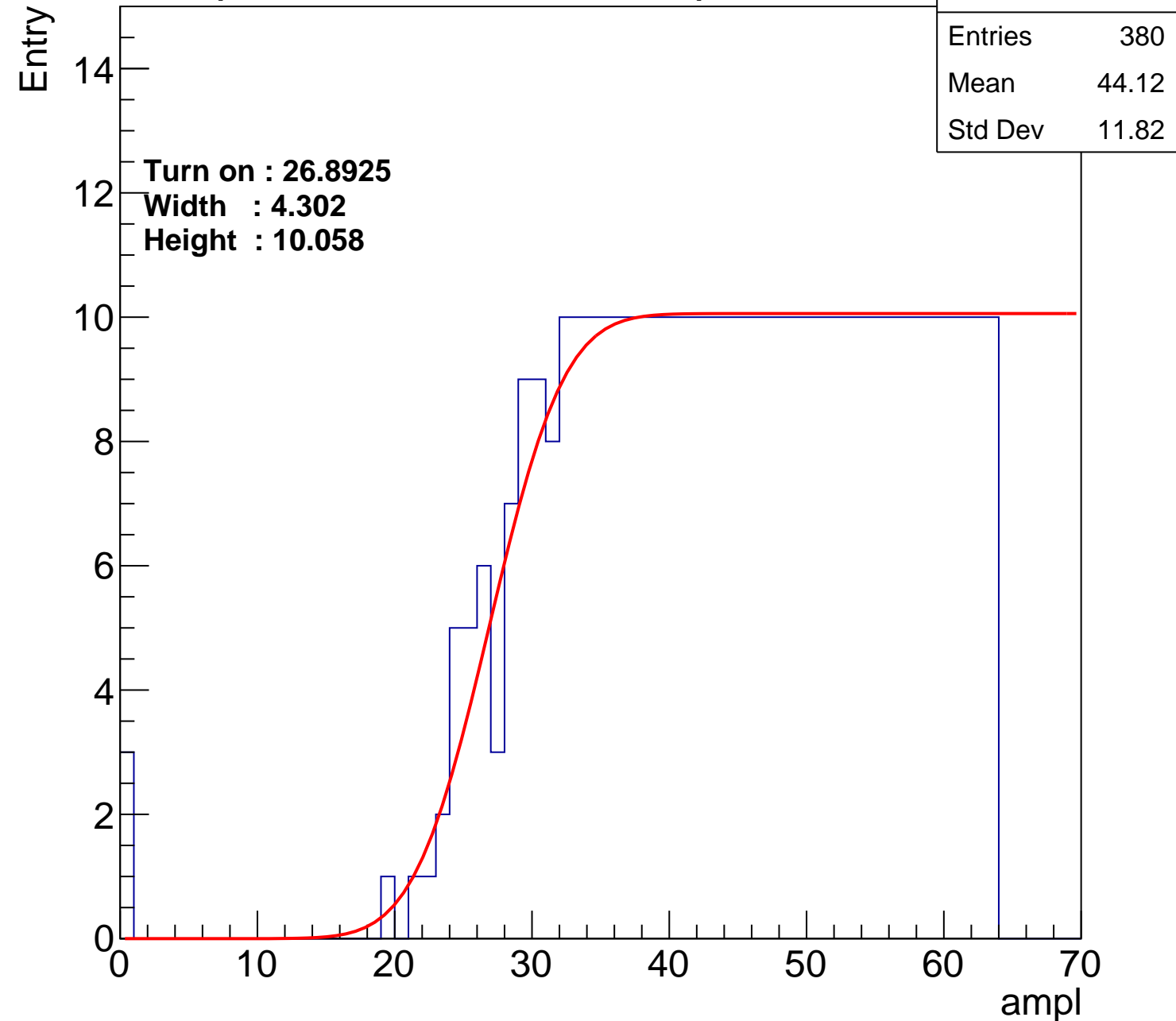
Width : 4.302

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch120

calib_packv5_042523_0143.root, FC#7, port C2

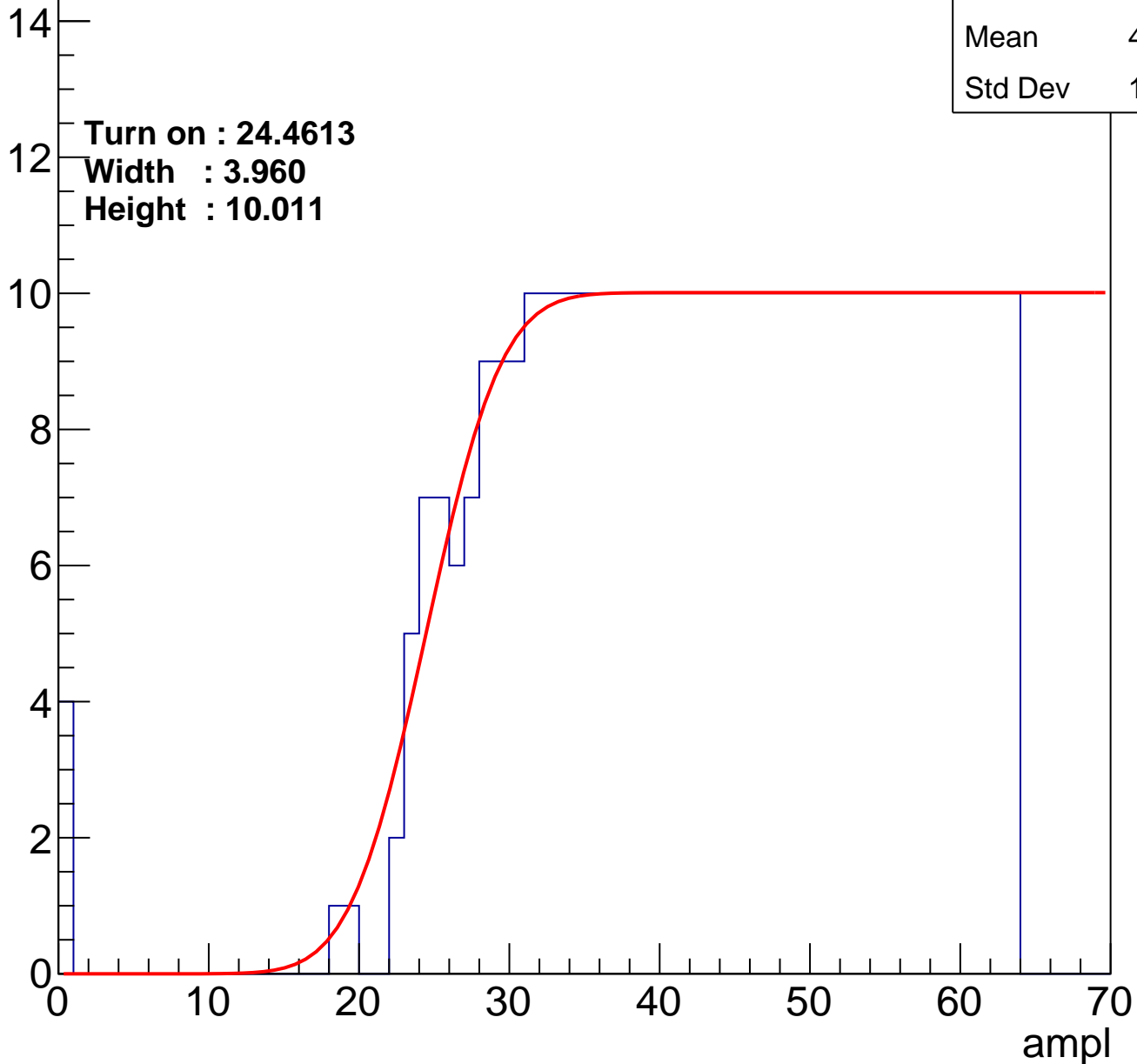
Entries	397
Mean	43.27
Std Dev	12.32

Turn on : 24.4613

Width : 3.960

Height : 10.011

Entry



B1L103S, U26-ch121

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.53
Std Dev	11.41

Turn on : 27.2048

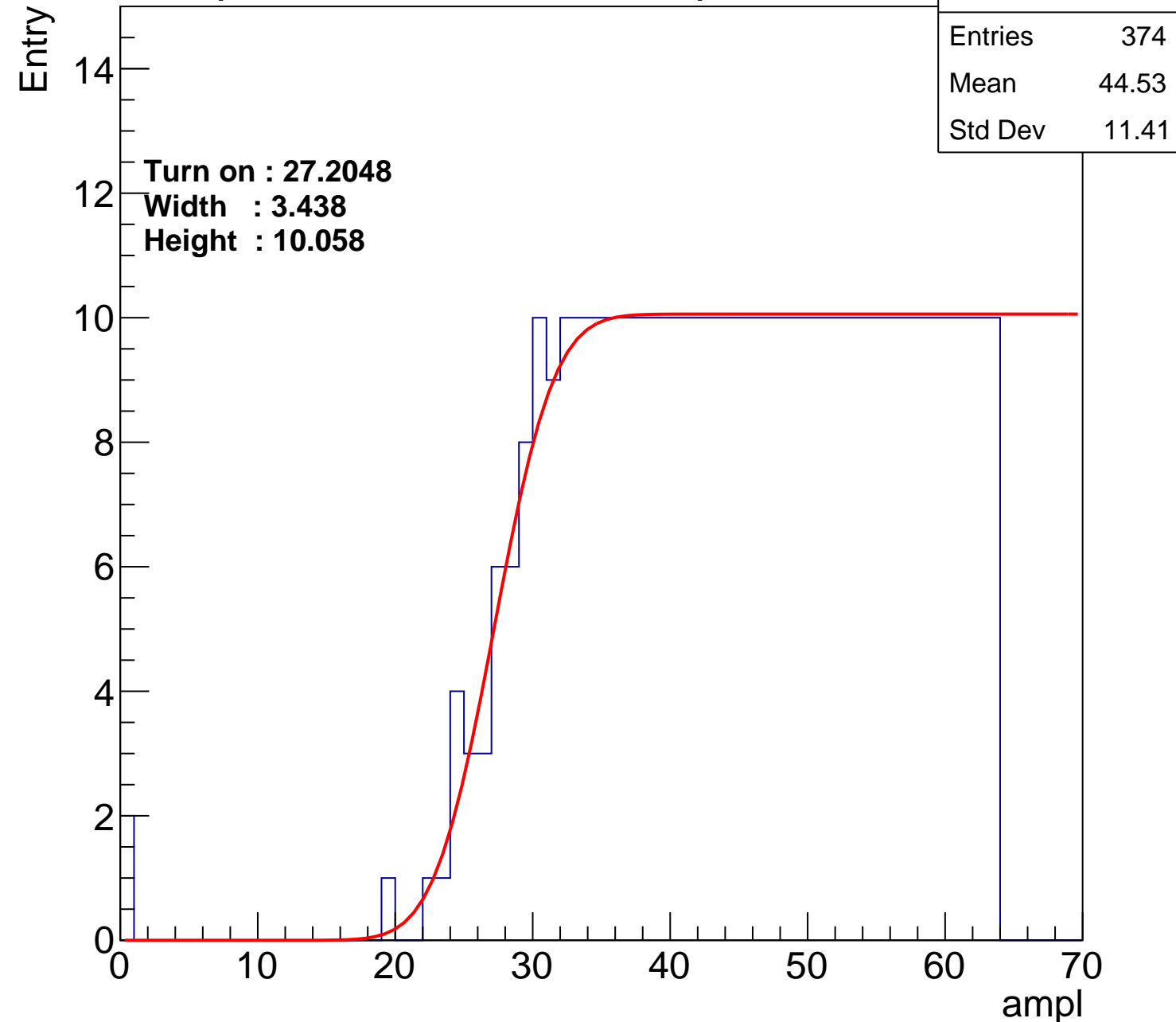
Width : 3.438

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch122

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	44.9
Std Dev	11.33

Turn on : 27.8579

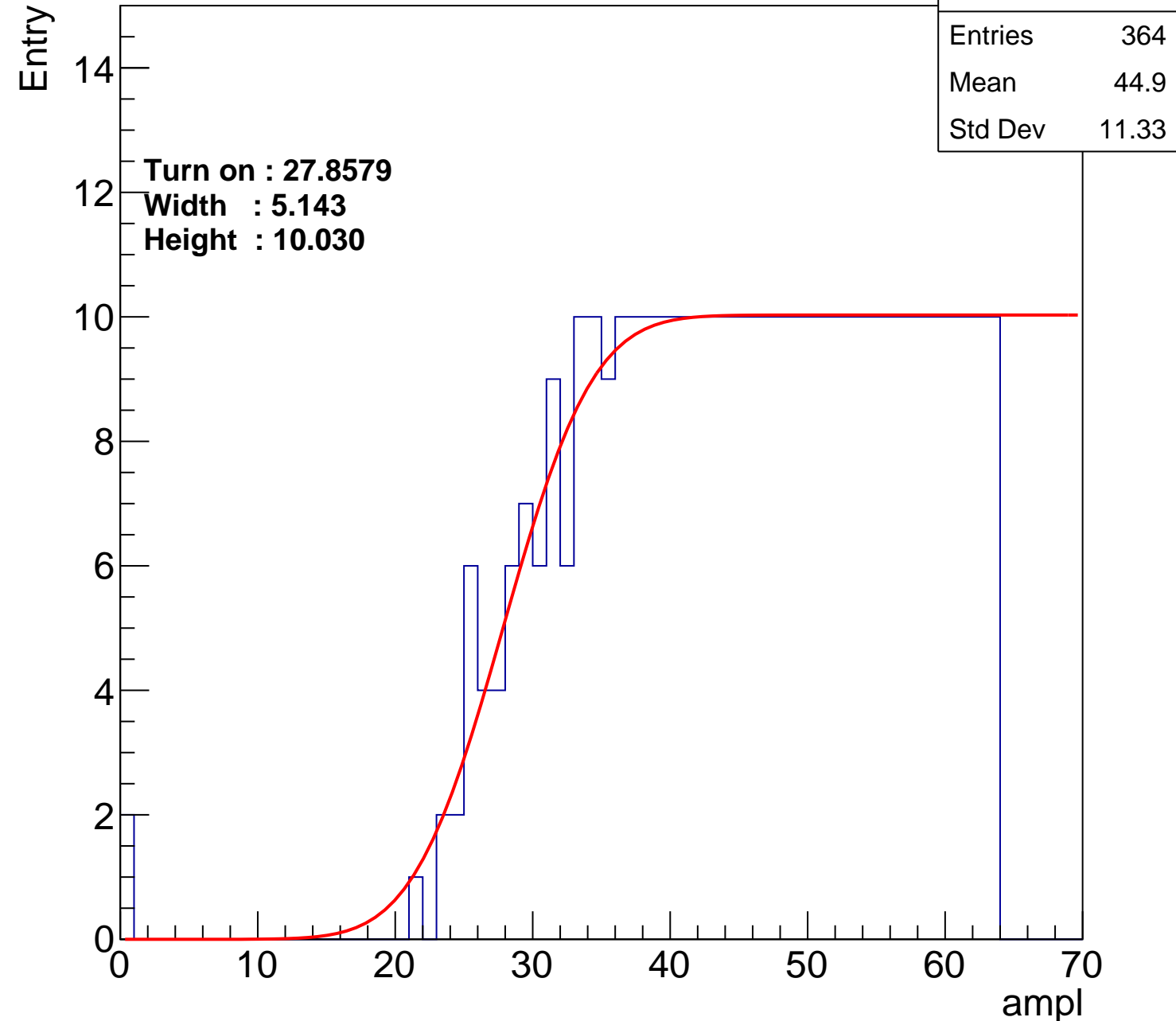
Width : 5.143

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch123

calib_packv5_042523_0143.root, FC#7, port C2

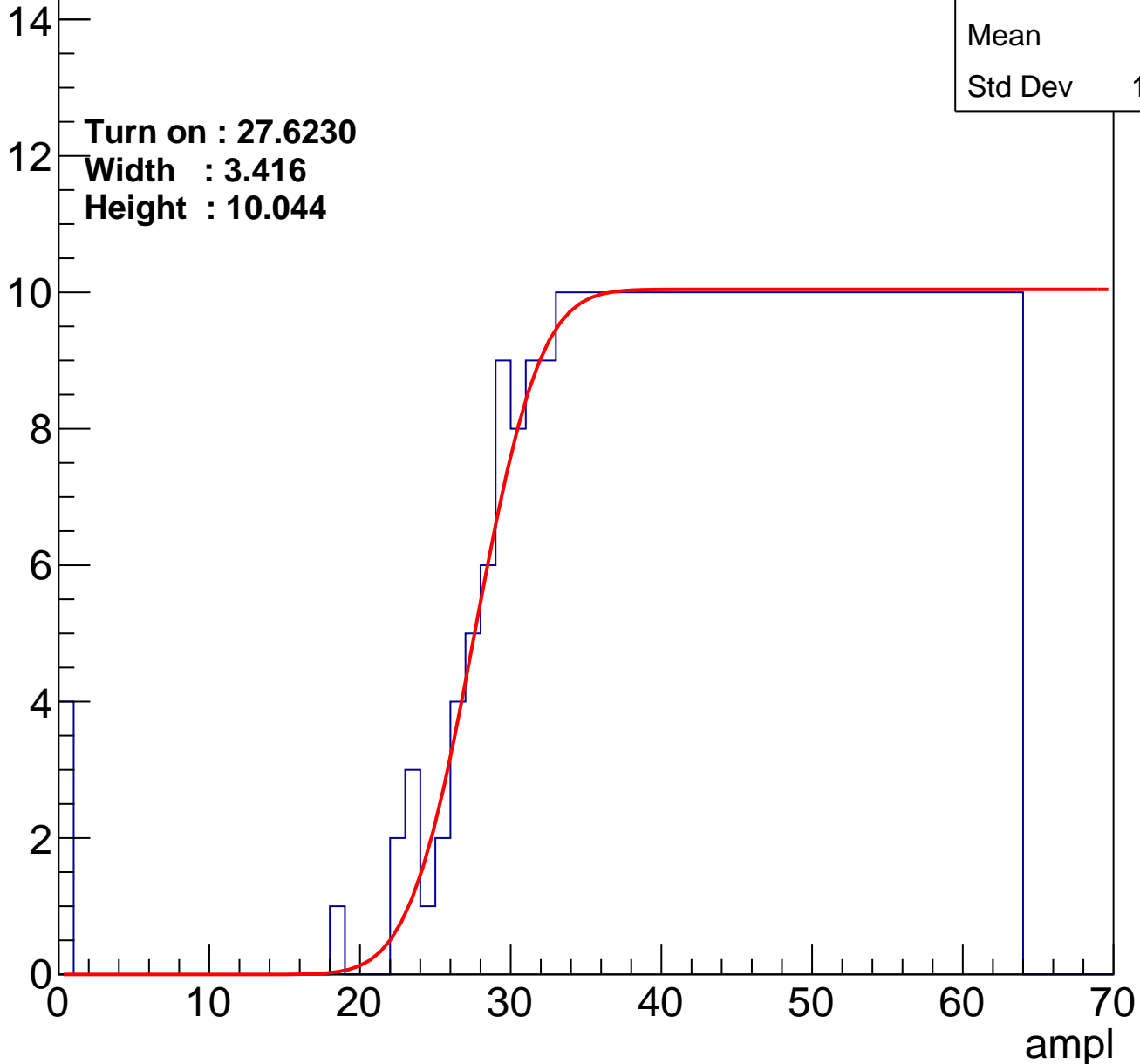
Entries	373
Mean	44.4
Std Dev	11.83

Turn on : 27.6230

Width : 3.416

Height : 10.044

Entry



B1L103S, U26-ch124

calib_packv5_042523_0143.root, FC#7, port C2

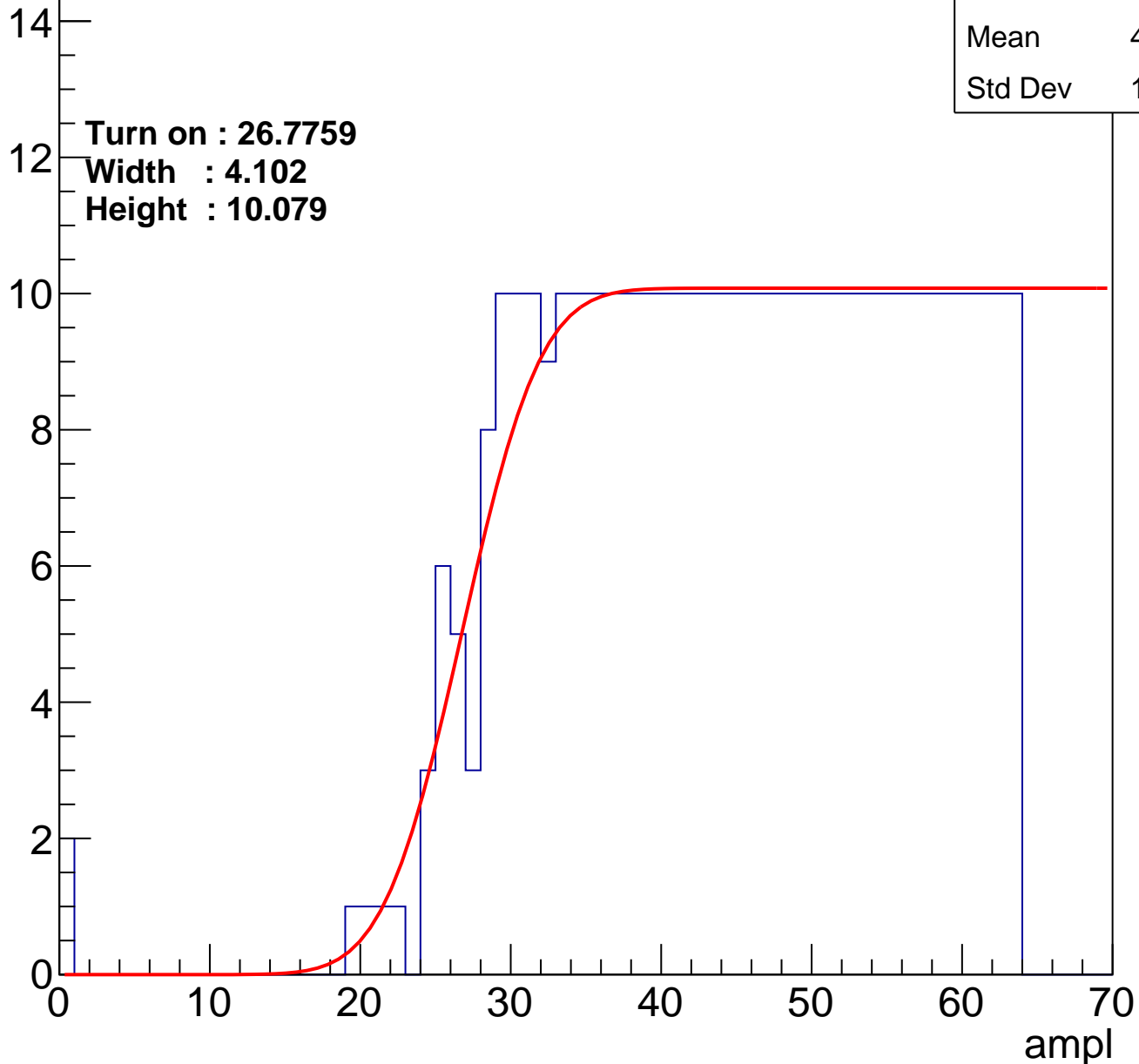
Entries	380
Mean	44.23
Std Dev	11.58

Turn on : 26.7759

Width : 4.102

Height : 10.079

Entry



B1L103S, U26-ch125

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	43.02
Std Dev	12.34

Turn on : 24.1538

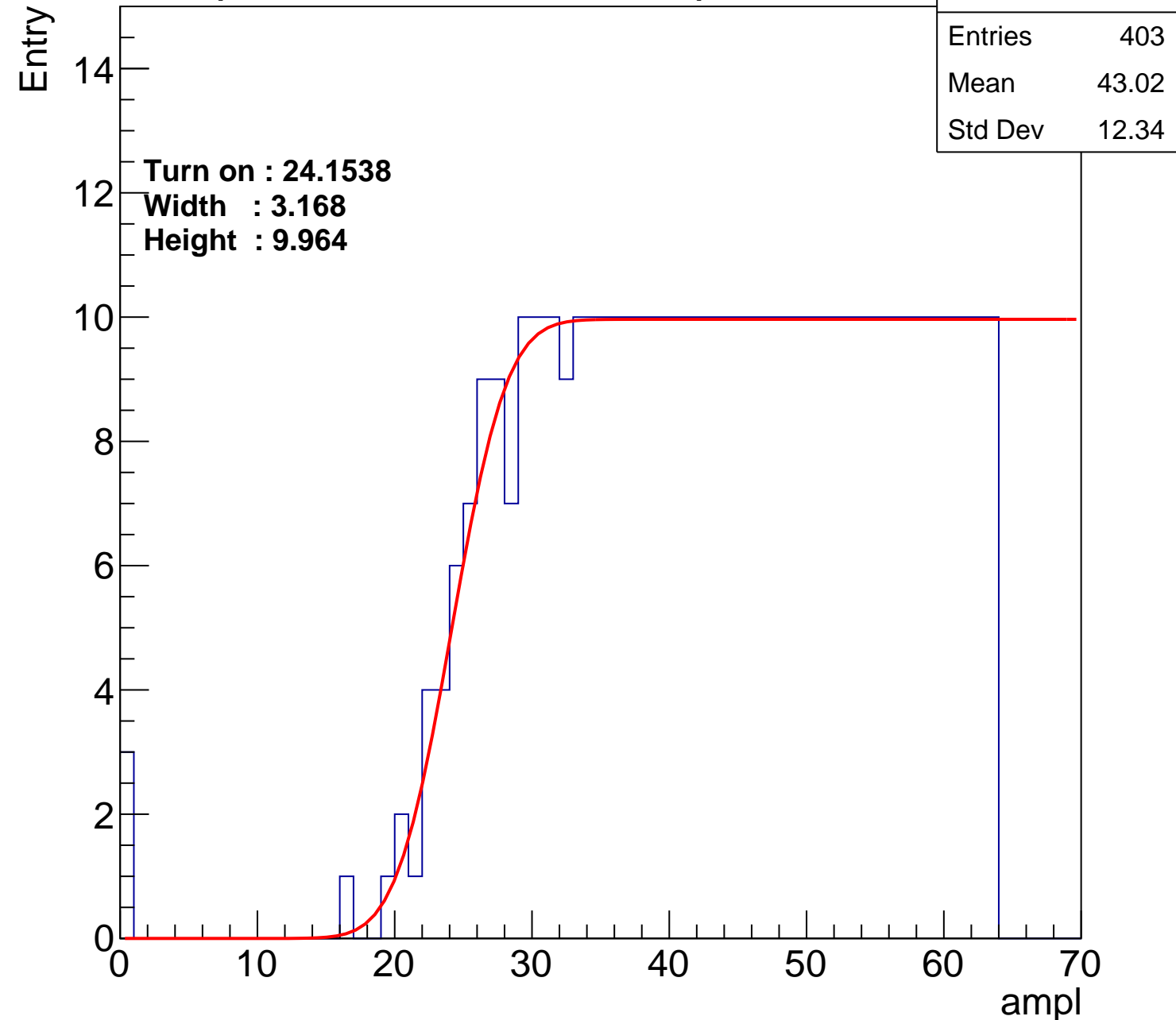
Width : 3.168

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch126

calib_packv5_042523_0143.root, FC#7, port C2

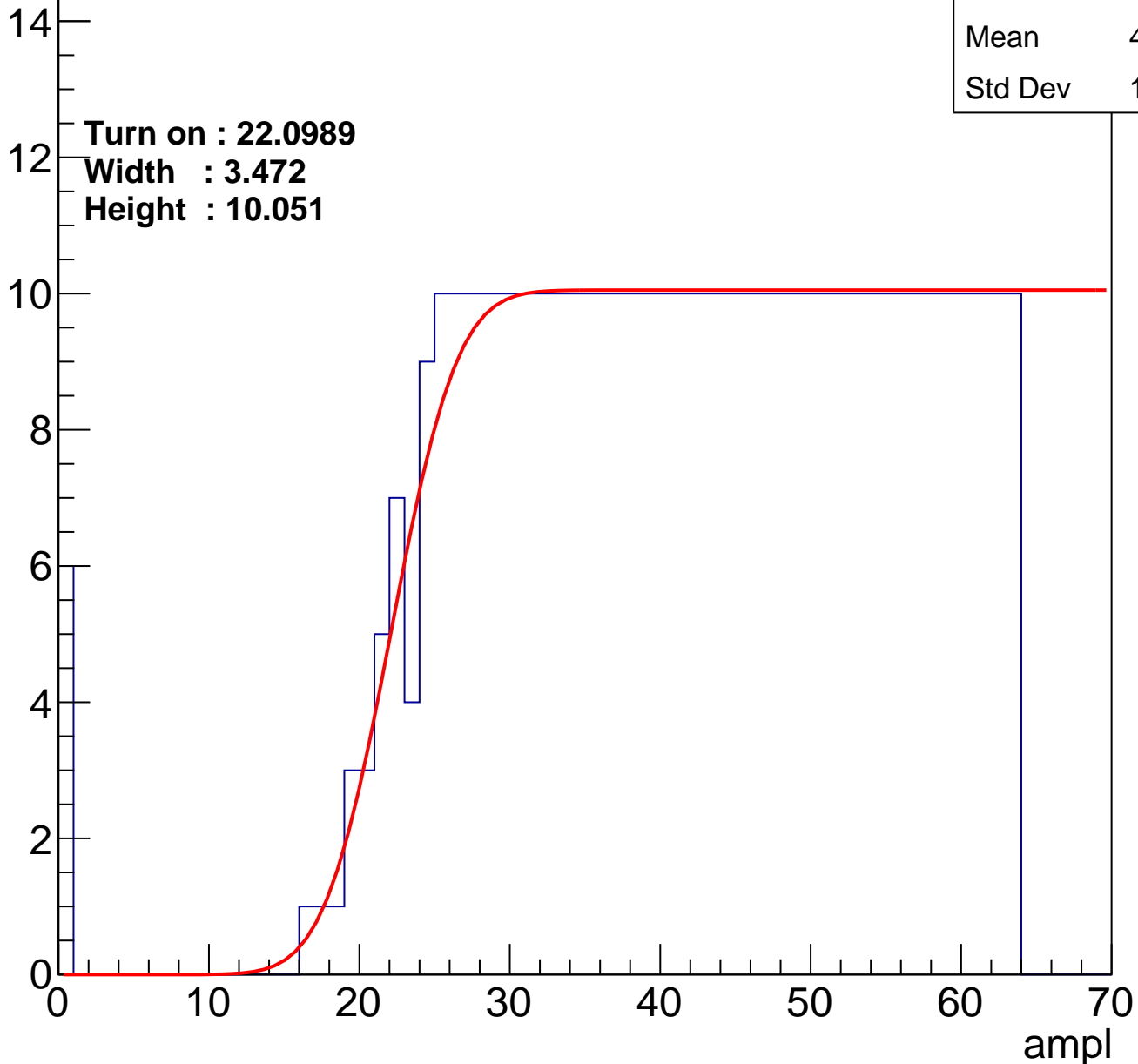
Entries	430
Mean	41.62
Std Dev	13.27

Turn on : 22.0989

Width : 3.472

Height : 10.051

Entry



B1L103S, U26-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.46
Std Dev	11.98

Turn on : 25.1566

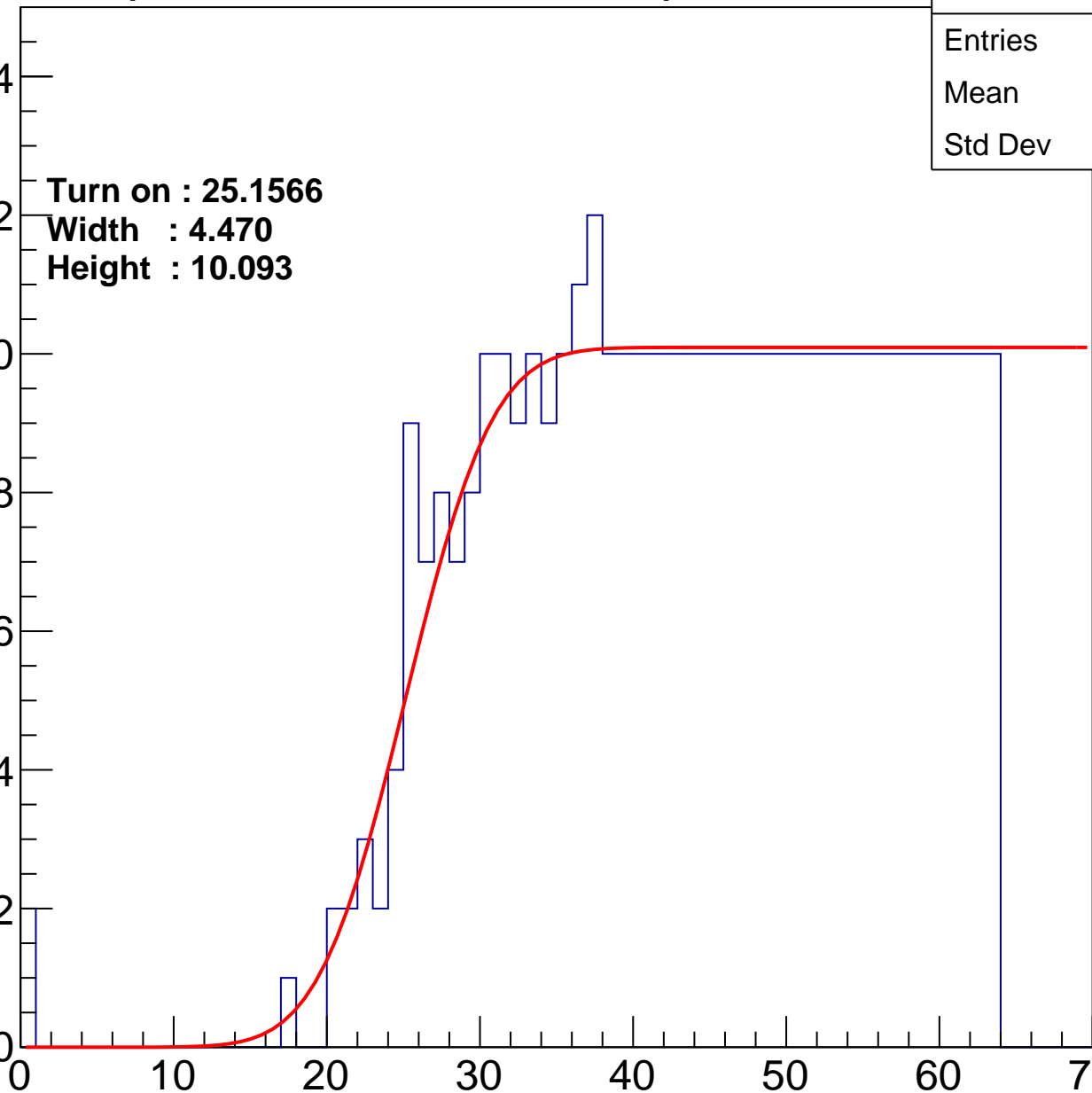
Width : 4.470

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U26-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.46
Std Dev	11.98

Turn on : 25.1566

Width : 4.470

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl

