

B1L103S, U25-ch0

calib_packv5_042523_0143.root, FC#7, port C2

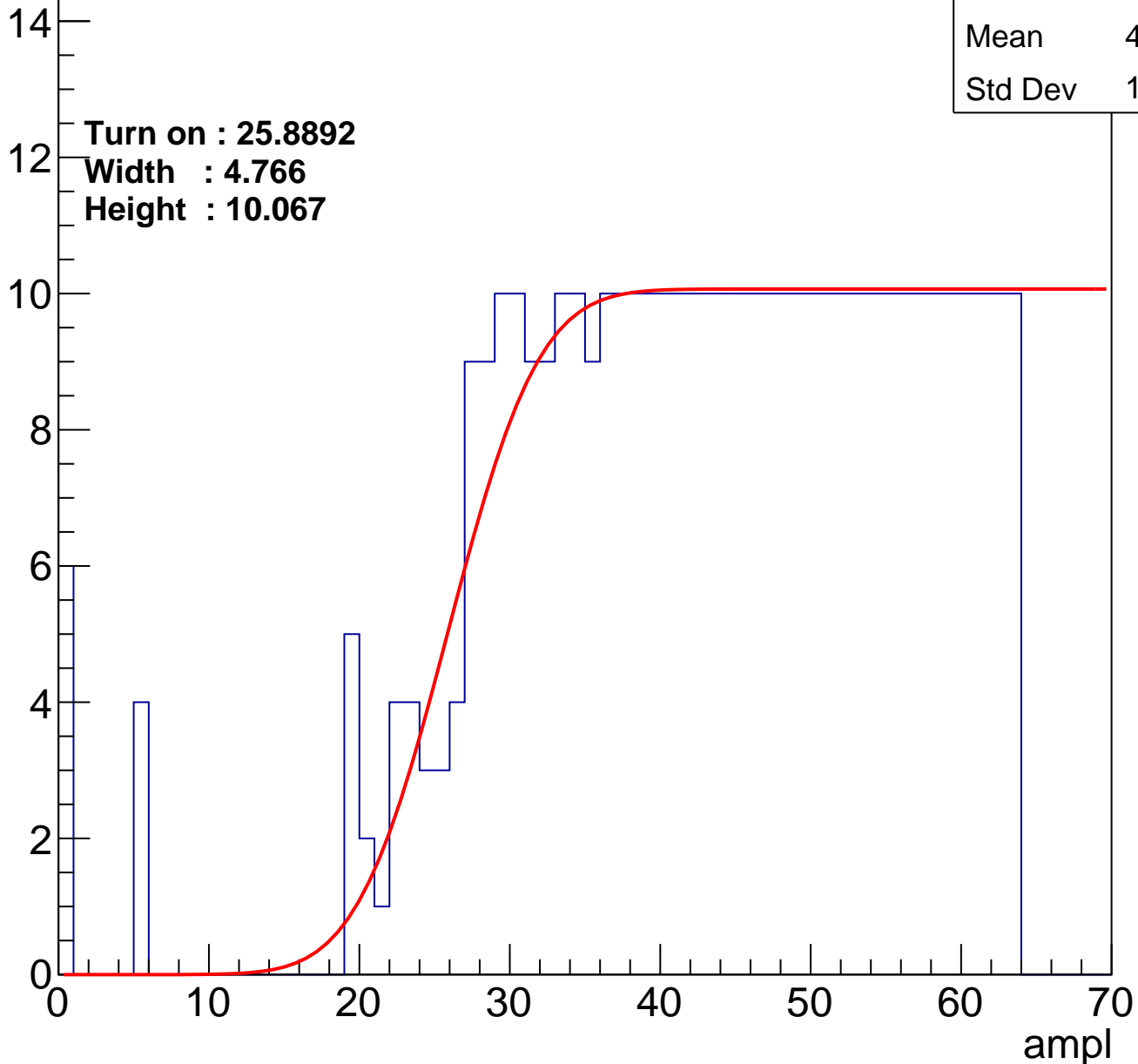
Entries	401
Mean	42.65
Std Dev	13.28

Turn on : 25.8892

Width : 4.766

Height : 10.067

Entry



B1L103S, U25-ch1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	349
Mean	45.74
Std Dev	10.82

Turn on : 29.9342

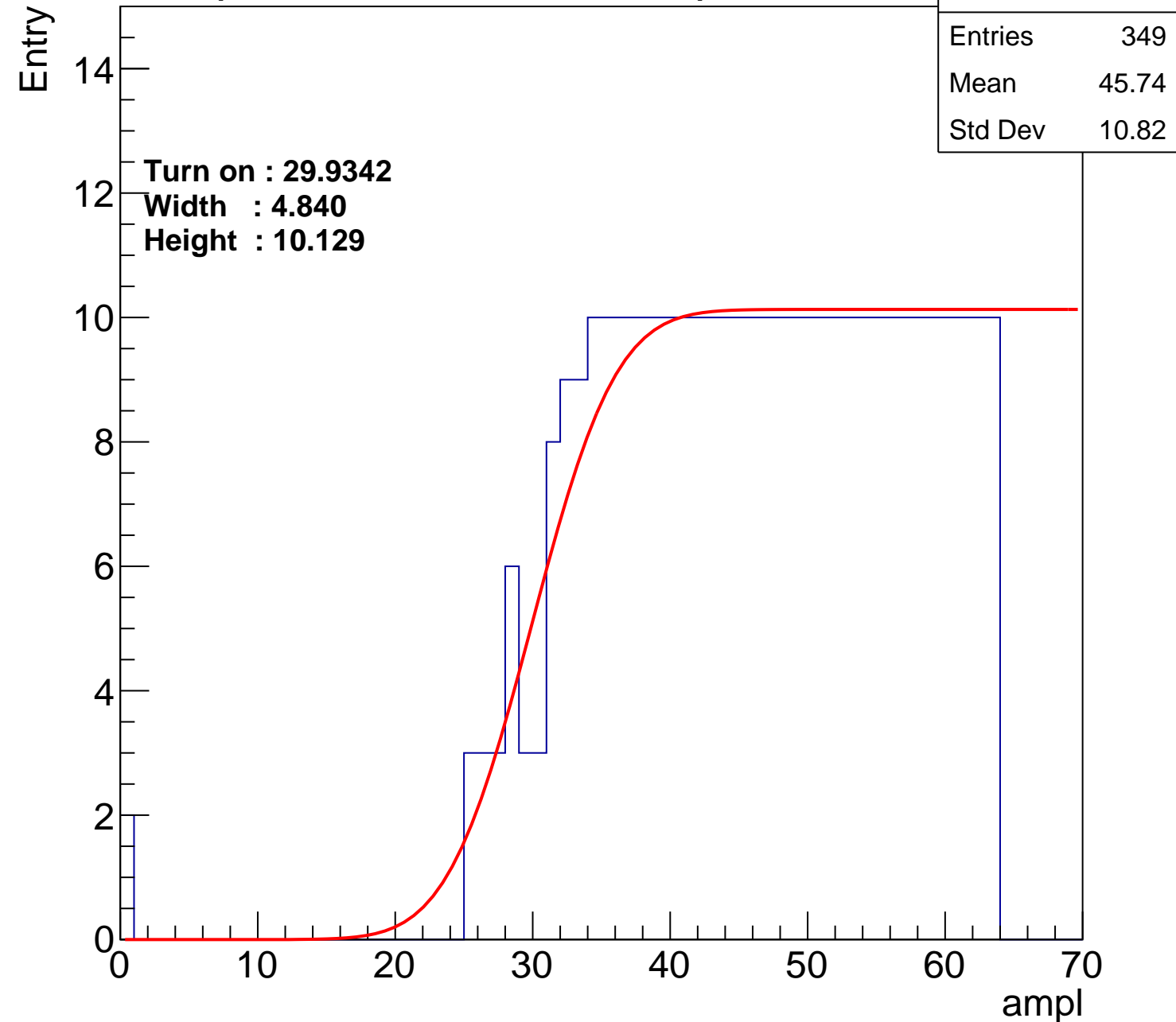
Width : 4.840

Height : 10.129

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.89
Std Dev	11.25

Turn on : 28.5631

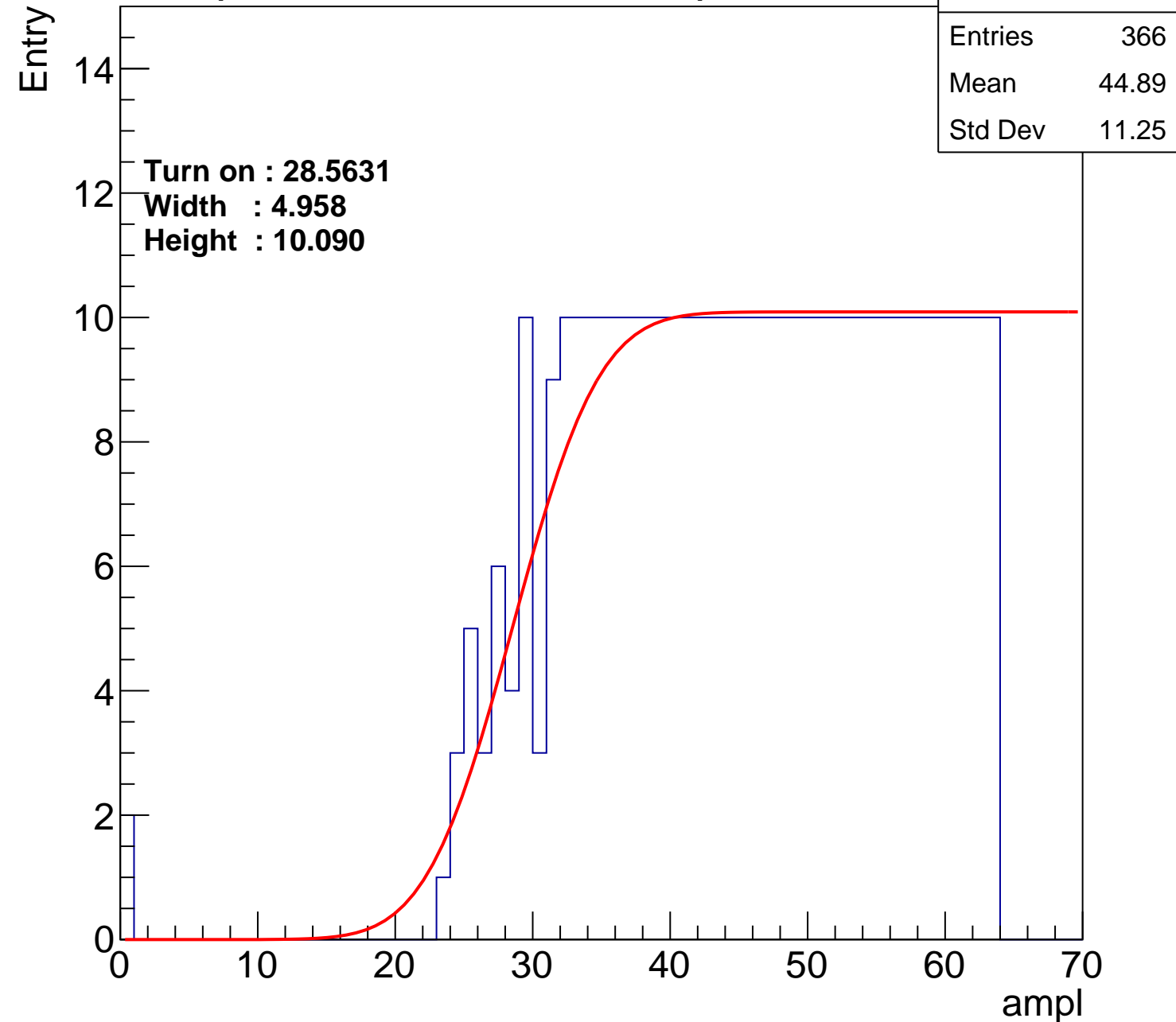
Width : 4.958

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.84
Std Dev	11.23

Turn on : 27.6871

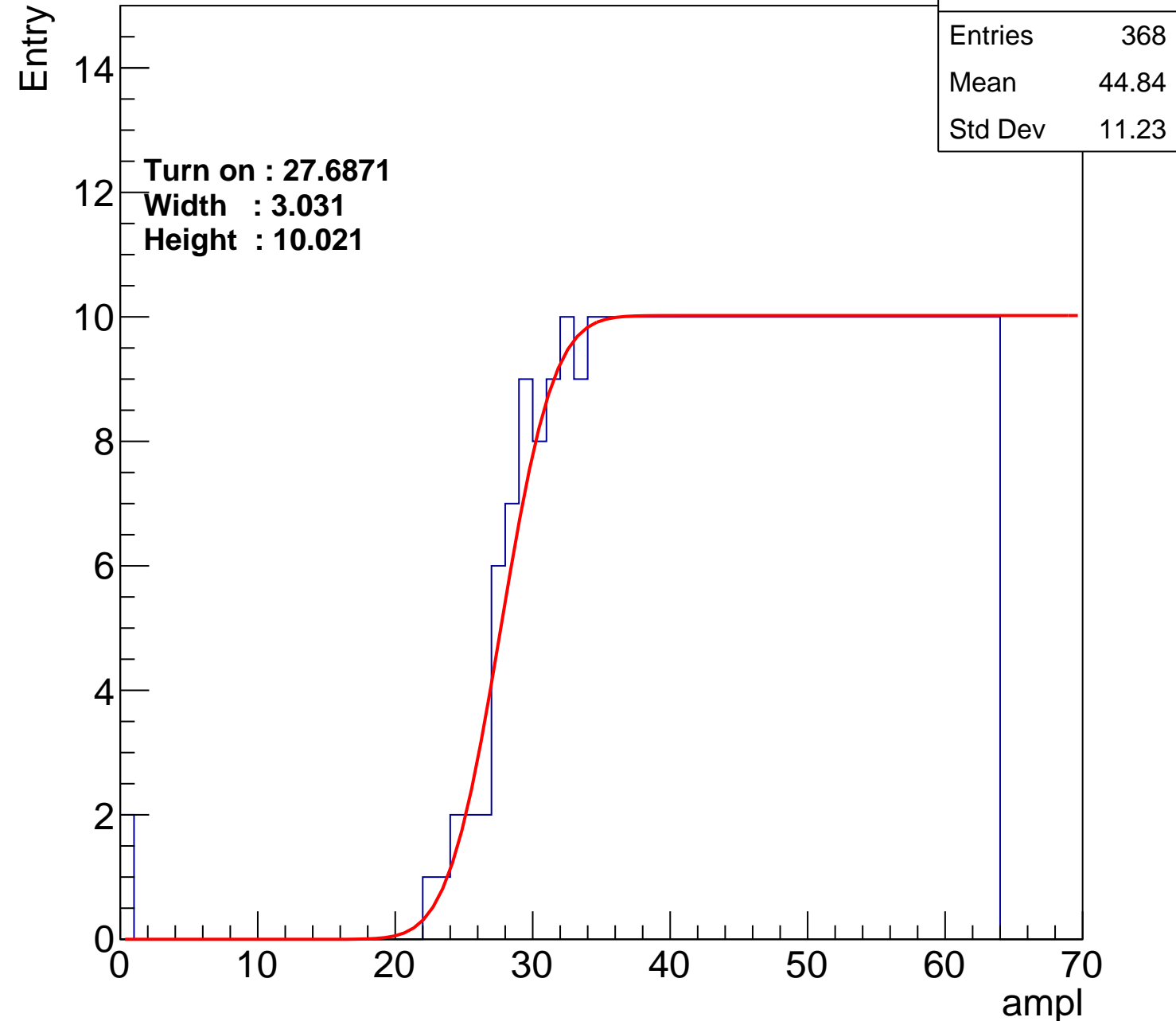
Width : 3.031

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.6105

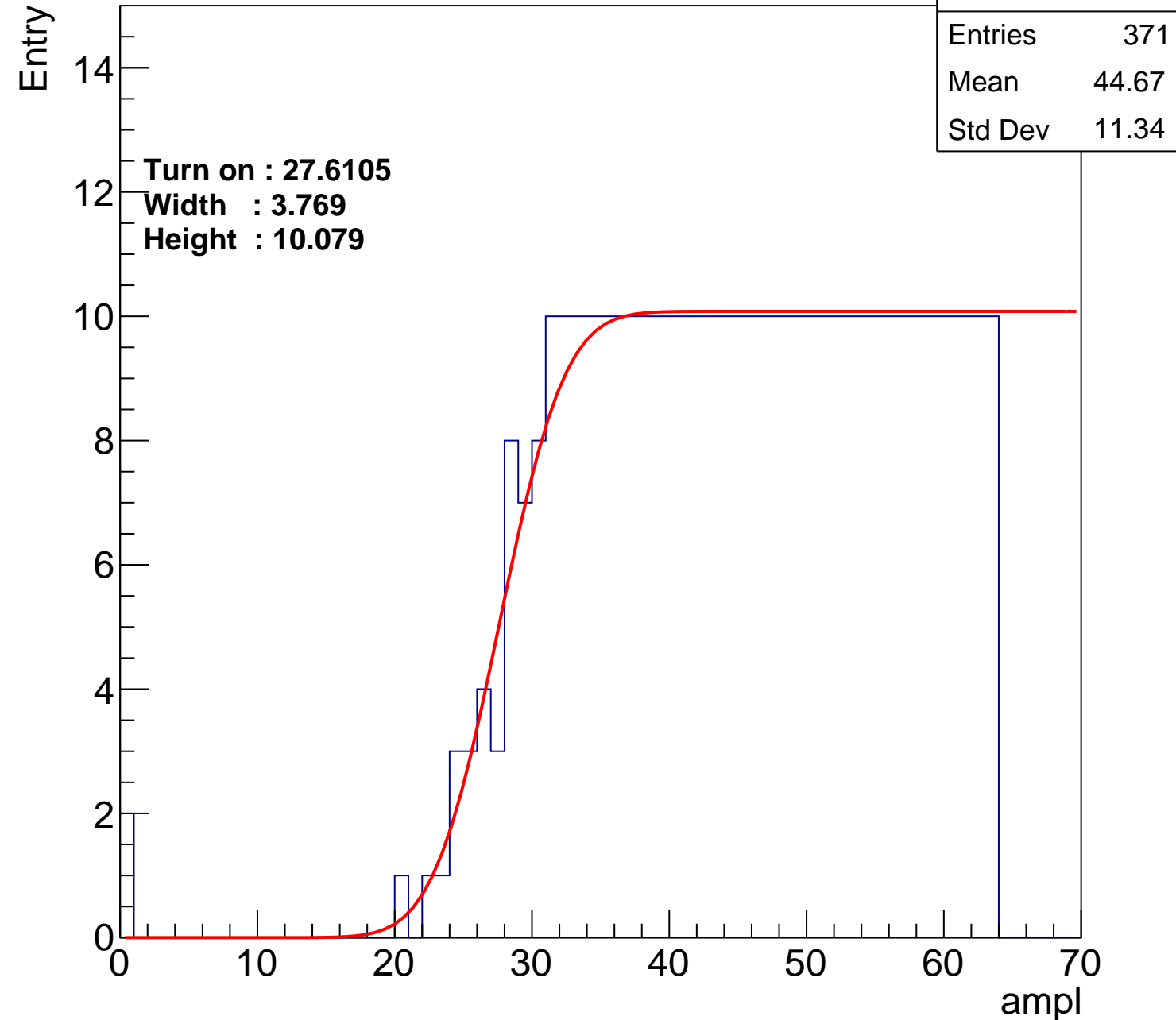
Width : 3.769

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.74
Std Dev	11.09

Turn on : 27.1682

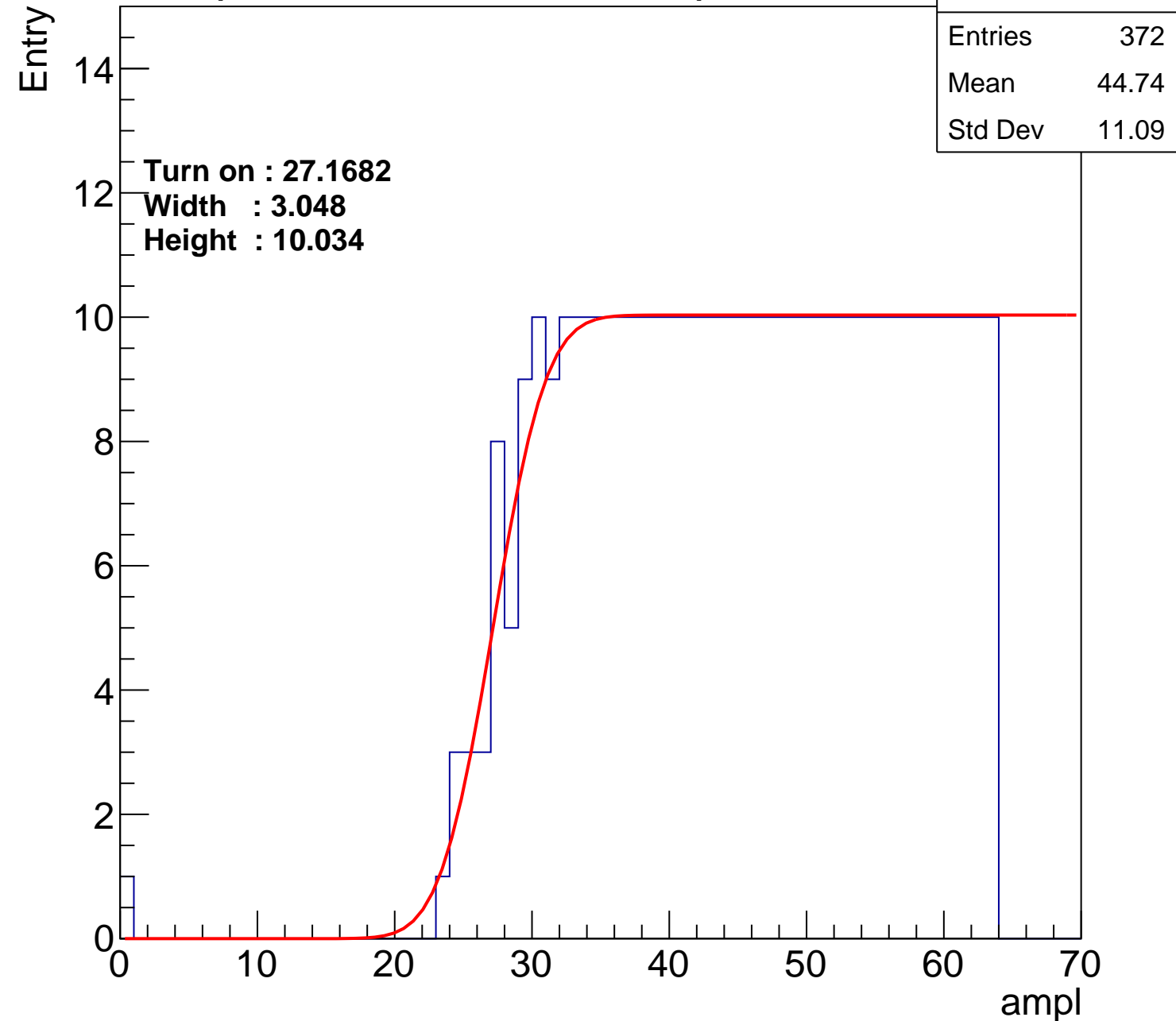
Width : 3.048

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.69
Std Dev	11.11

Turn on : 27.5078

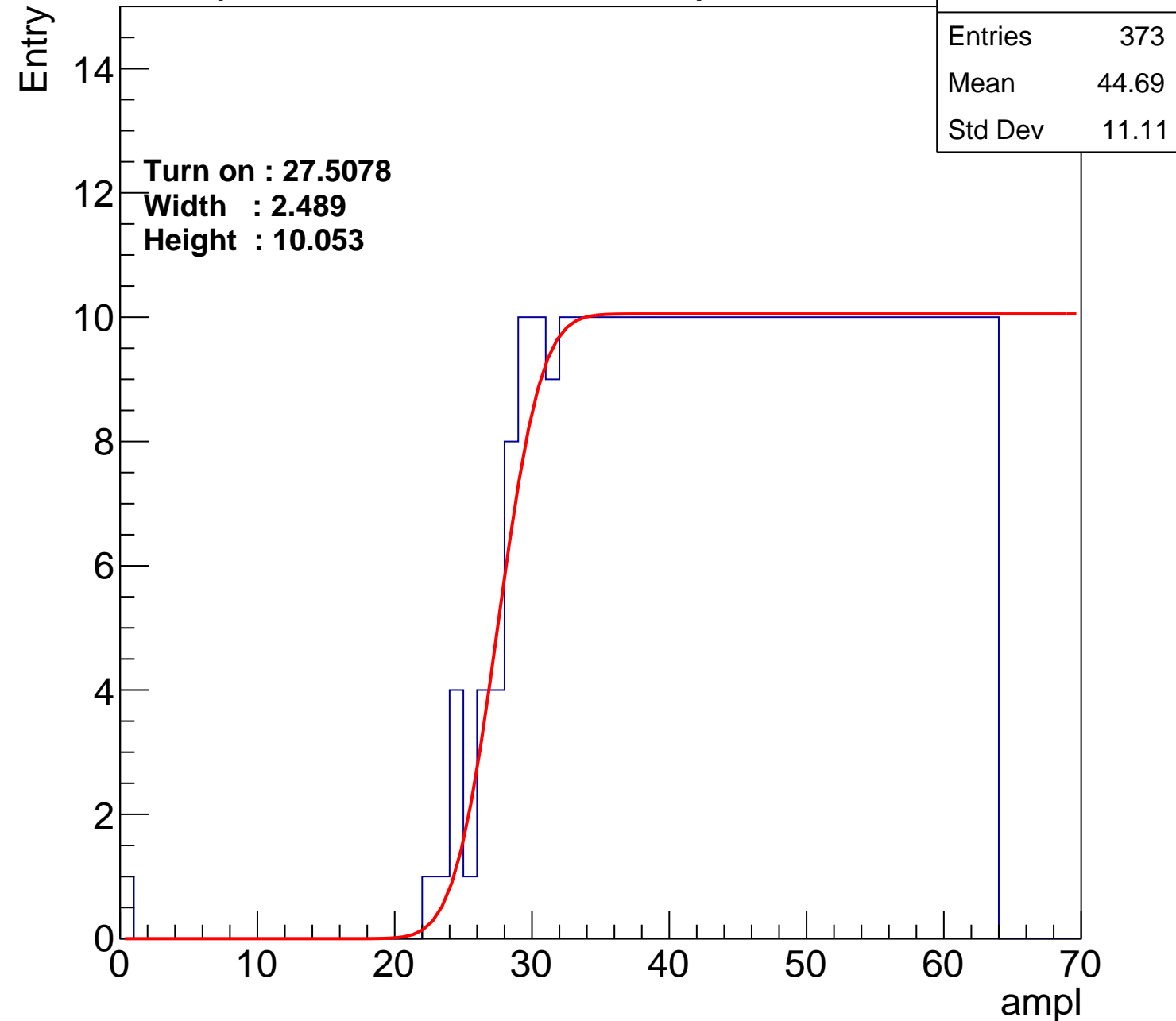
Width : 2.489

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch7

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.98
Std Dev	11.11

Turn on : 27.7089

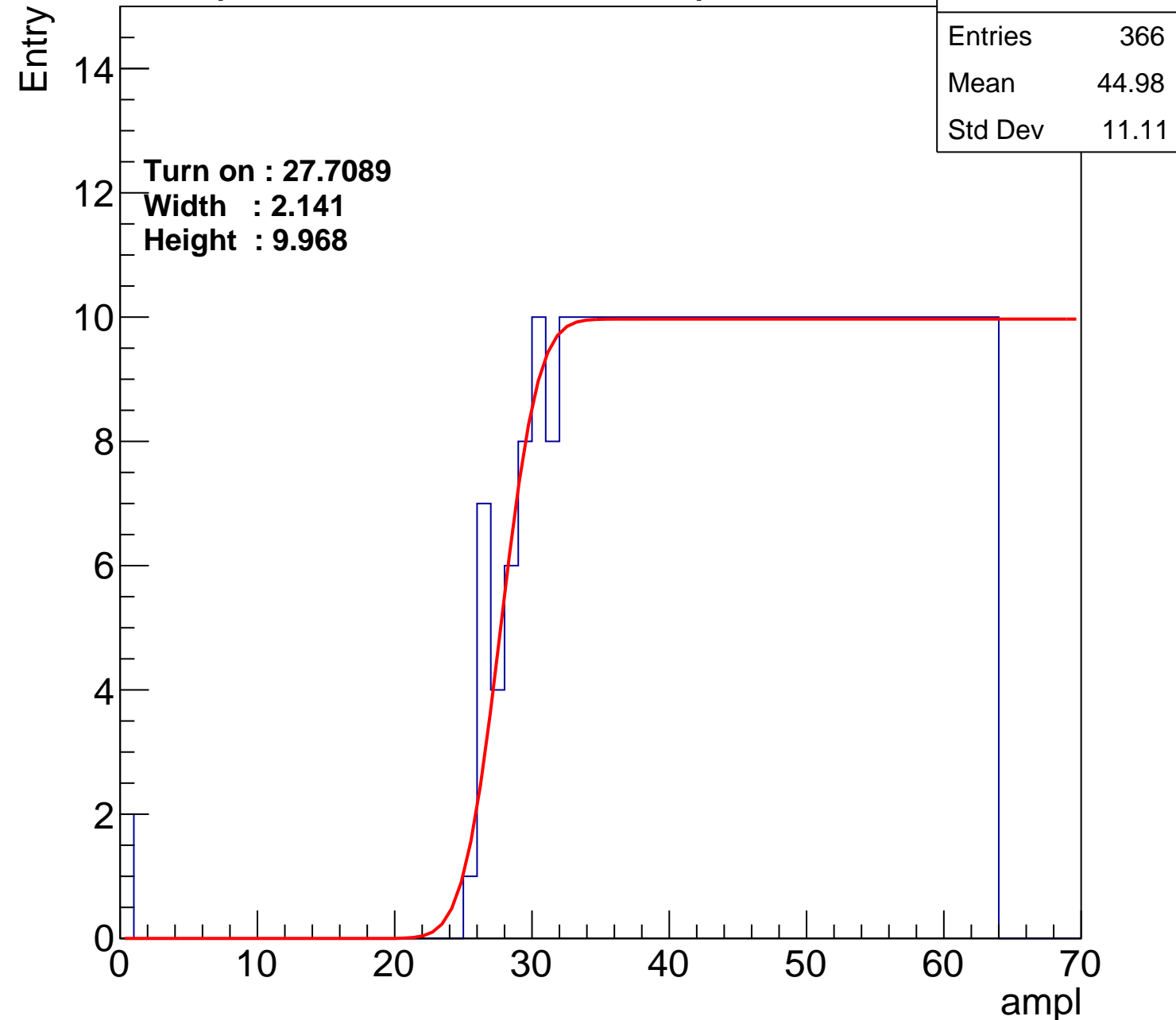
Width : 2.141

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch8

calib_packv5_042523_0143.root, FC#7, port C2

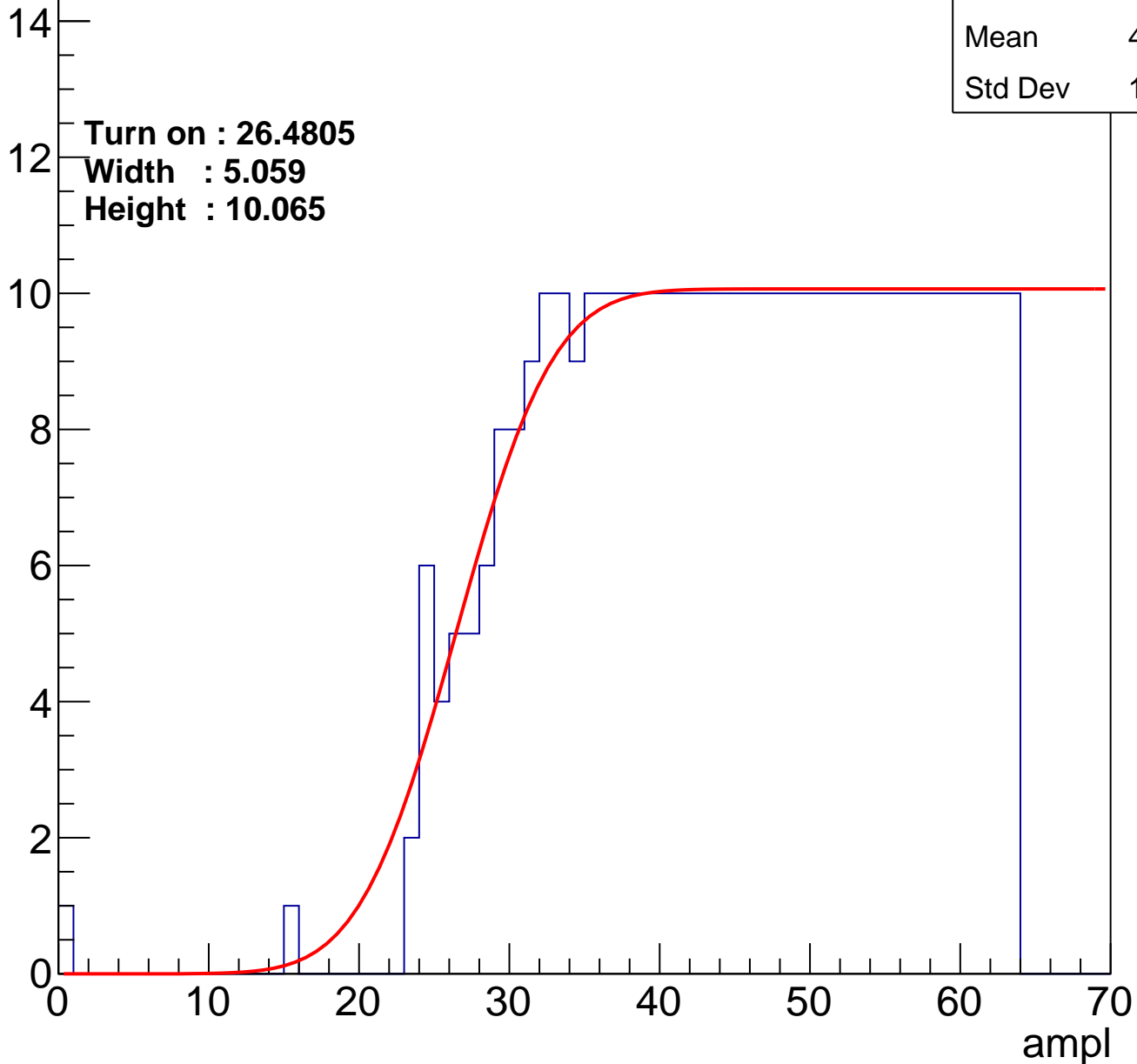
Entries	374
Mean	44.53
Std Dev	11.33

Turn on : 26.4805

Width : 5.059

Height : 10.065

Entry



B1L103S, U25-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	354
Mean	45.39
Std Dev	11.29

Turn on : 28.9275

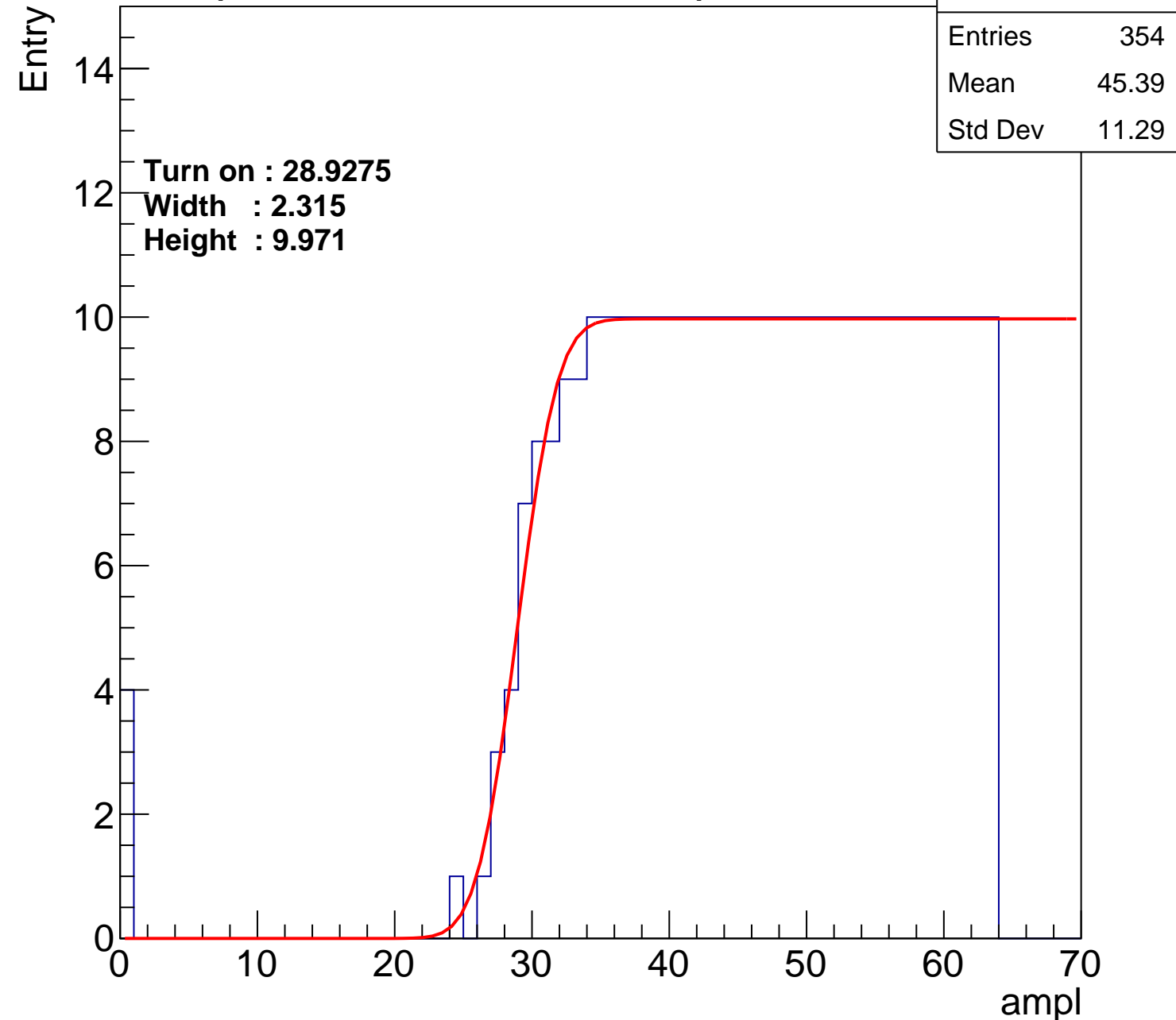
Width : 2.315

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch10

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.48
Std Dev	11.62

Turn on : 27.8061

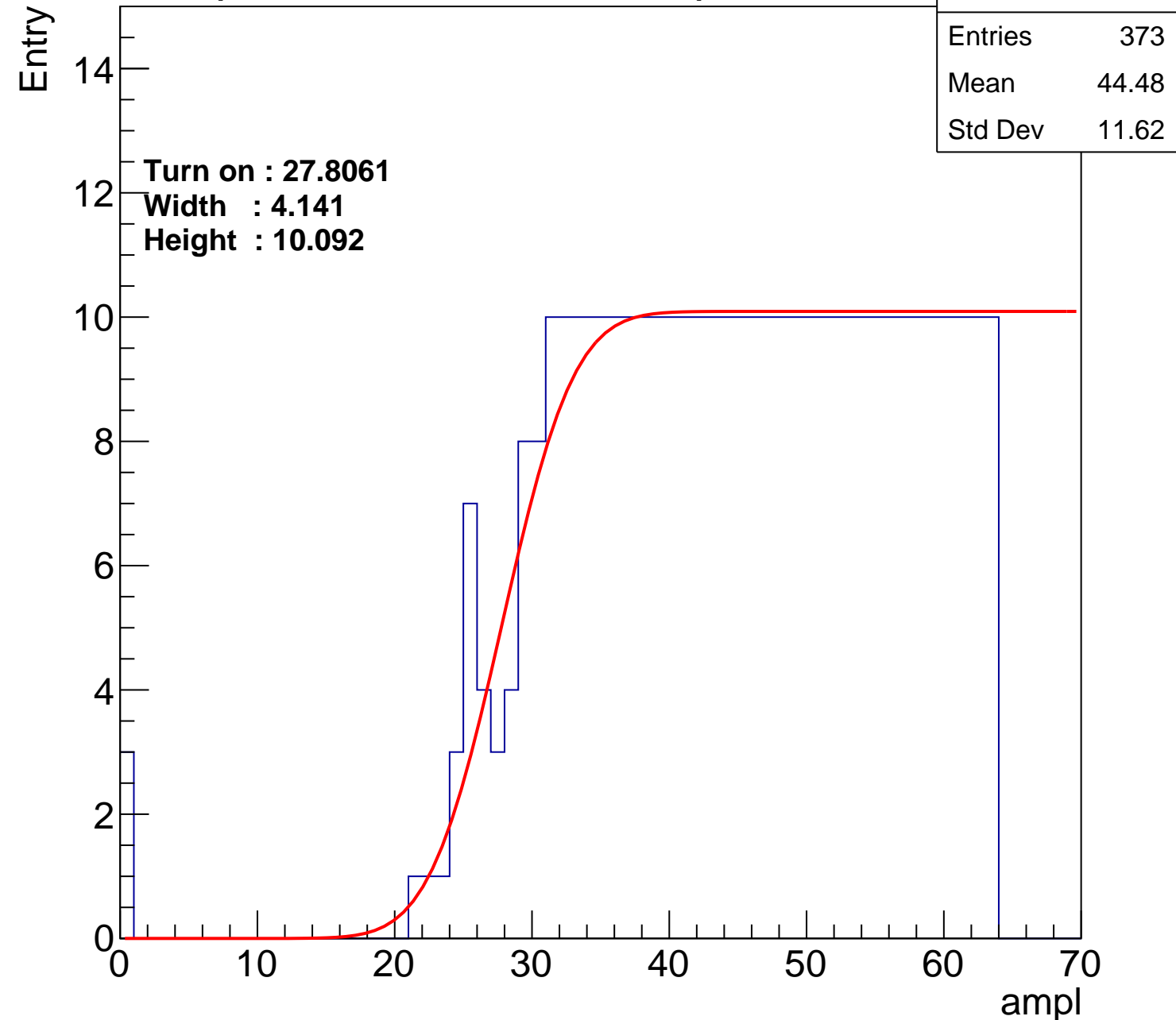
Width : 4.141

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch11

calib_packv5_042523_0143.root, FC#7, port C2

Entries	352
Mean	45.65
Std Dev	10.69

Turn on : 29.1535

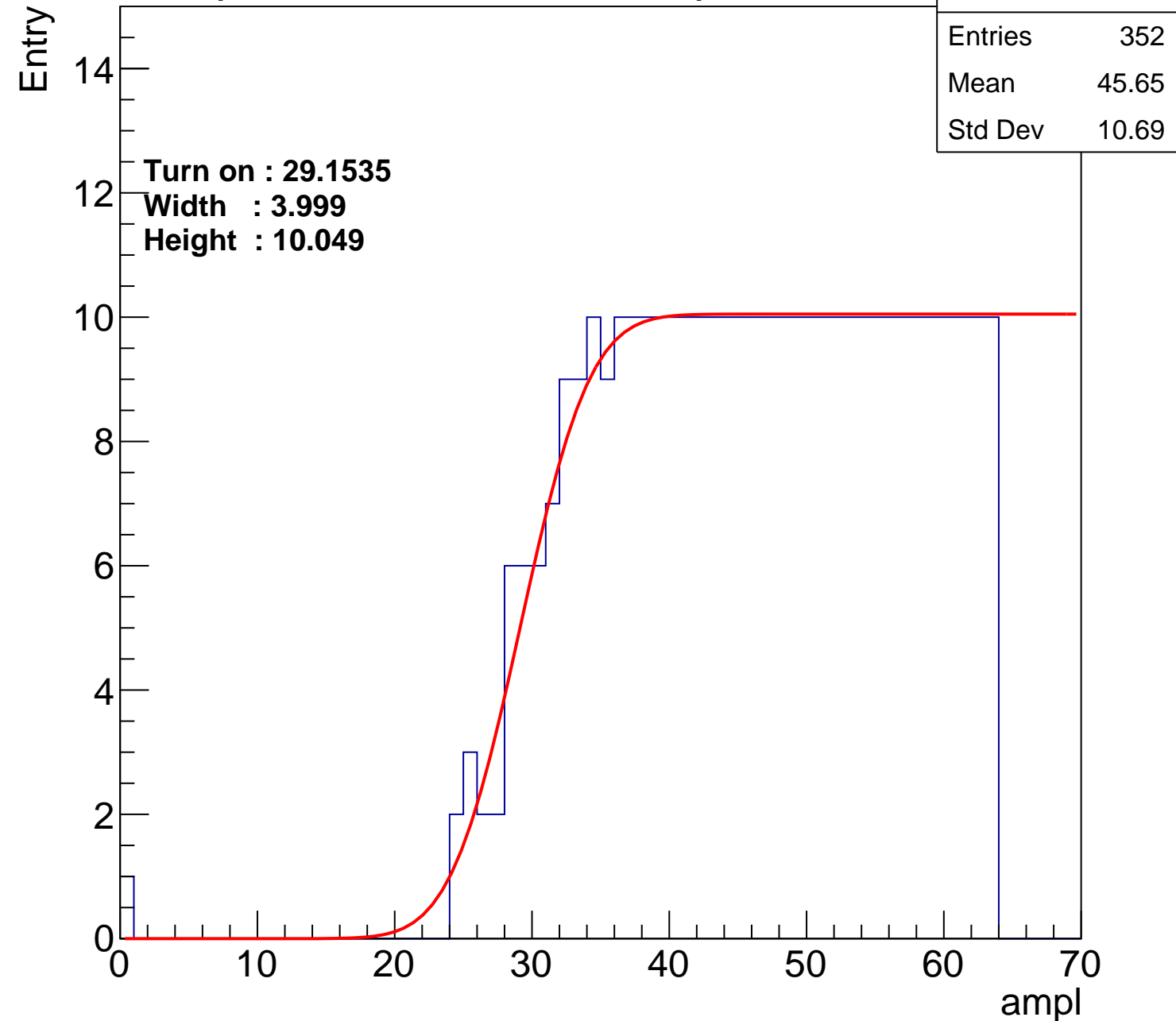
Width : 3.999

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch12

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 25.8715

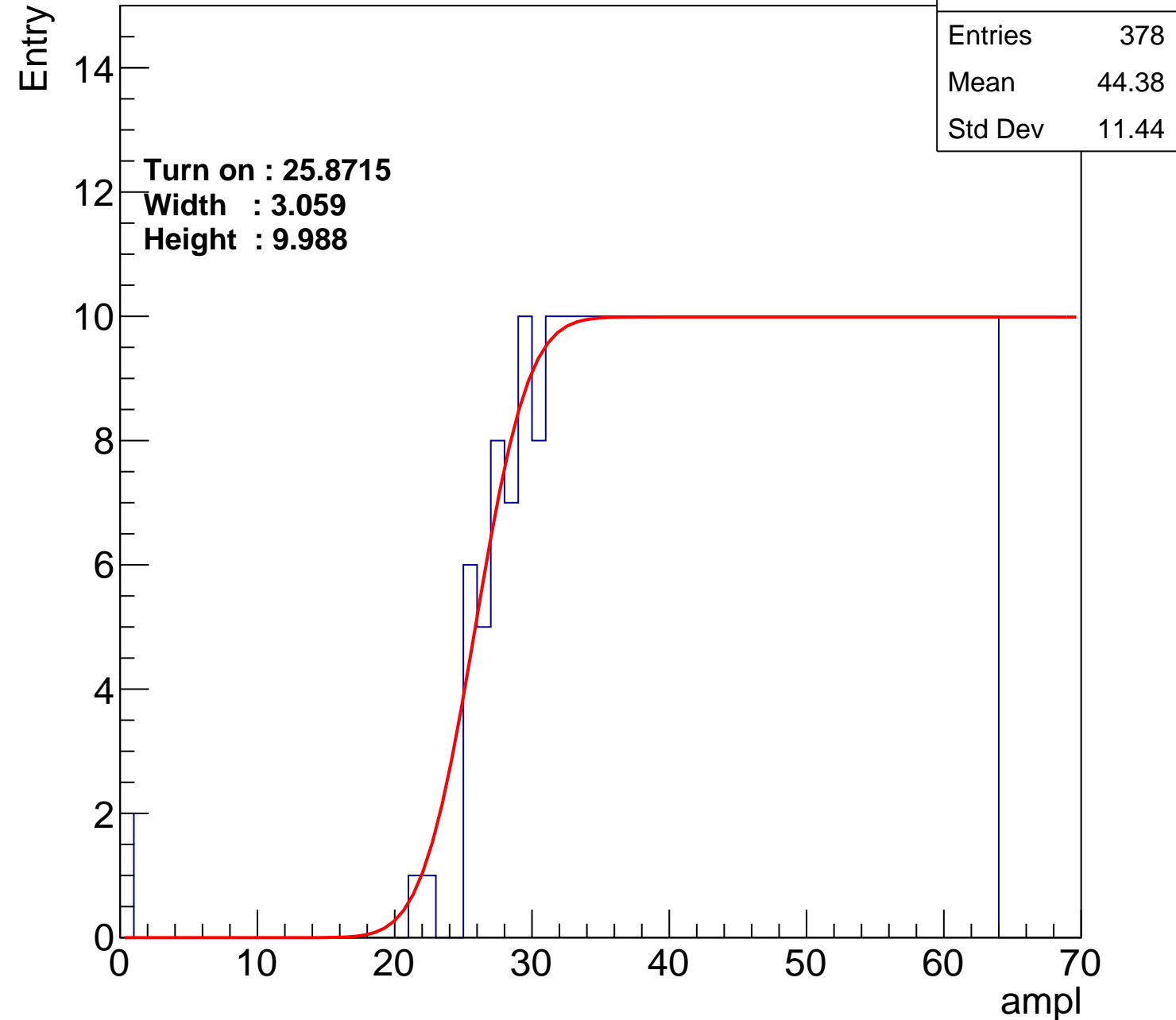
Width : 3.059

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch13

calib_packv5_042523_0143.root, FC#7, port C2

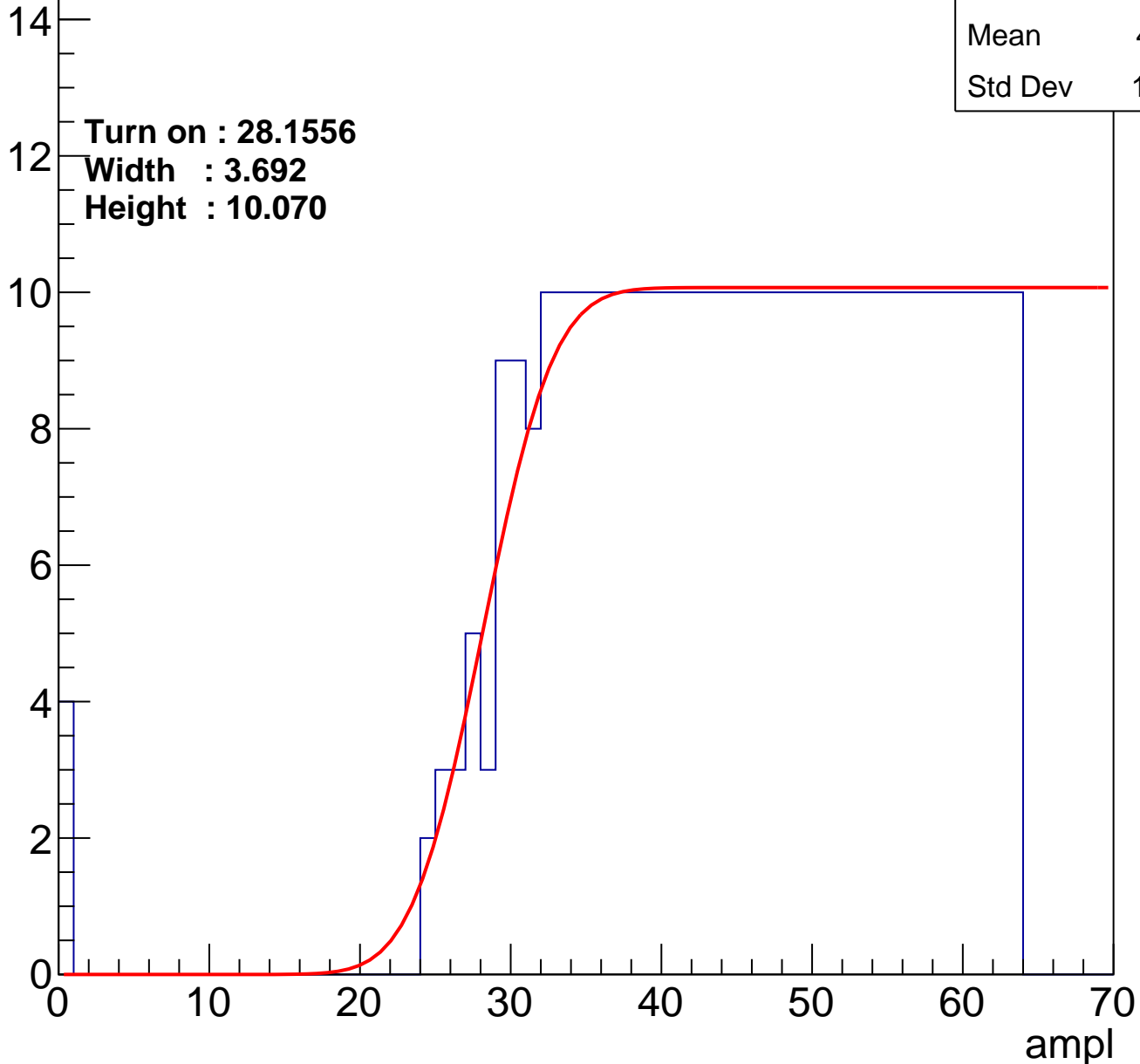
Entries	366
Mean	44.81
Std Dev	11.56

Turn on : 28.1556

Width : 3.692

Height : 10.070

Entry



B1L103S, U25-ch14

calib_packv5_042523_0143.root, FC#7, port C2

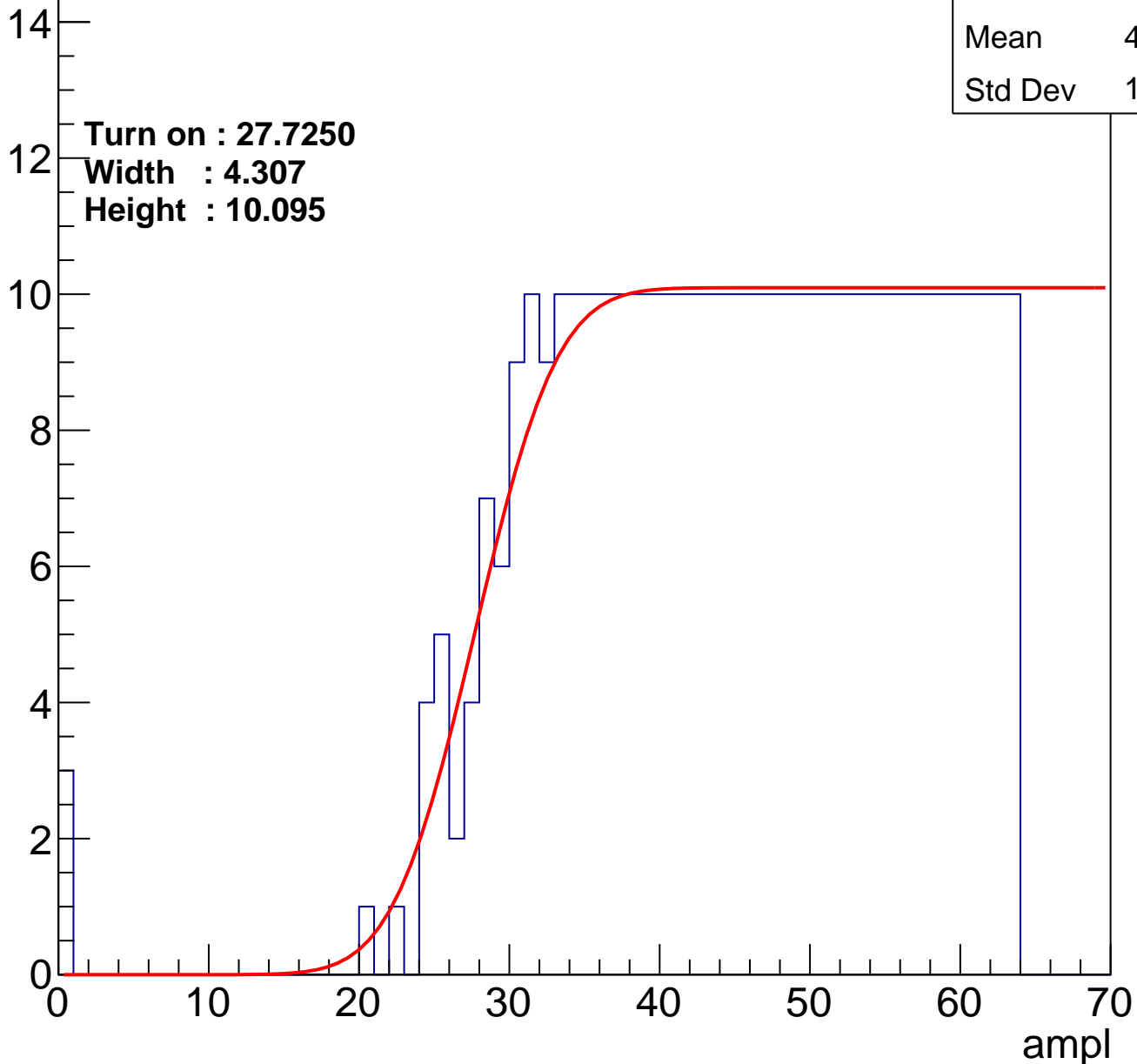
Entries	371
Mean	44.58
Std Dev	11.56

Turn on : 27.7250

Width : 4.307

Height : 10.095

Entry



B1L103S, U25-ch15

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.21
Std Dev	11.01

Turn on : 28.3335

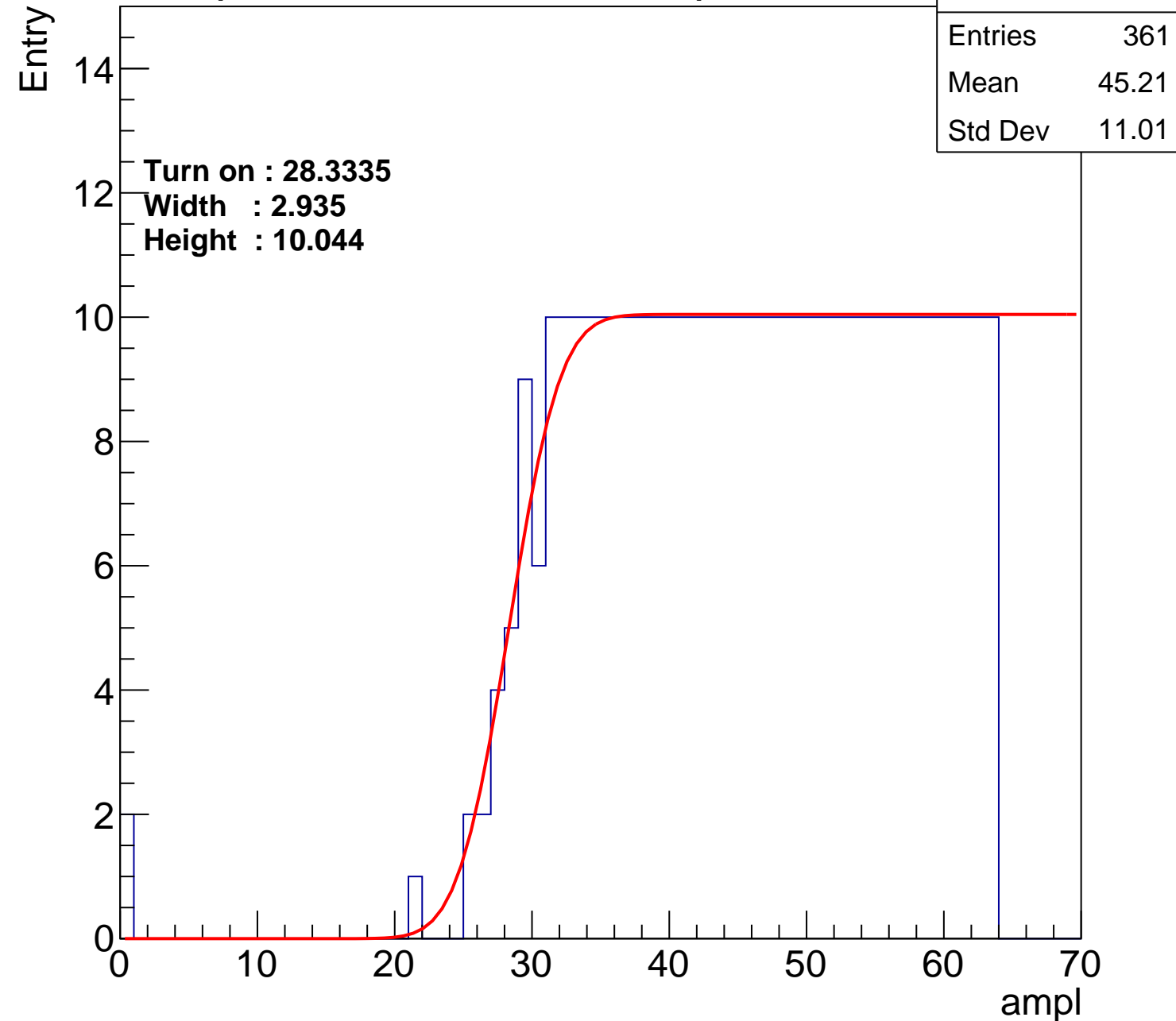
Width : 2.935

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch16

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.27
Std Dev	11.02

Turn on : 29.3396

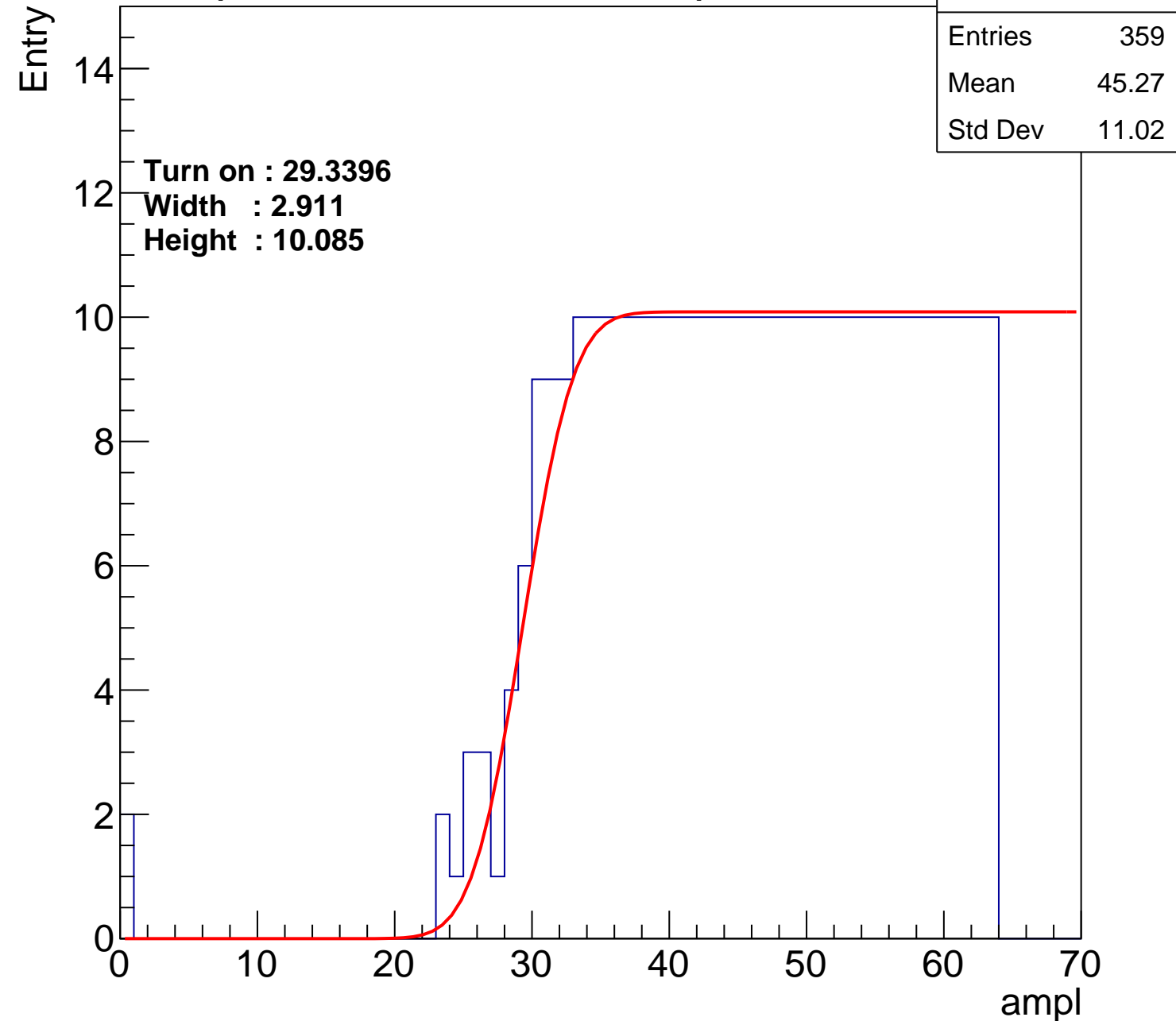
Width : 2.911

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch17

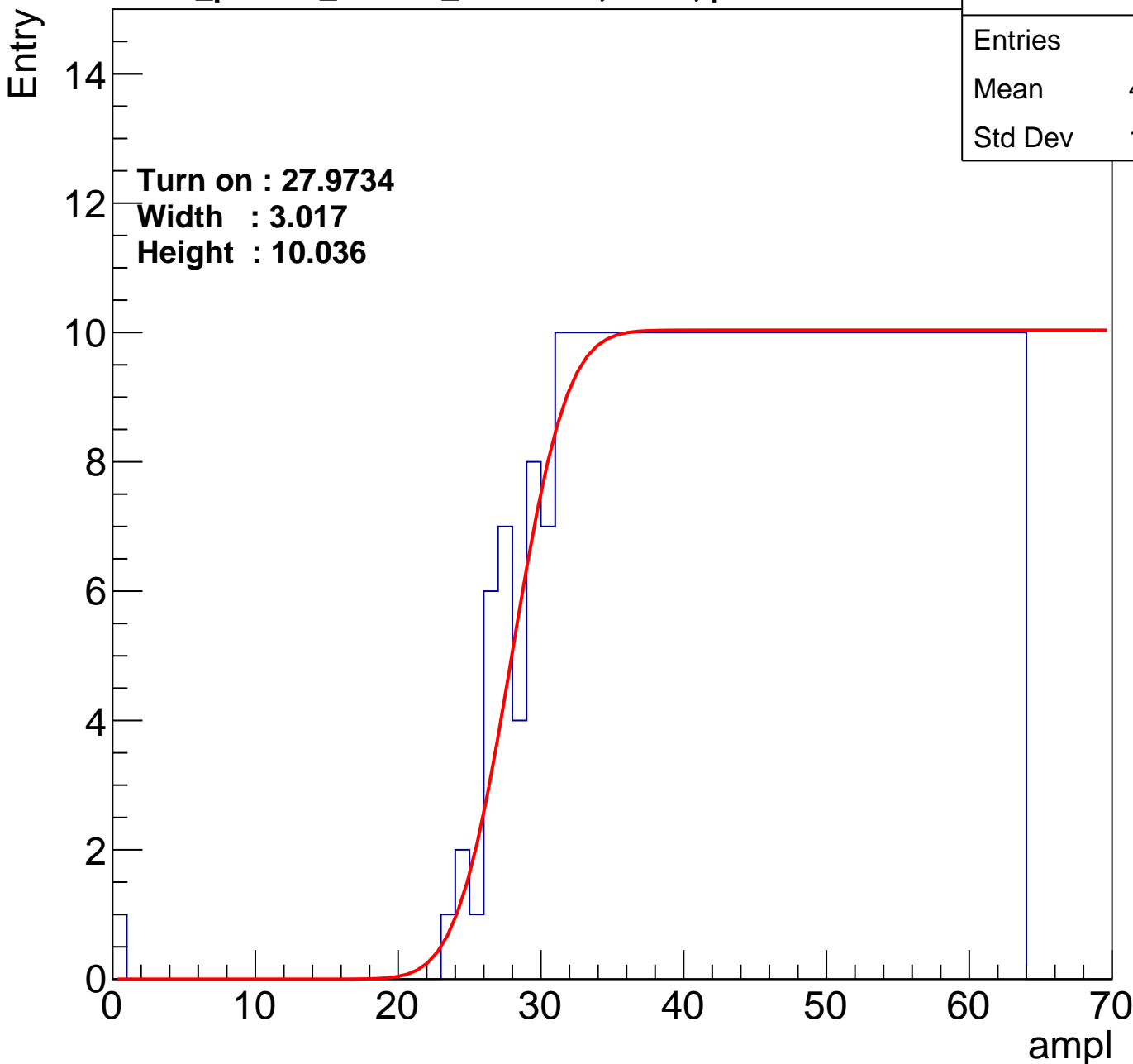
calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.97
Std Dev	10.98

Turn on : 27.9734

Width : 3.017

Height : 10.036



B1L103S, U25-ch18

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.36
Std Dev	10.93

Turn on : 28.7958

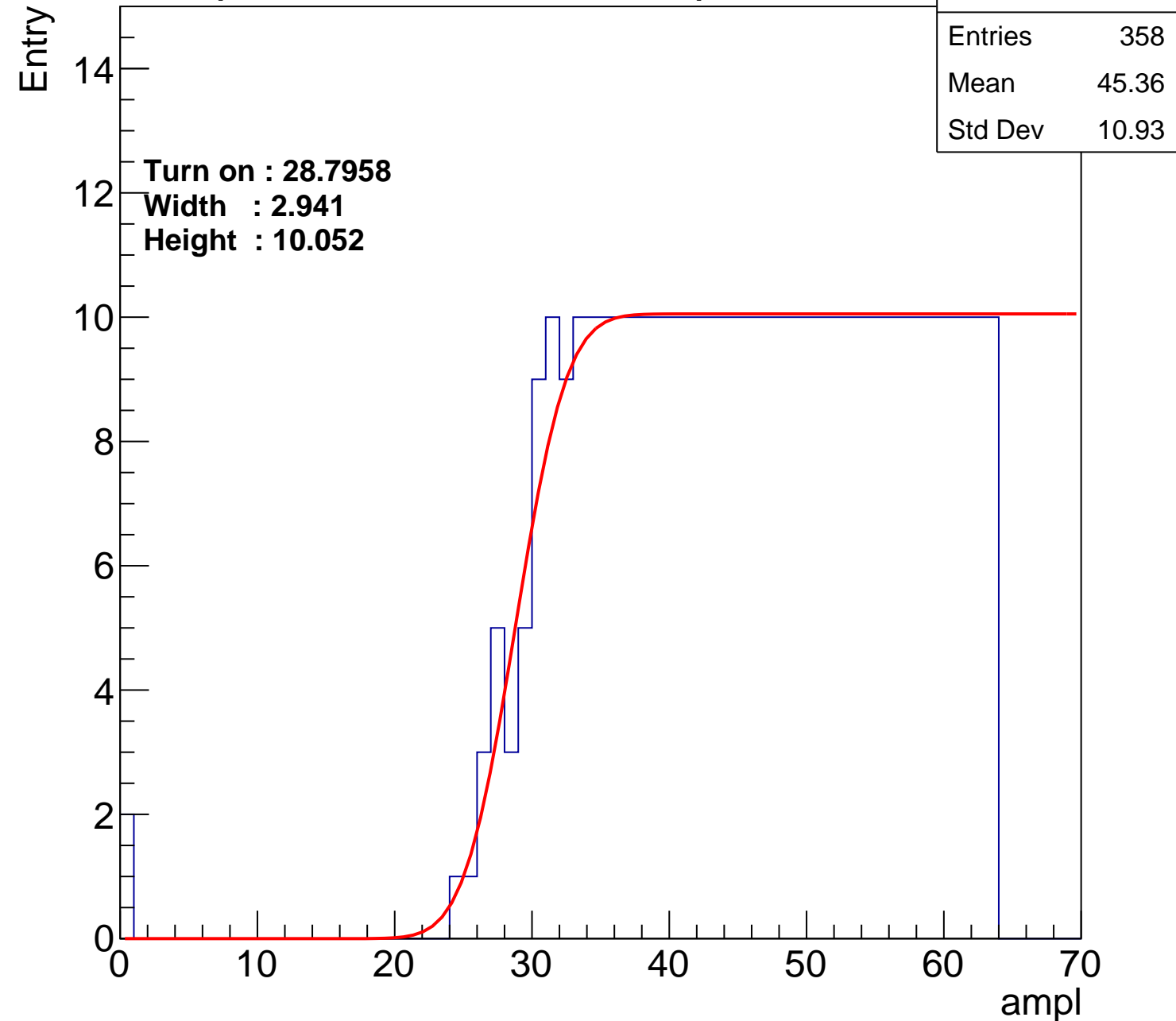
Width : 2.941

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch19

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.84
Std Dev	11.08

Turn on : 27.0191

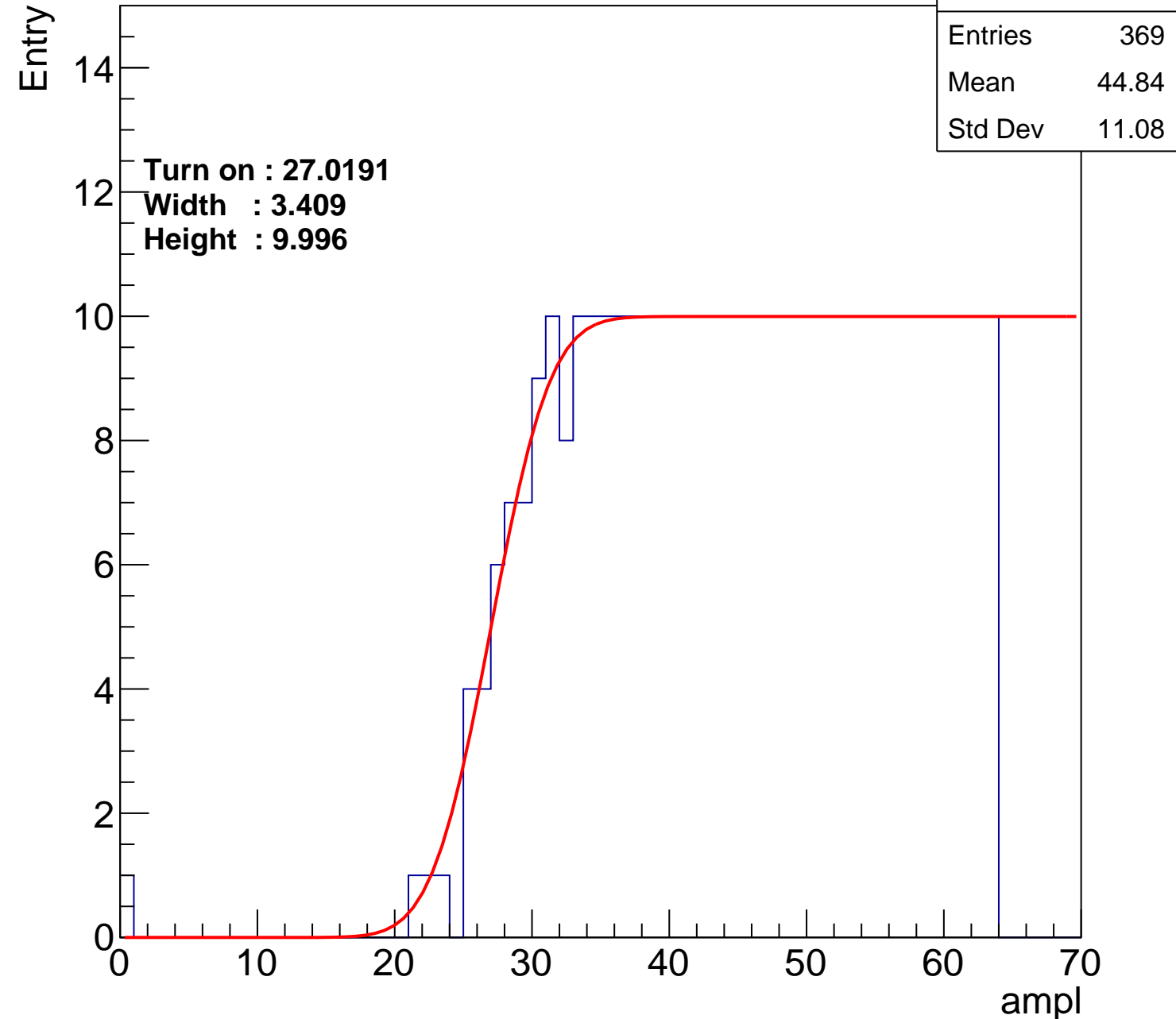
Width : 3.409

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch20

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.31
Std Dev	11.85

Turn on : 27.6544

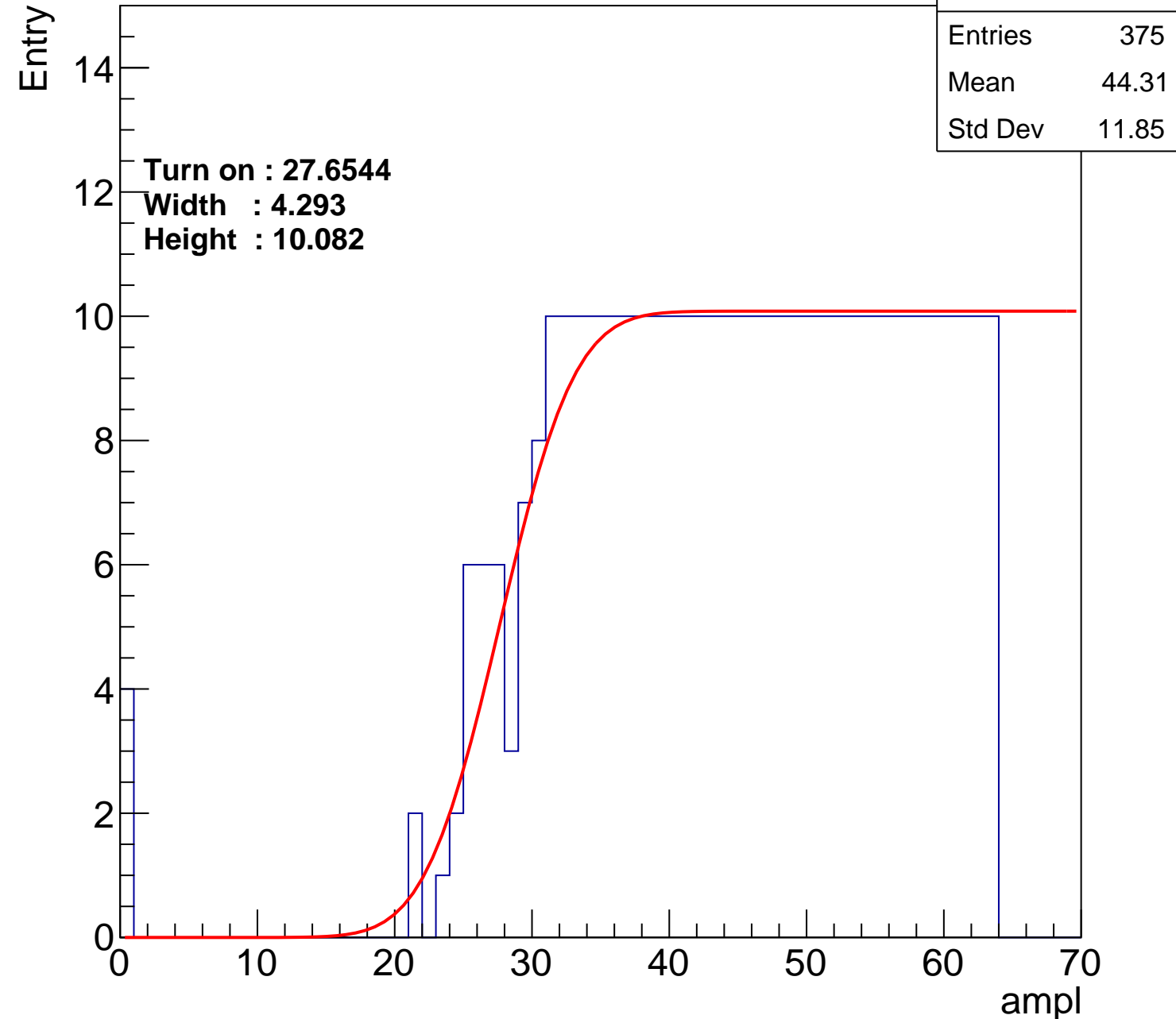
Width : 4.293

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch21

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.98
Std Dev	11.18

Turn on : 27.9833

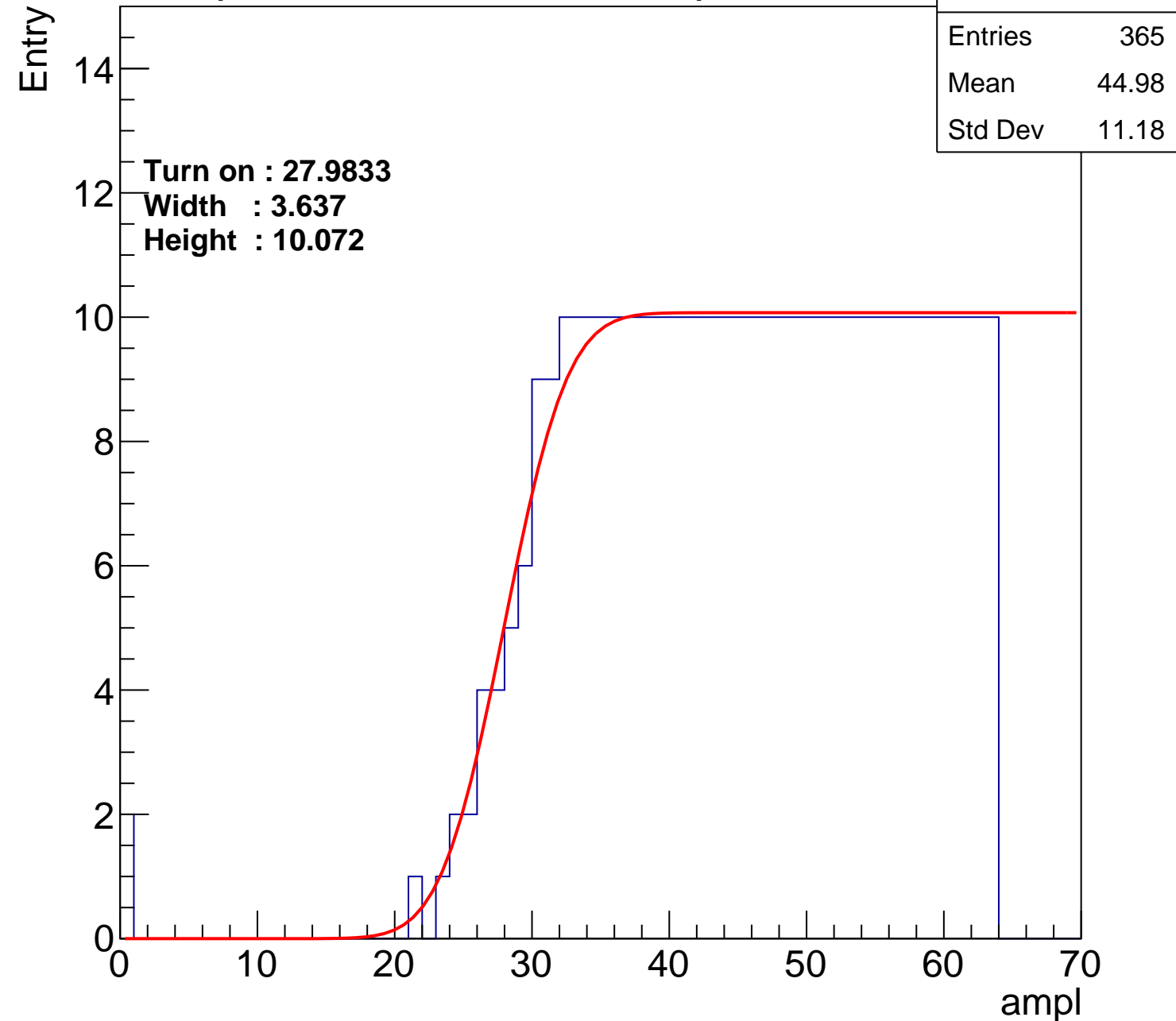
Width : 3.637

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch22

calib_packv5_042523_0143.root, FC#7, port C2

Entries	401
Mean	43.08
Std Dev	12.4

Turn on : 24.5757

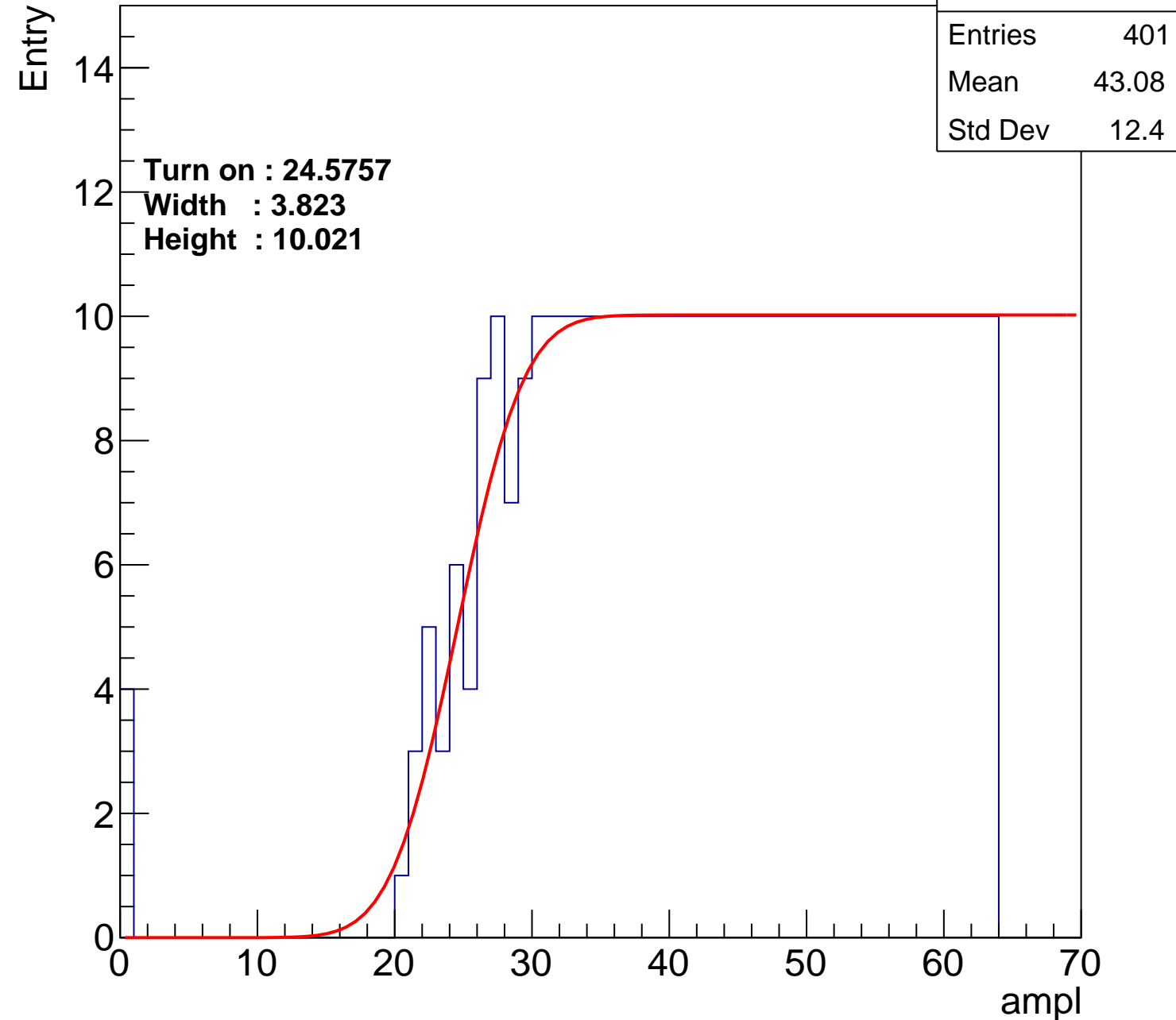
Width : 3.823

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch23

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.35
Std Dev	11.3

Turn on : 28.7425

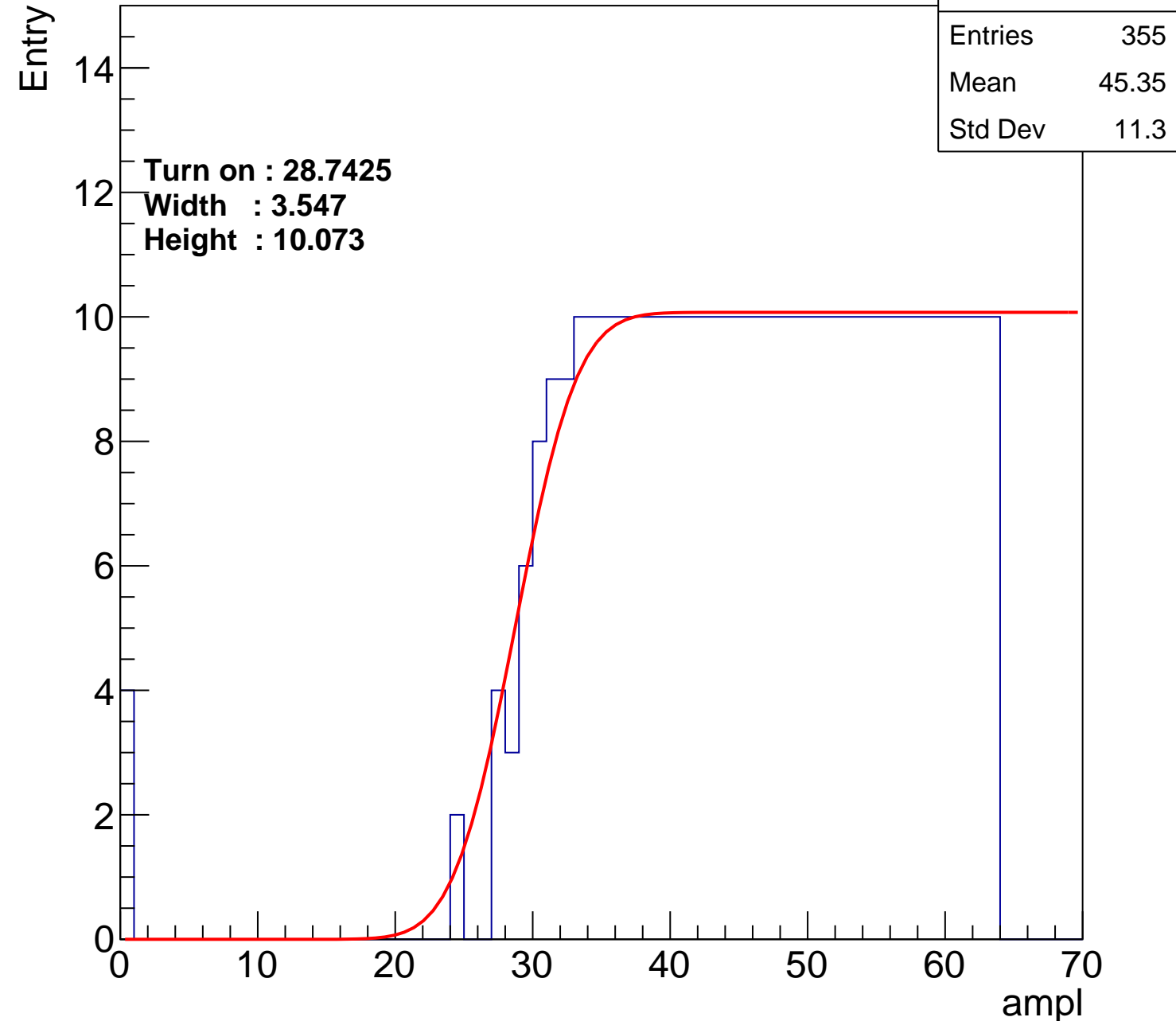
Width : 3.547

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch24

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	44.98
Std Dev	11.3

Turn on : 27.4896

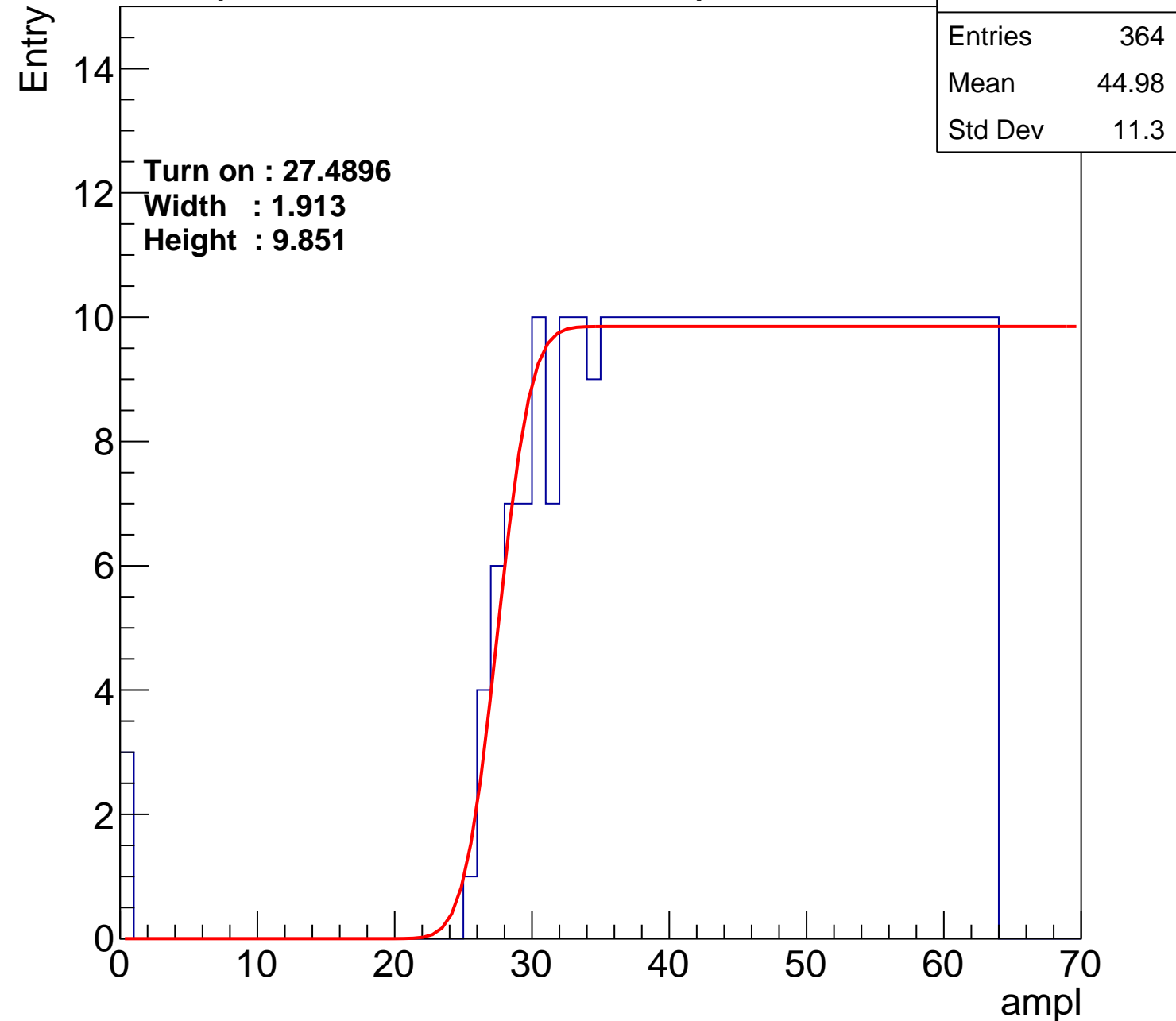
Width : 1.913

Height : 9.851

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch25

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.58
Std Dev	11.33

Turn on : 26.7295

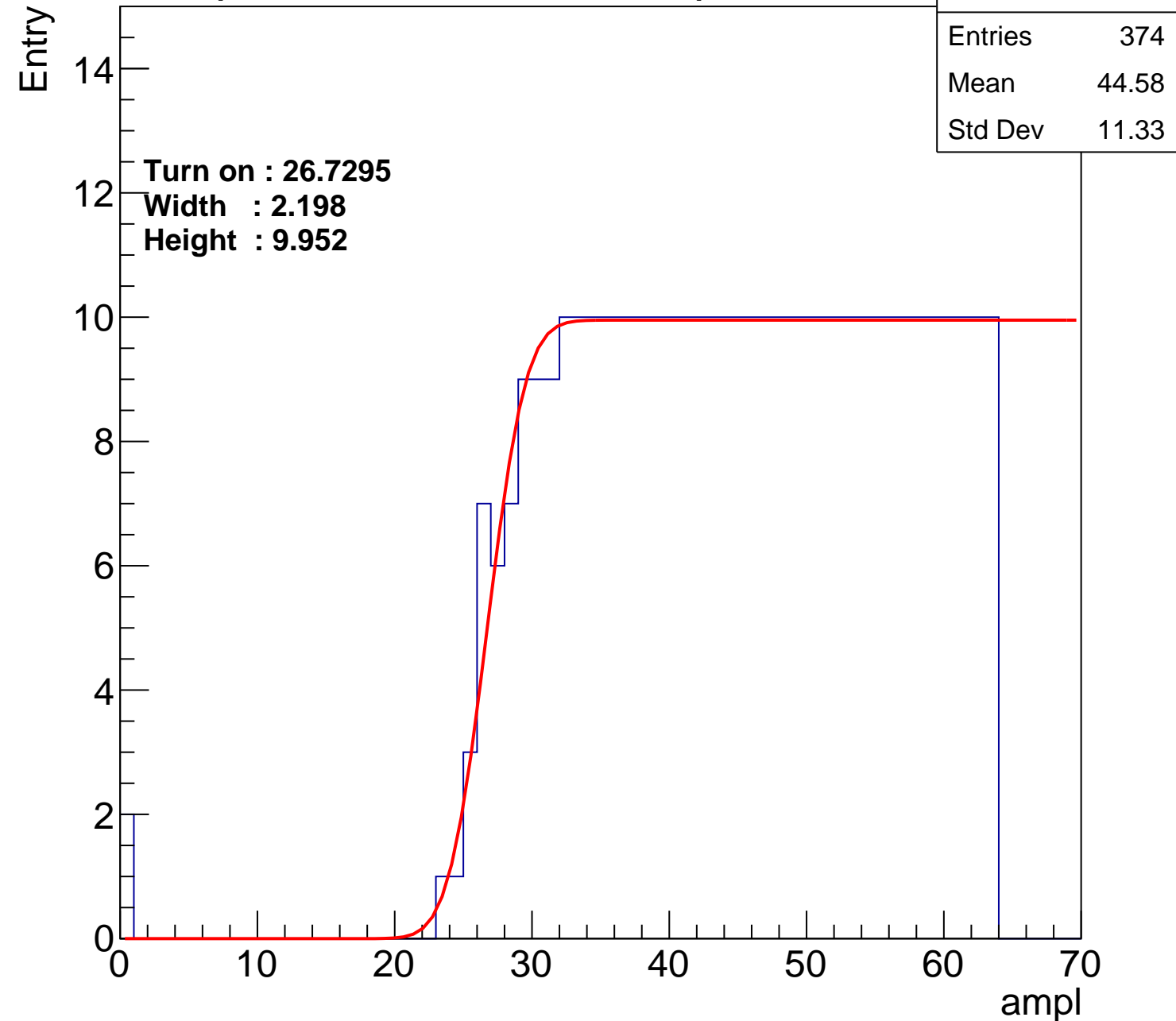
Width : 2.198

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch26

calib_packv5_042523_0143.root, FC#7, port C2

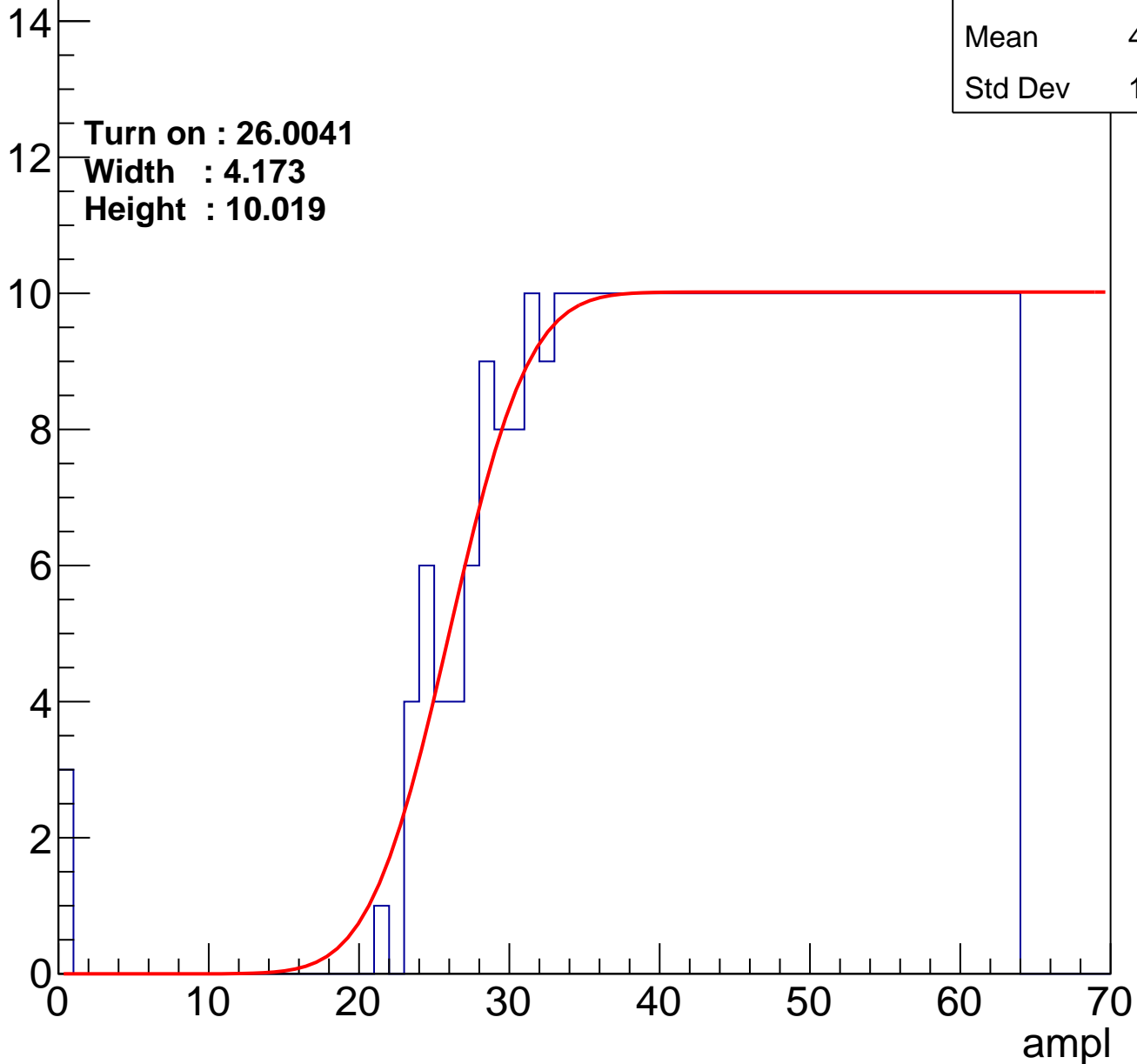
Entries	382
Mean	44.04
Std Dev	11.82

Turn on : 26.0041

Width : 4.173

Height : 10.019

Entry



B1L103S, U25-ch27

calib_packv5_042523_0143.root, FC#7, port C2

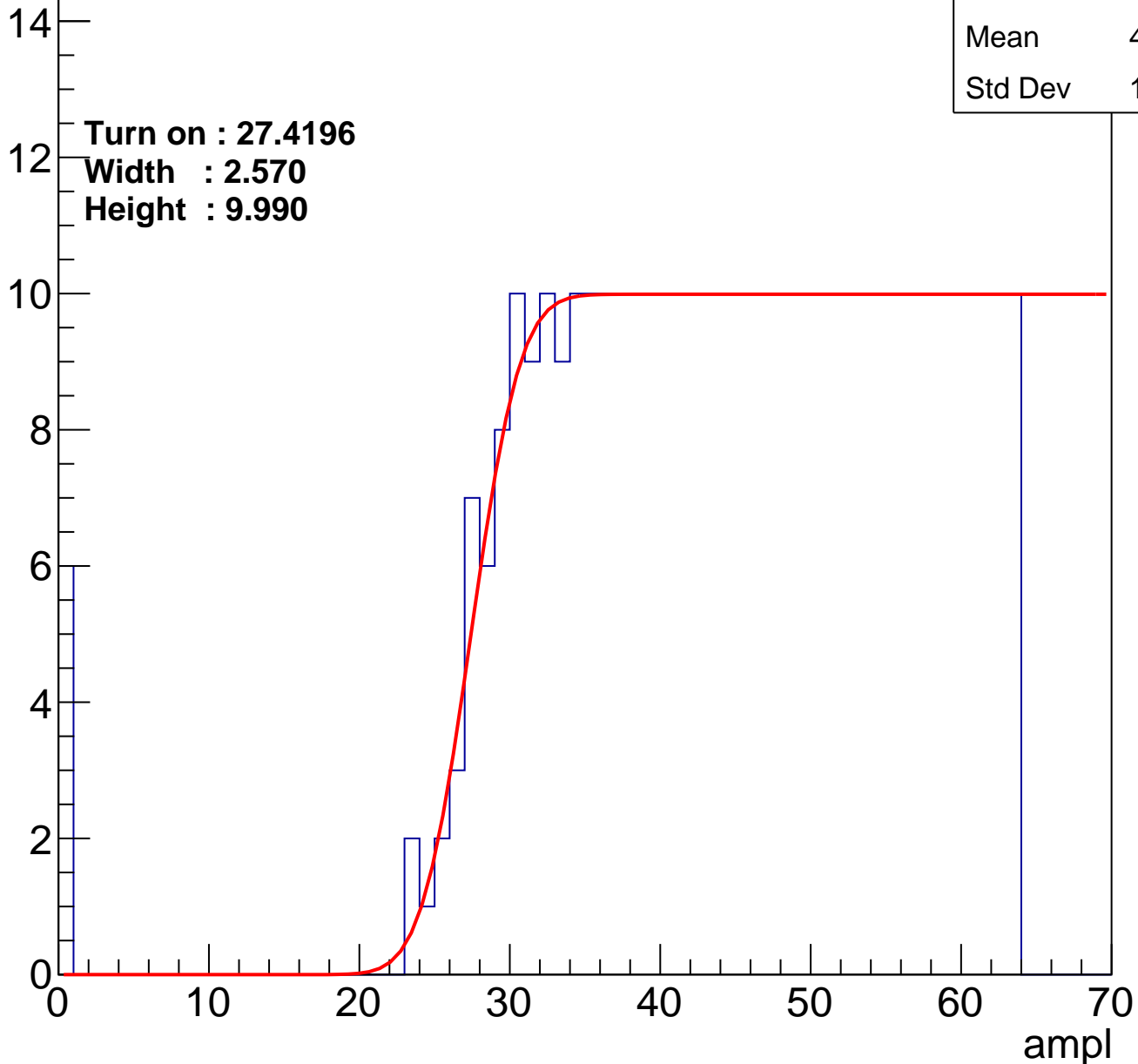
Entries	373
Mean	44.32
Std Dev	12.08

Turn on : 27.4196

Width : 2.570

Height : 9.990

Entry



B1L103S, U25-ch28

calib_packv5_042523_0143.root, FC#7, port C2

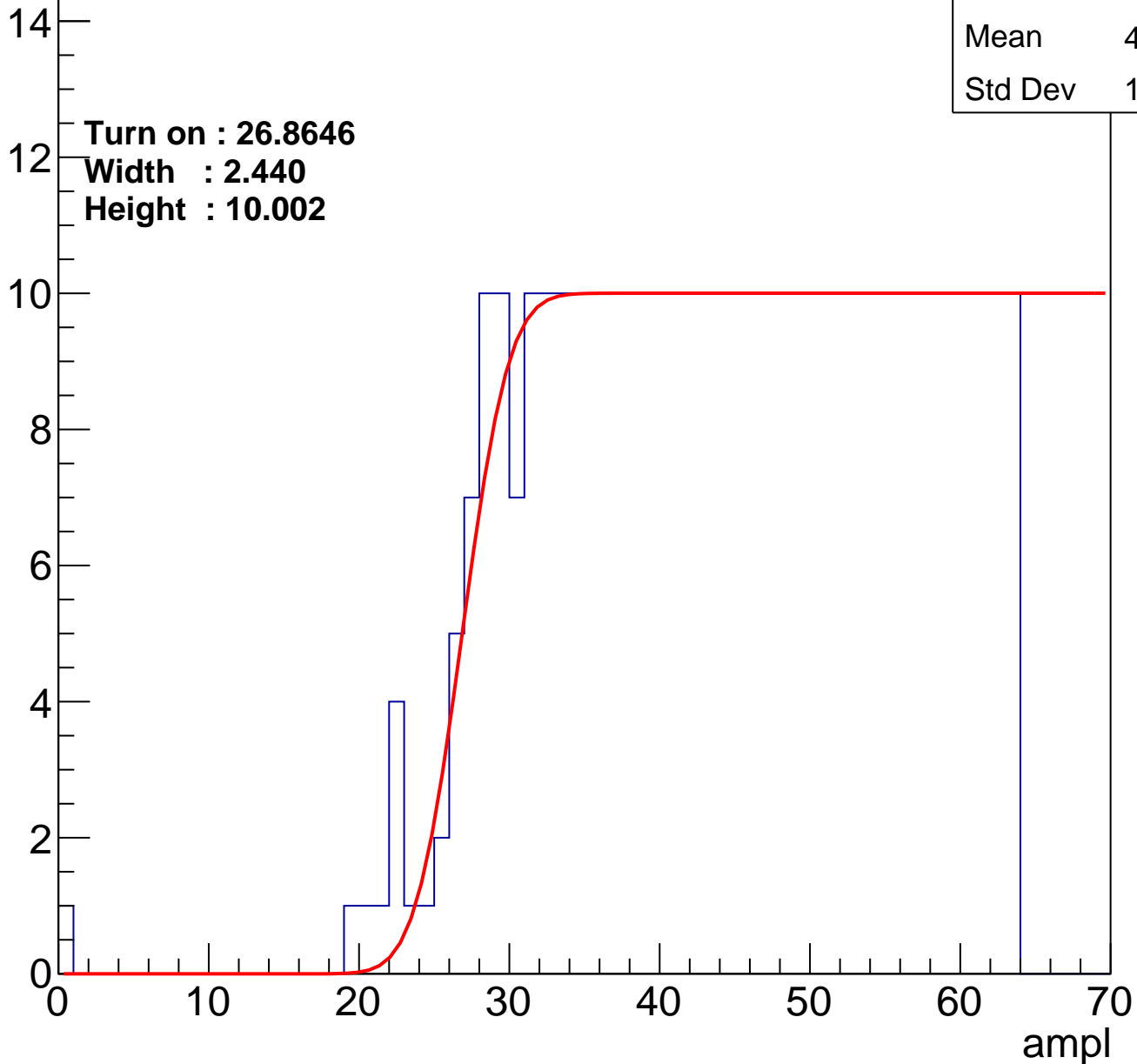
Entries	381
Mean	44.24
Std Dev	11.44

Turn on : 26.8646

Width : 2.440

Height : 10.002

Entry



B1L103S, U25-ch29

calib_packv5_042523_0143.root, FC#7, port C2

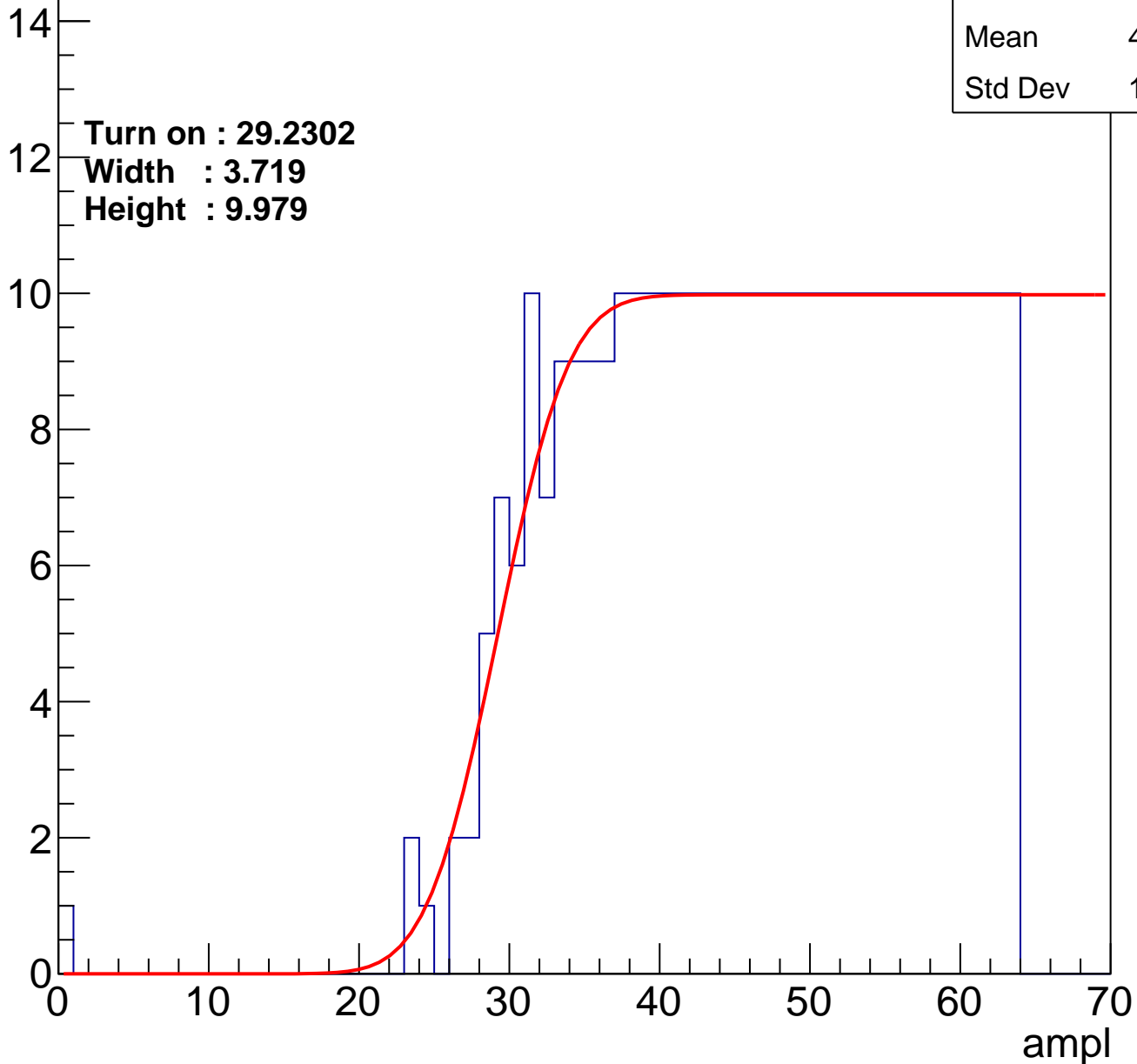
Entries	349
Mean	45.77
Std Dev	10.63

Turn on : 29.2302

Width : 3.719

Height : 9.979

Entry



B1L103S, U25-ch30

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	44.92
Std Dev	11.74

Turn on : 28.5793

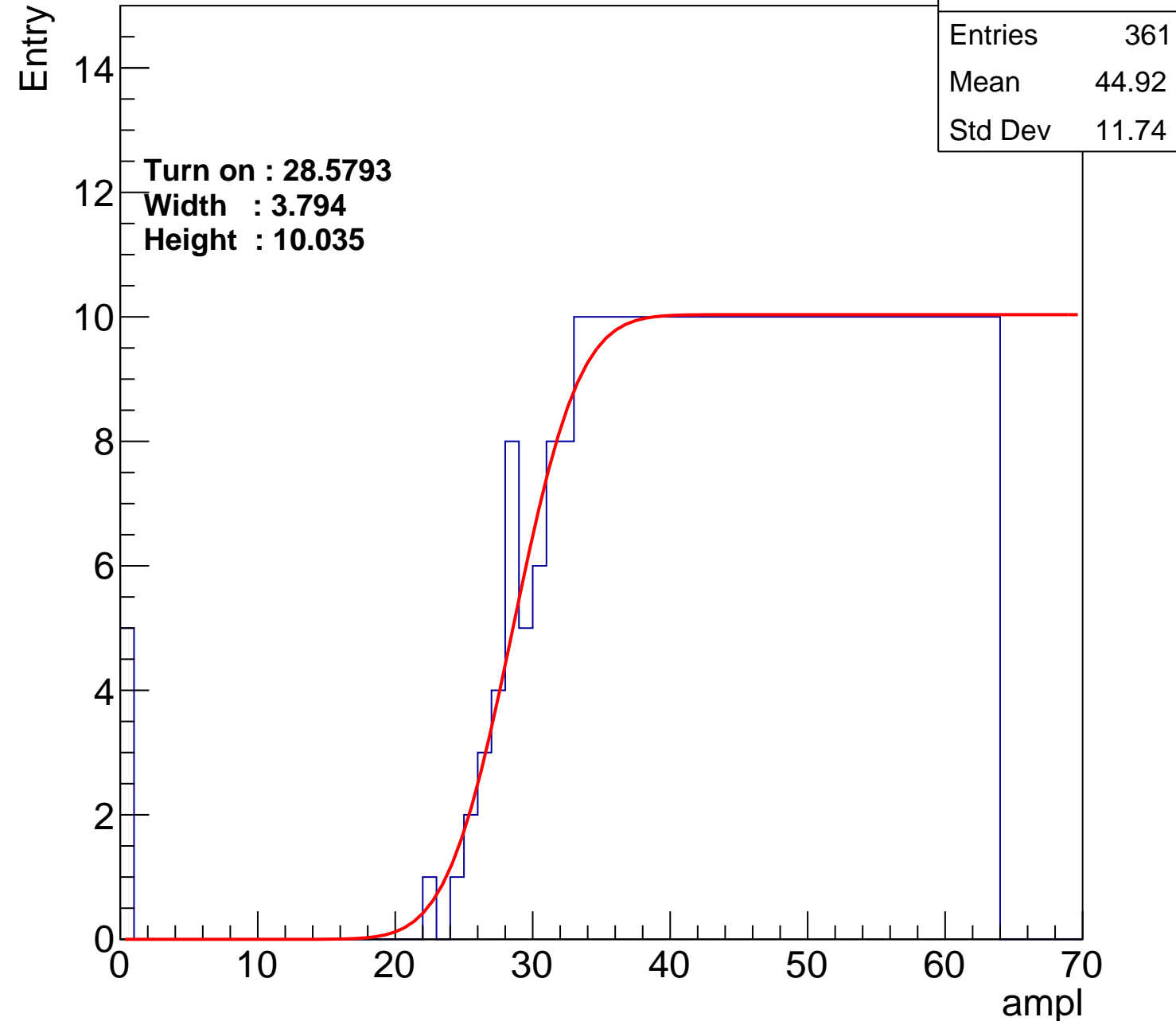
Width : 3.794

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch31

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.31
Std Dev	10.82

Turn on : 29.0686

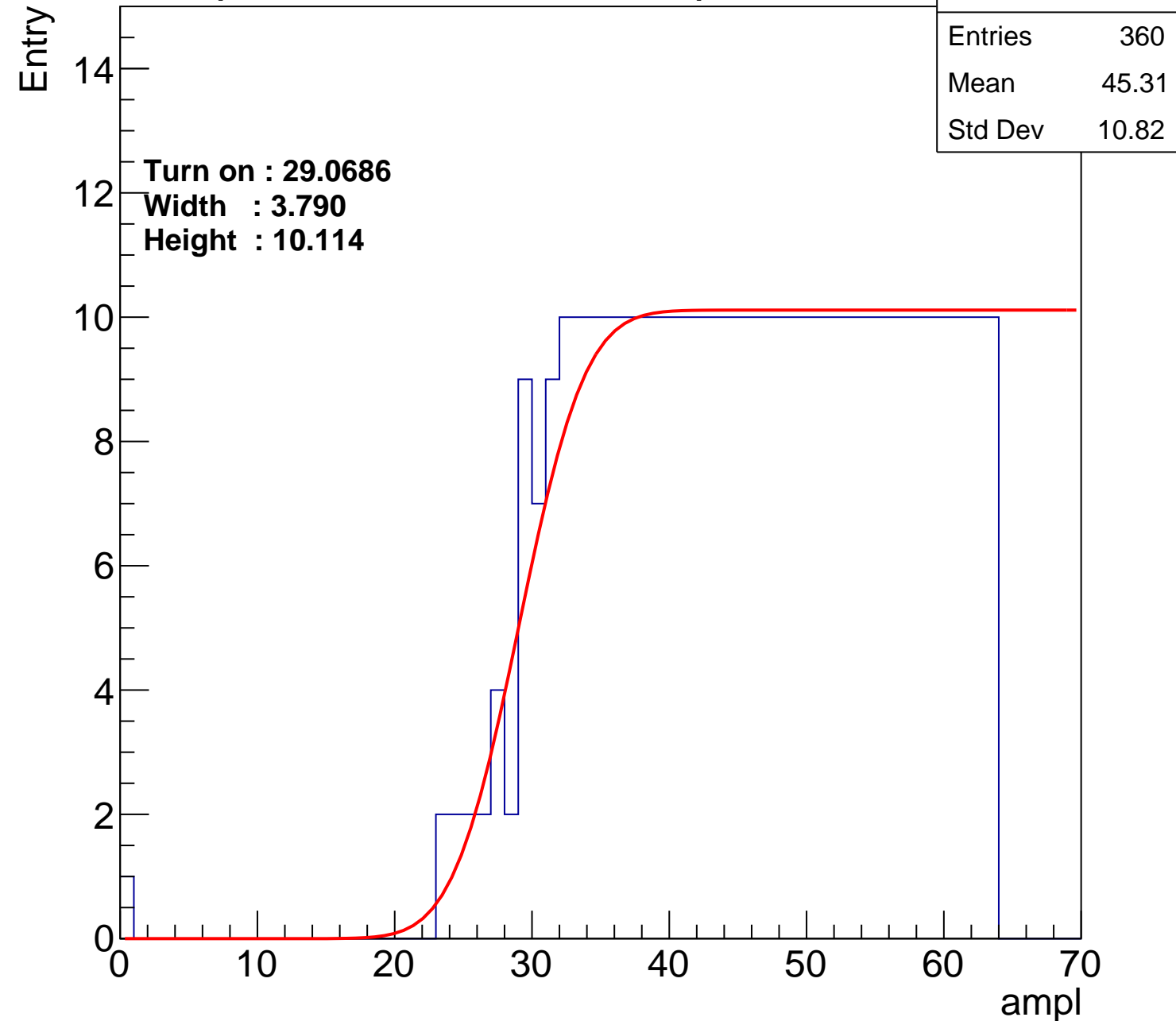
Width : 3.790

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.83
Std Dev	11.11

Turn on : 28.1277

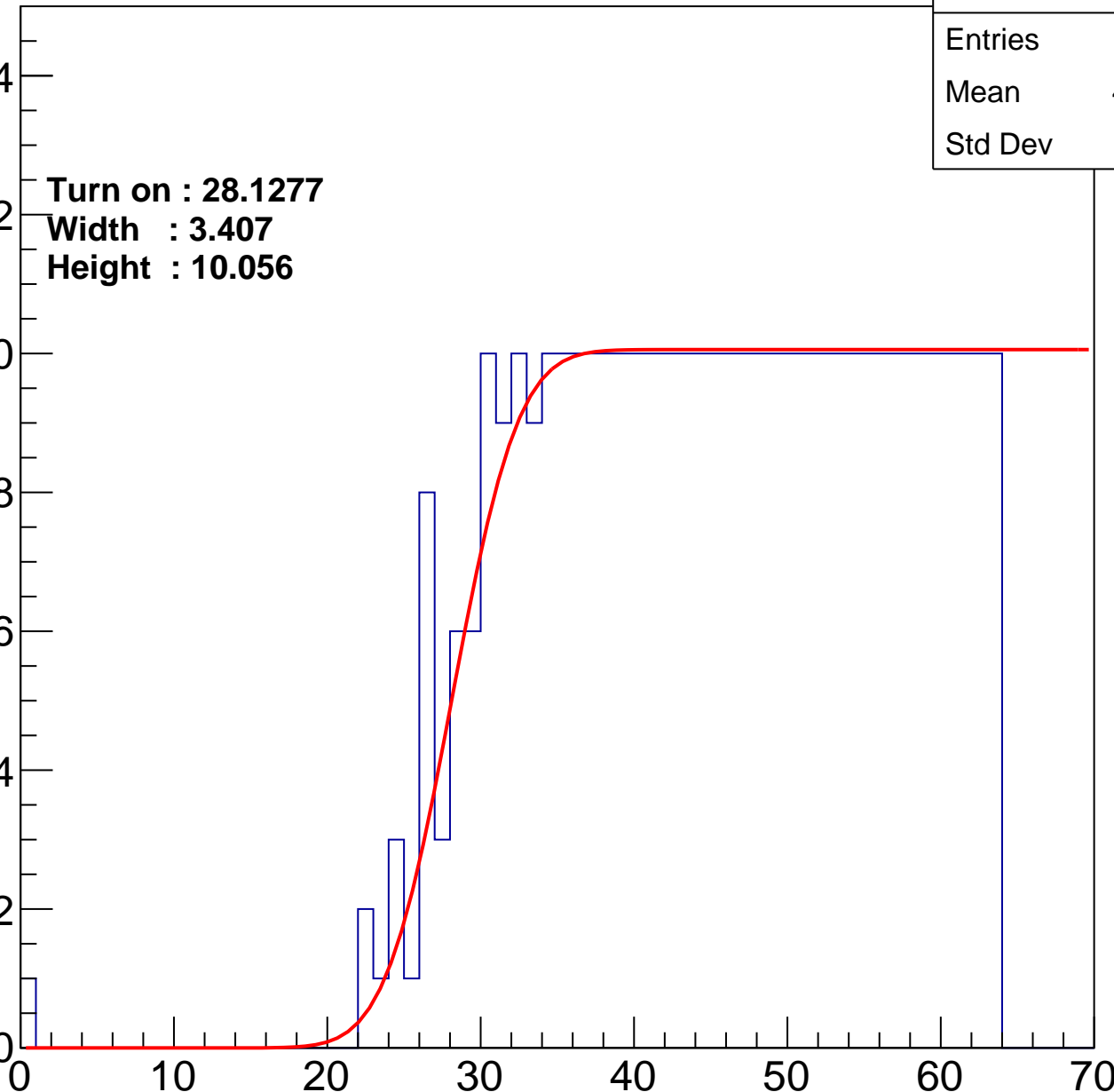
Width : 3.407

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch33

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.2963

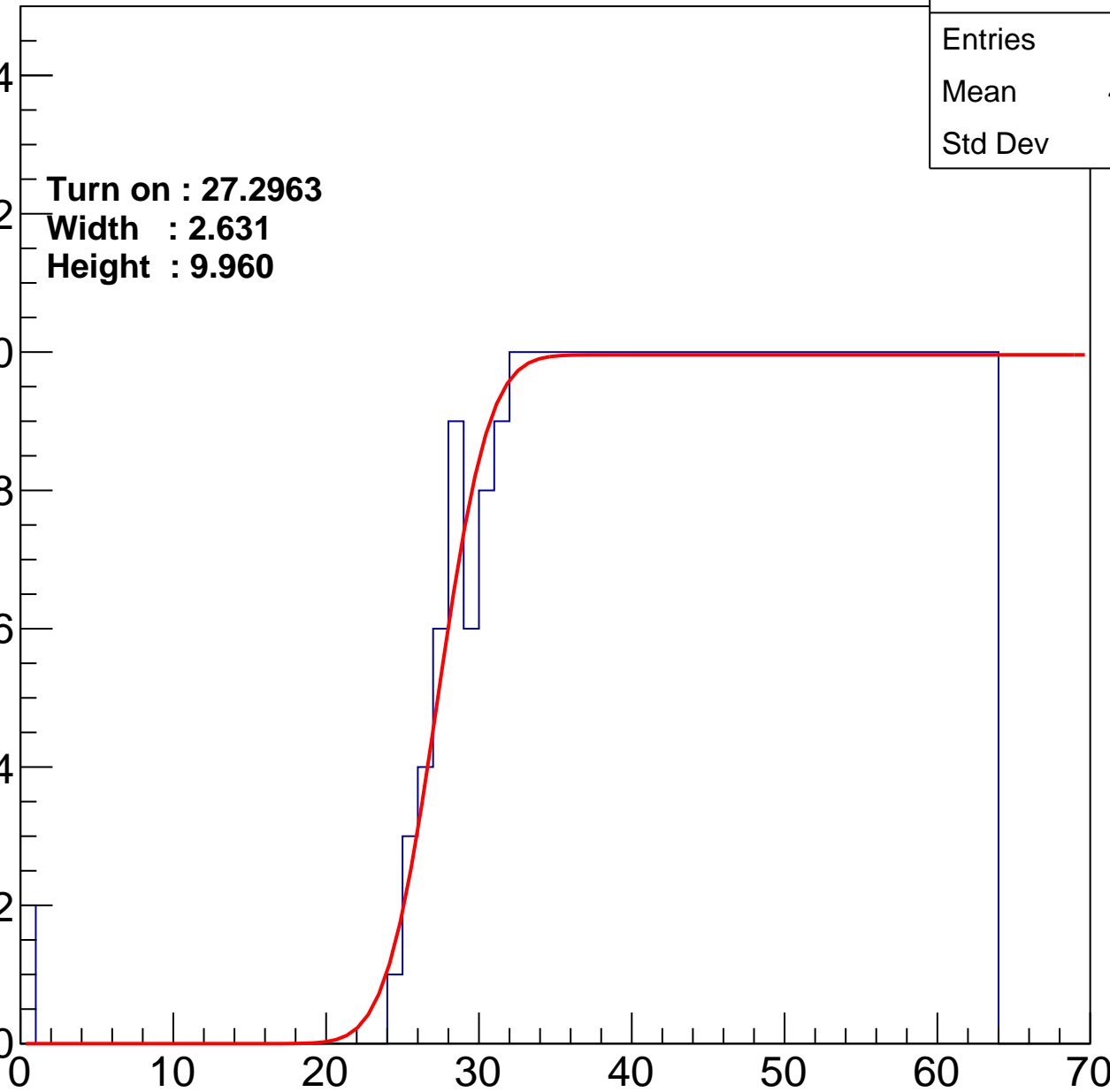
Width : 2.631

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch34

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	44.05
Std Dev	11.51

Turn on : 25.9416

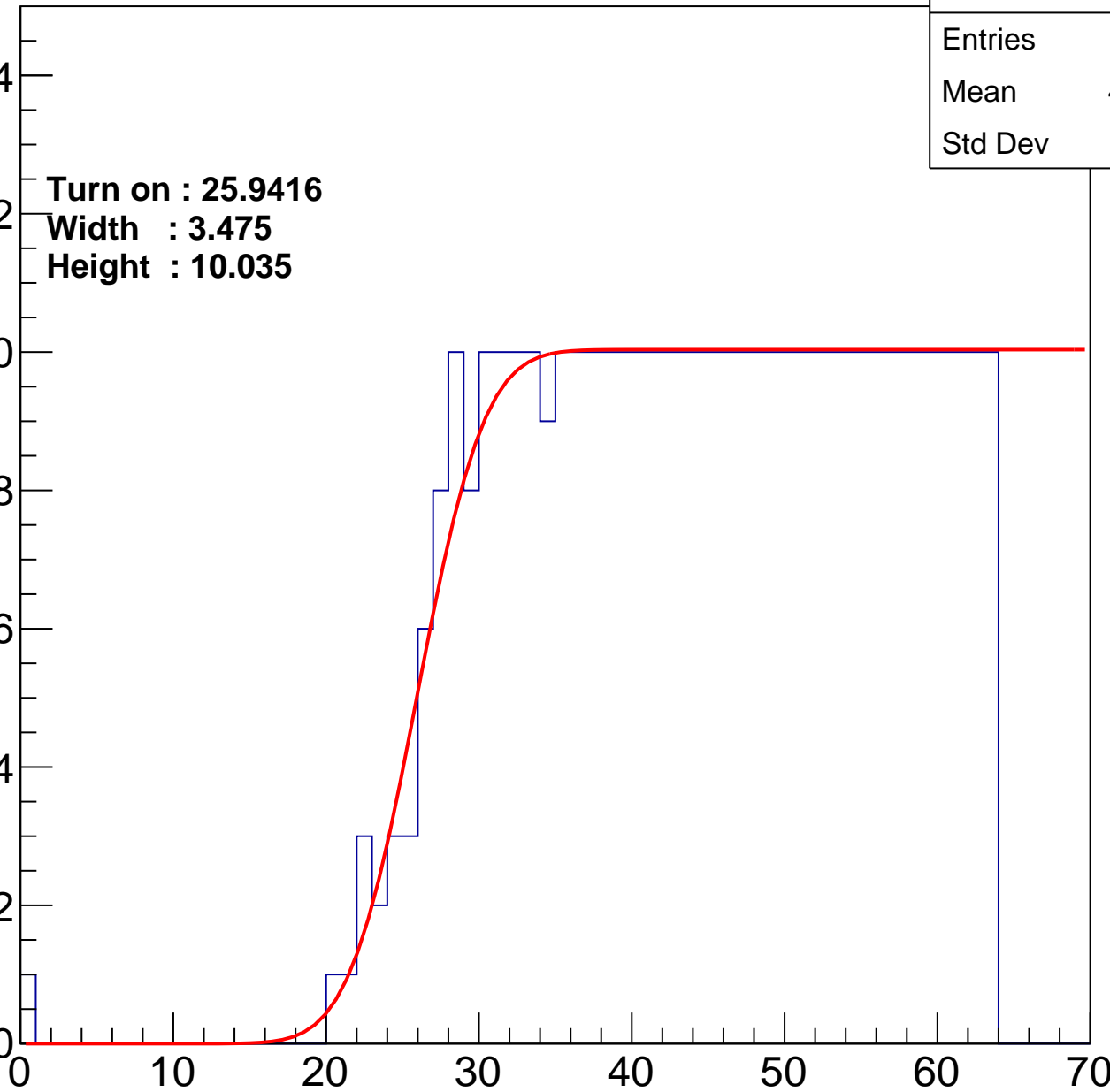
Width : 3.475

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch35

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.6
Std Dev	11.54

Turn on : 27.4747

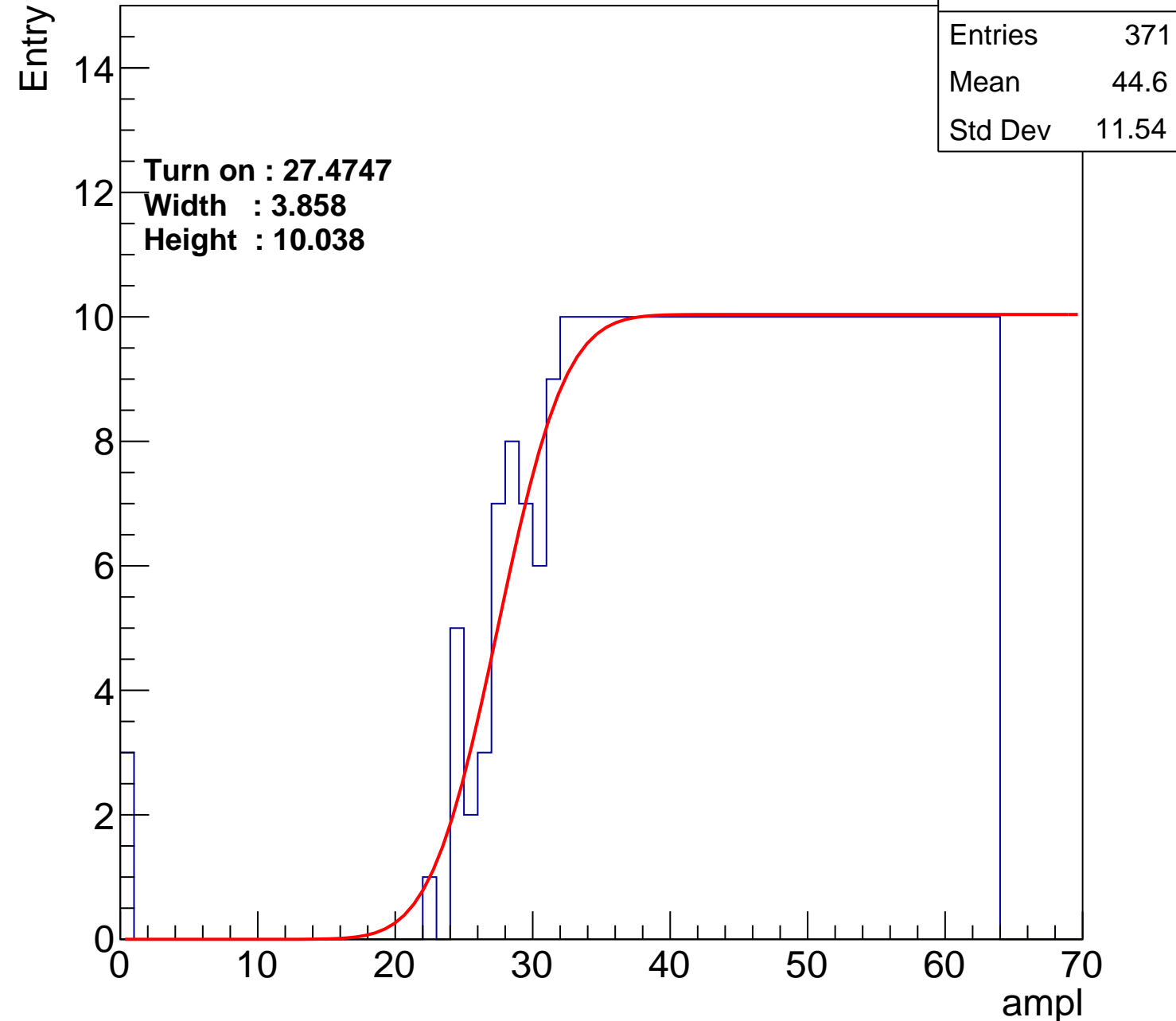
Width : 3.858

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch36

calib_packv5_042523_0143.root, FC#7, port C2

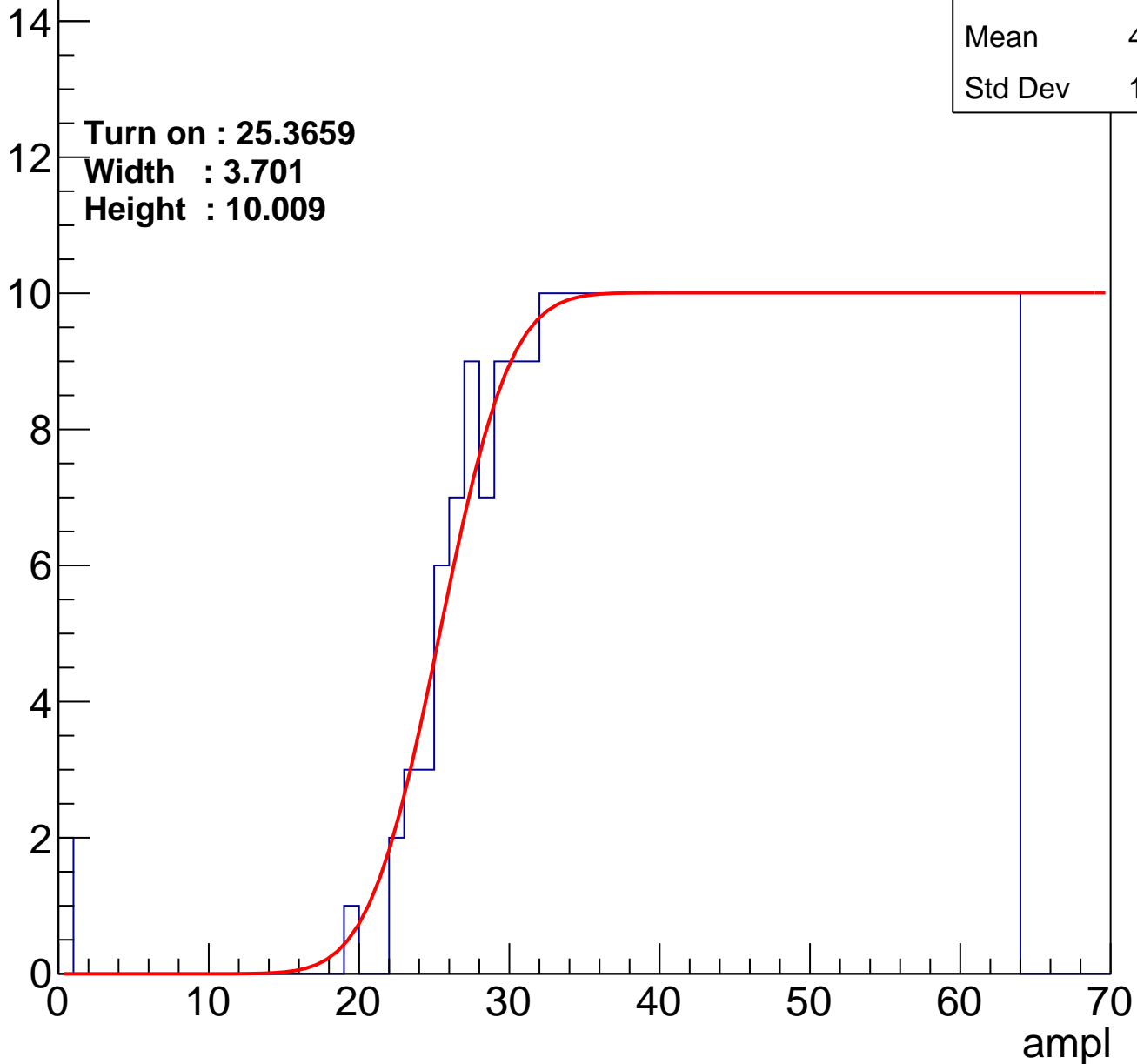
Entries	387
Mean	43.89
Std Dev	11.74

Turn on : 25.3659

Width : 3.701

Height : 10.009

Entry



B1L103S, U25-ch37

calib_packv5_042523_0143.root, FC#7, port C2

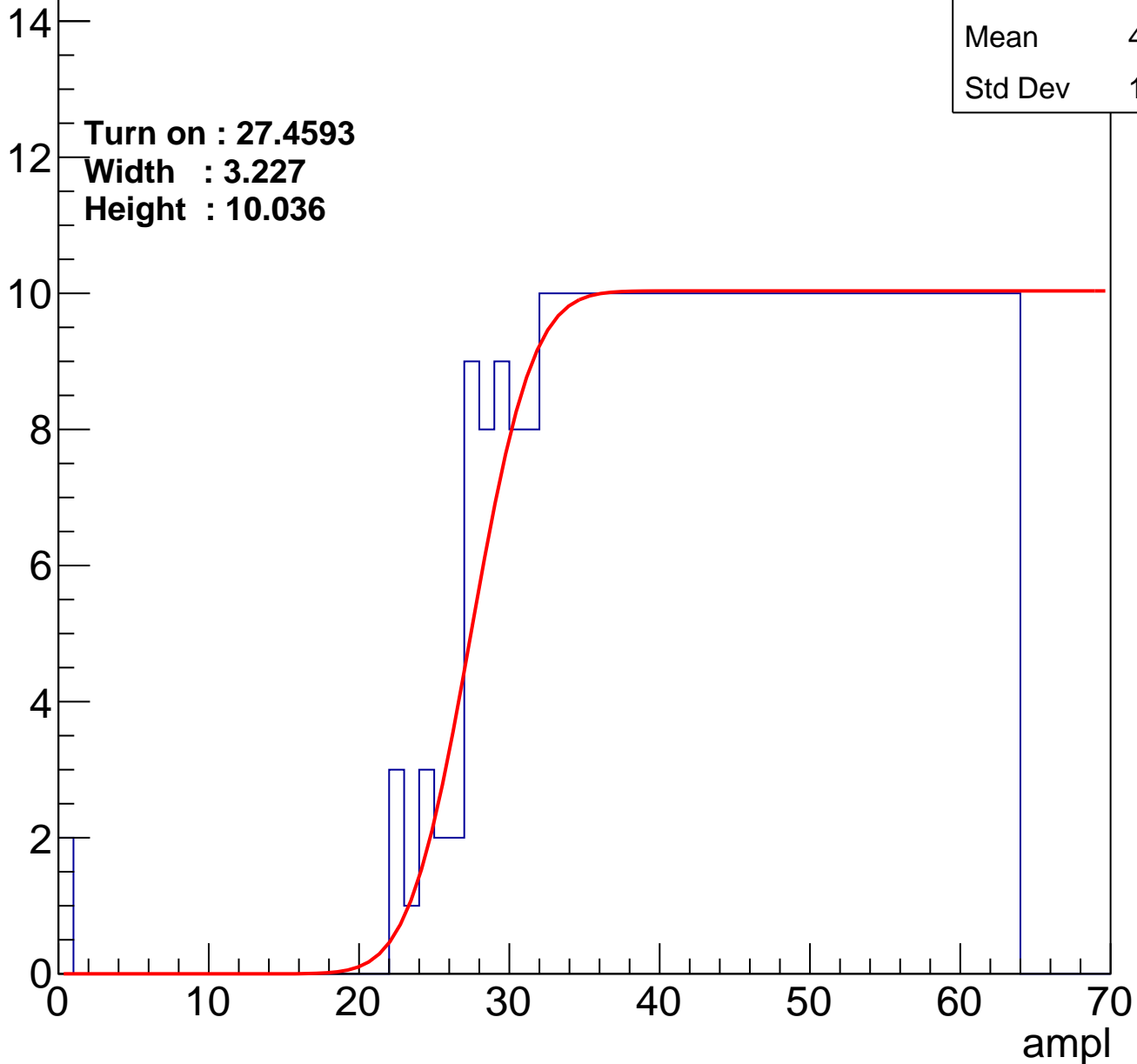
Entries	375
Mean	44.48
Std Dev	11.44

Turn on : 27.4593

Width : 3.227

Height : 10.036

Entry



B1L103S, U25-ch38

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	44.18
Std Dev	11.36

Turn on : 25.9233

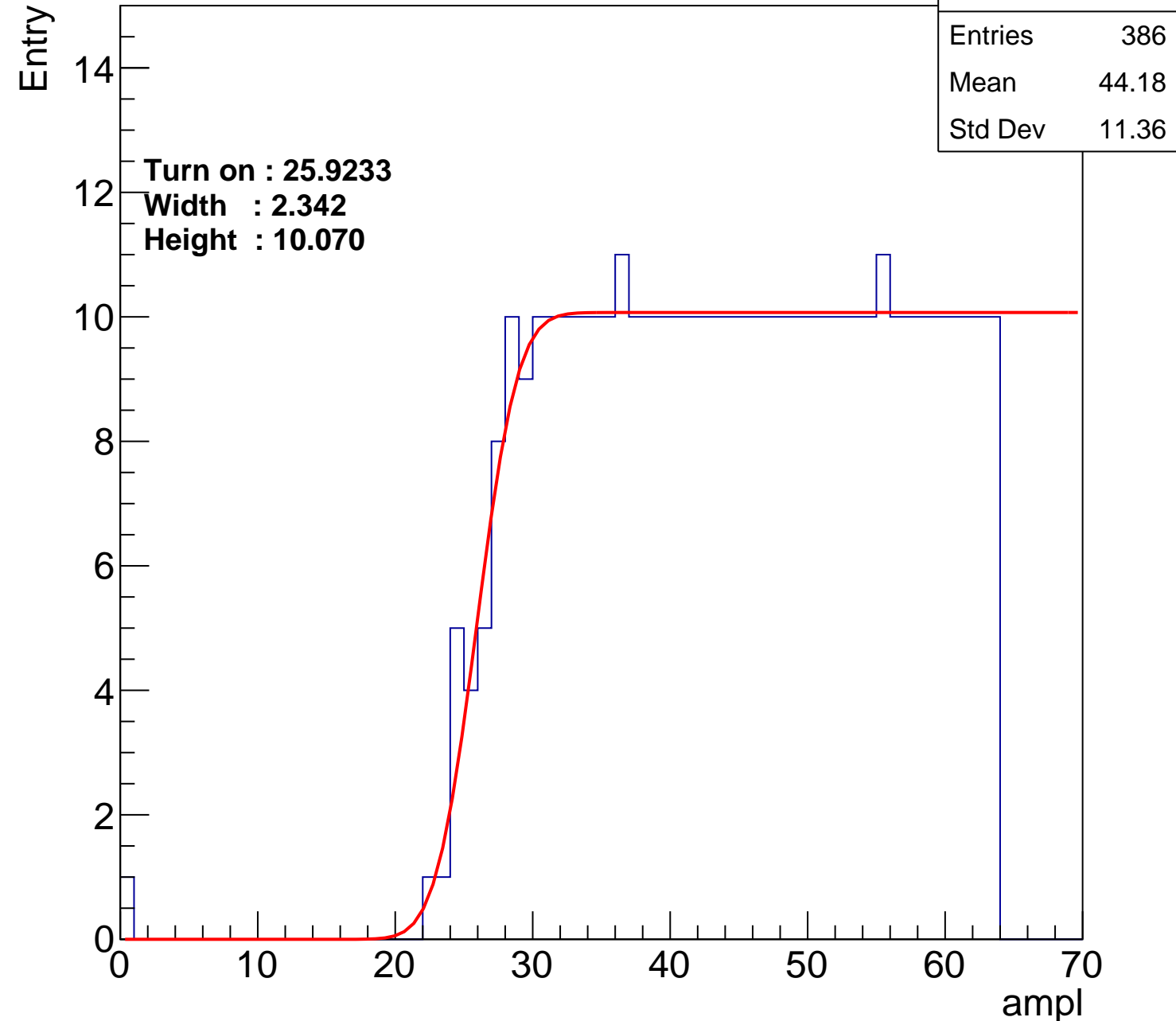
Width : 2.342

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch39

calib_packv5_042523_0143.root, FC#7, port C2

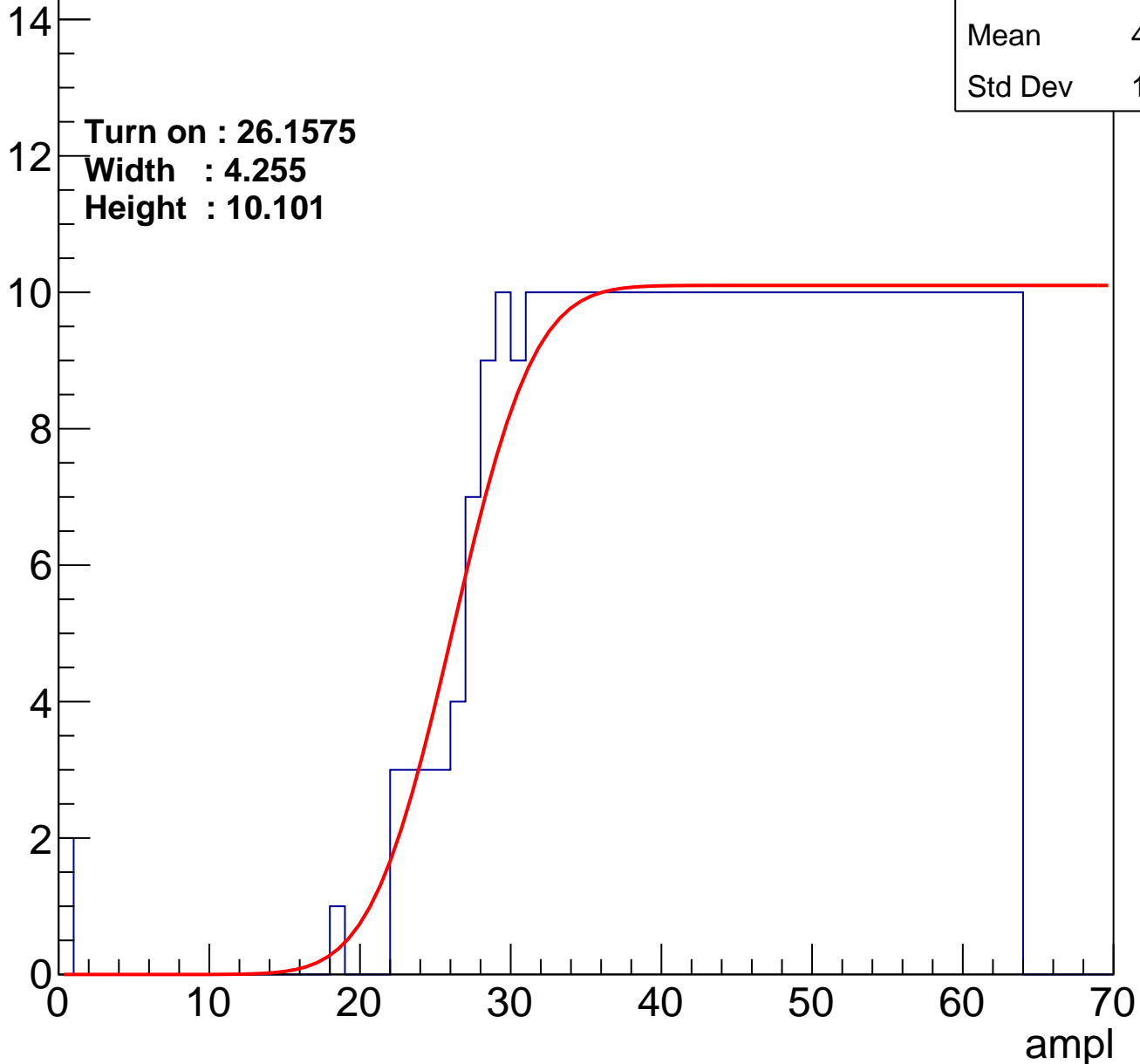
Entries	384
Mean	44.05
Std Dev	11.65

Turn on : 26.1575

Width : 4.255

Height : 10.101

Entry



B1L103S, U25-ch40

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.96
Std Dev	11.6

Turn on : 25.4146

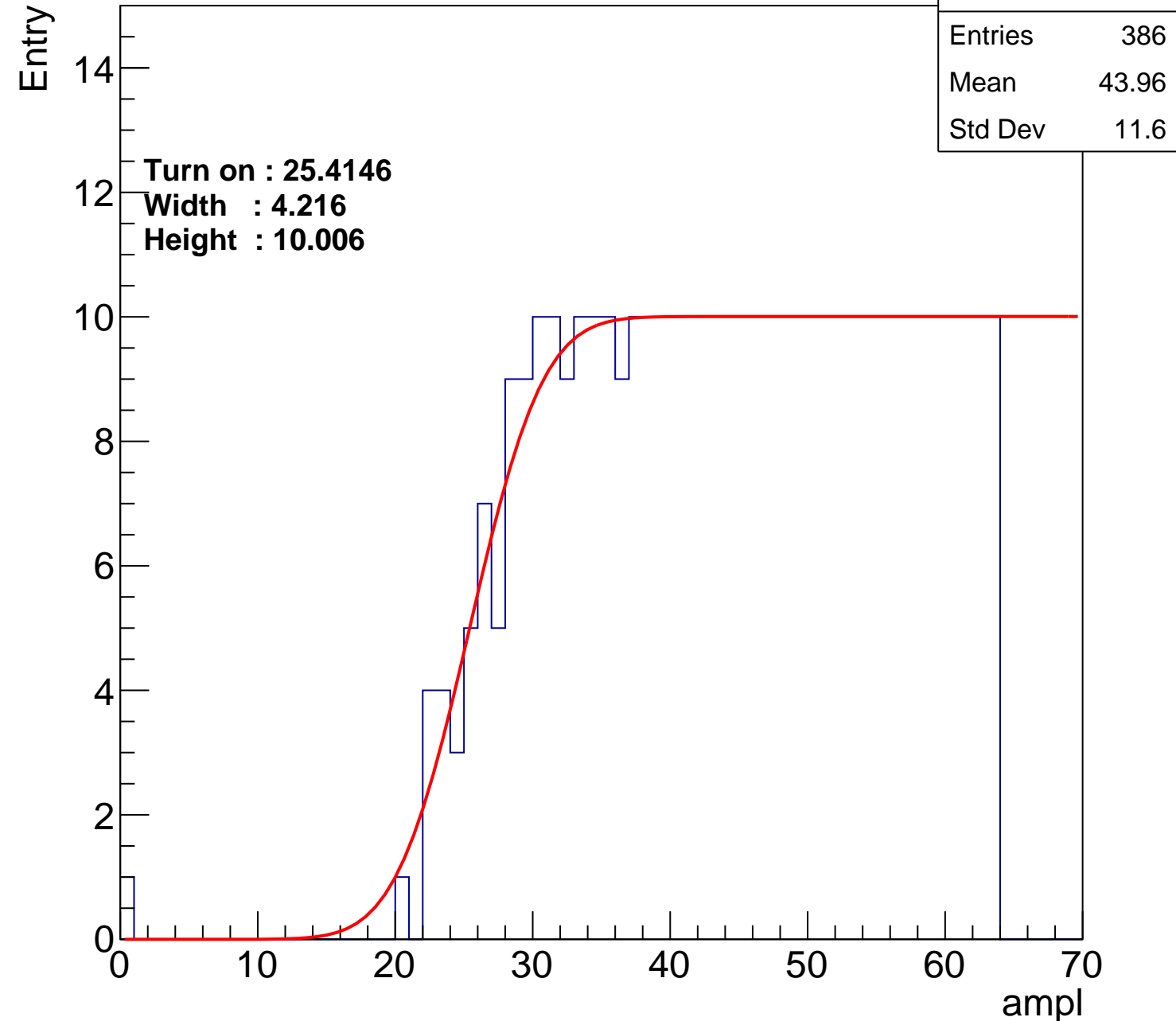
Width : 4.216

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch41

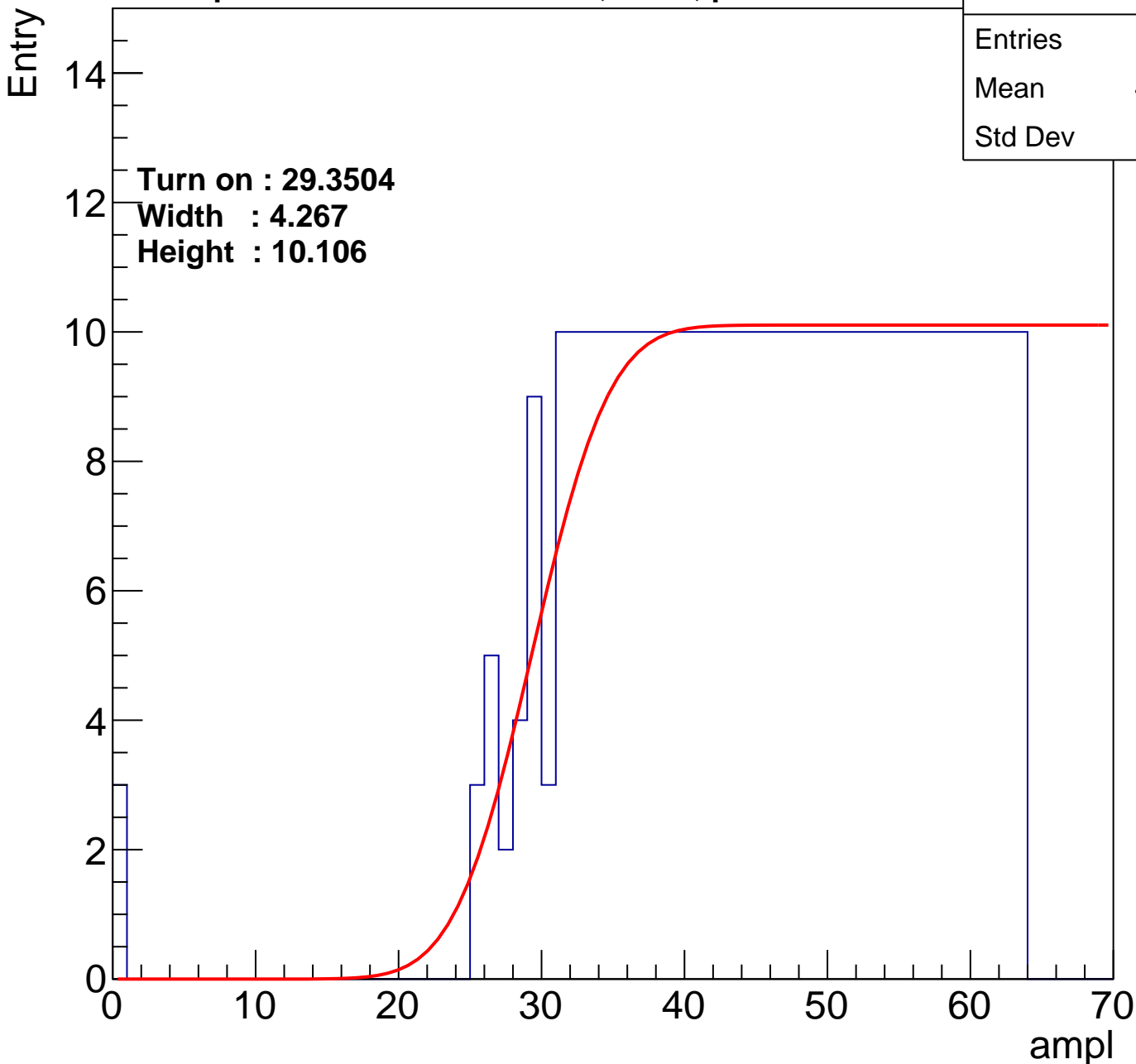
calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.21
Std Dev	11.21

Turn on : 29.3504

Width : 4.267

Height : 10.106



B1L103S, U25-ch42

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.32
Std Dev	11.41

Turn on : 26.7629

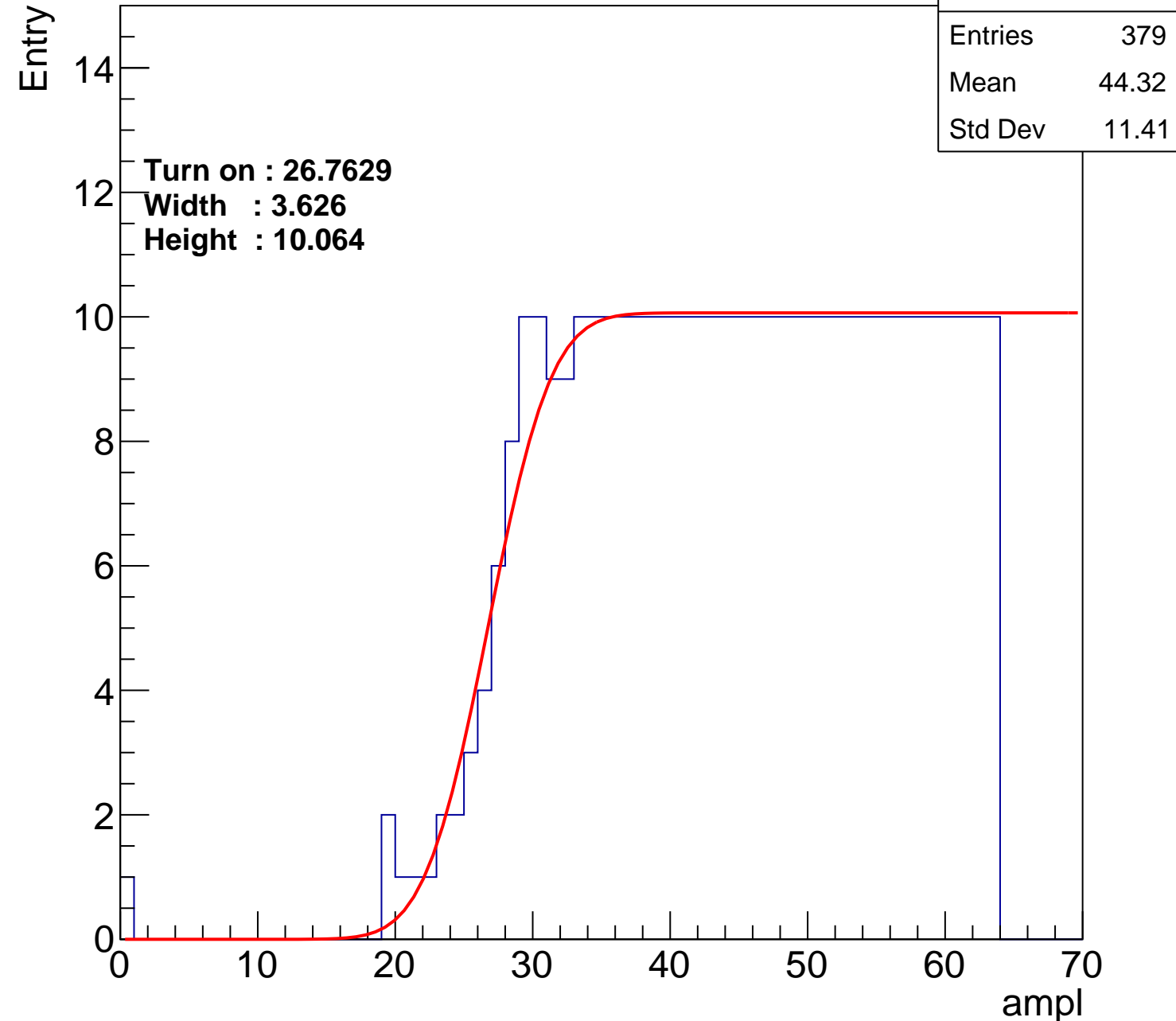
Width : 3.626

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch43

calib_packv5_042523_0143.root, FC#7, port C2

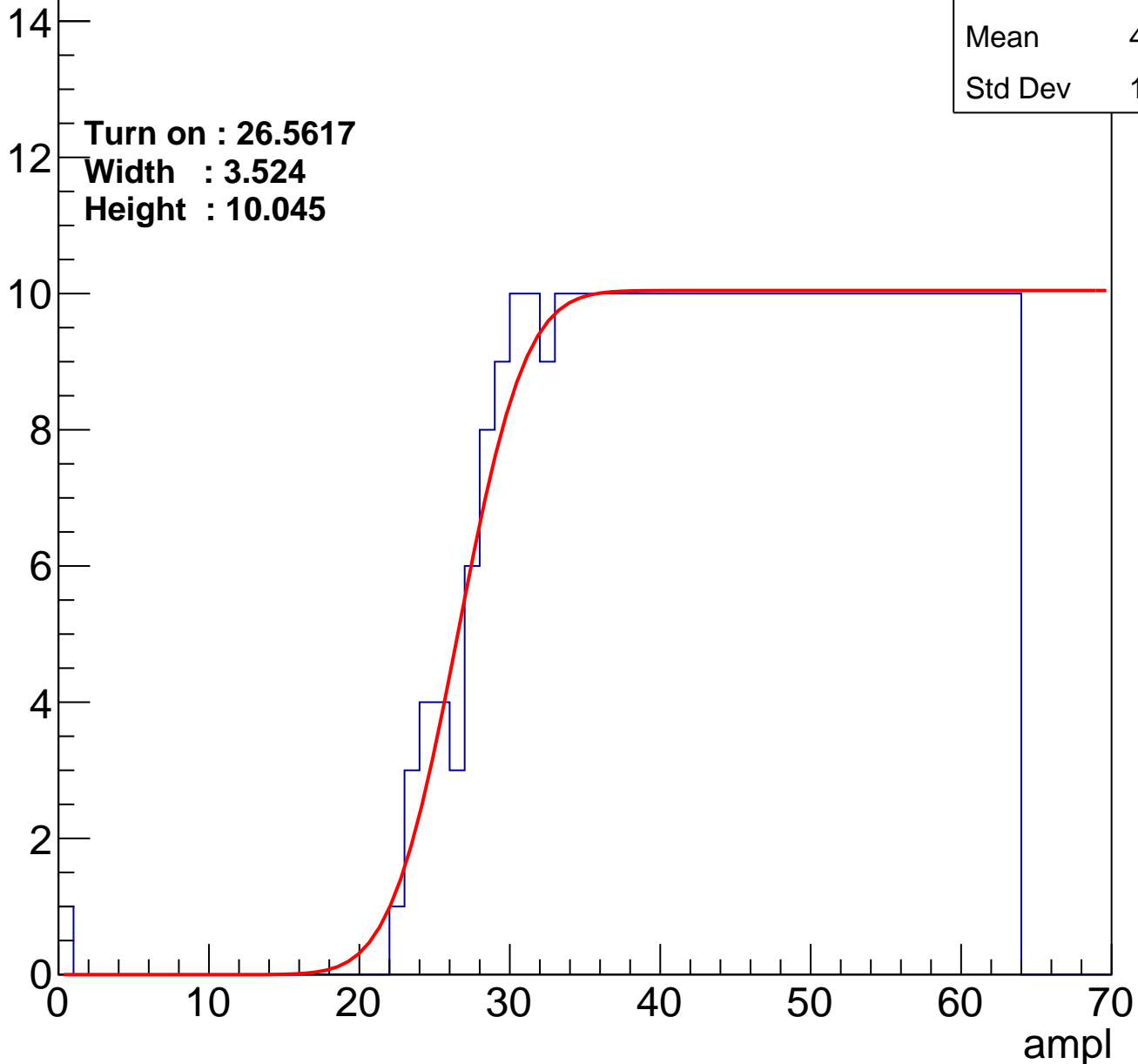
Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 26.5617

Width : 3.524

Height : 10.045

Entry



B1L103S, U25-ch44

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.52
Std Dev	12.2

Turn on : 25.5092

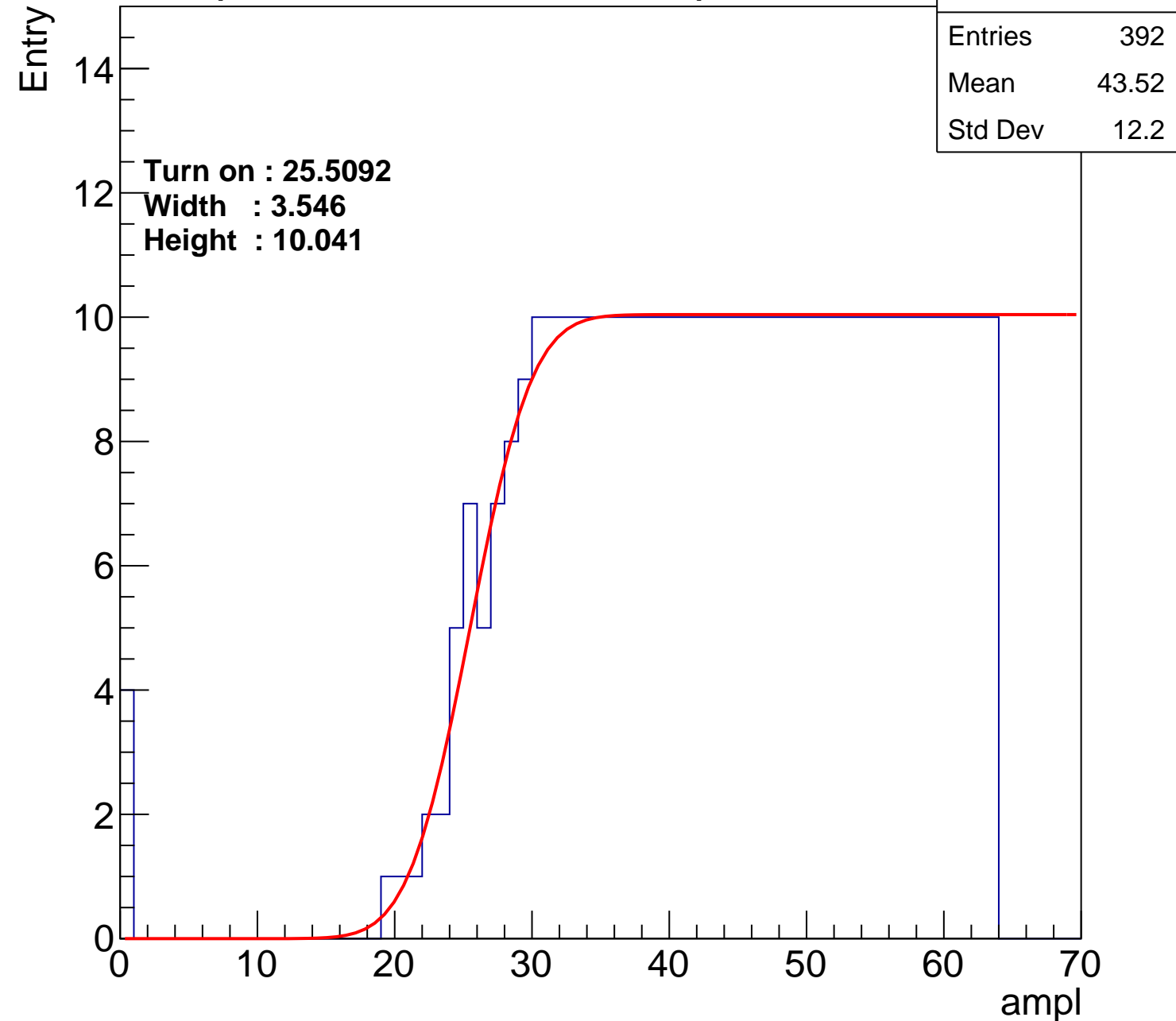
Width : 3.546

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch45

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.9518

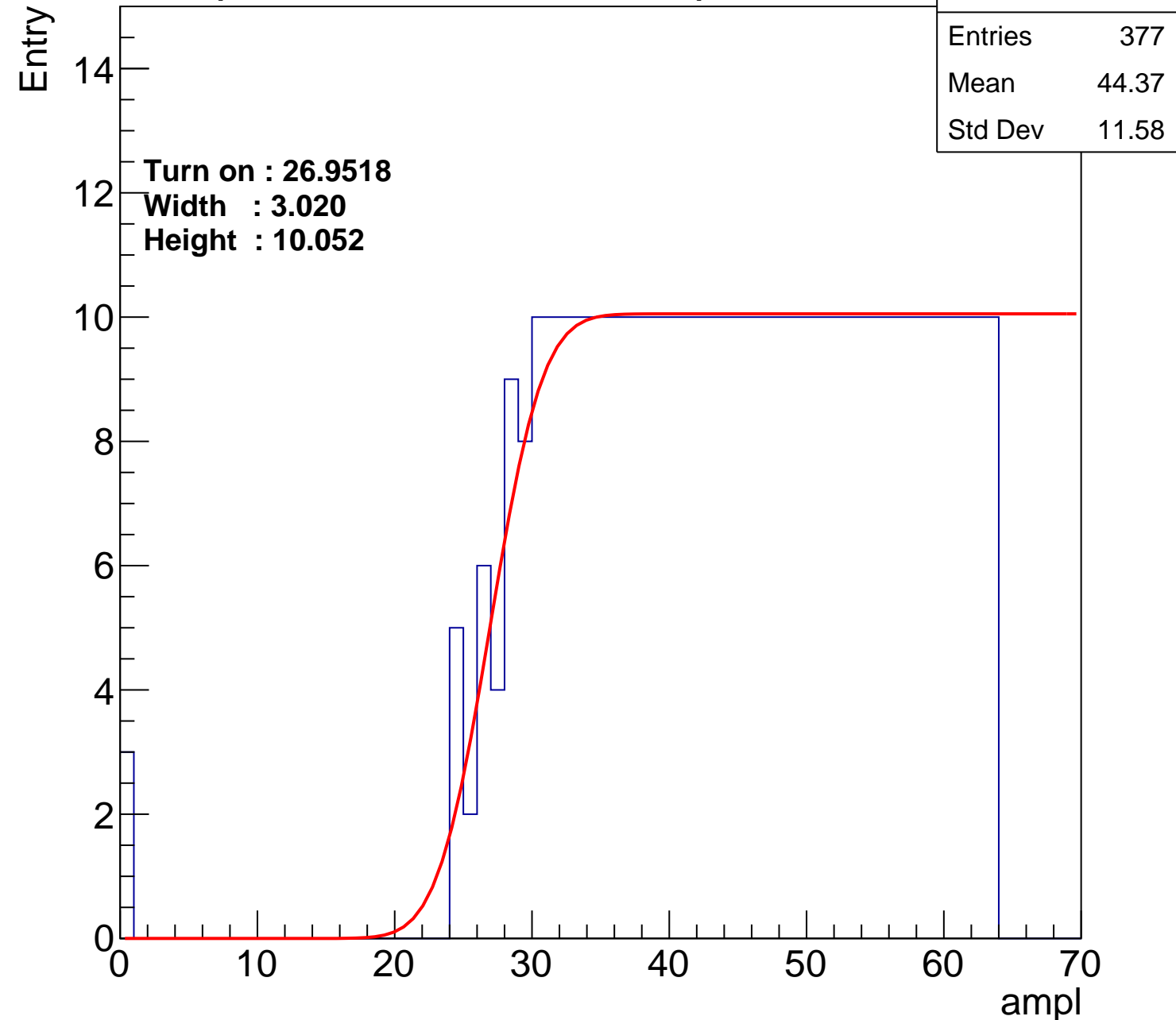
Width : 3.020

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch46

calib_packv5_042523_0143.root, FC#7, port C2

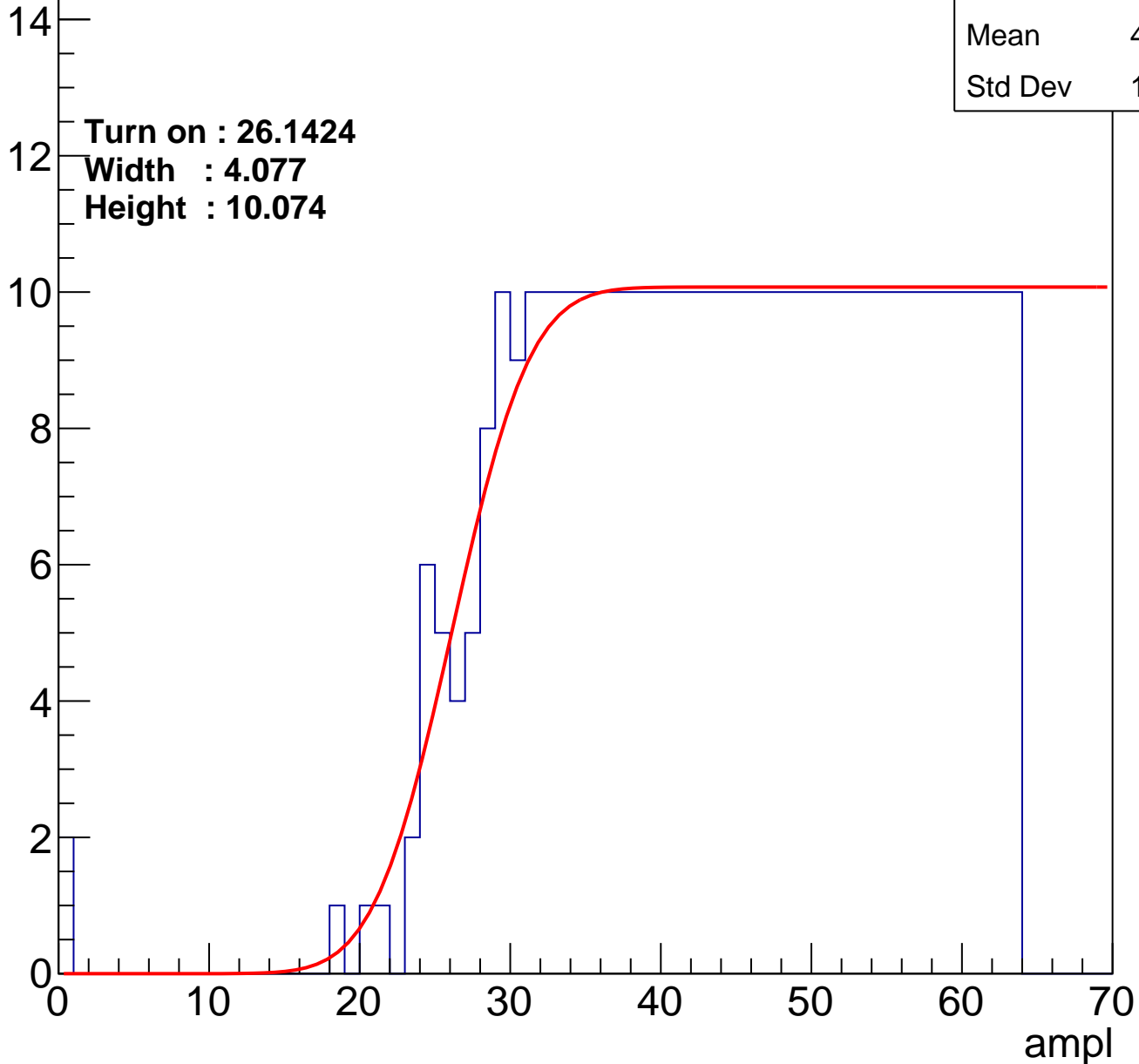
Entries	384
Mean	44.03
Std Dev	11.69

Turn on : 26.1424

Width : 4.077

Height : 10.074

Entry



B1L103S, U25-ch47

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.55
Std Dev	11.56

Turn on : 27.7286

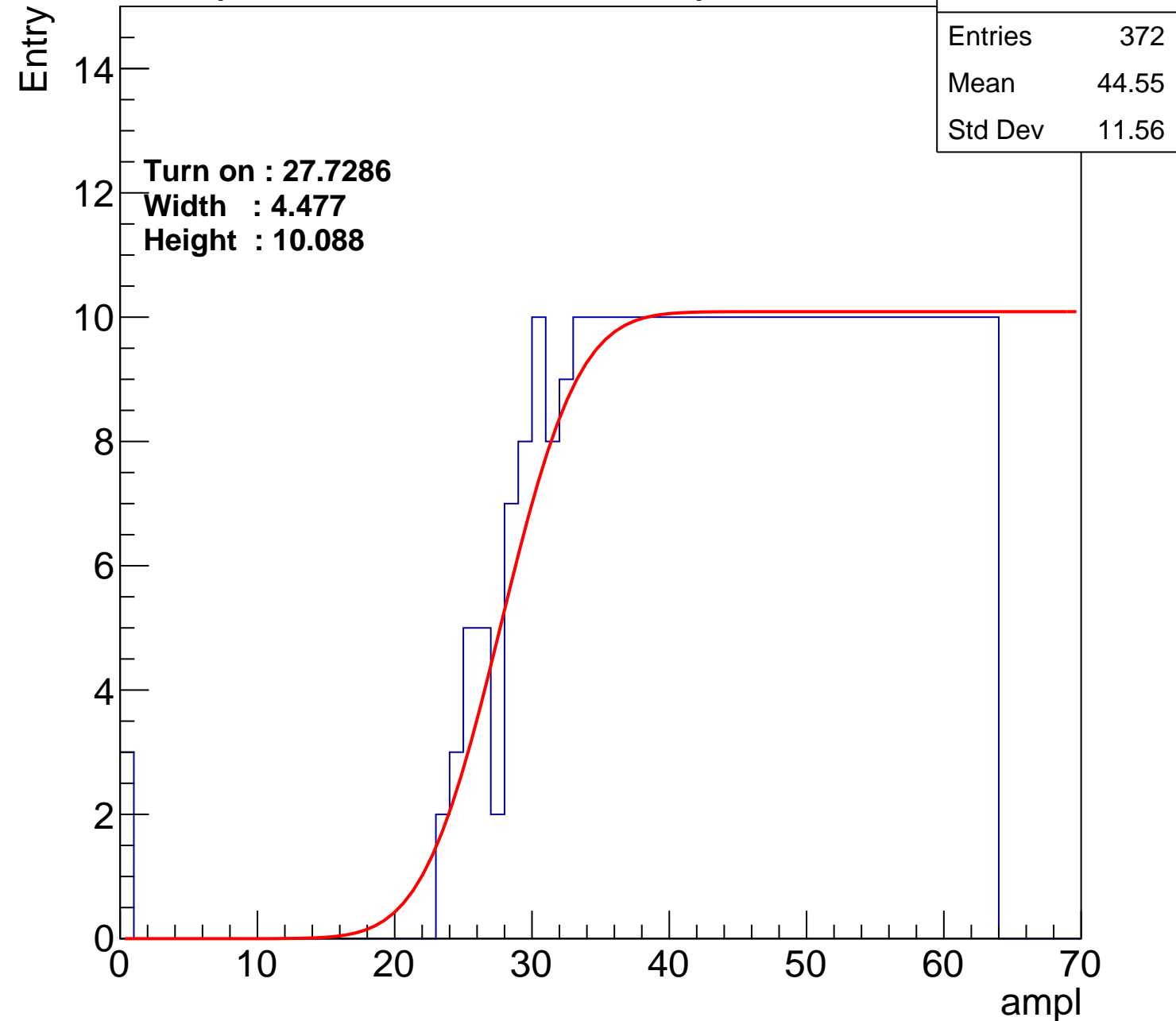
Width : 4.477

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.63
Std Dev	12.18

Turn on : 25.8291

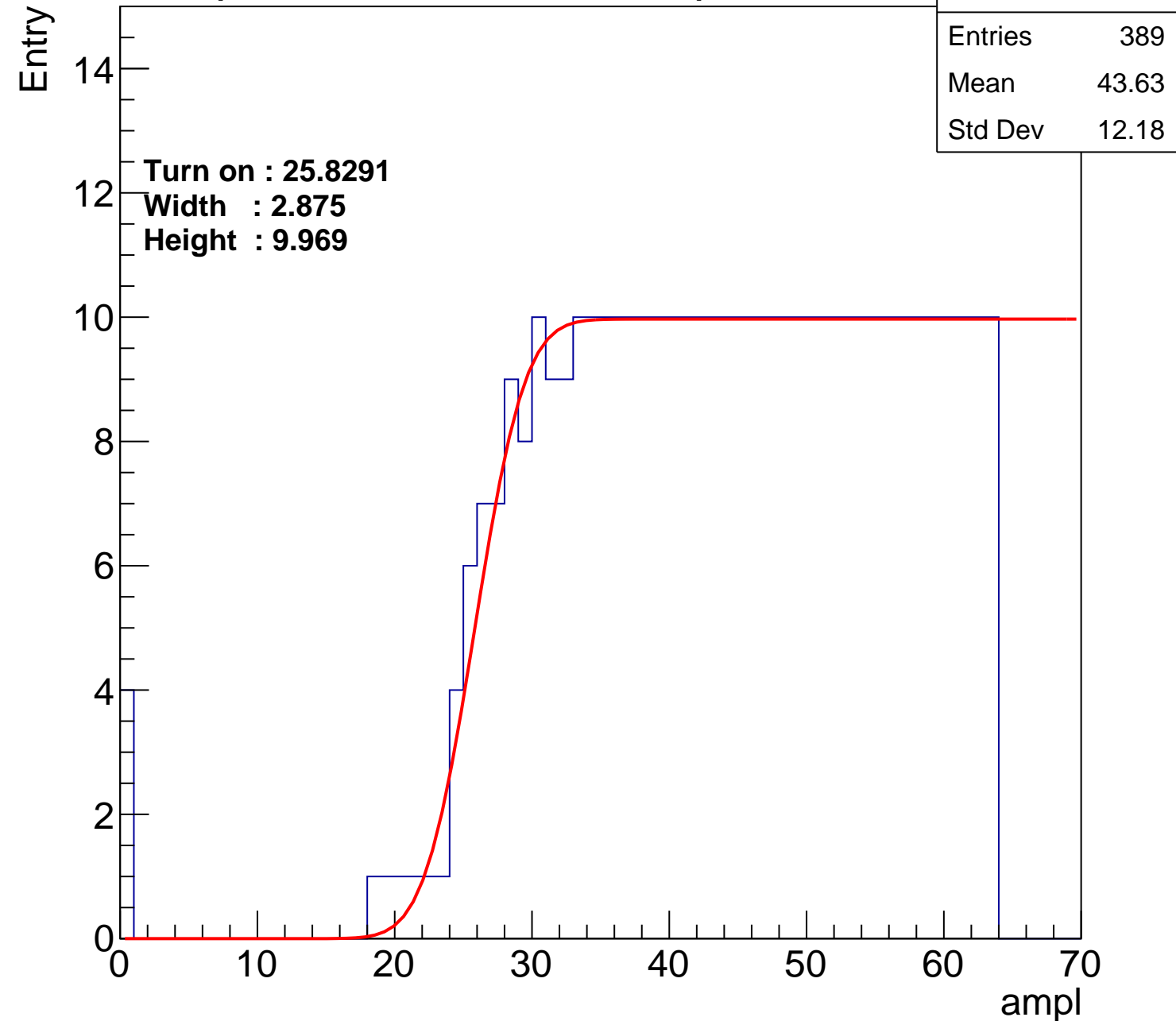
Width : 2.875

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch49

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.55
Std Dev	11.69

Turn on : 27.6425

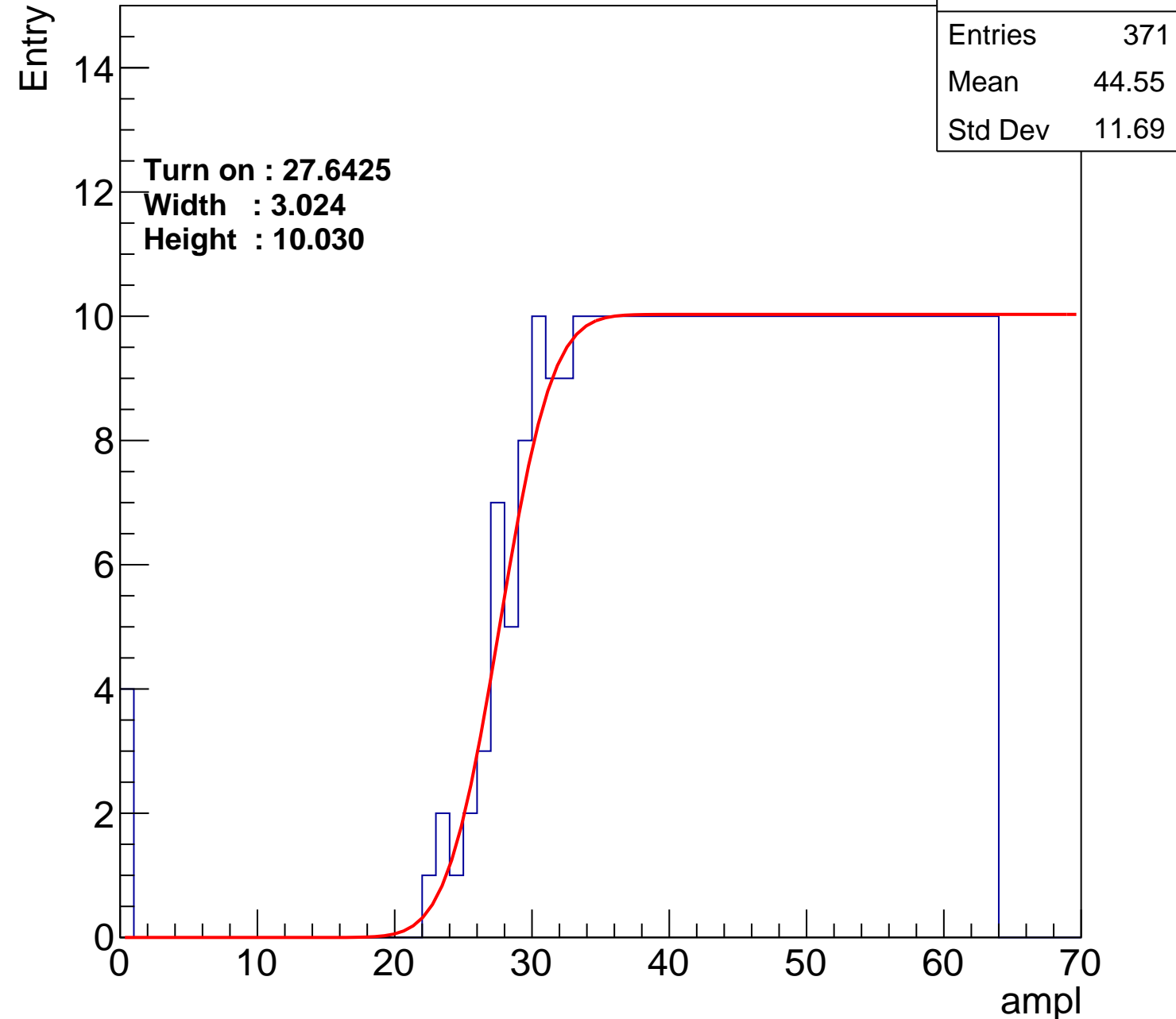
Width : 3.024

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch50

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 26.7905

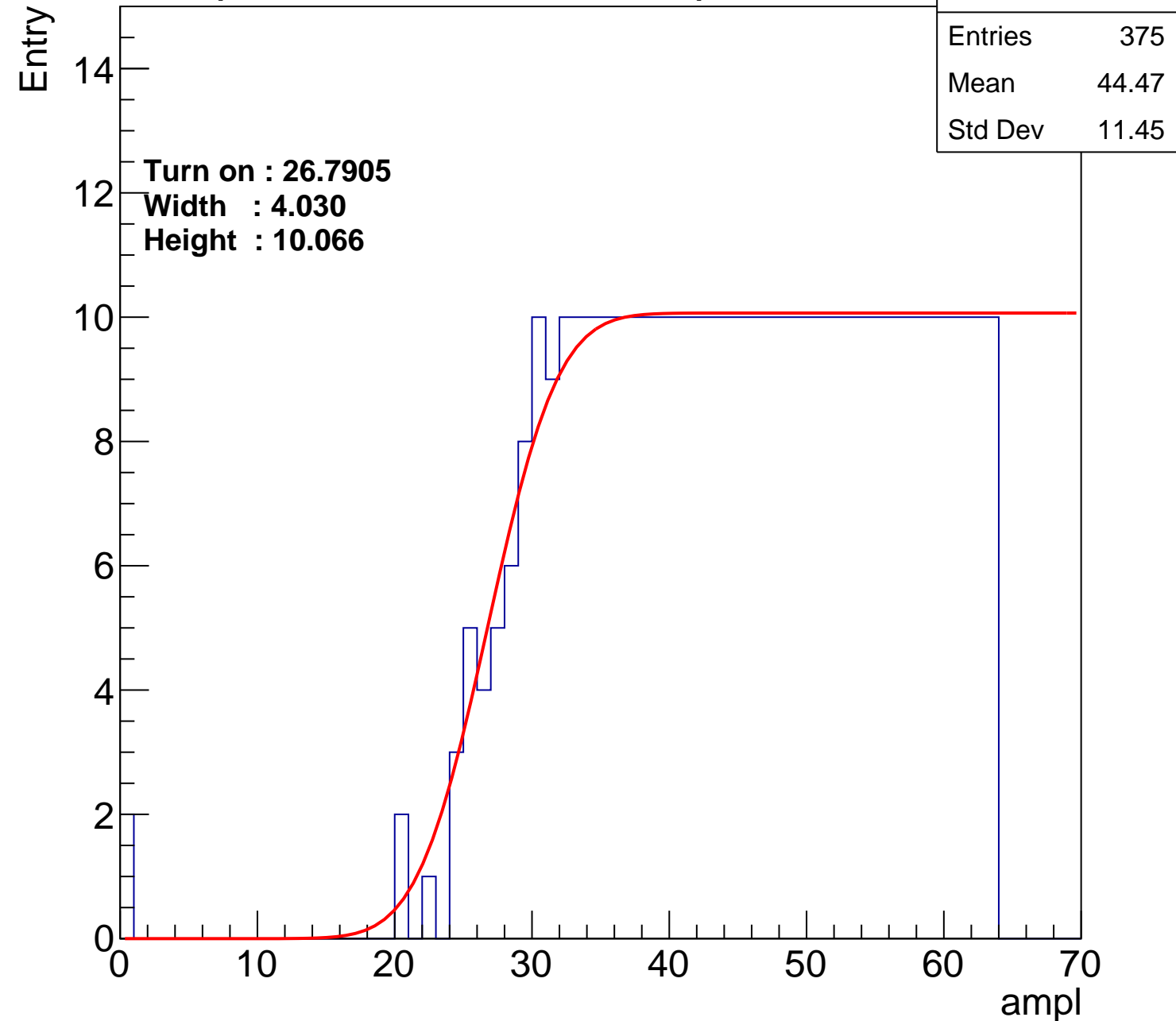
Width : 4.030

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch51

calib_packv5_042523_0143.root, FC#7, port C2

Entries	353
Mean	45.56
Std Dev	10.87

Turn on : 28.9608

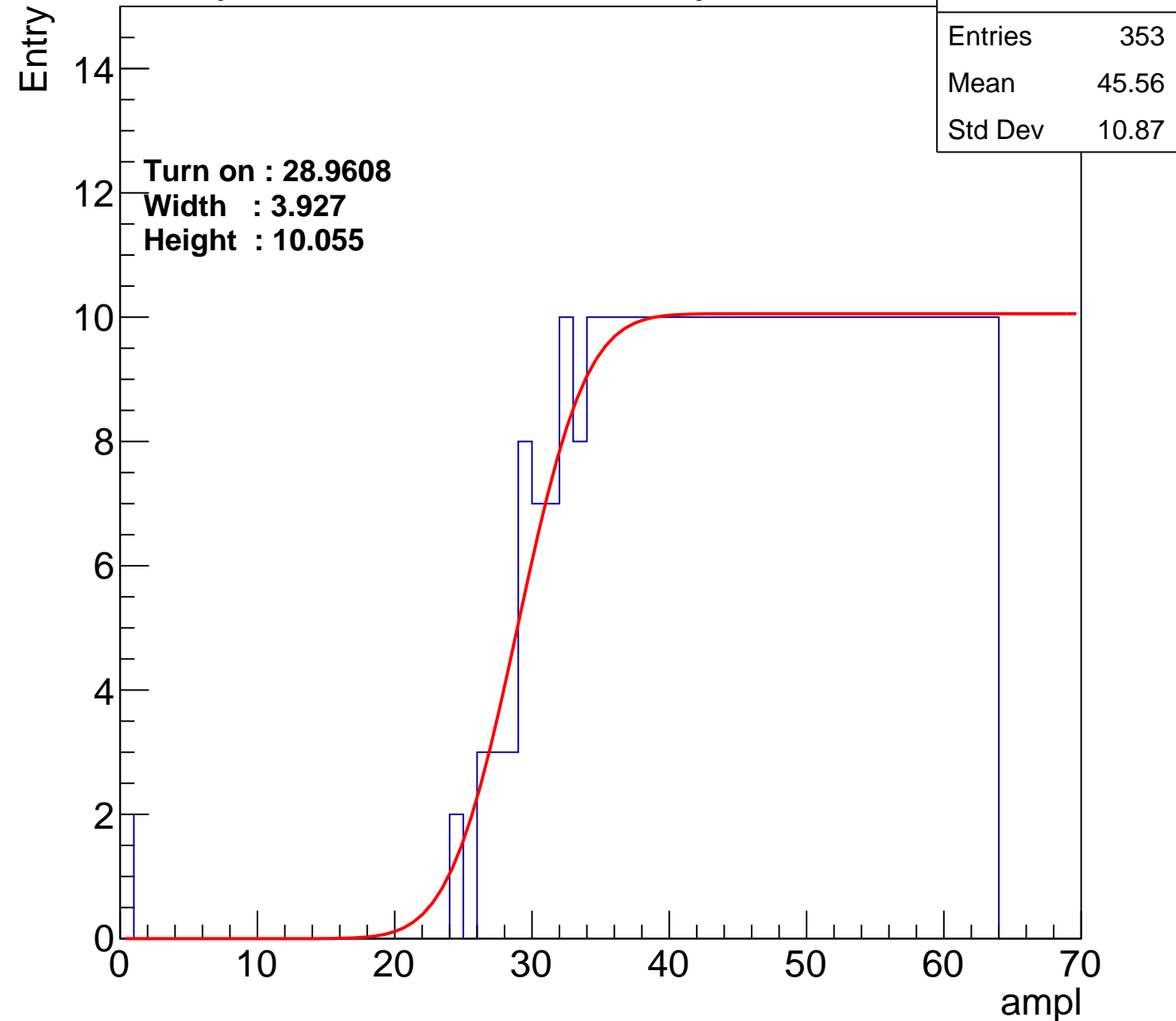
Width : 3.927

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch52

calib_packv5_042523_0143.root, FC#7, port C2

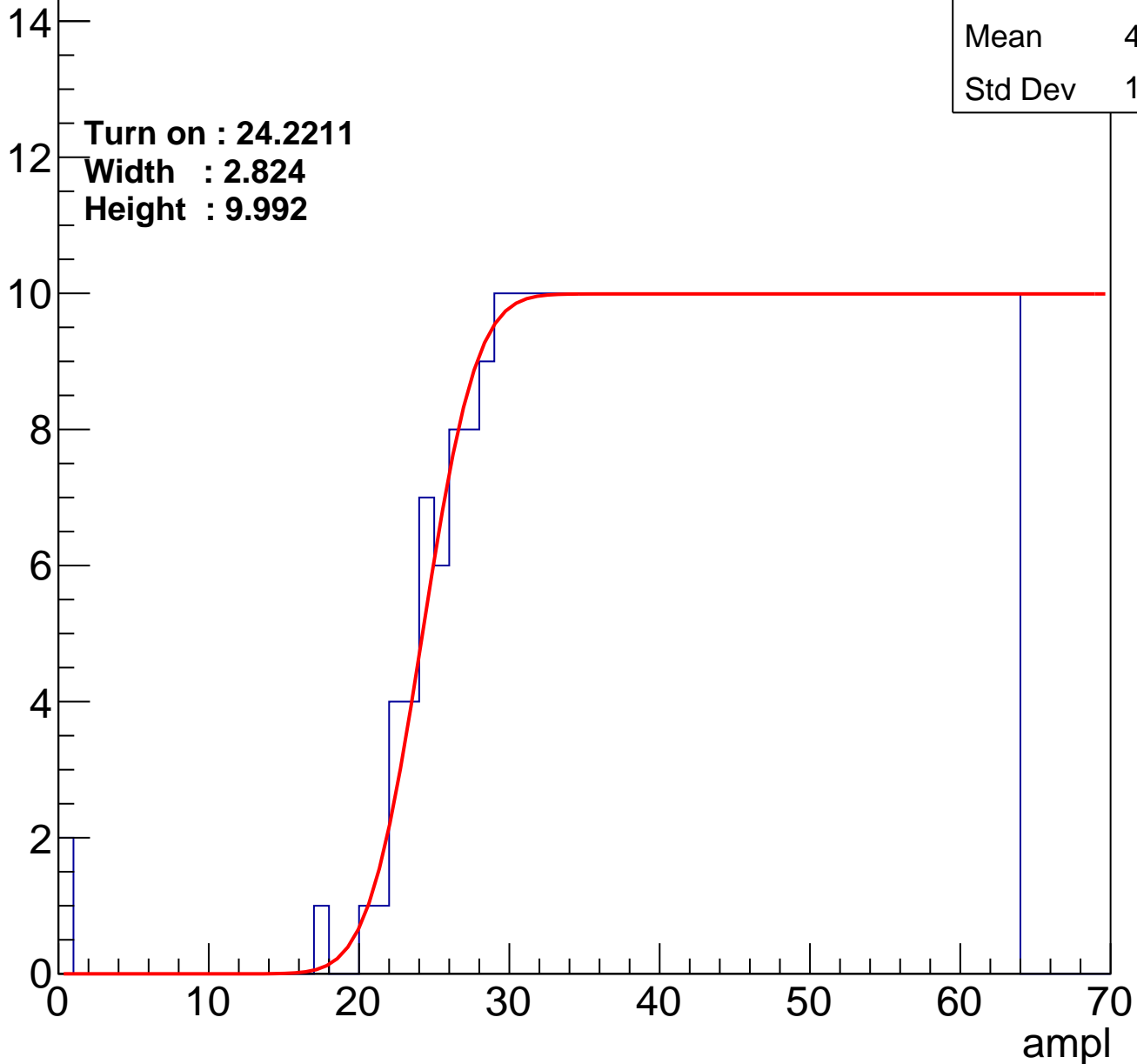
Entries	401
Mean	43.22
Std Dev	12.07

Turn on : 24.2211

Width : 2.824

Height : 9.992

Entry



B1L103S, U25-ch53

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.9382

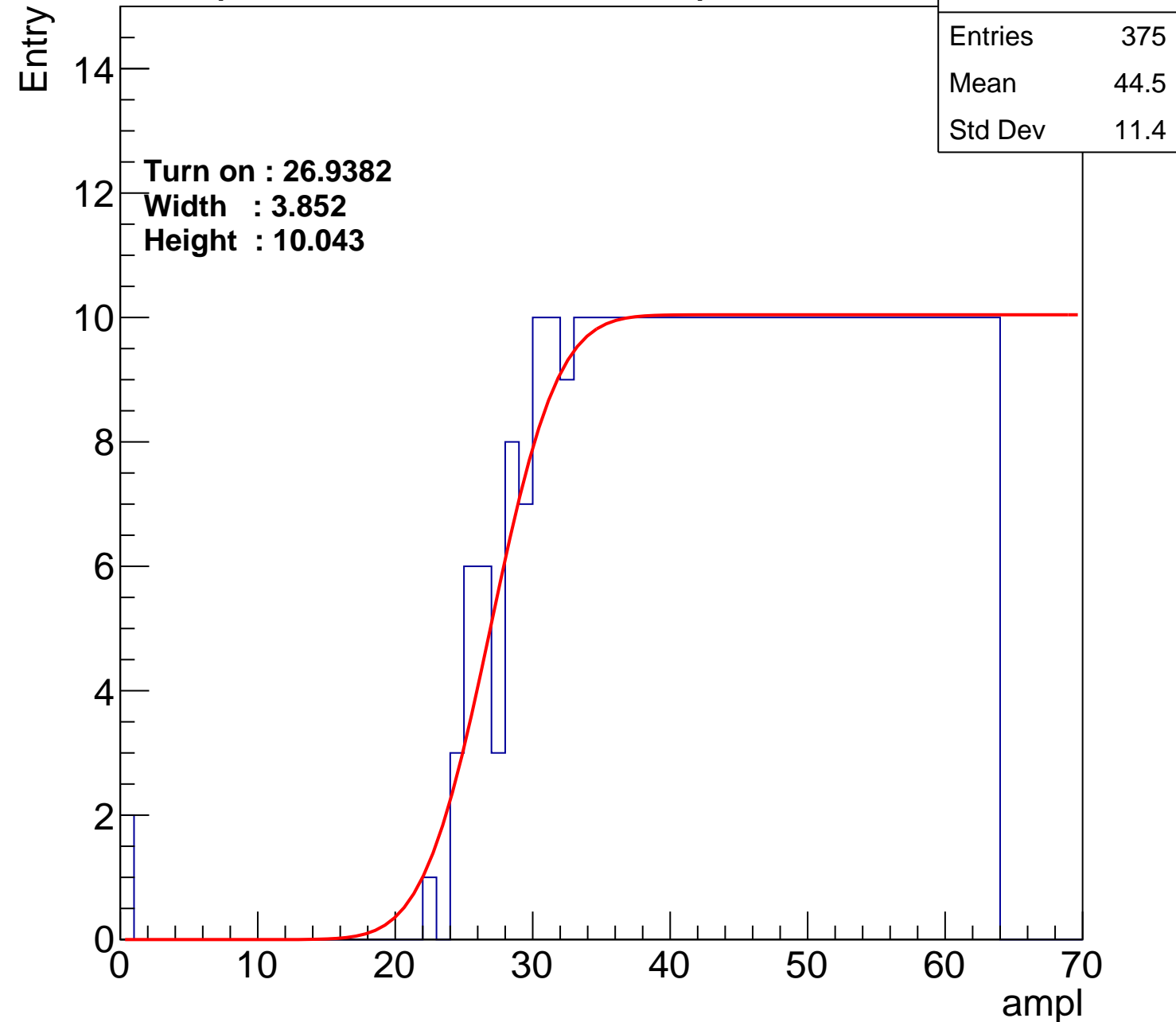
Width : 3.852

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	43.98
Std Dev	12.07

Turn on : 26.6633

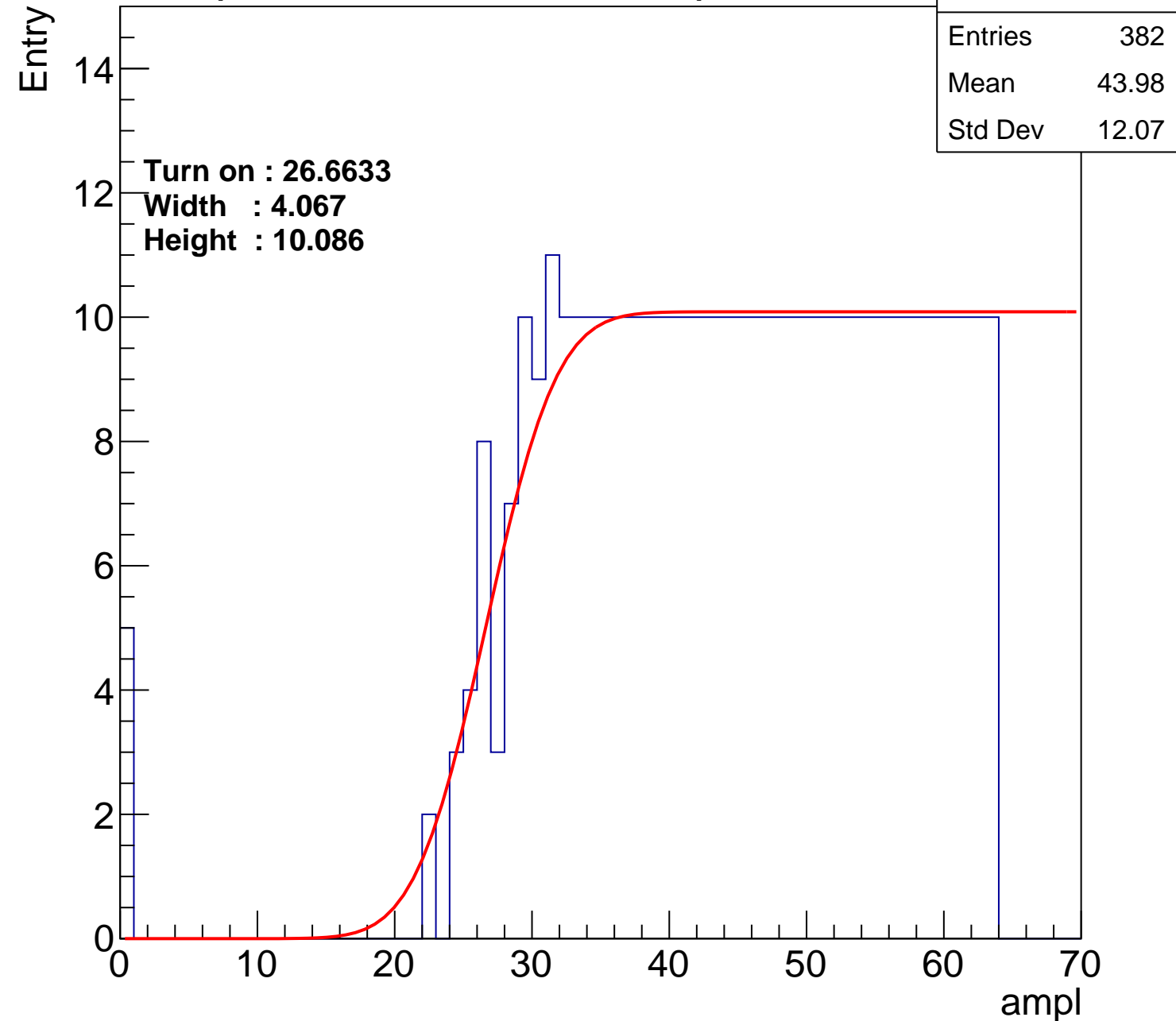
Width : 4.067

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch55

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.99
Std Dev	11.15

Turn on : 27.8401

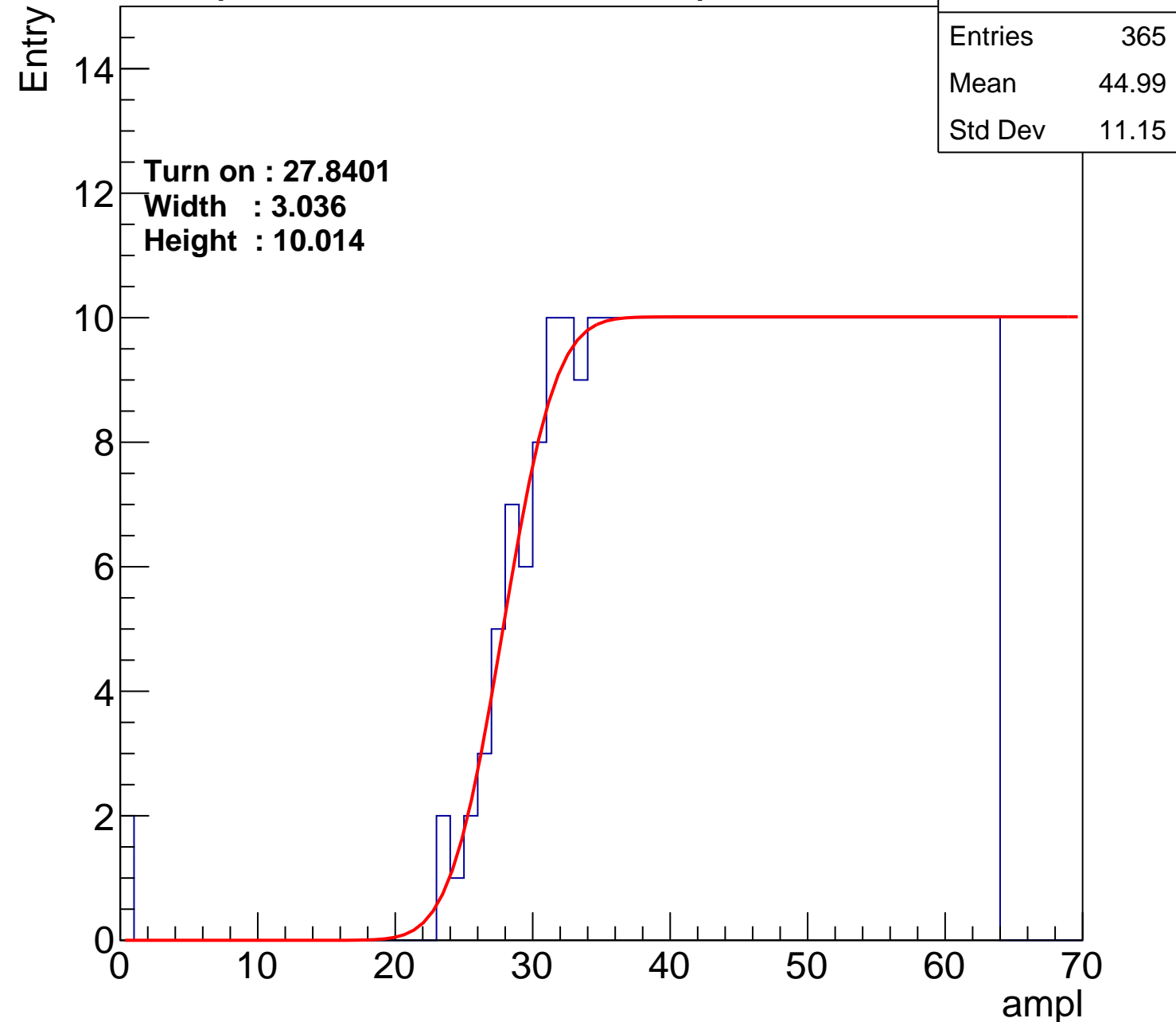
Width : 3.036

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch56

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.06
Std Dev	11.73

Turn on : 26.8830

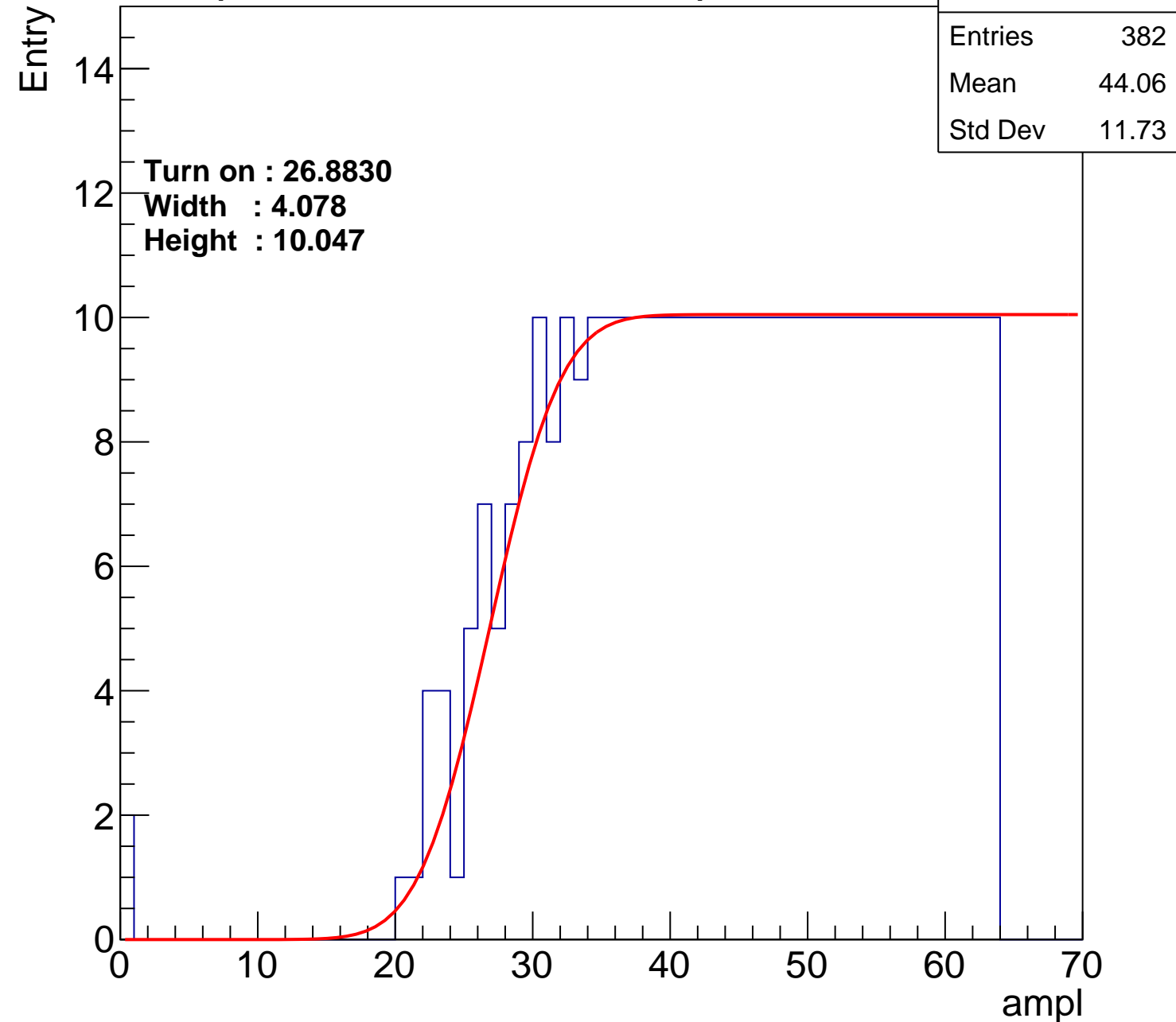
Width : 4.078

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch57

calib_packv5_042523_0143.root, FC#7, port C2

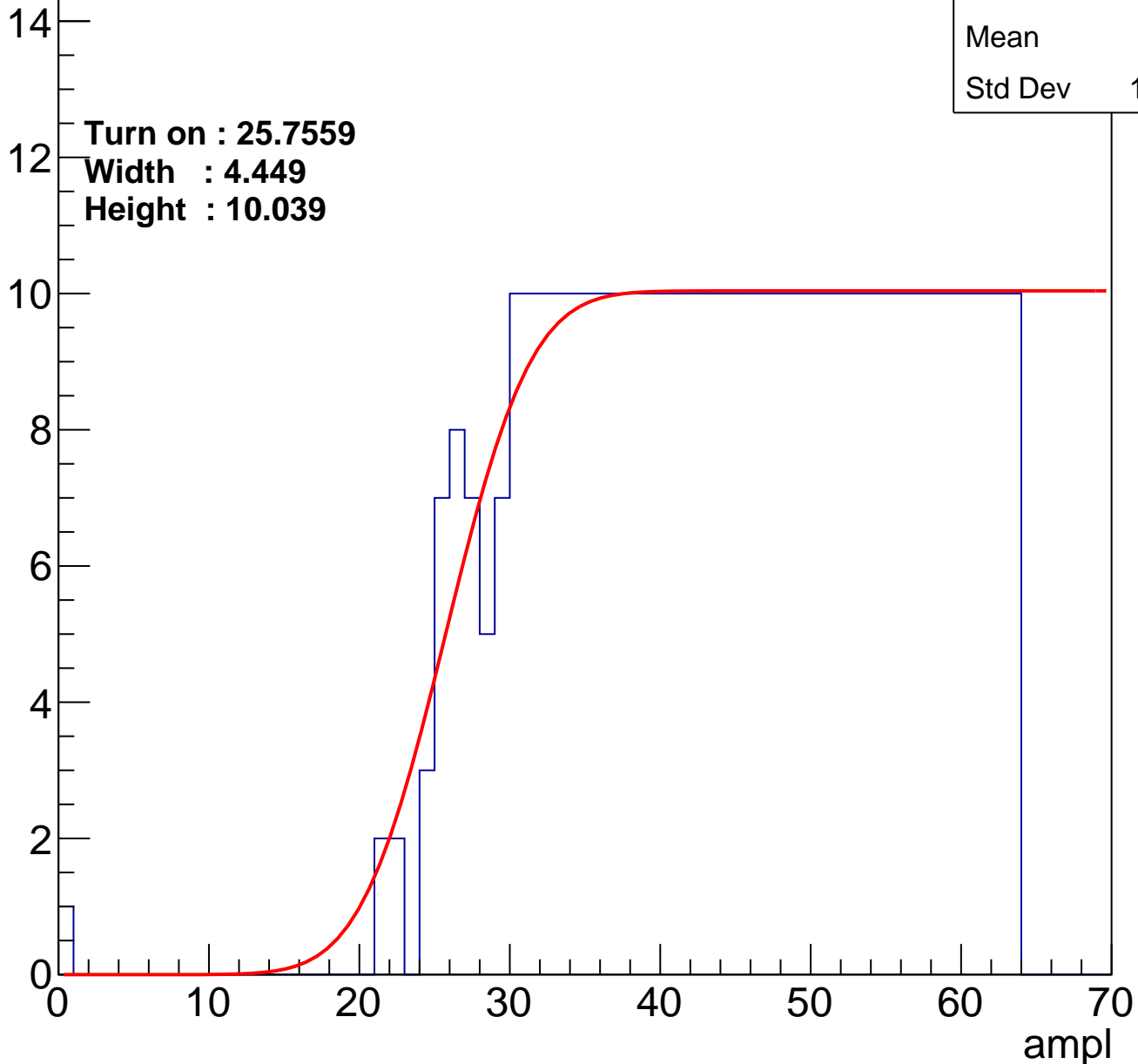
Entries	382
Mean	44.2
Std Dev	11.44

Turn on : 25.7559

Width : 4.449

Height : 10.039

Entry



B1L103S, U25-ch58

calib_packv5_042523_0143.root, FC#7, port C2

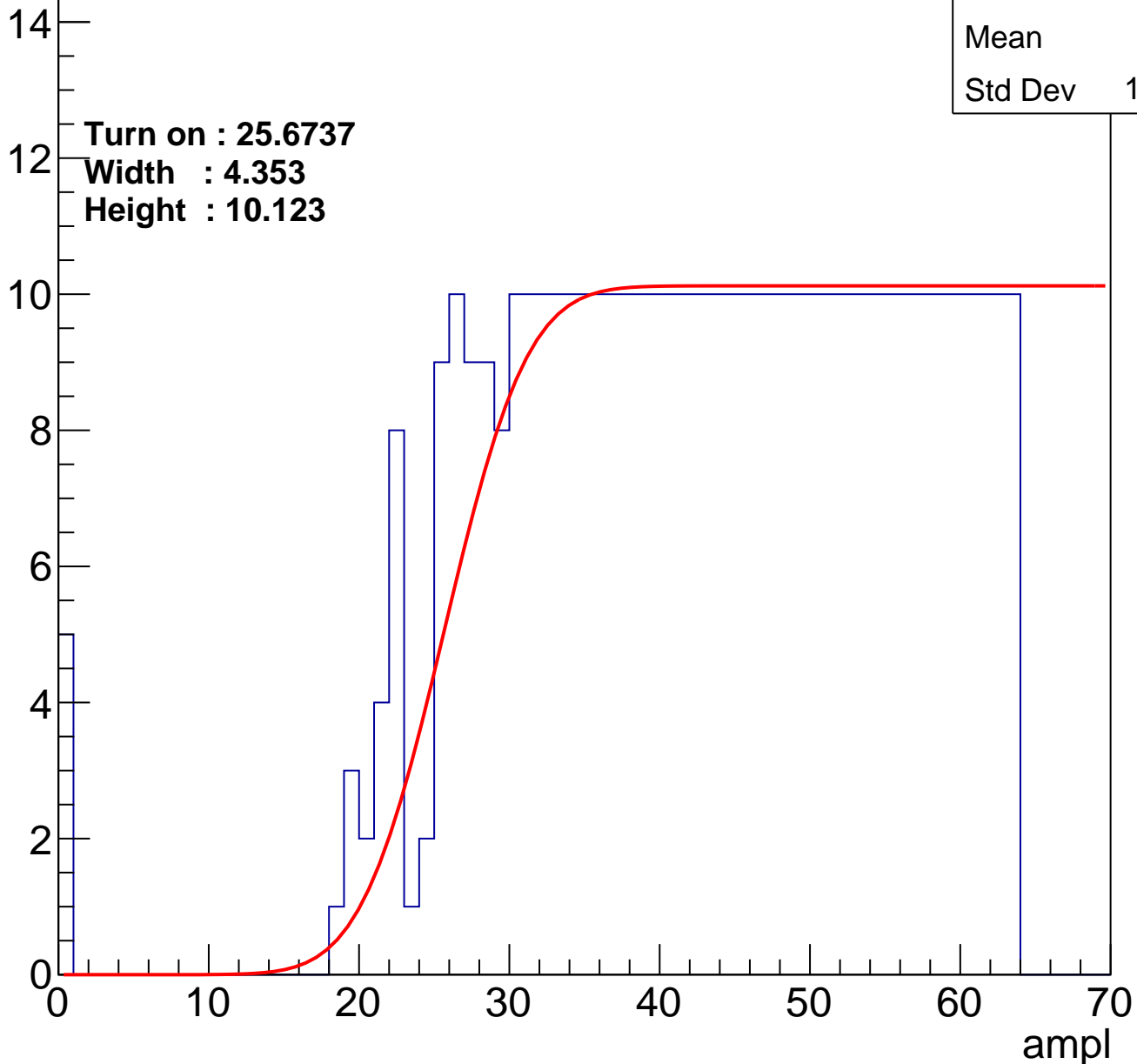
Entries	411
Mean	42.5
Std Dev	12.84

Turn on : 25.6737

Width : 4.353

Height : 10.123

Entry



B1L103S, U25-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.72
Std Dev	11.5

Turn on : 27.7941

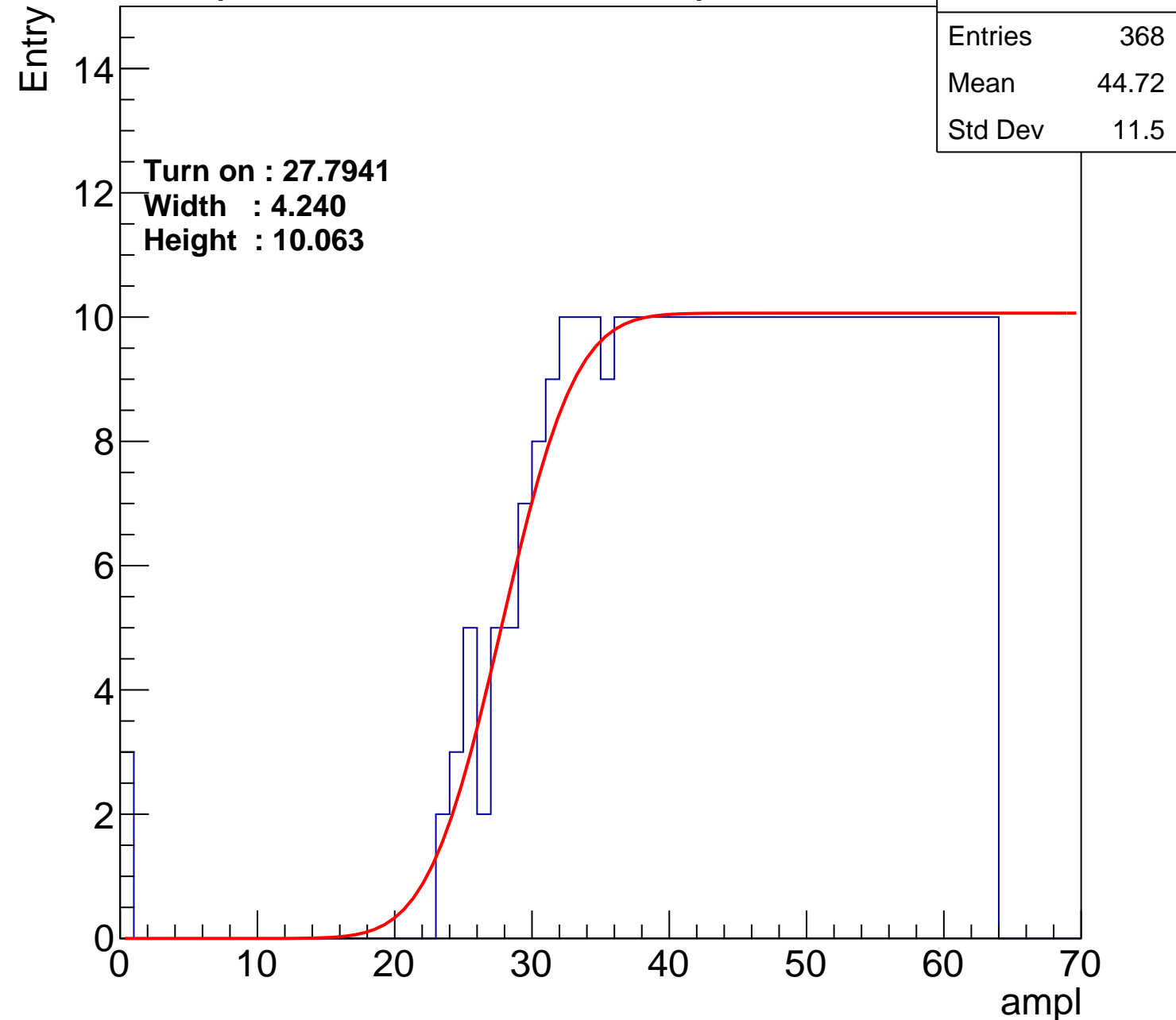
Width : 4.240

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch60

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.45
Std Dev	12.11

Turn on : 26.0489

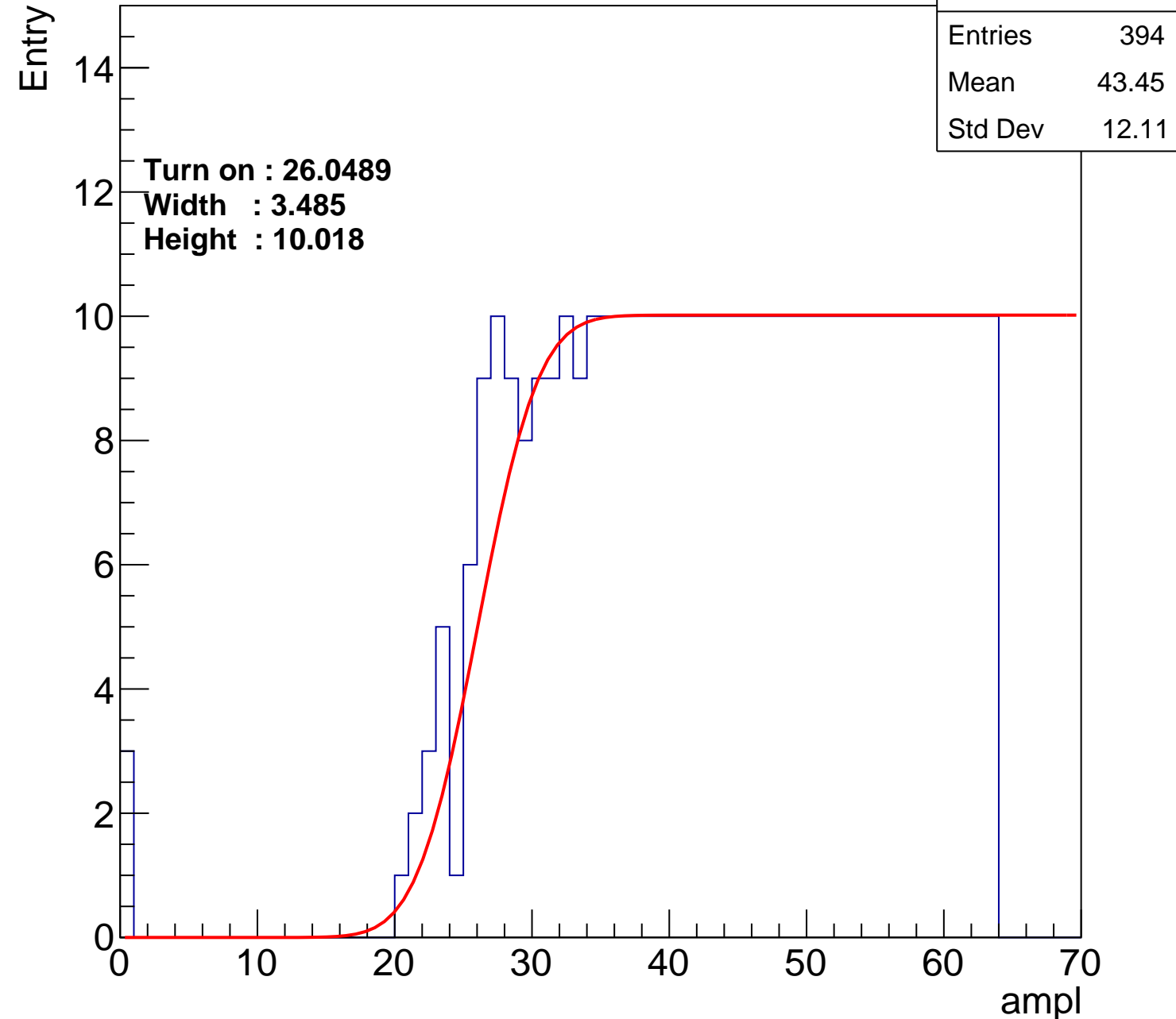
Width : 3.485

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch61

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.81
Std Dev	11.46

Turn on : 28.1713

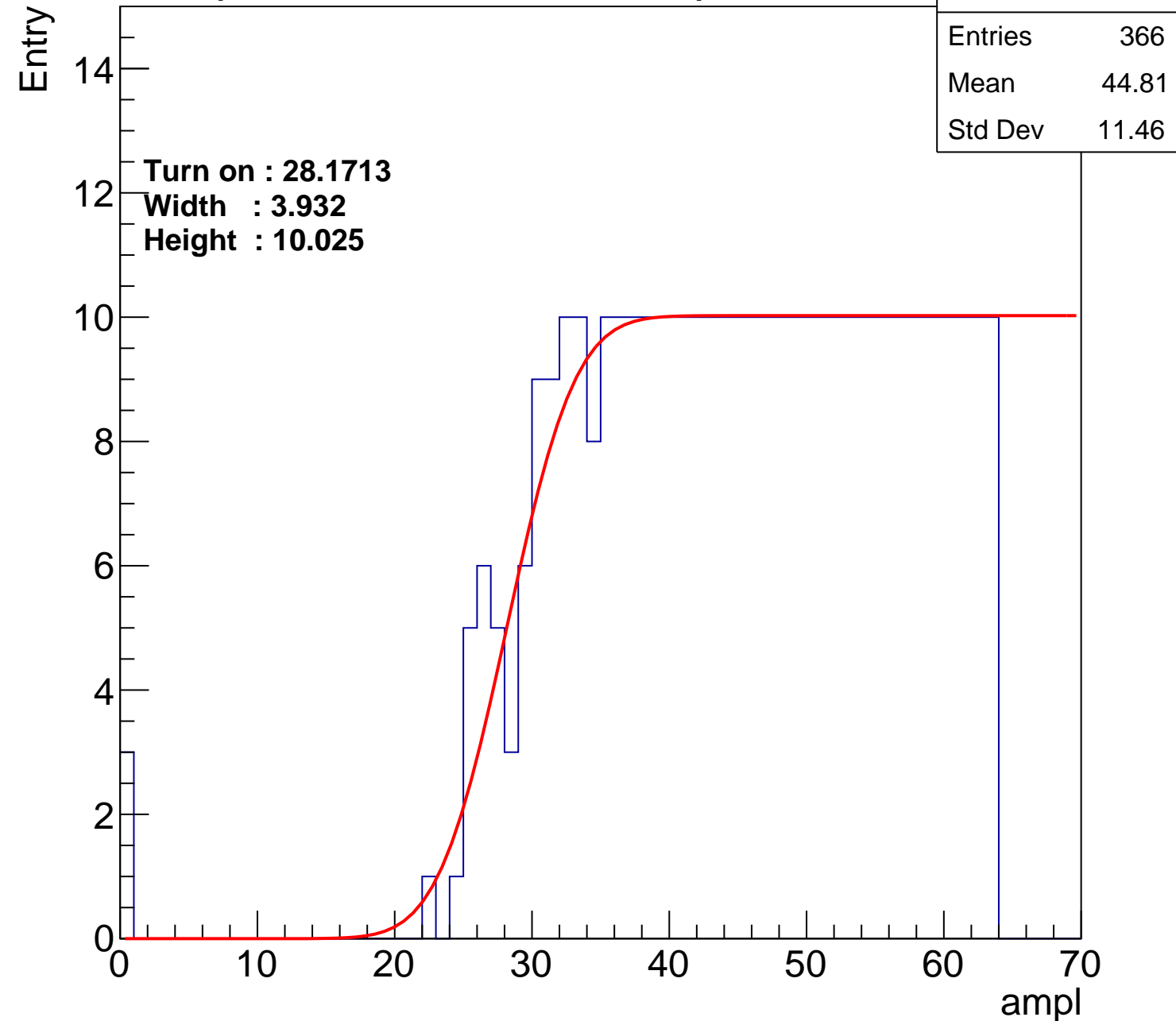
Width : 3.932

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch62

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.43
Std Dev	11.52

Turn on : 26.4467

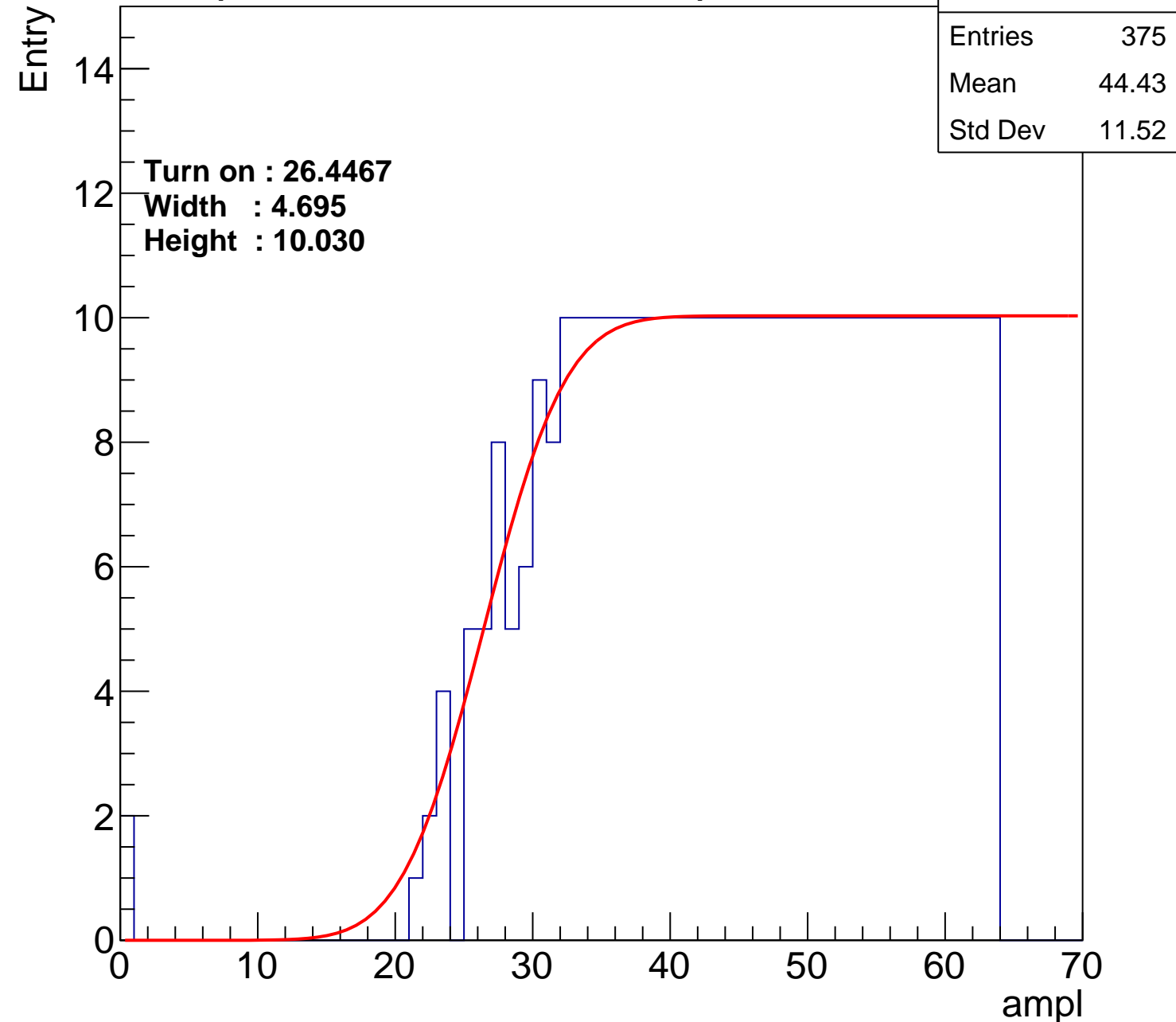
Width : 4.695

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch63

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 27.0424

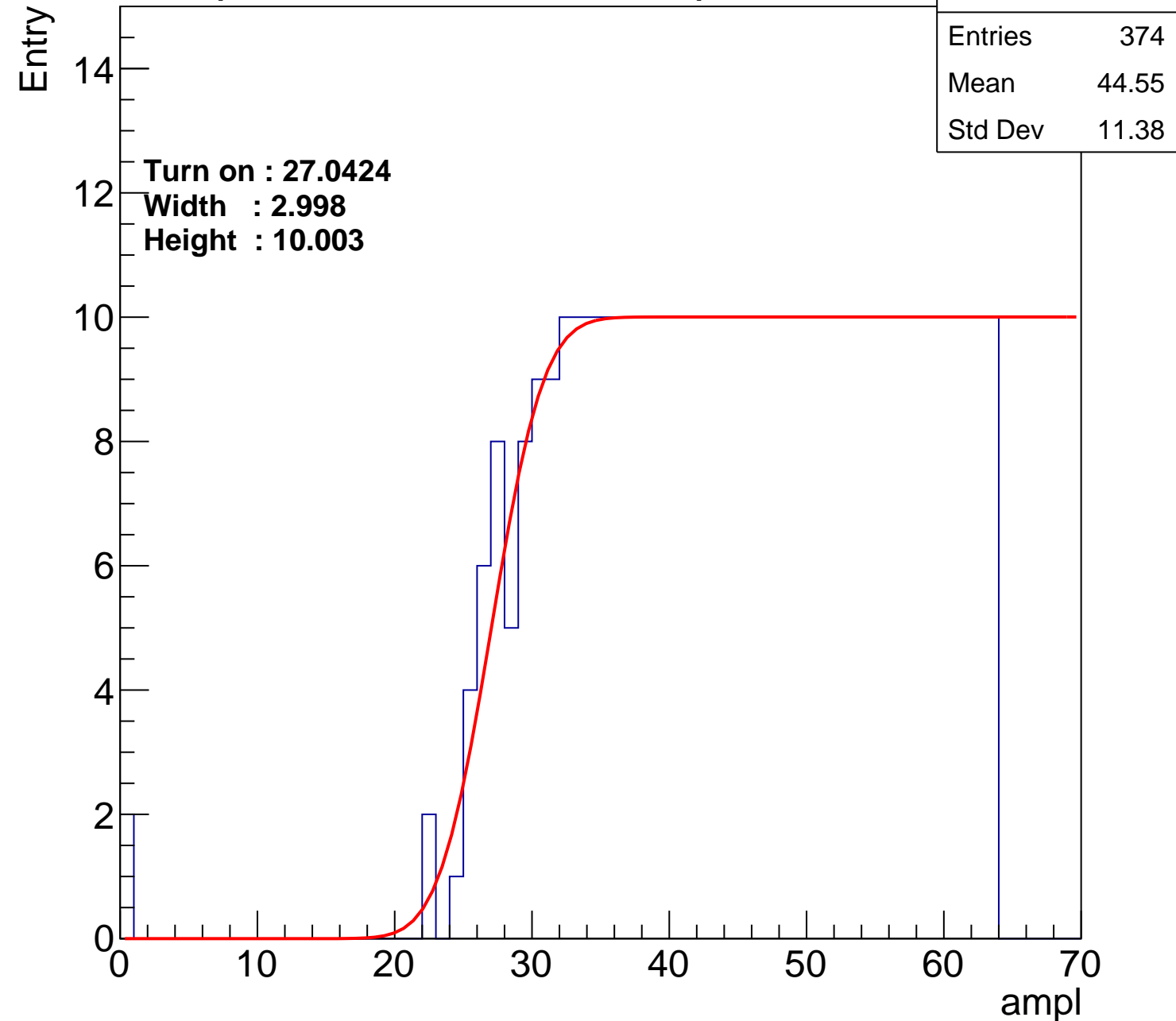
Width : 2.998

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch64

calib_packv5_042523_0143.root, FC#7, port C2

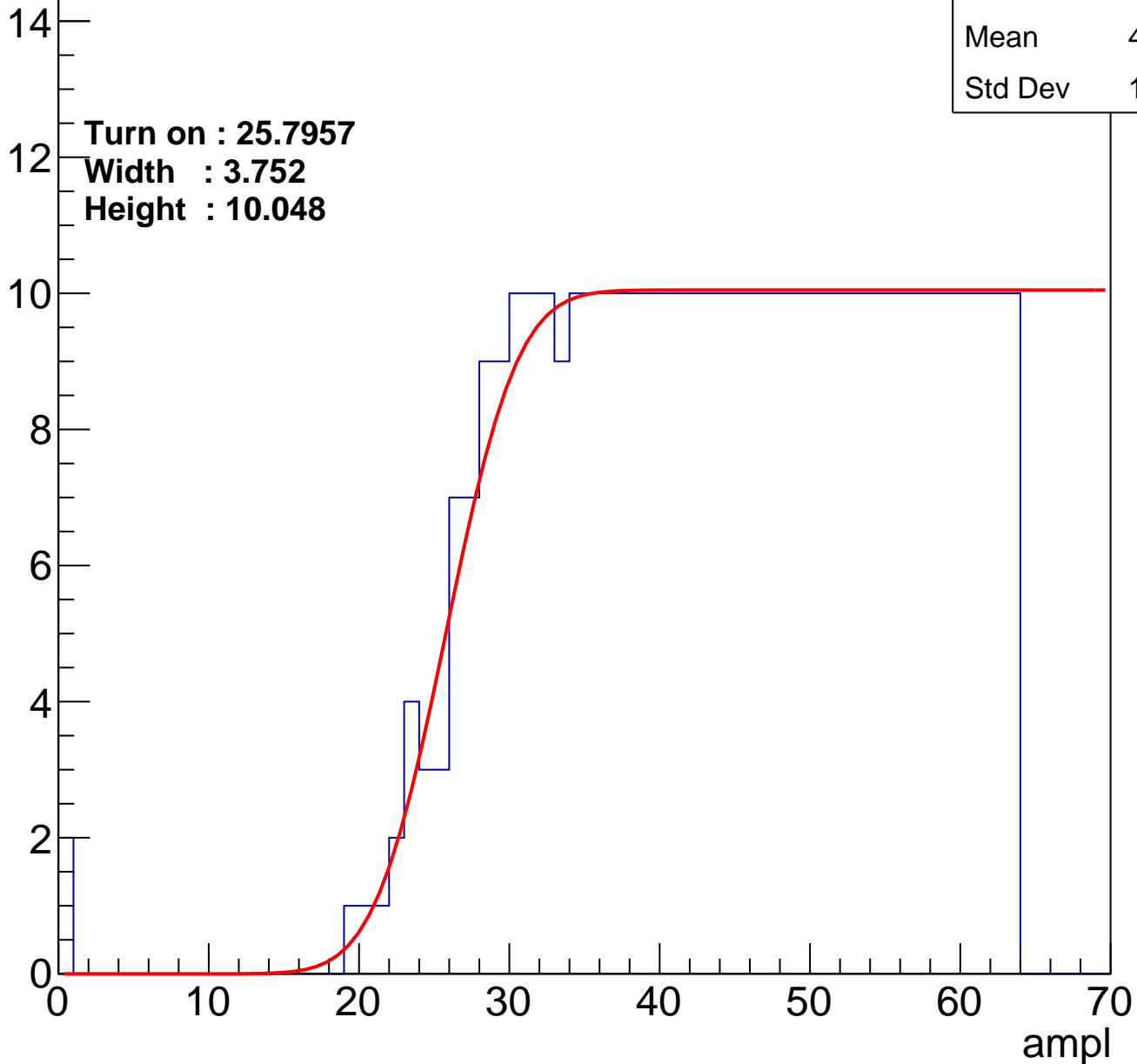
Entries	388
Mean	43.82
Std Dev	11.79

Turn on : 25.7957

Width : 3.752

Height : 10.048

Entry



B1L103S, U25-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	349
Mean	45.73
Std Dev	10.83

Turn on : 30.1830

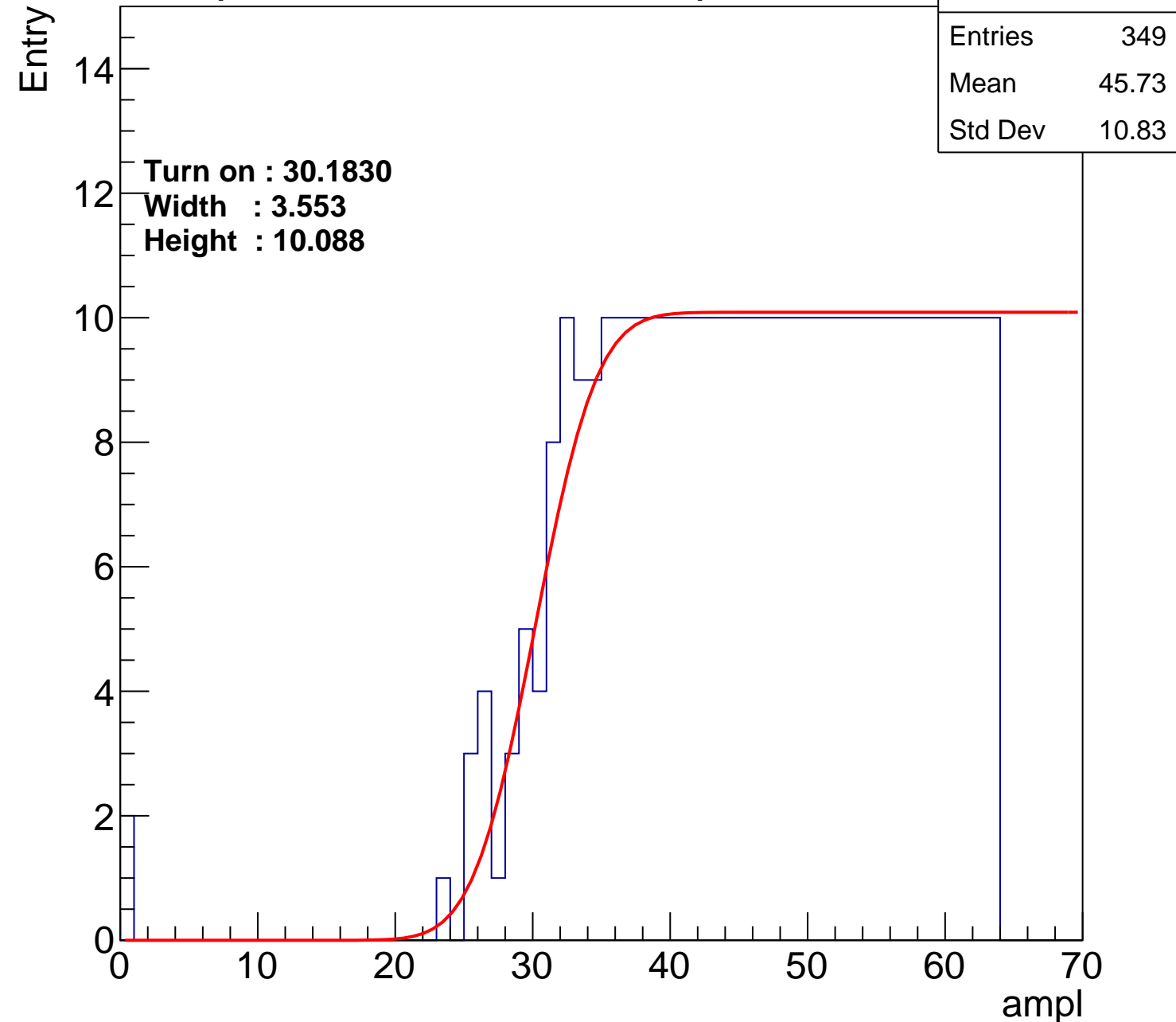
Width : 3.553

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch66

calib_packv5_042523_0143.root, FC#7, port C2

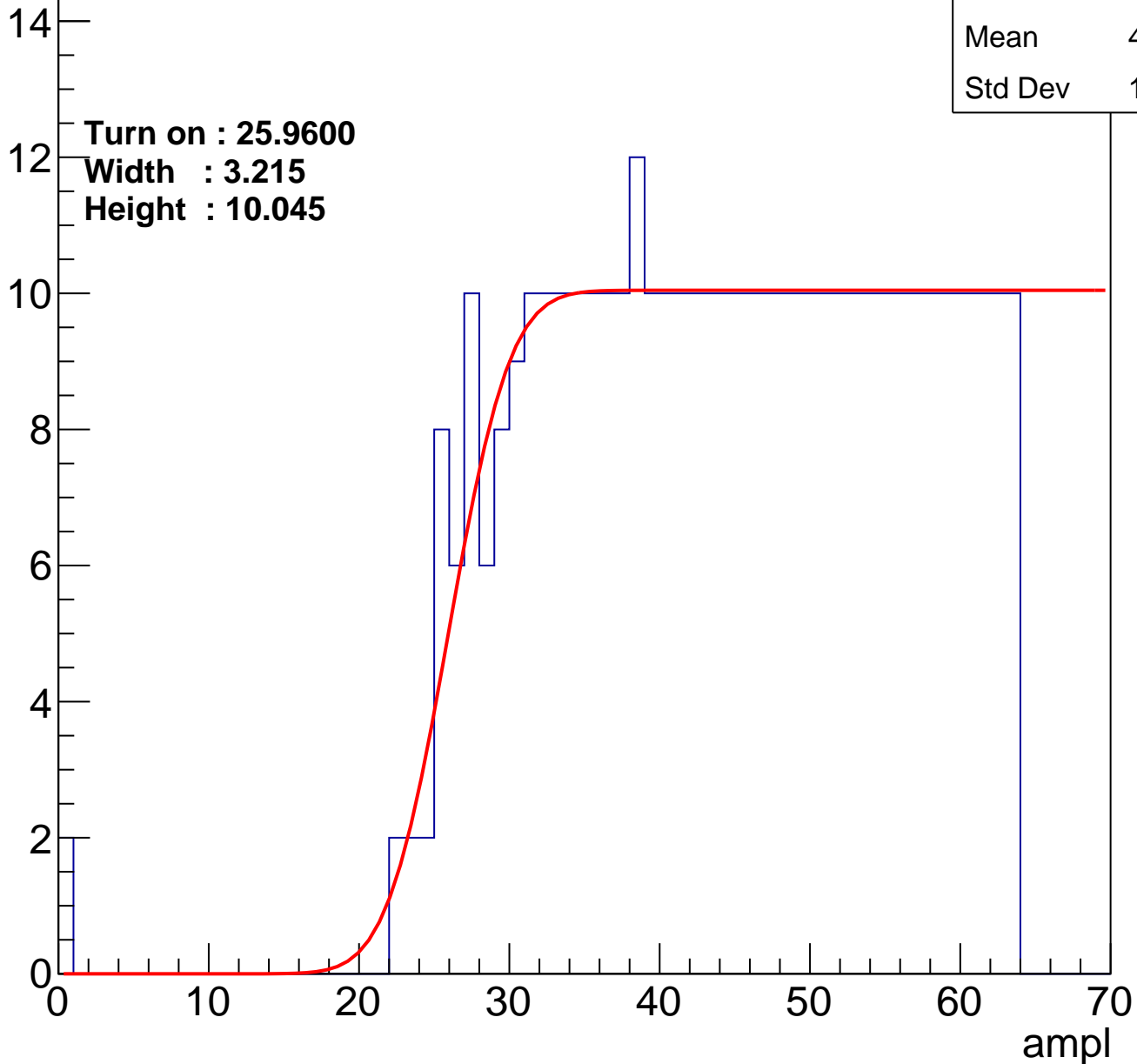
Entries	387
Mean	43.98
Std Dev	11.63

Turn on : 25.9600

Width : 3.215

Height : 10.045

Entry



B1L103S, U25-ch67

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.79
Std Dev	11.3

Turn on : 28.2799

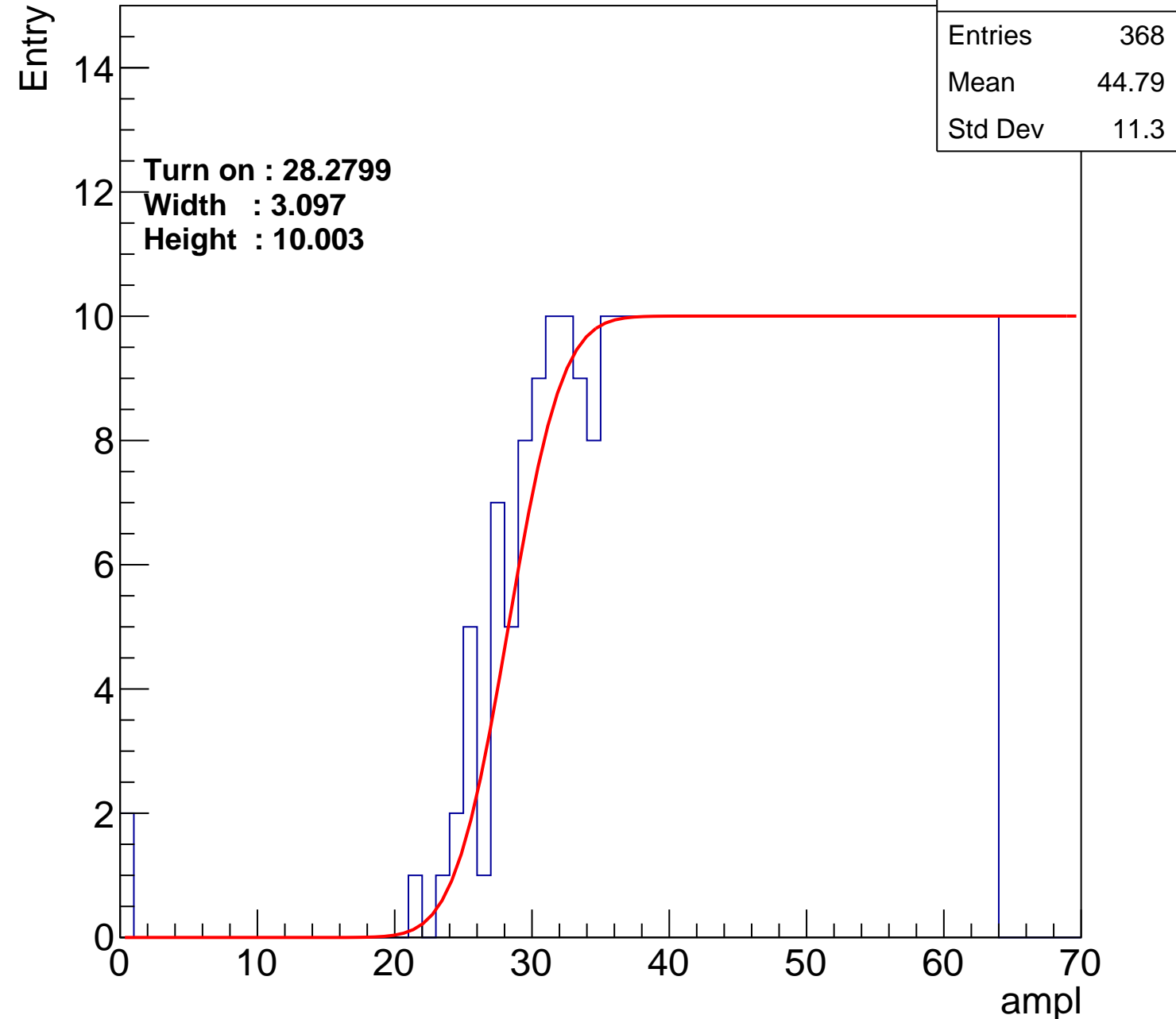
Width : 3.097

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch68

calib_packv5_042523_0143.root, FC#7, port C2

Entries	404
Mean	43.03
Std Dev	12.22

Turn on : 24.0983

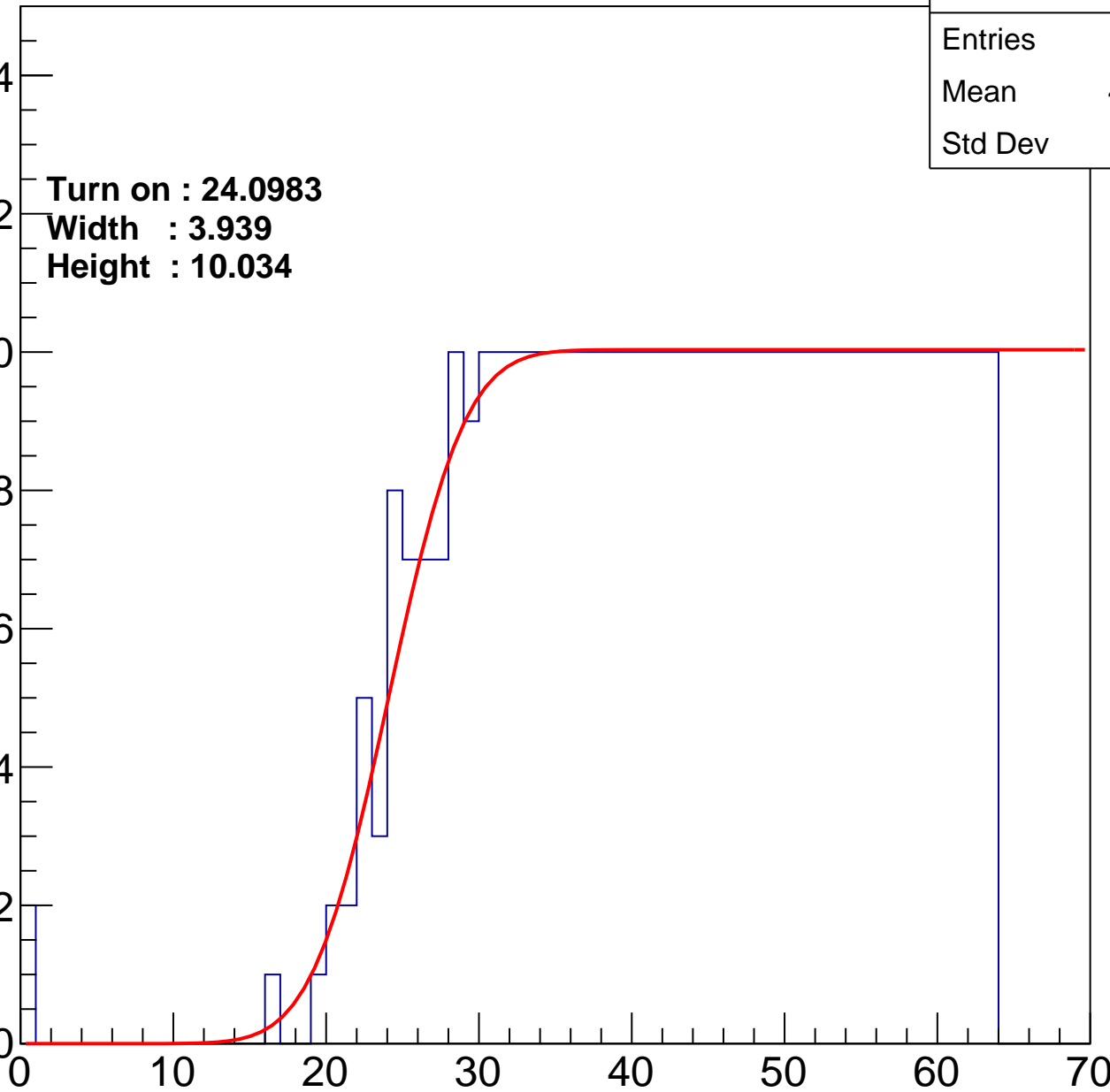
Width : 3.939

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch69

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.17
Std Dev	11.78

Turn on : 26.8831

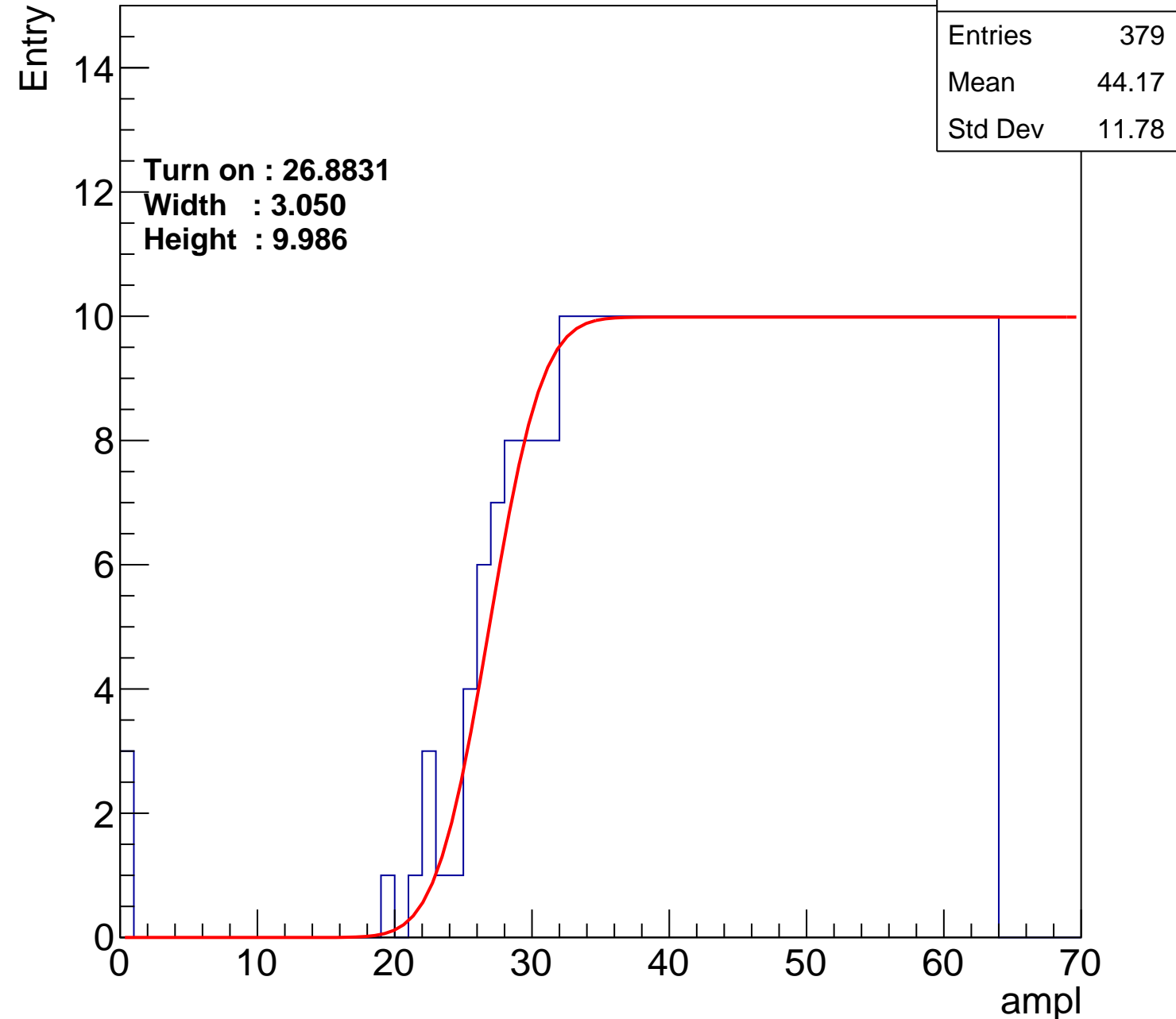
Width : 3.050

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch70

calib_packv5_042523_0143.root, FC#7, port C2

Entries	400
Mean	43.24
Std Dev	12.09

Turn on : 24.6226

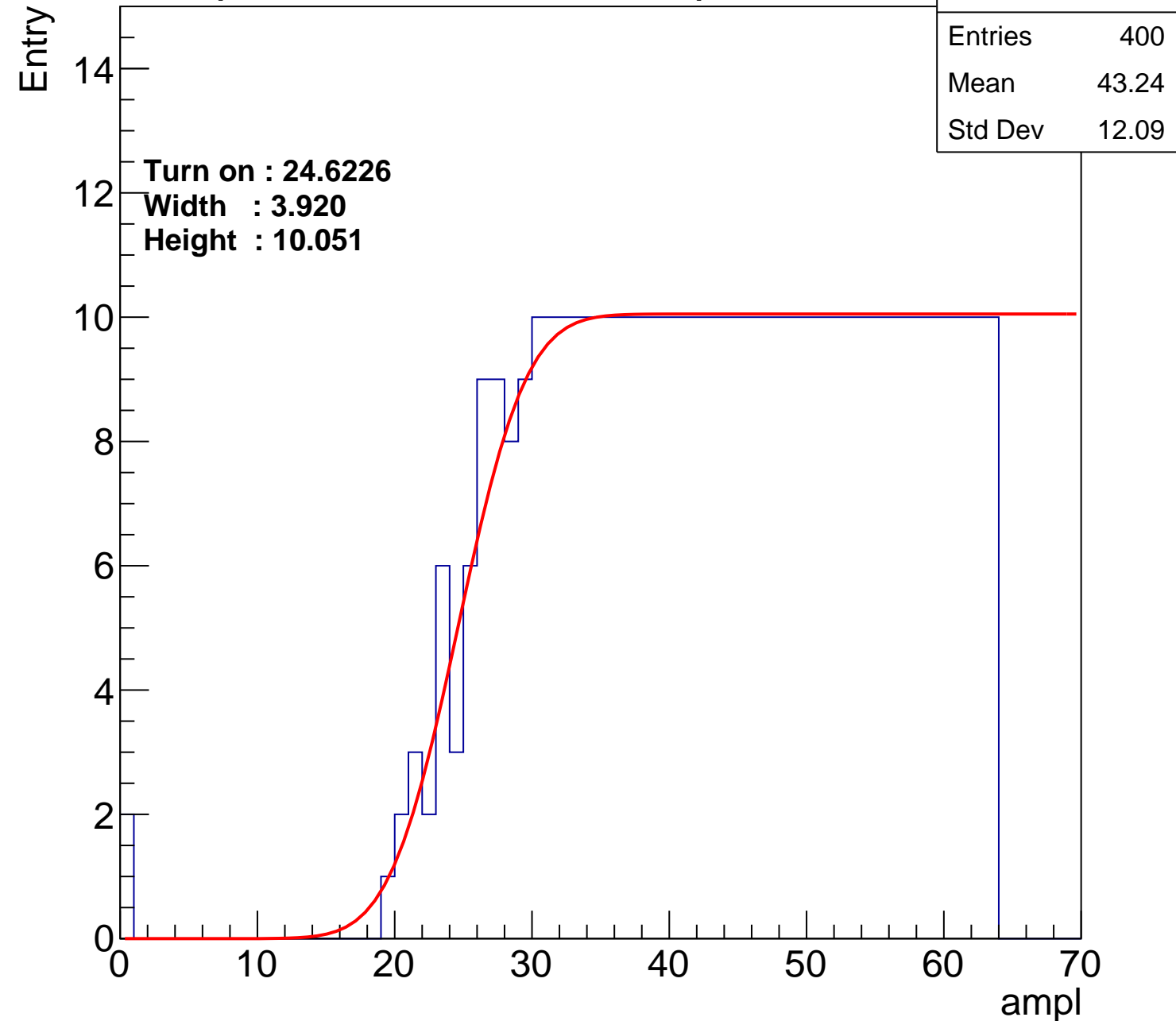
Width : 3.920

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch71

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.85
Std Dev	11.63

Turn on : 25.1488

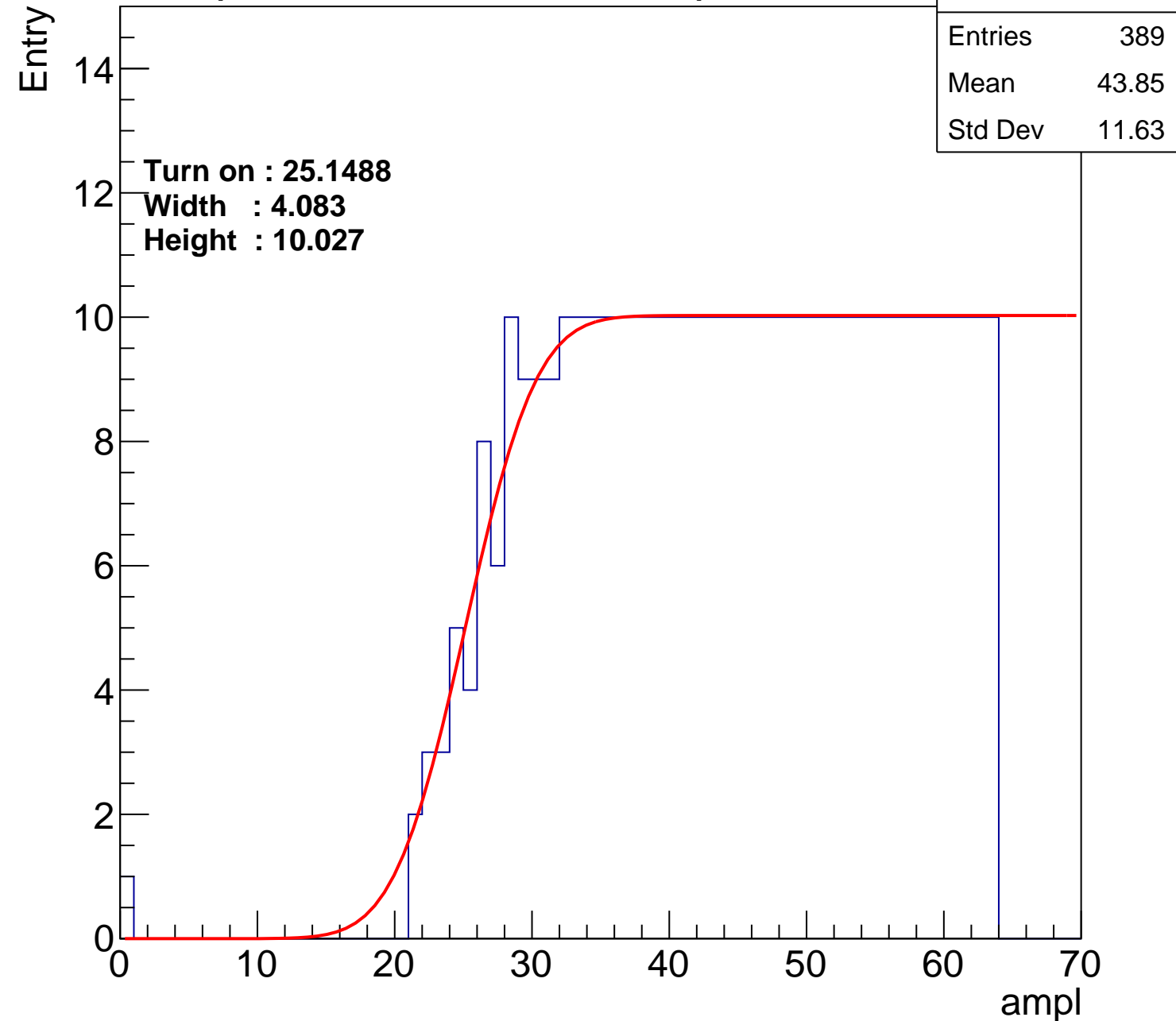
Width : 4.083

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch72

calib_packv5_042523_0143.root, FC#7, port C2

Entries	404
Mean	42.97
Std Dev	12.43

Turn on : 23.8376

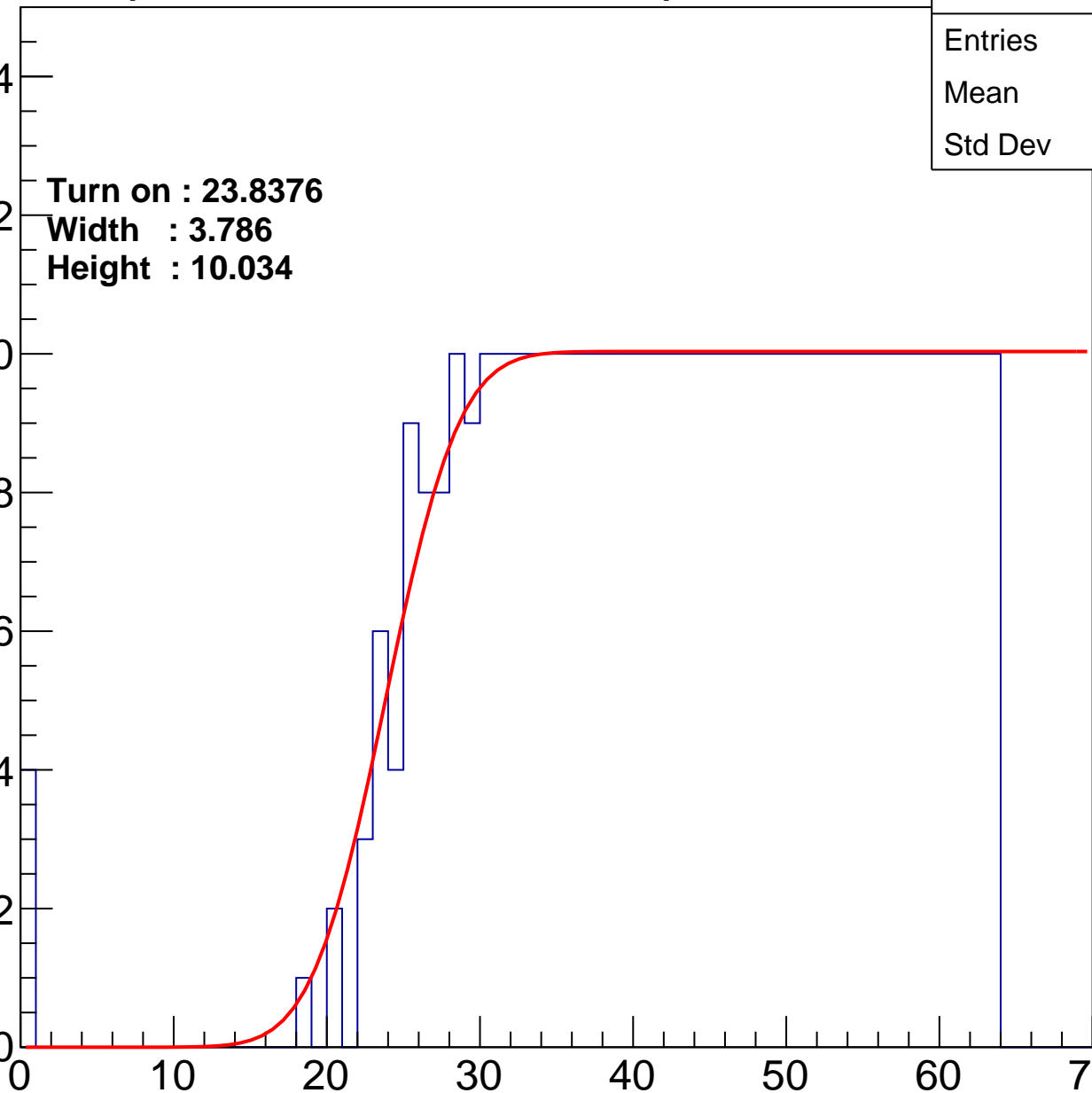
Width : 3.786

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch73

calib_packv5_042523_0143.root, FC#7, port C2

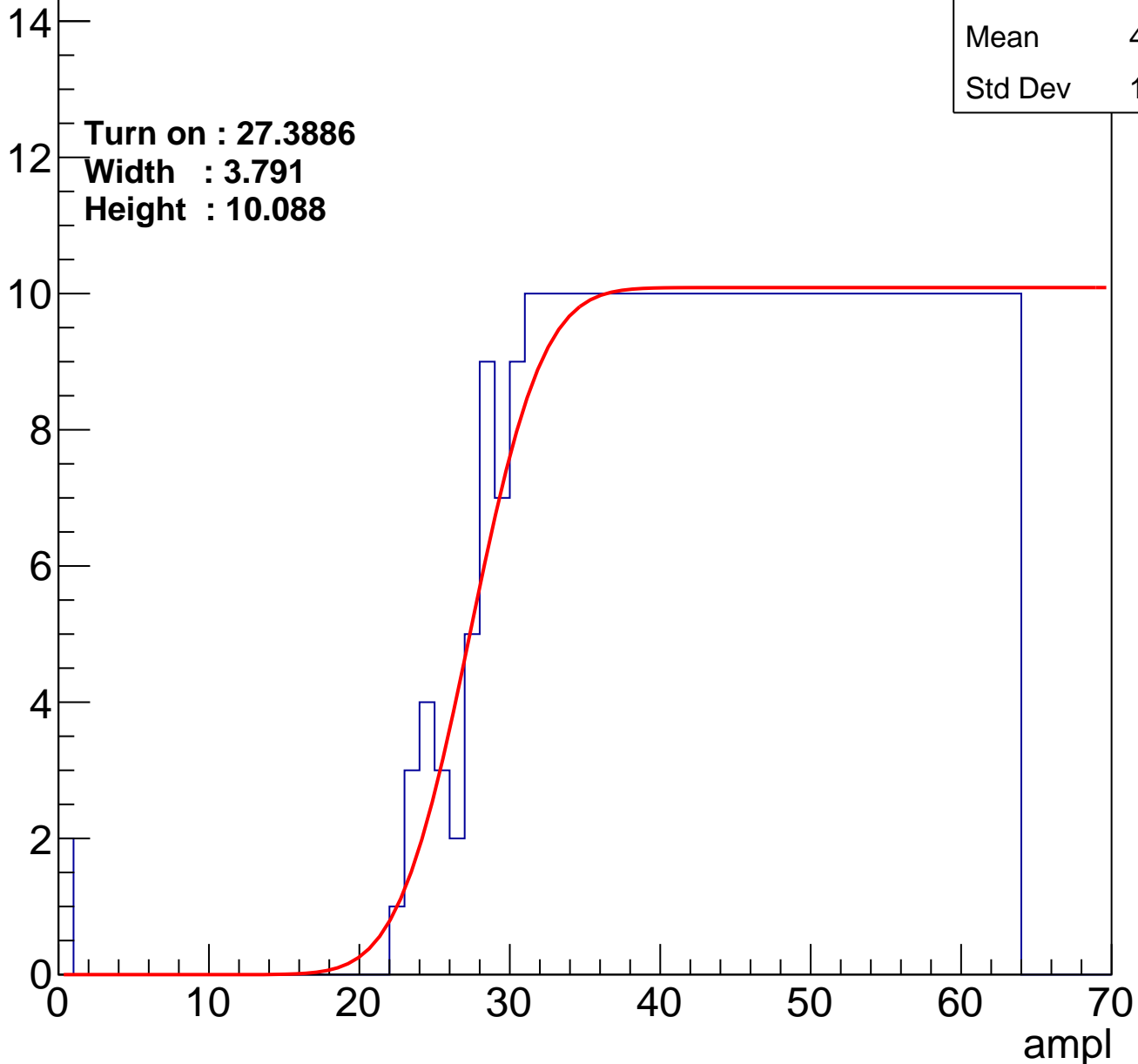
Entries	375
Mean	44.49
Std Dev	11.42

Turn on : 27.3886

Width : 3.791

Height : 10.088

Entry



B1L103S, U25-ch74

calib_packv5_042523_0143.root, FC#7, port C2

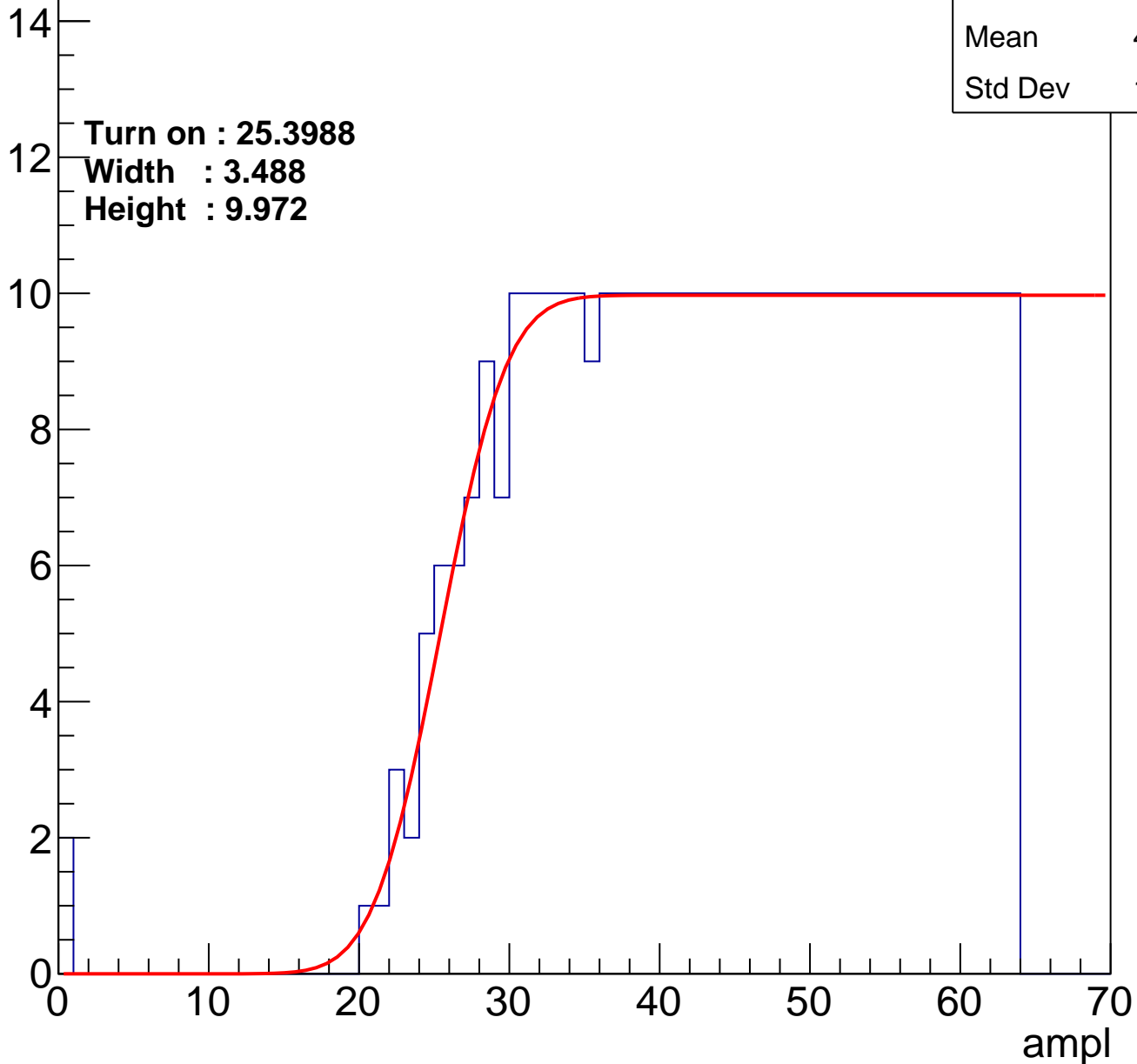
Entries	388
Mean	43.81
Std Dev	11.81

Turn on : 25.3988

Width : 3.488

Height : 9.972

Entry



B1L103S, U25-ch75

calib_packv5_042523_0143.root, FC#7, port C2

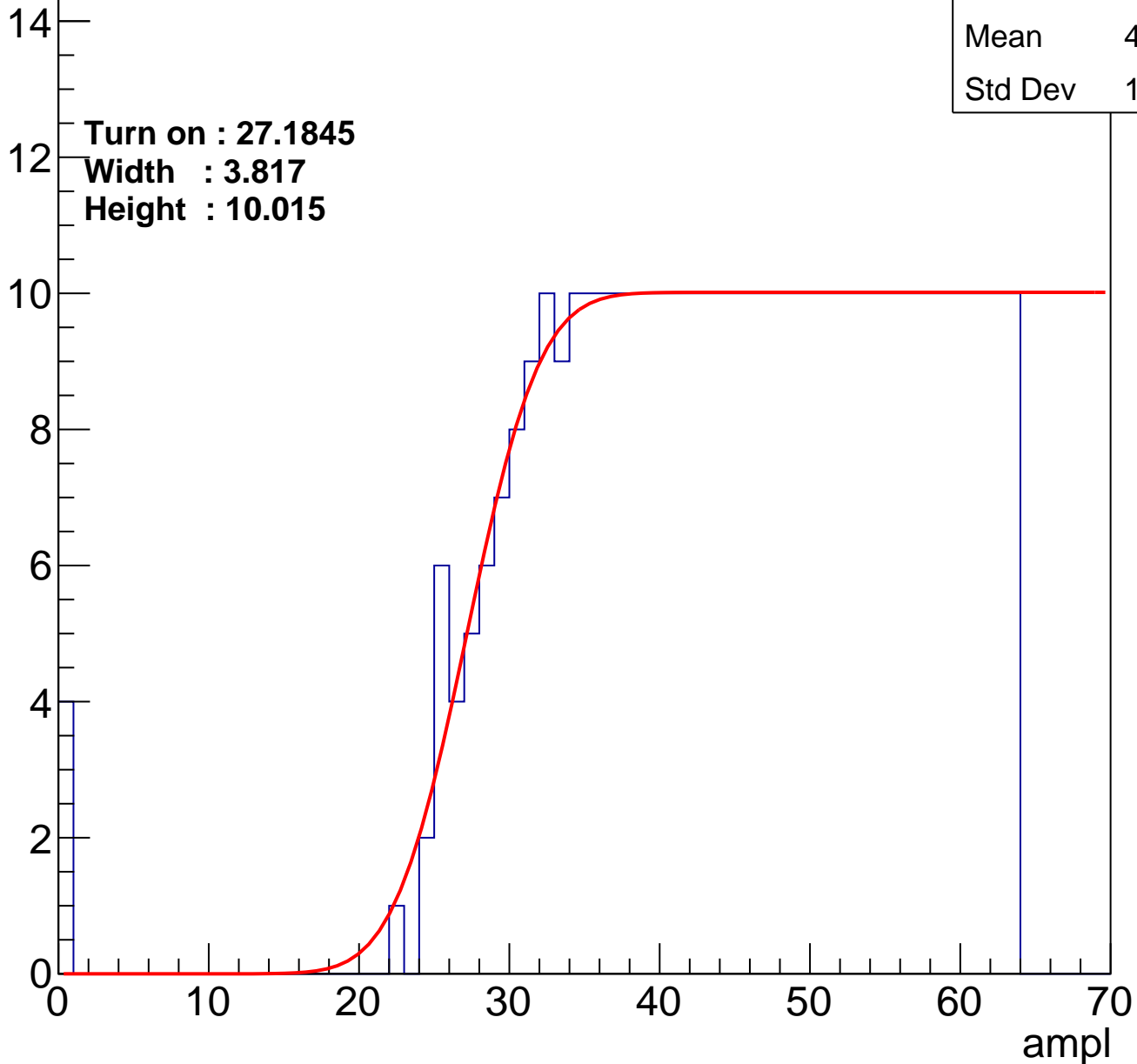
Entries	371
Mean	44.52
Std Dev	11.74

Turn on : 27.1845

Width : 3.817

Height : 10.015

Entry



B1L103S, U25-ch76

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.34
Std Dev	11.5

Turn on : 26.2797

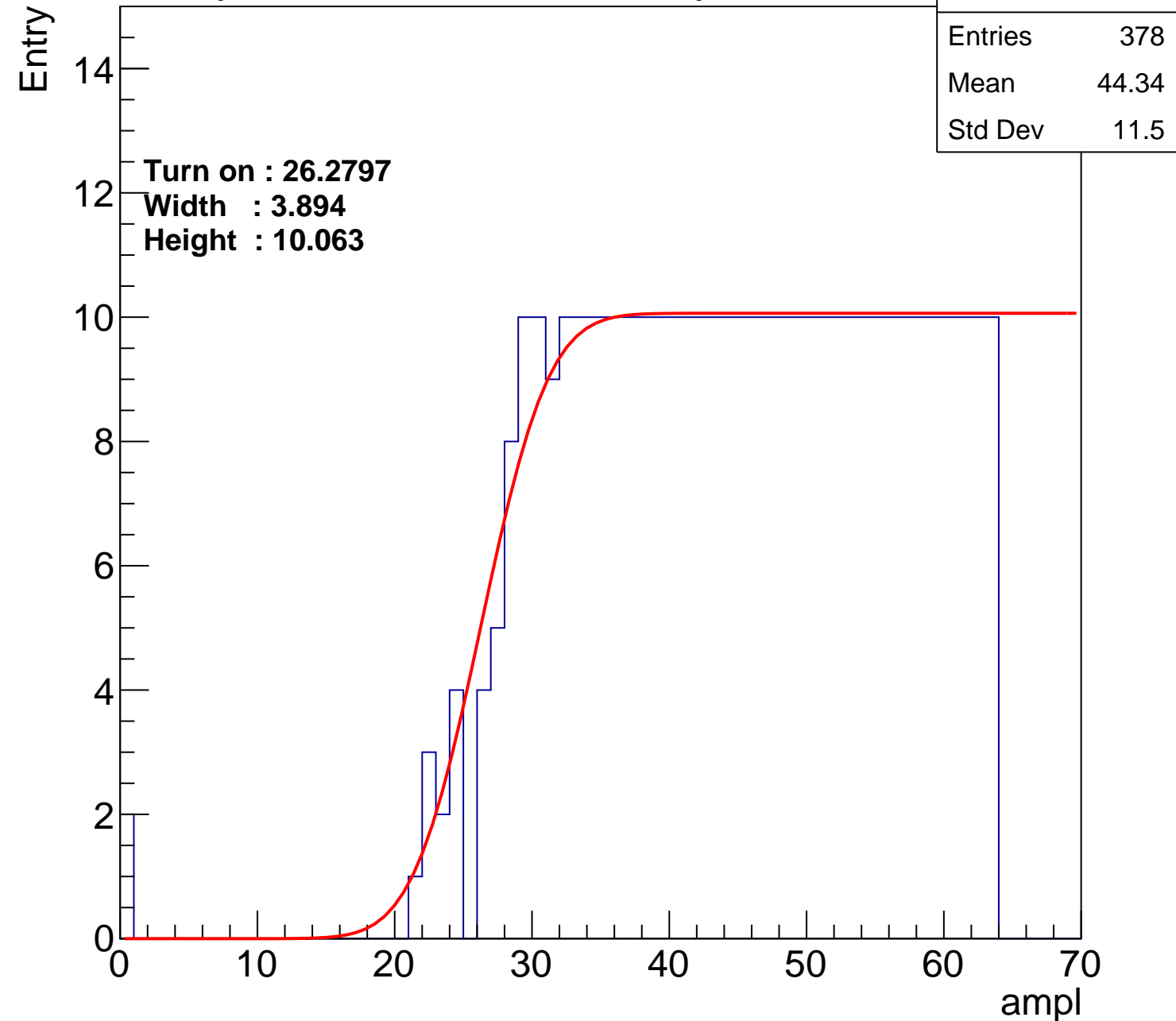
Width : 3.894

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch77

calib_packv5_042523_0143.root, FC#7, port C2

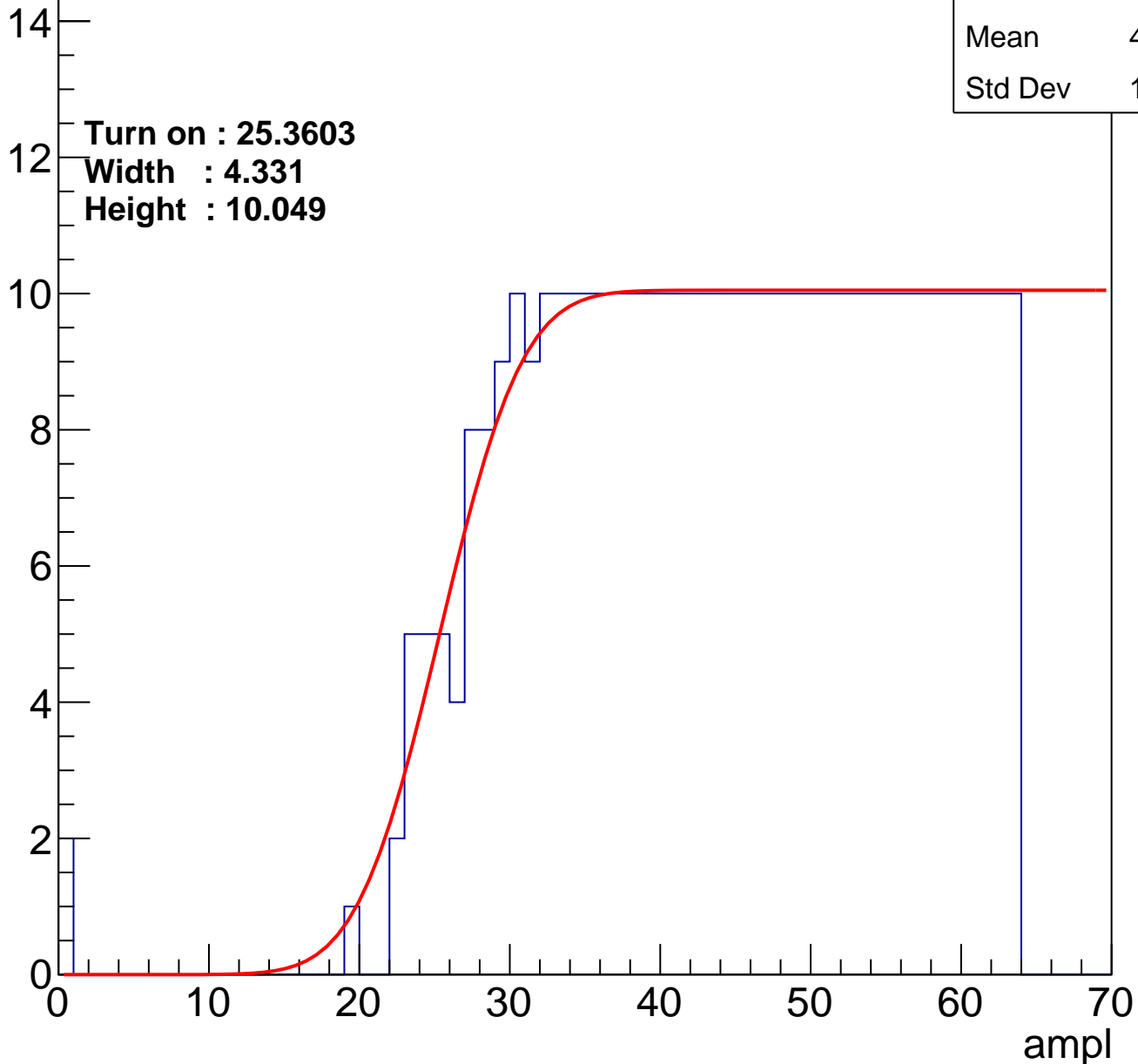
Entries	388
Mean	43.83
Std Dev	11.78

Turn on : 25.3603

Width : 4.331

Height : 10.049

Entry



B1L103S, U25-ch78

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.27
Std Dev	11.55

Turn on : 26.6683

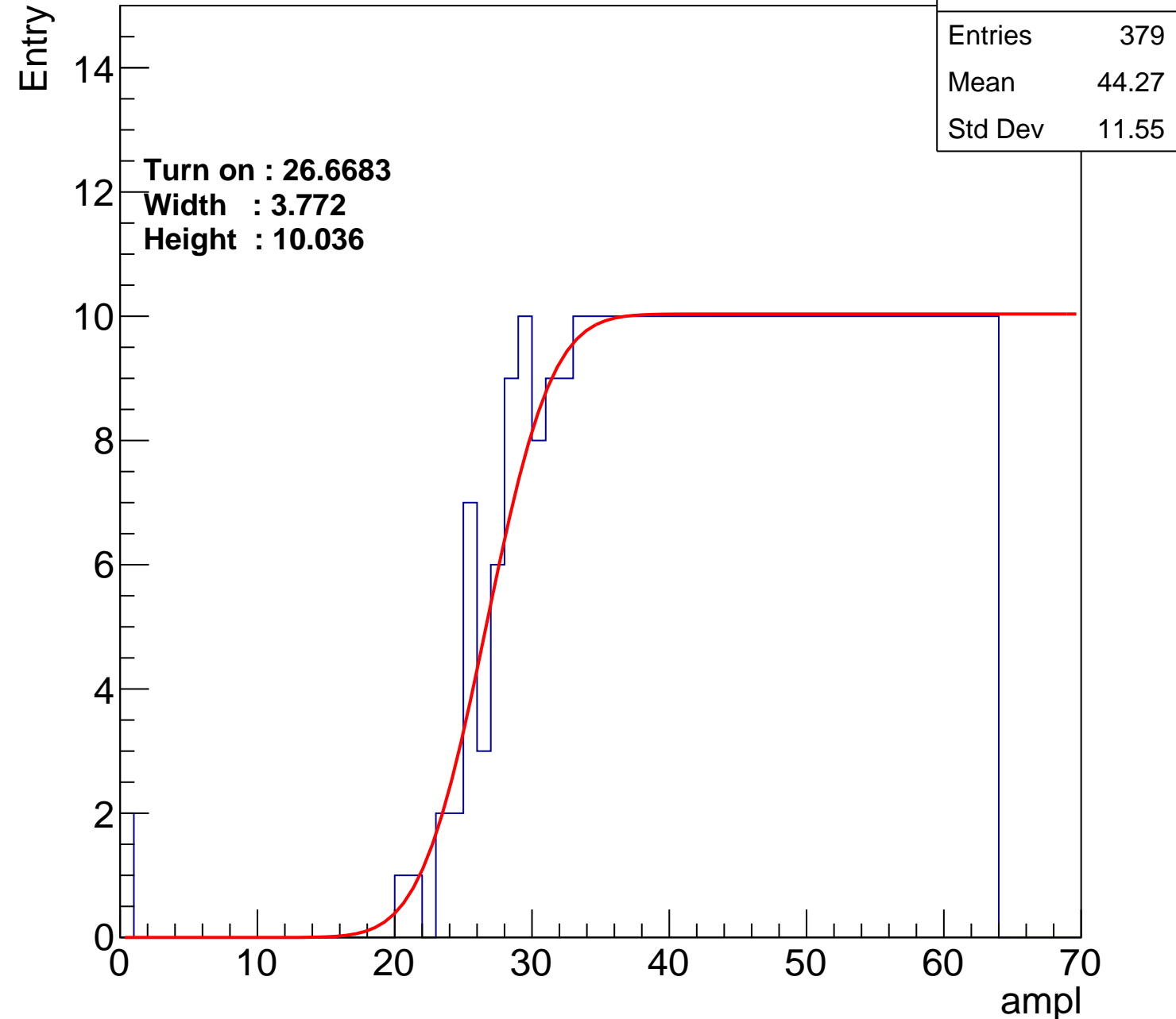
Width : 3.772

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch79

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 26.7741

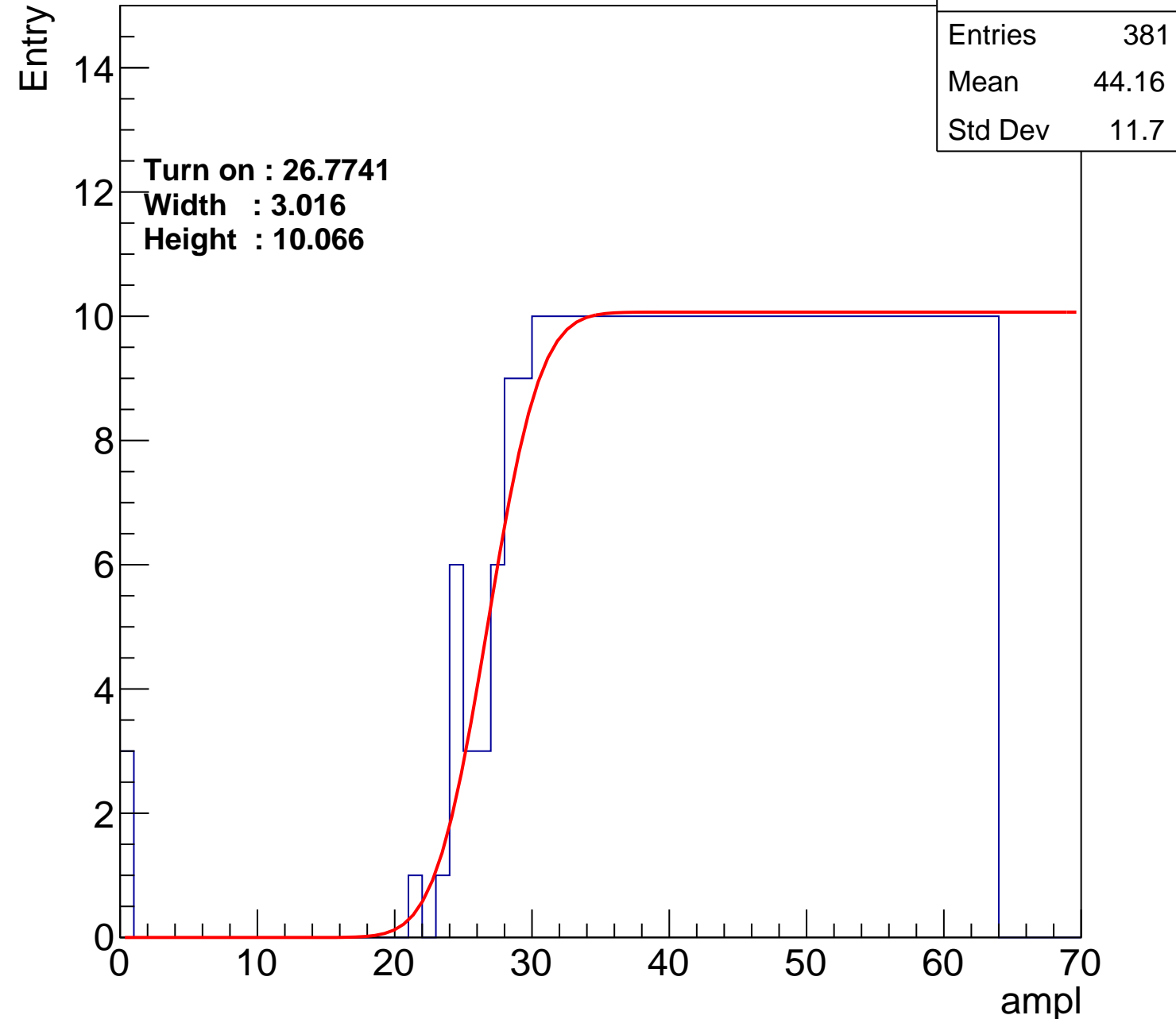
Width : 3.016

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch80

calib_packv5_042523_0143.root, FC#7, port C2

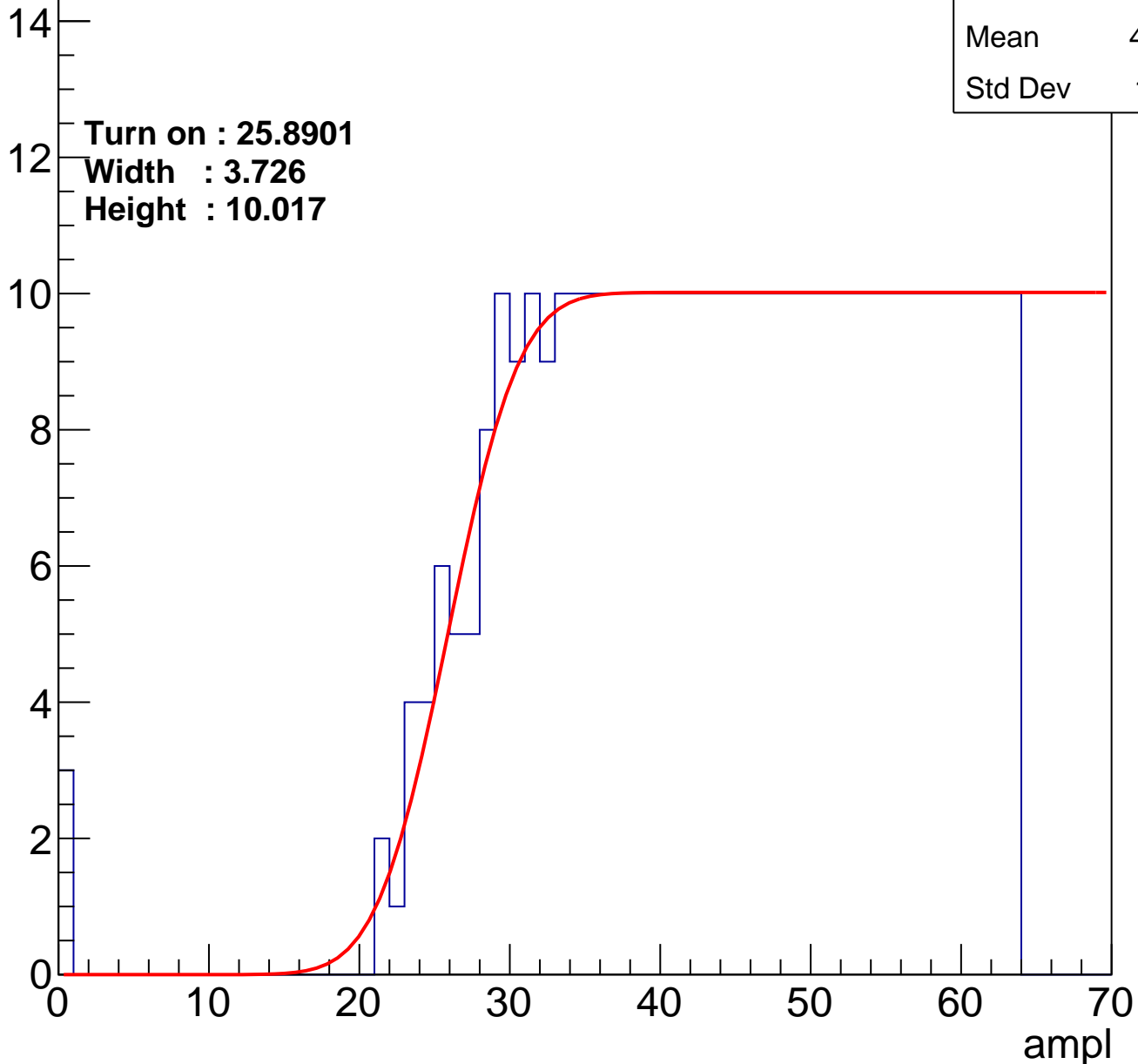
Entries	386
Mean	43.86
Std Dev	11.91

Turn on : 25.8901

Width : 3.726

Height : 10.017

Entry



B1L103S, U25-ch81

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	44.9
Std Dev	11.45

Turn on : 28.8186

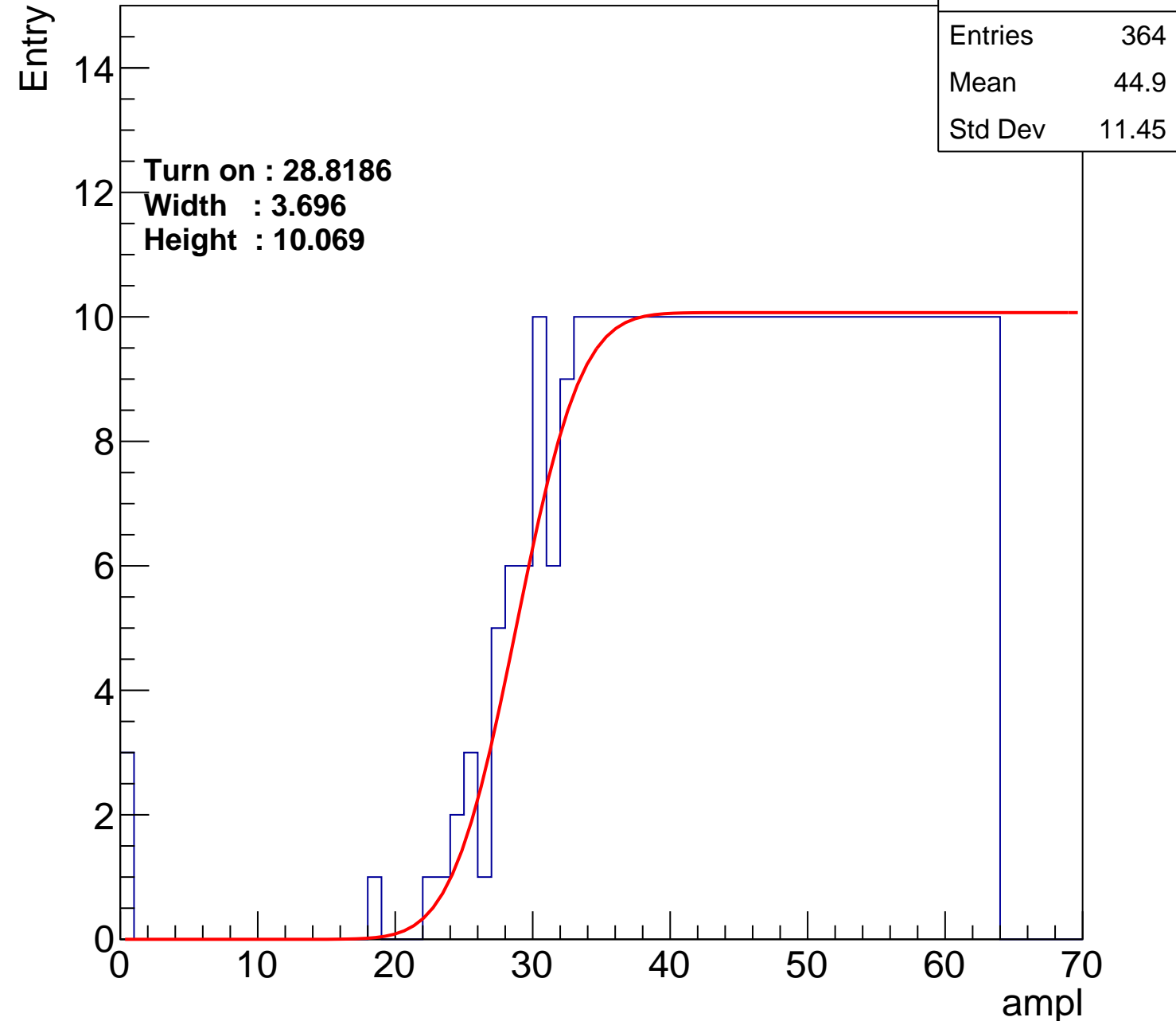
Width : 3.696

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch82

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.66
Std Dev	11.89

Turn on : 25.4089

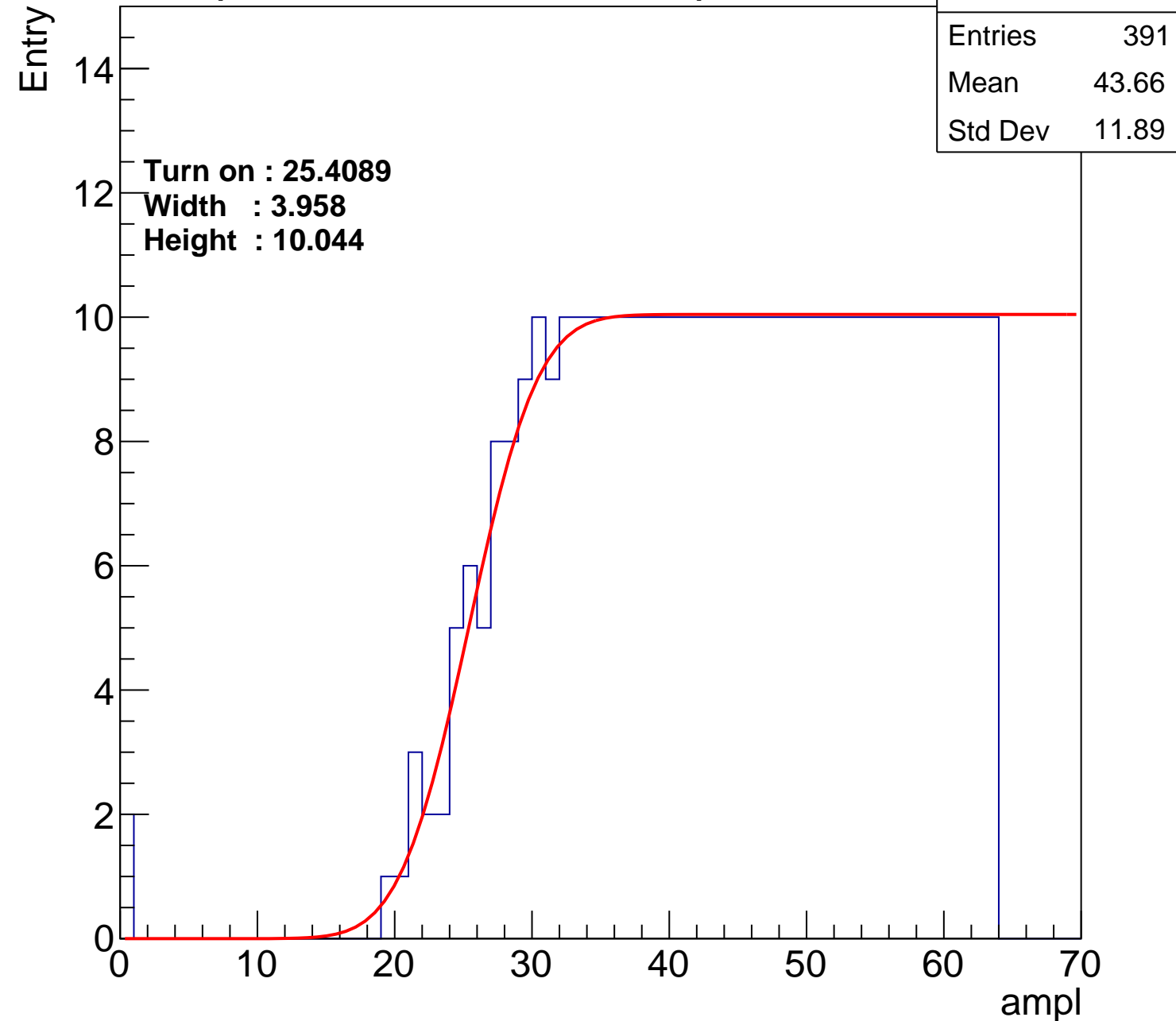
Width : 3.958

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch83

calib_packv5_042523_0143.root, FC#7, port C2

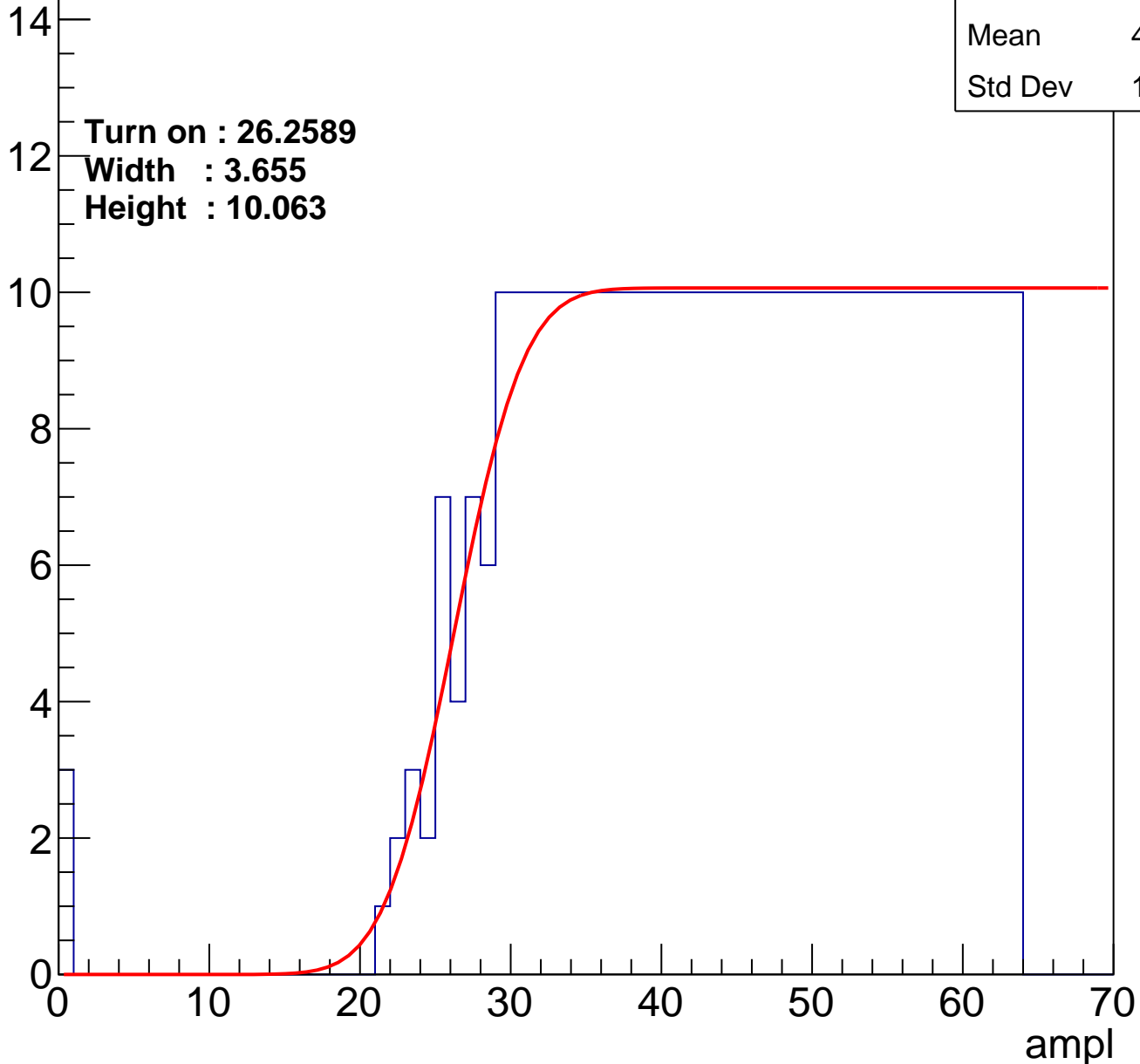
Entries	385
Mean	43.94
Std Dev	11.83

Turn on : 26.2589

Width : 3.655

Height : 10.063

Entry



B1L103S, U25-ch84

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.3
Std Dev	11.57

Turn on : 26.6315

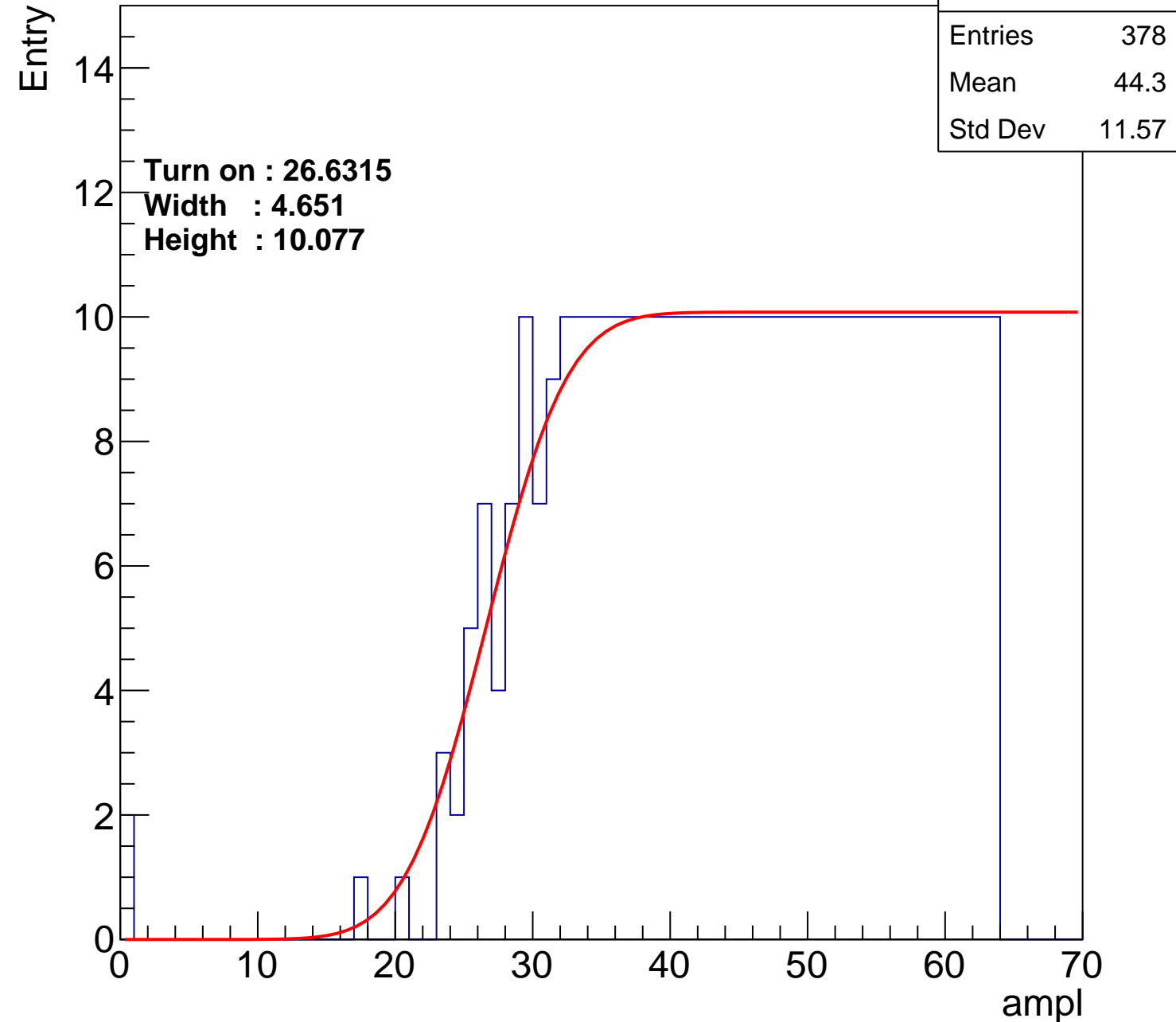
Width : 4.651

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch85

calib_packv5_042523_0143.root, FC#7, port C2

Entries	354
Mean	45.36
Std Dev	11.34

Turn on : 29.1335

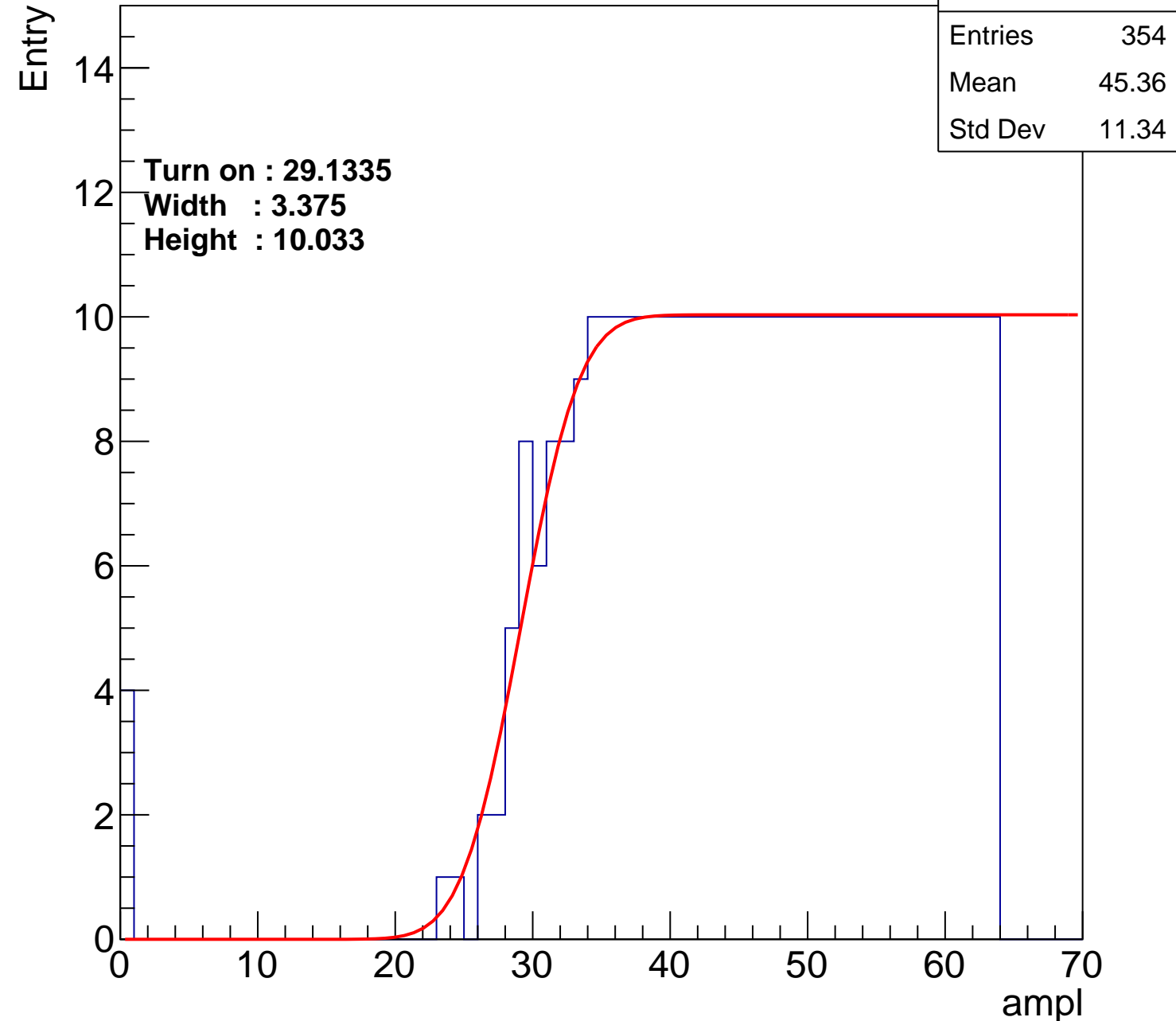
Width : 3.375

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch86

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.58
Std Dev	11.85

Turn on : 24.7445

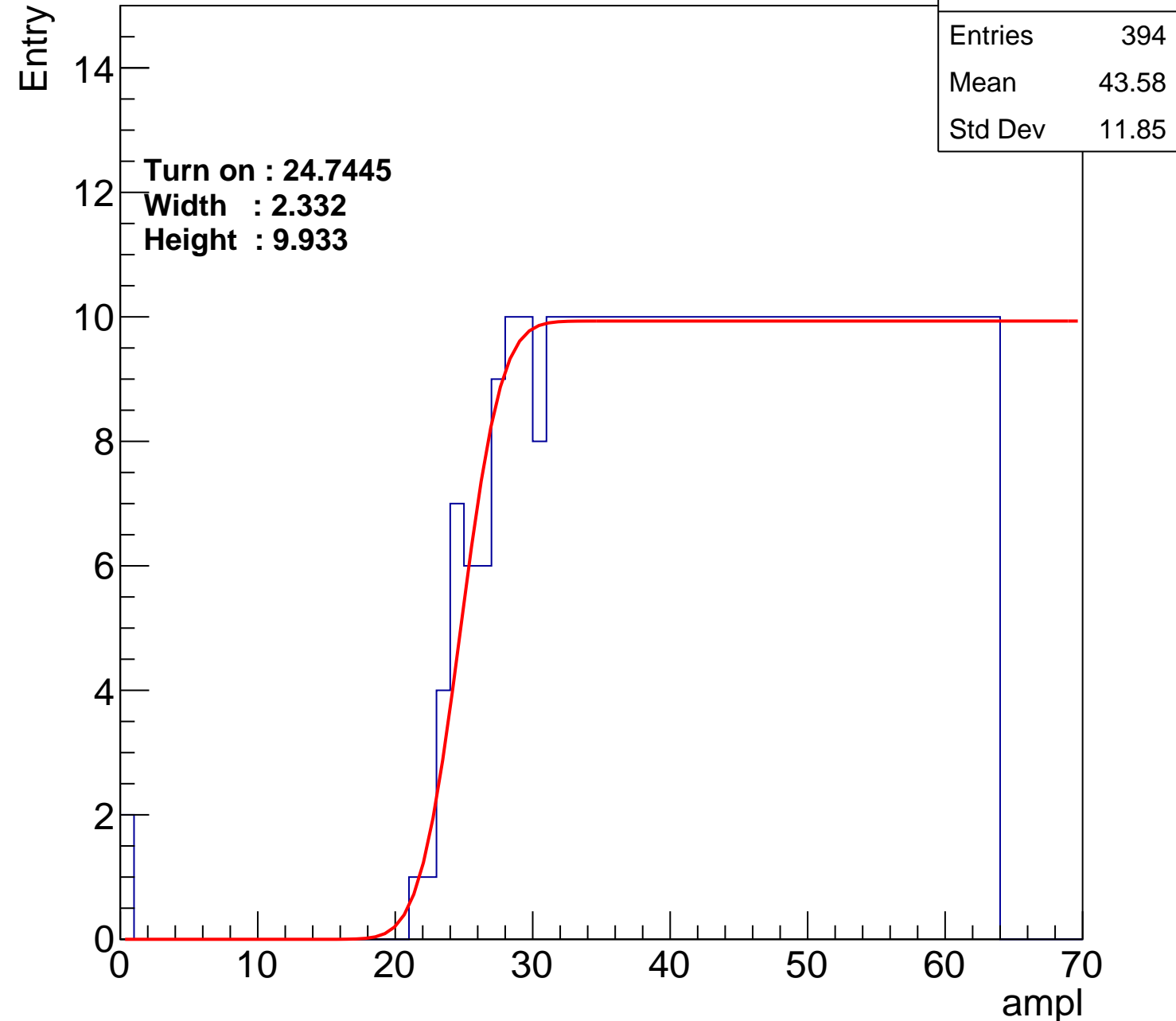
Width : 2.332

Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch87

calib_packv5_042523_0143.root, FC#7, port C2

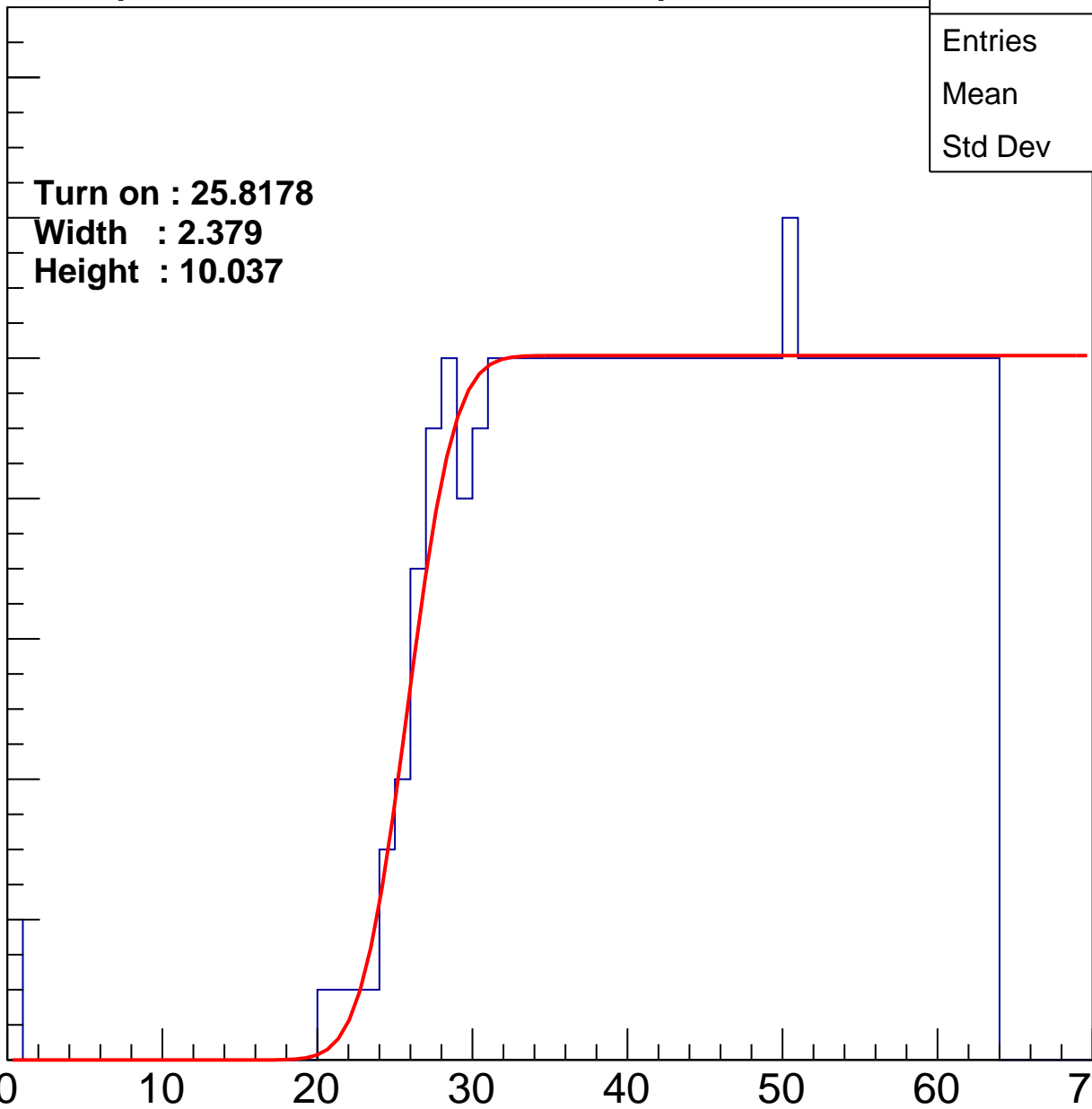
Entry

14
12
10
8
6
4
2
0

Turn on : 25.8178
Width : 2.379
Height : 10.037

Entries	388
Mean	44.01
Std Dev	11.63

ampl



B1L103S, U25-ch88

calib_packv5_042523_0143.root, FC#7, port C2

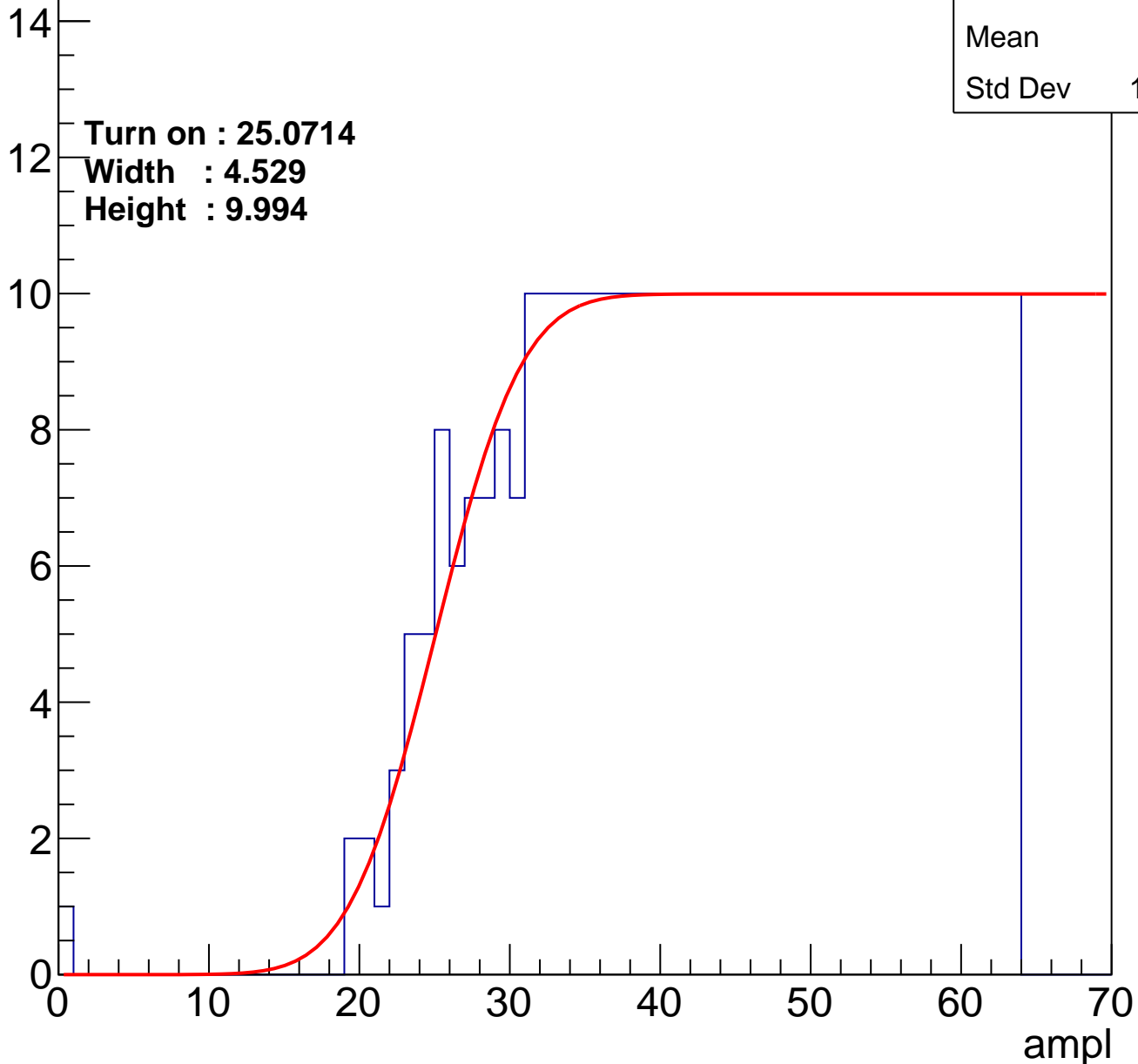
Entries	392
Mean	43.6
Std Dev	11.86

Turn on : 25.0714

Width : 4.529

Height : 9.994

Entry



B1L103S, U25-ch89

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.1848

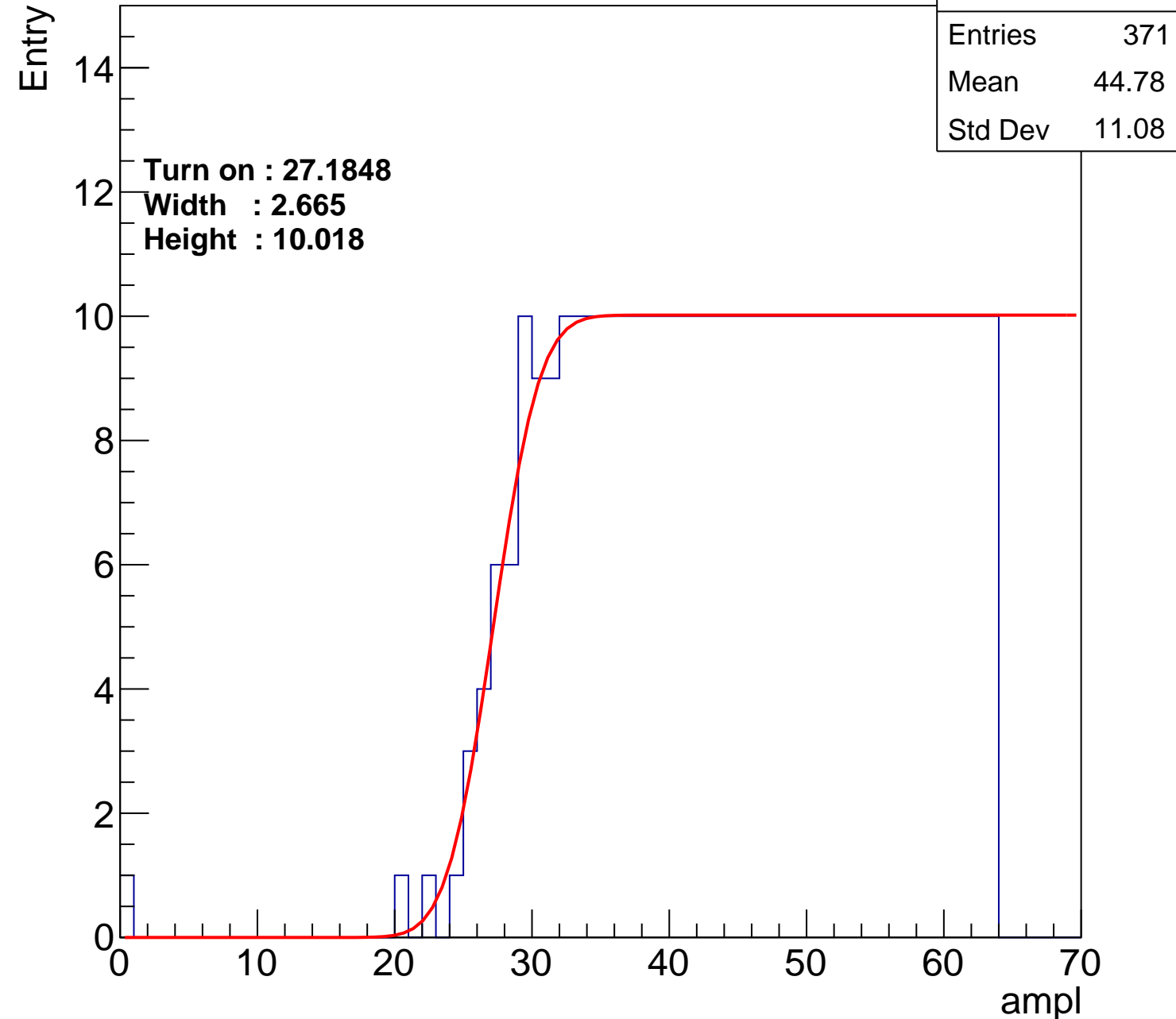
Width : 2.665

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch90

calib_packv5_042523_0143.root, FC#7, port C2

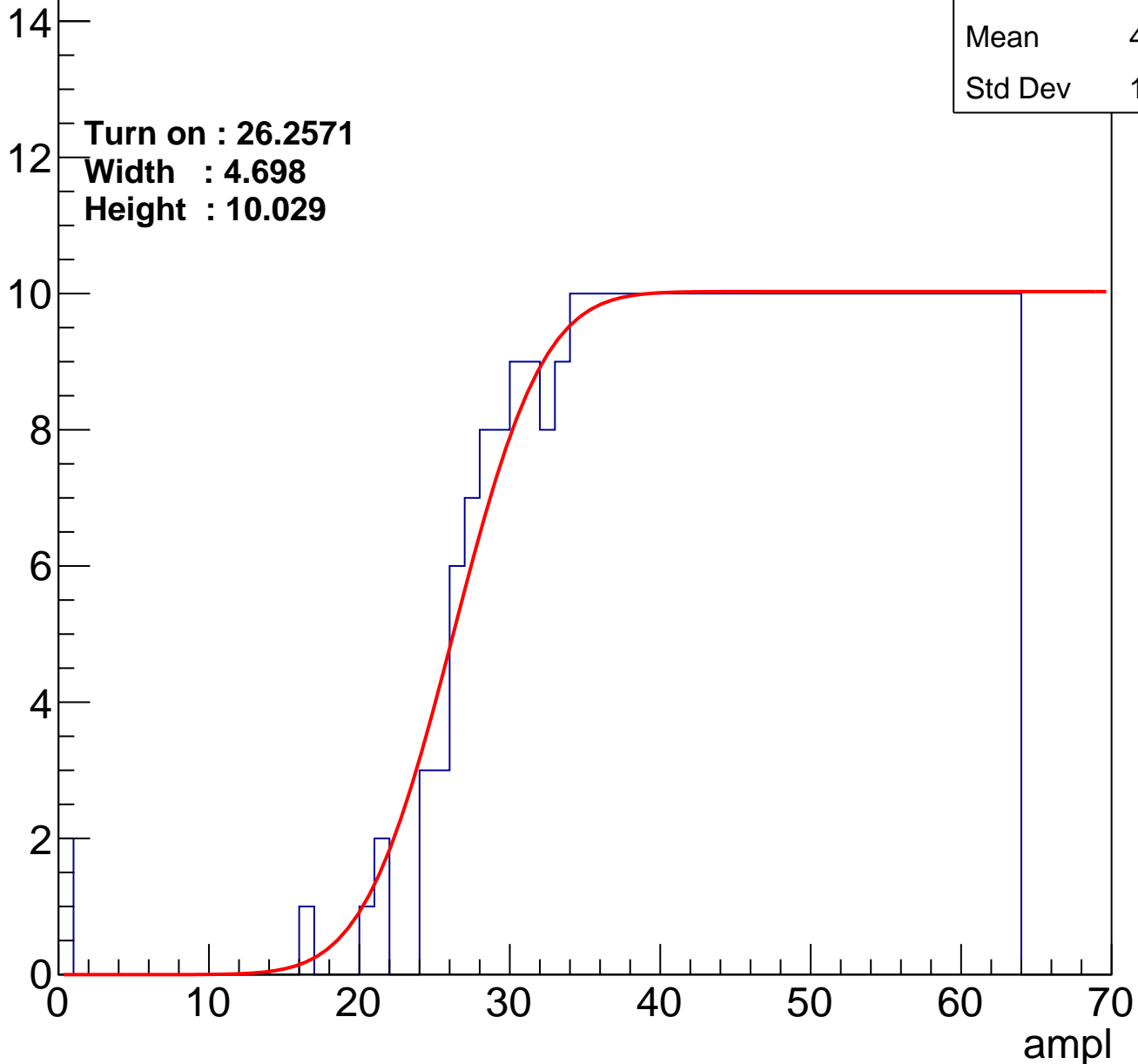
Entries	376
Mean	44.36
Std Dev	11.58

Turn on : 26.2571

Width : 4.698

Height : 10.029

Entry



B1L103S, U25-ch91

calib_packv5_042523_0143.root, FC#7, port C2

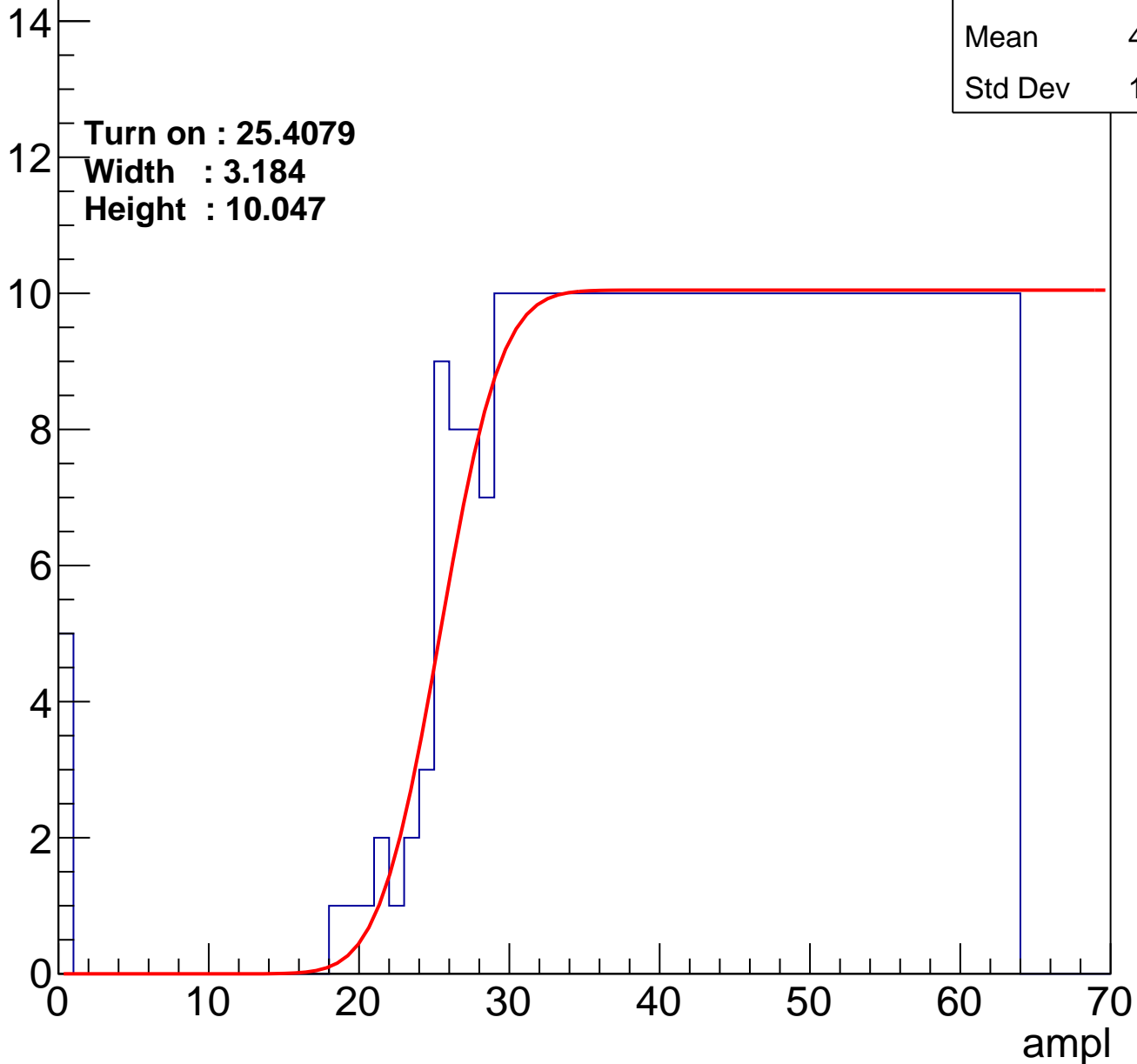
Entries	398
Mean	43.18
Std Dev	12.48

Turn on : 25.4079

Width : 3.184

Height : 10.047

Entry



calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.66
Std Dev	11.92

Std Dev	11.92
---------	-------

Height : 9.925



B1L103S, U25-ch93

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.78
Std Dev	11.07

Turn on : 26.8100

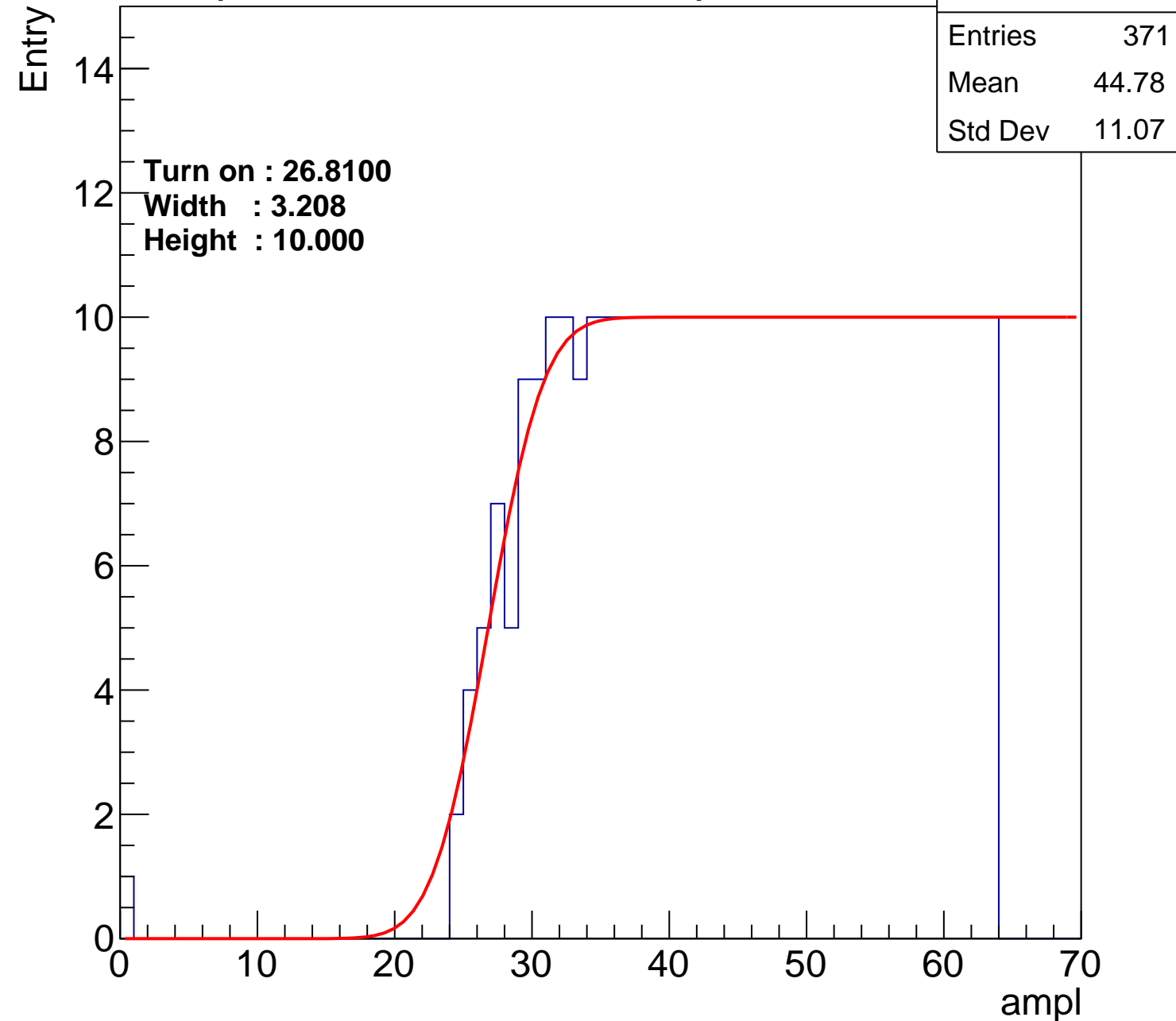
Width : 3.208

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch94

calib_packv5_042523_0143.root, FC#7, port C2

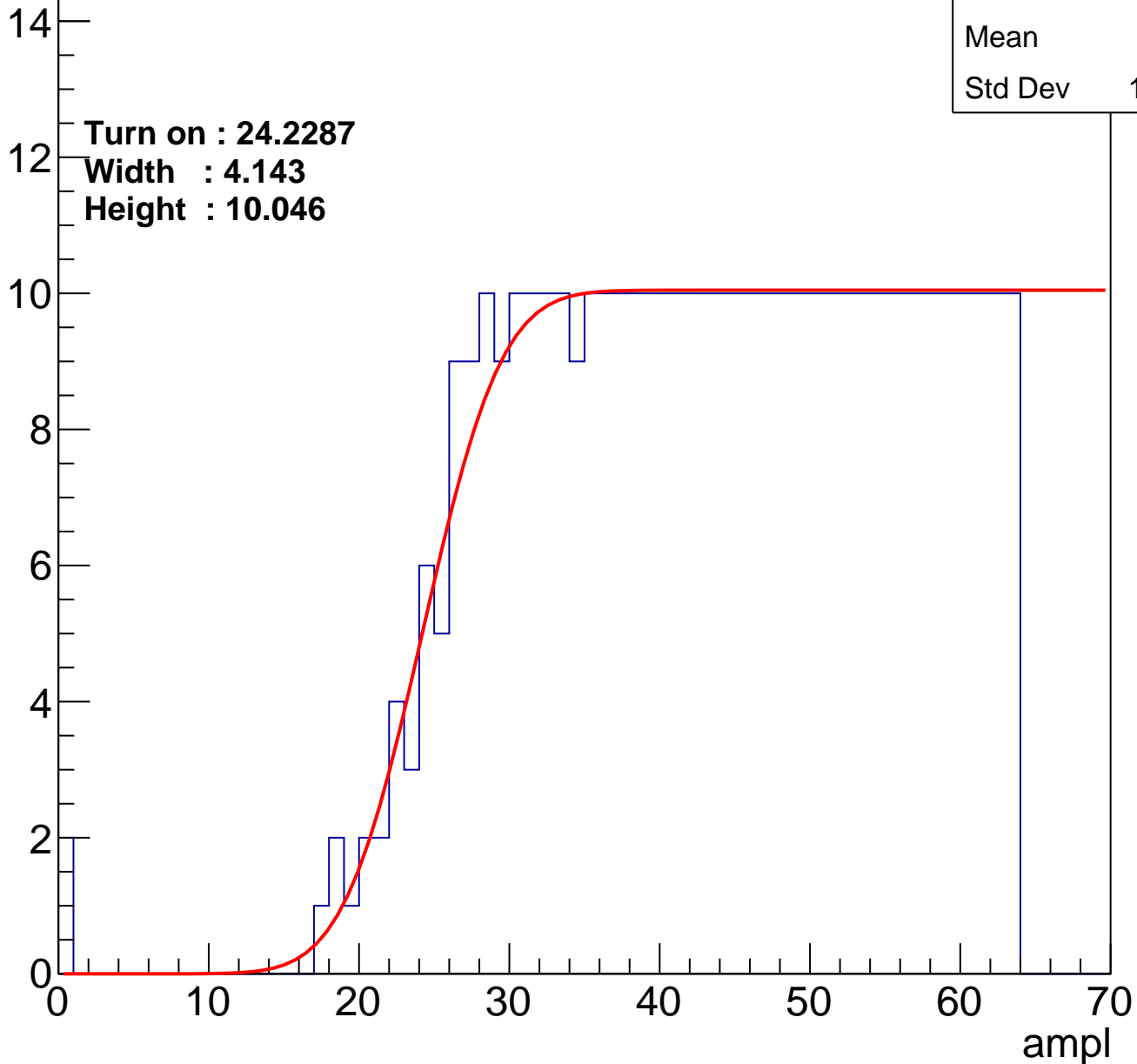
Entries	404
Mean	43
Std Dev	12.26

Turn on : 24.2287

Width : 4.143

Height : 10.046

Entry



B1L103S, U25-ch95

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.07
Std Dev	11.83

Turn on : 26.8079

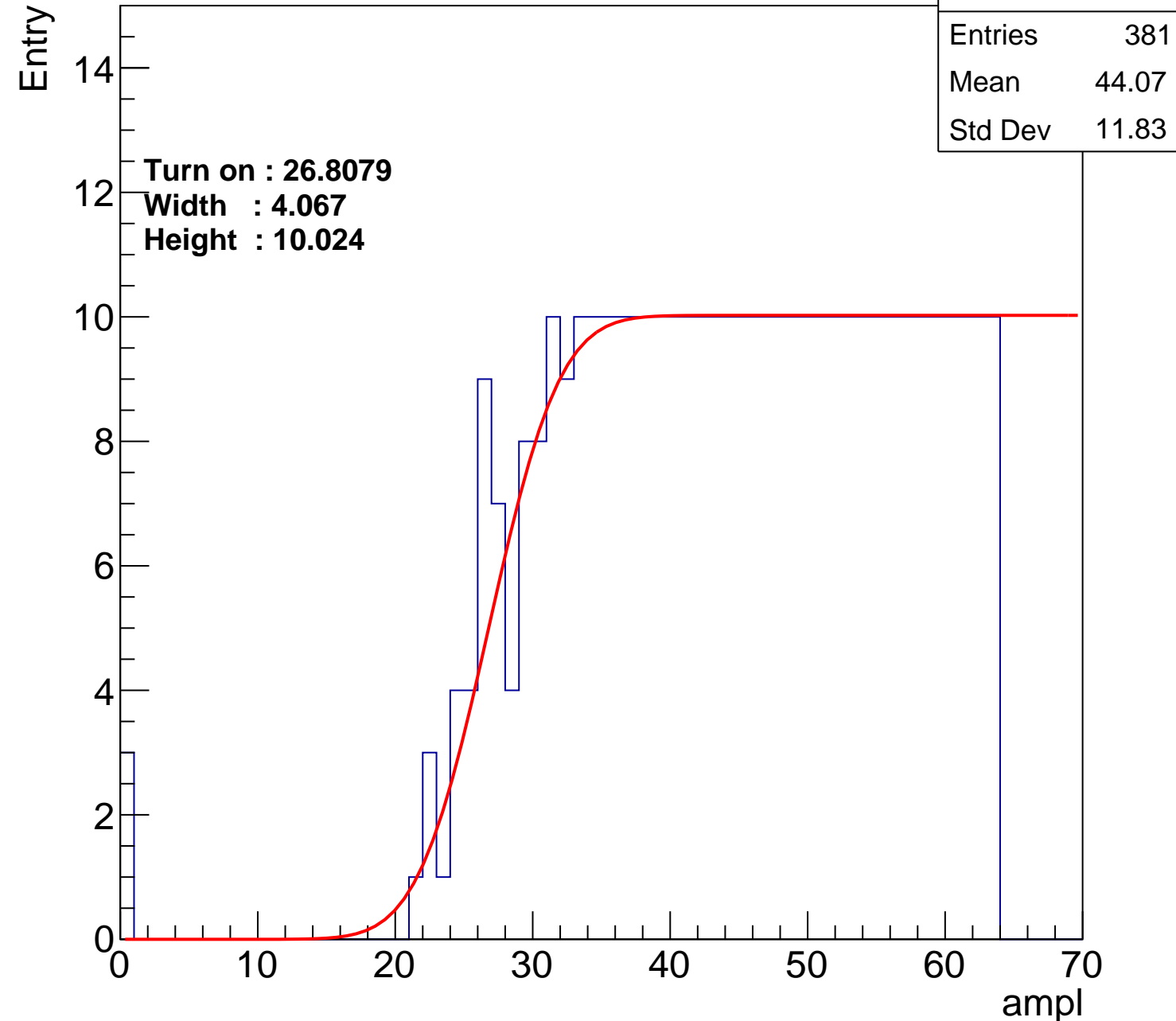
Width : 4.067

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch96

calib_packv5_042523_0143.root, FC#7, port C2

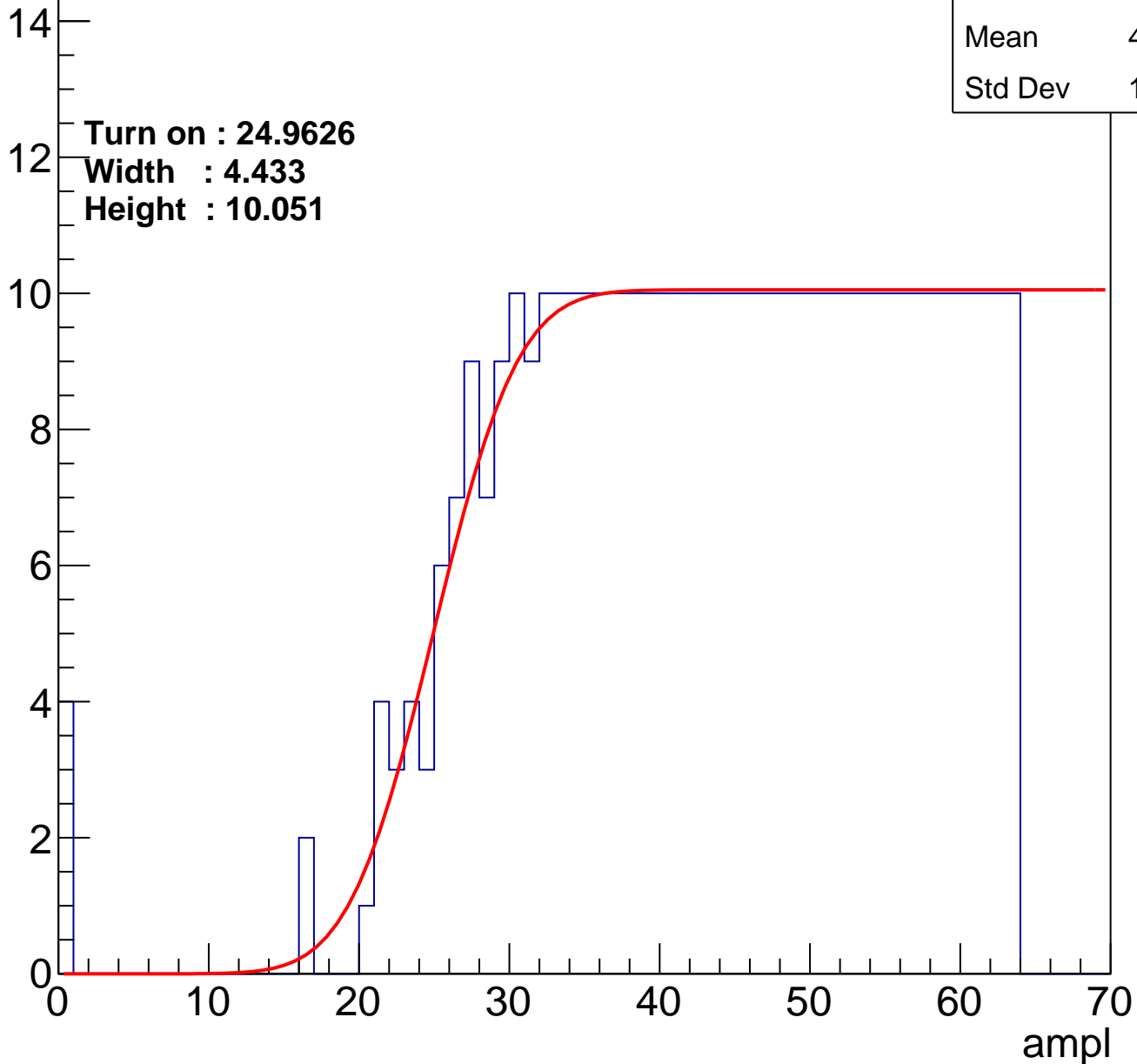
Entries	398
Mean	43.16
Std Dev	12.45

Turn on : 24.9626

Width : 4.433

Height : 10.051

Entry



B1L103S, U25-ch97

calib_packv5_042523_0143.root, FC#7, port C2

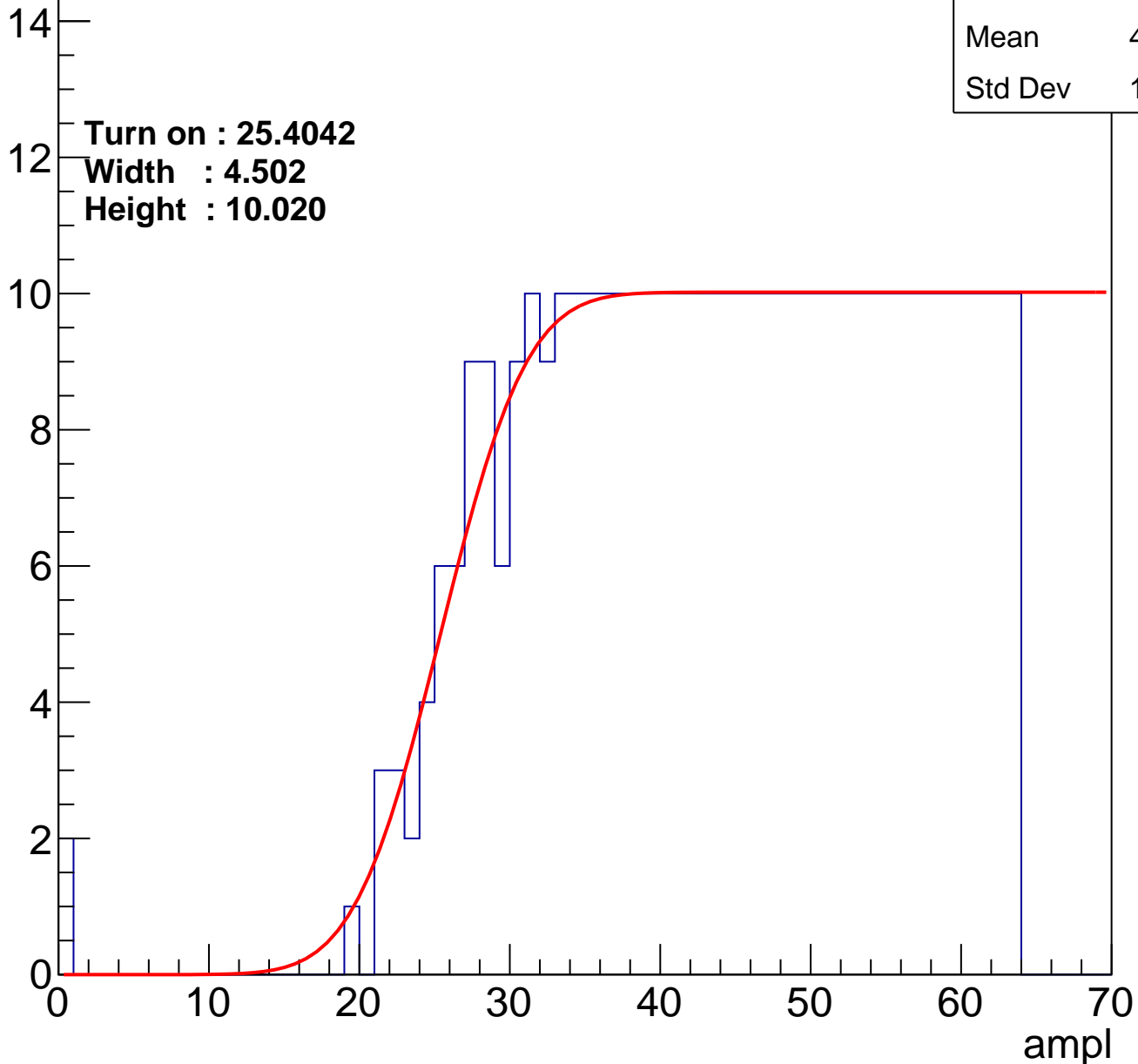
Entries	389
Mean	43.74
Std Dev	11.88

Turn on : 25.4042

Width : 4.502

Height : 10.020

Entry



B1L103S, U25-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.55
Std Dev	11.98

Turn on : 25.4400

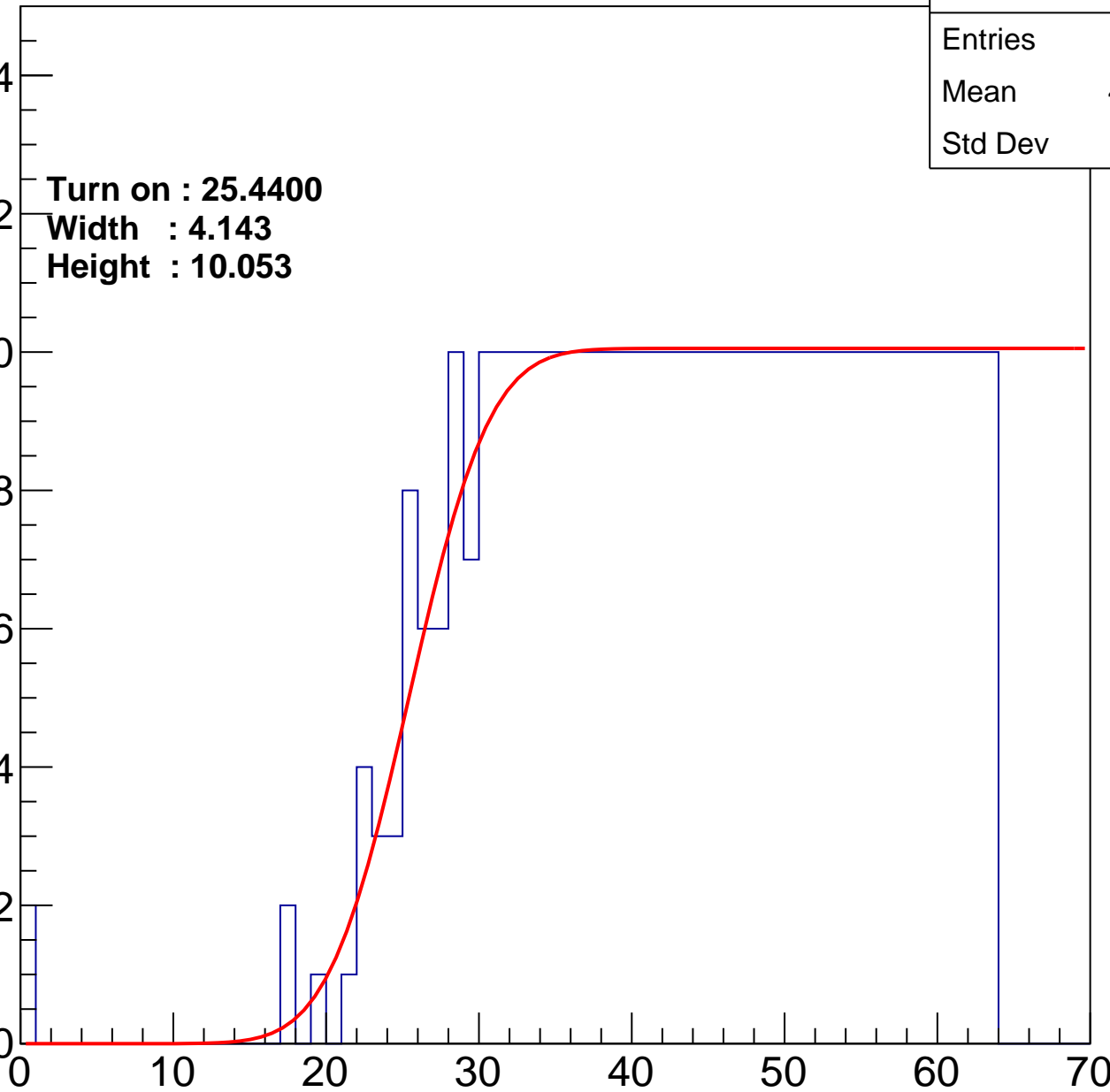
Width : 4.143

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch99

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.98
Std Dev	11.02

Turn on : 27.7184

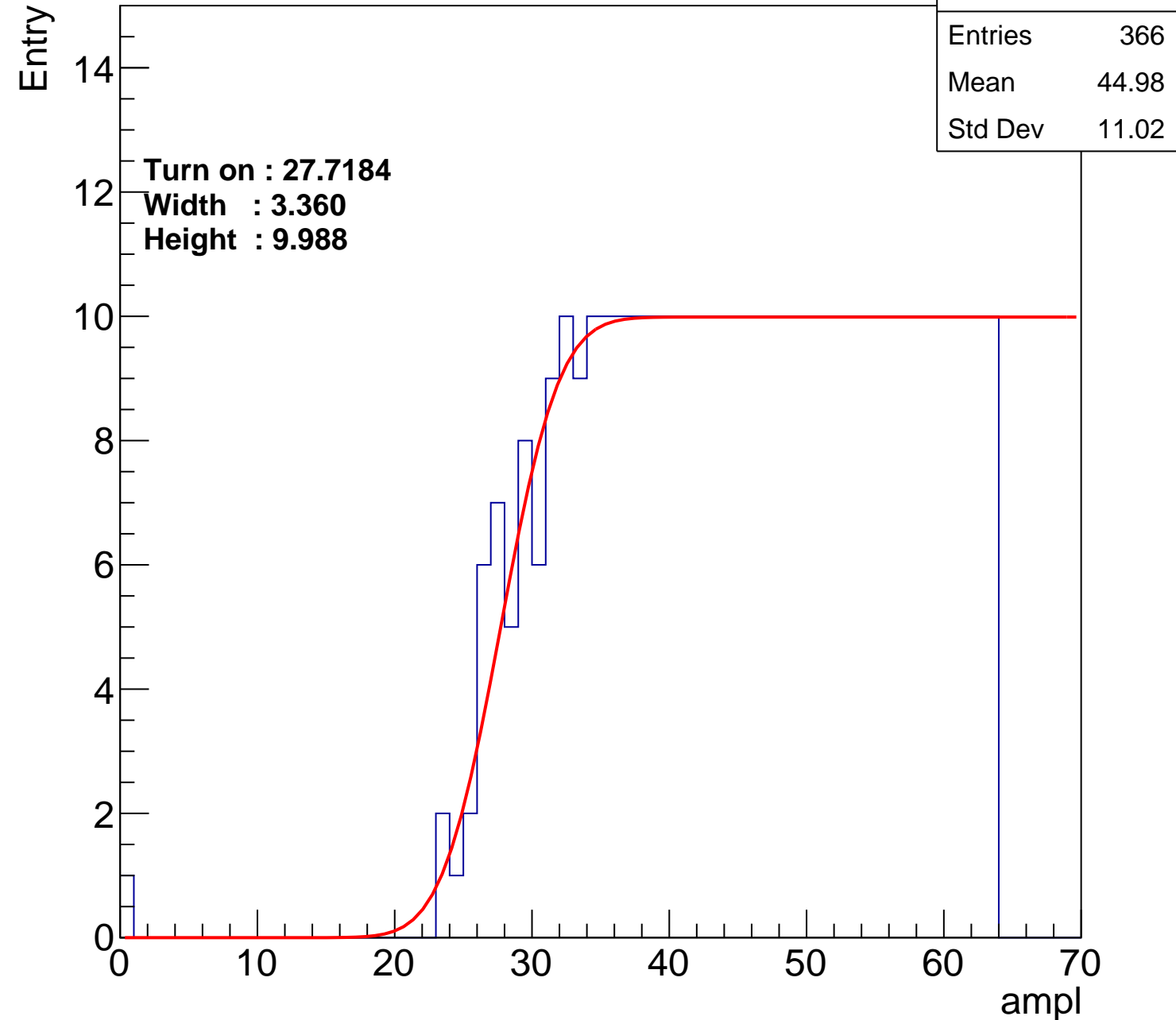
Width : 3.360

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch100

calib_packv5_042523_0143.root, FC#7, port C2

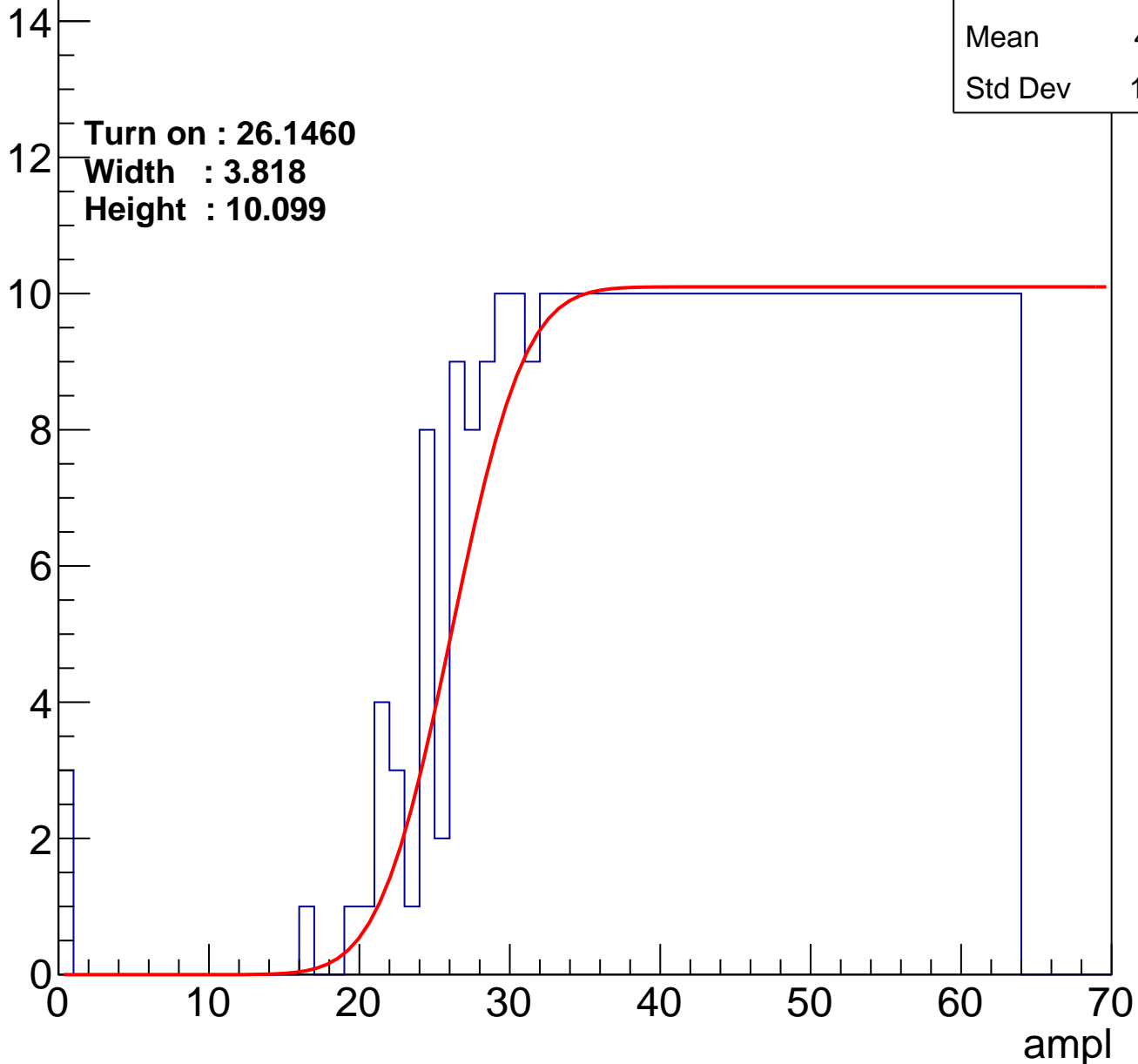
Entries	399
Mean	43.21
Std Dev	12.26

Turn on : 26.1460

Width : 3.818

Height : 10.099

Entry



B1L103S, U25-ch101

calib_packv5_042523_0143.root, FC#7, port C2

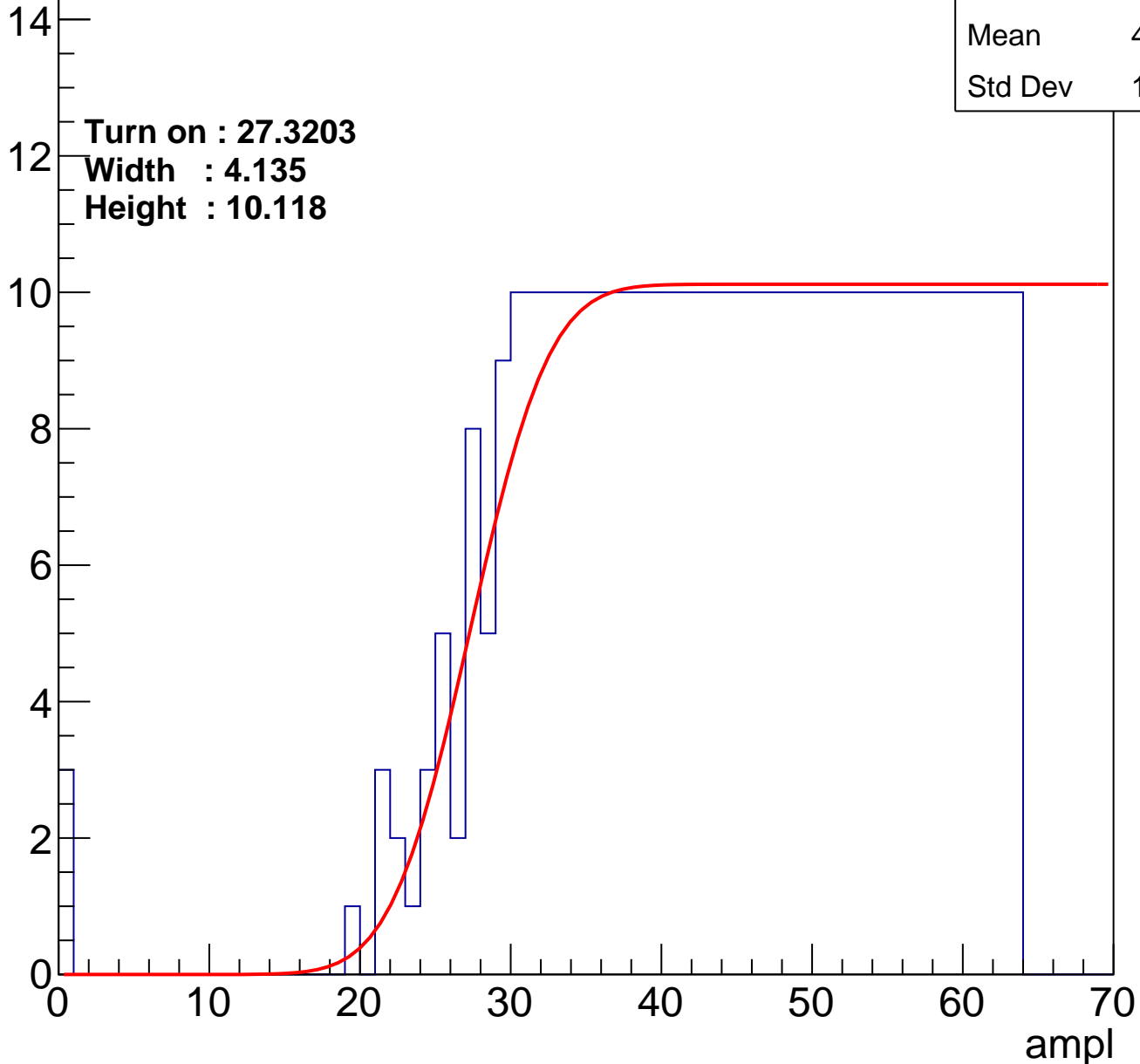
Entries	382
Mean	44.04
Std Dev	11.84

Turn on : 27.3203

Width : 4.135

Height : 10.118

Entry



B1L103S, U25-ch102

calib_packv5_042523_0143.root, FC#7, port C2

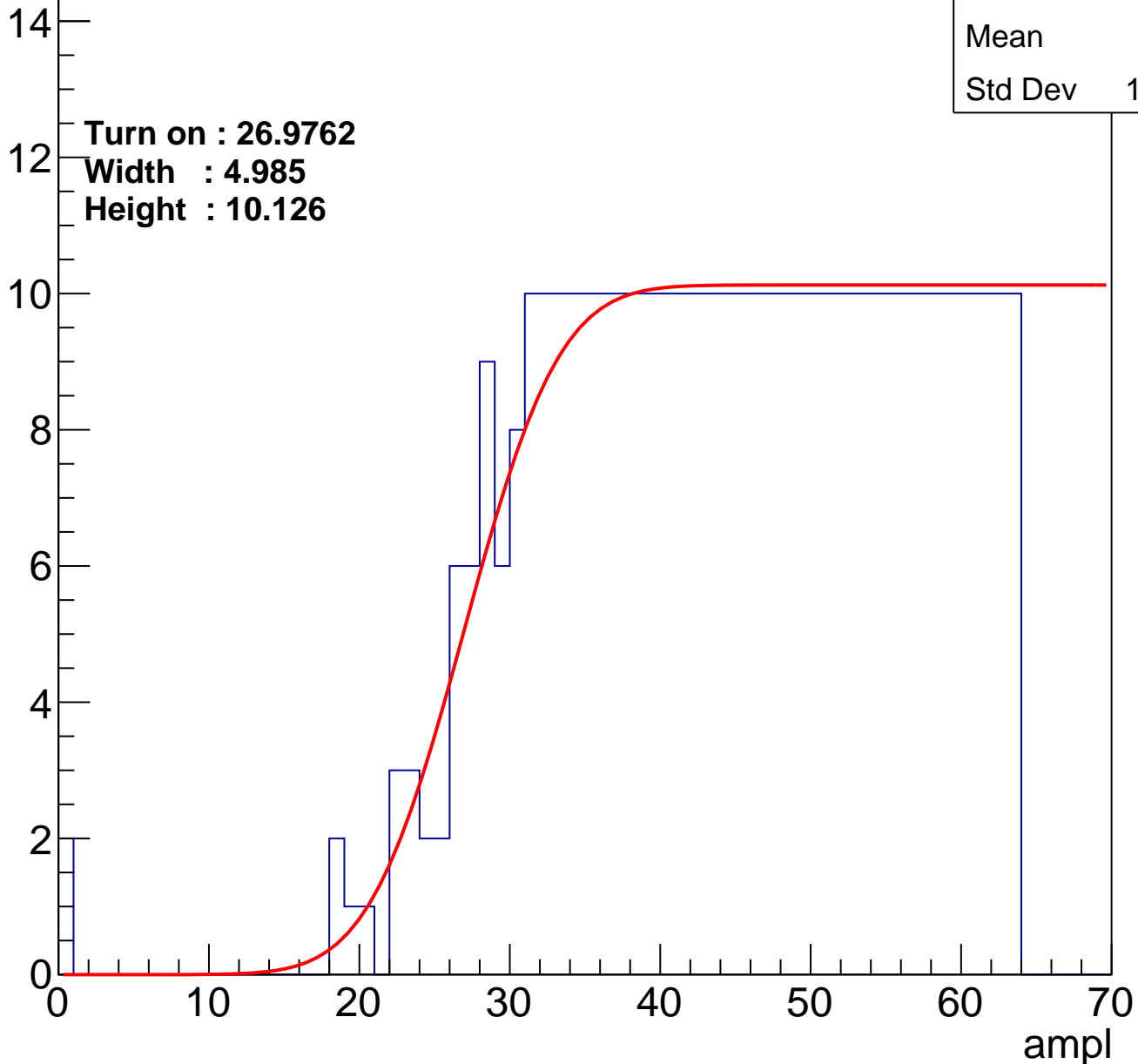
Entries	381
Mean	44.1
Std Dev	11.74

Turn on : 26.9762

Width : 4.985

Height : 10.126

Entry



B1L103S, U25-ch103

calib_packv5_042523_0143.root, FC#7, port C2

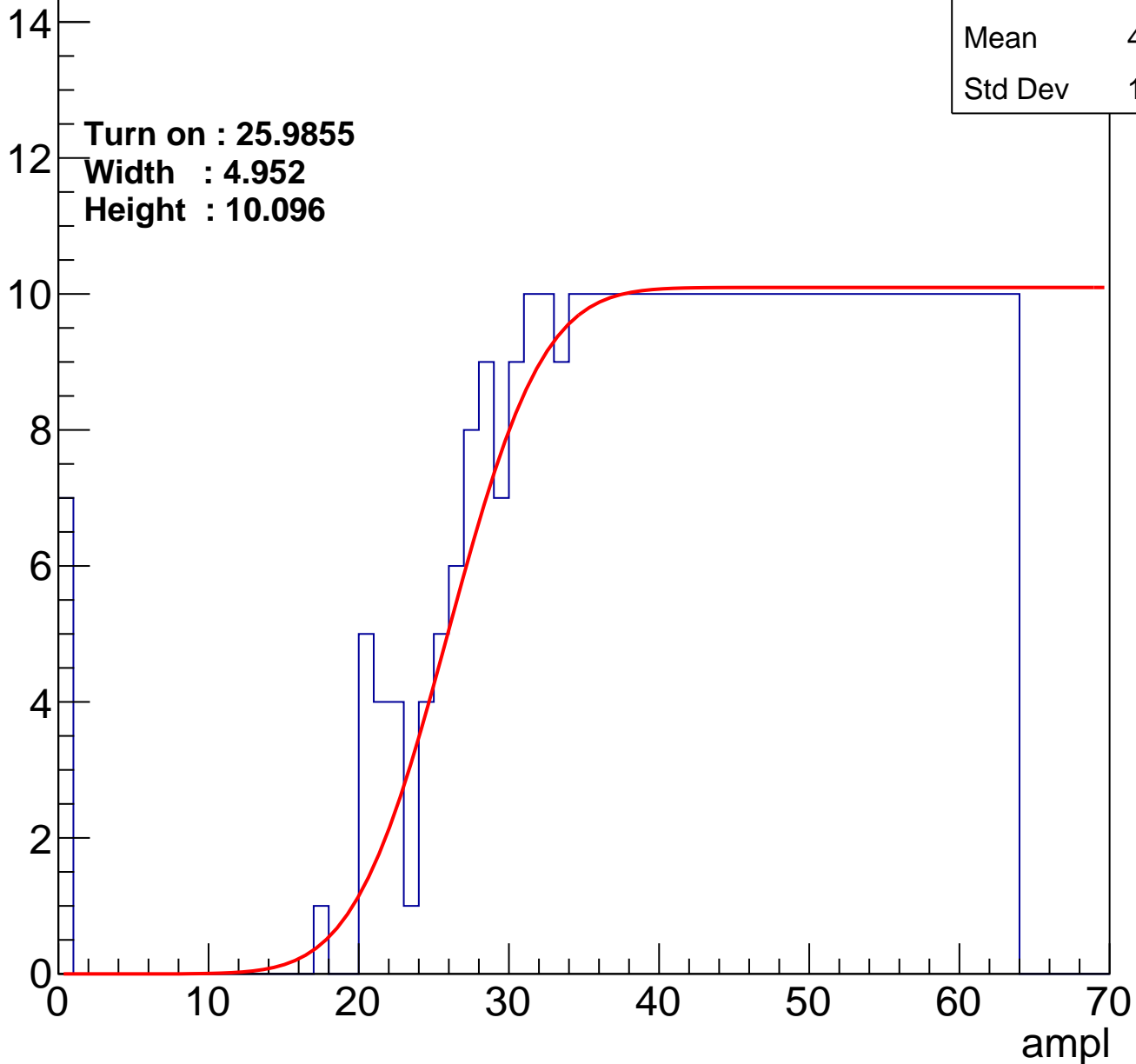
Entries	399
Mean	42.87
Std Dev	12.98

Turn on : 25.9855

Width : 4.952

Height : 10.096

Entry



B1L103S, U25-ch104

calib_packv5_042523_0143.root, FC#7, port C2

Entries	401
Mean	43.23
Std Dev	12.06

Turn on : 24.4888

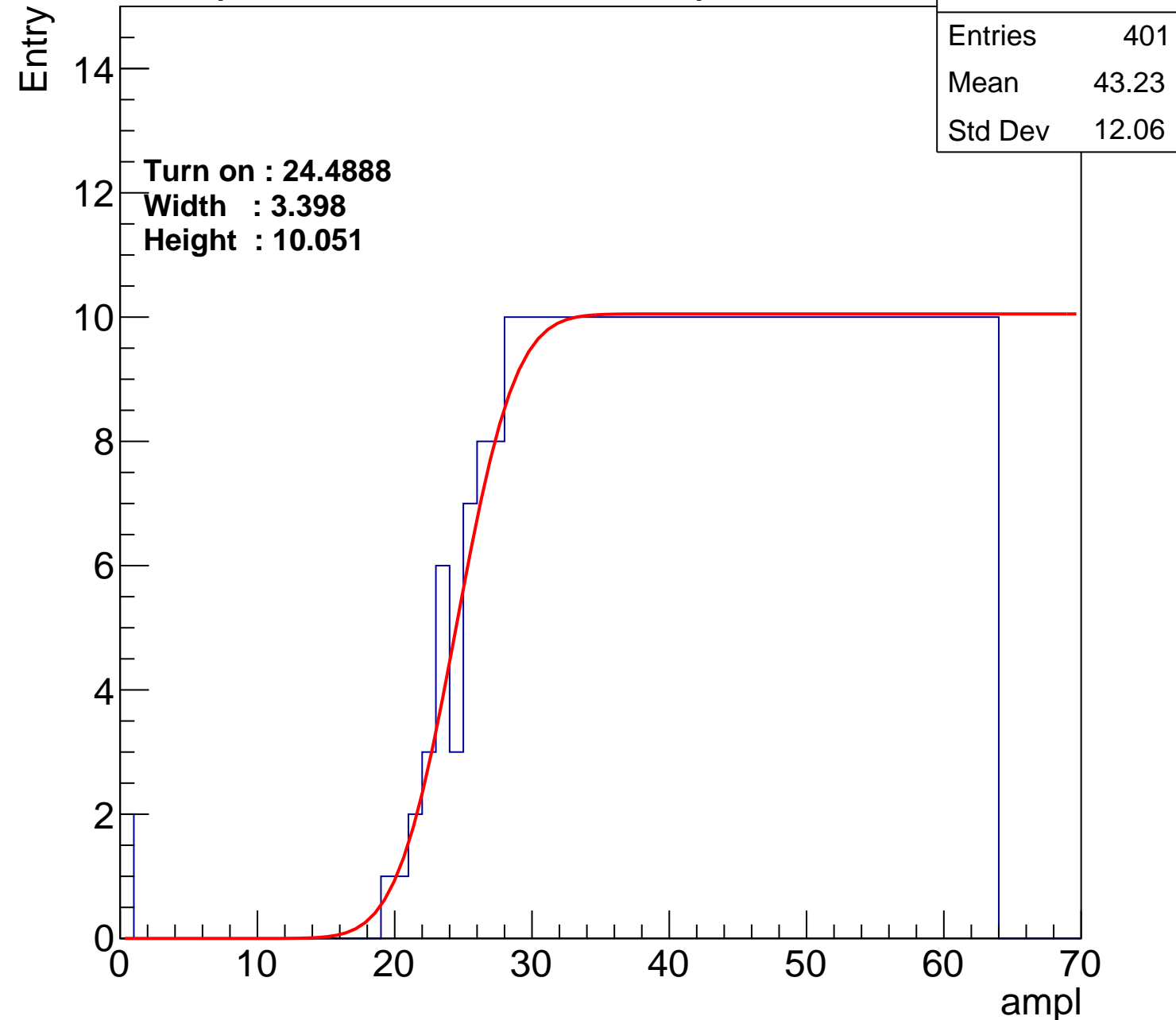
Width : 3.398

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch105

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.81
Std Dev	12.06

Turn on : 26.2817

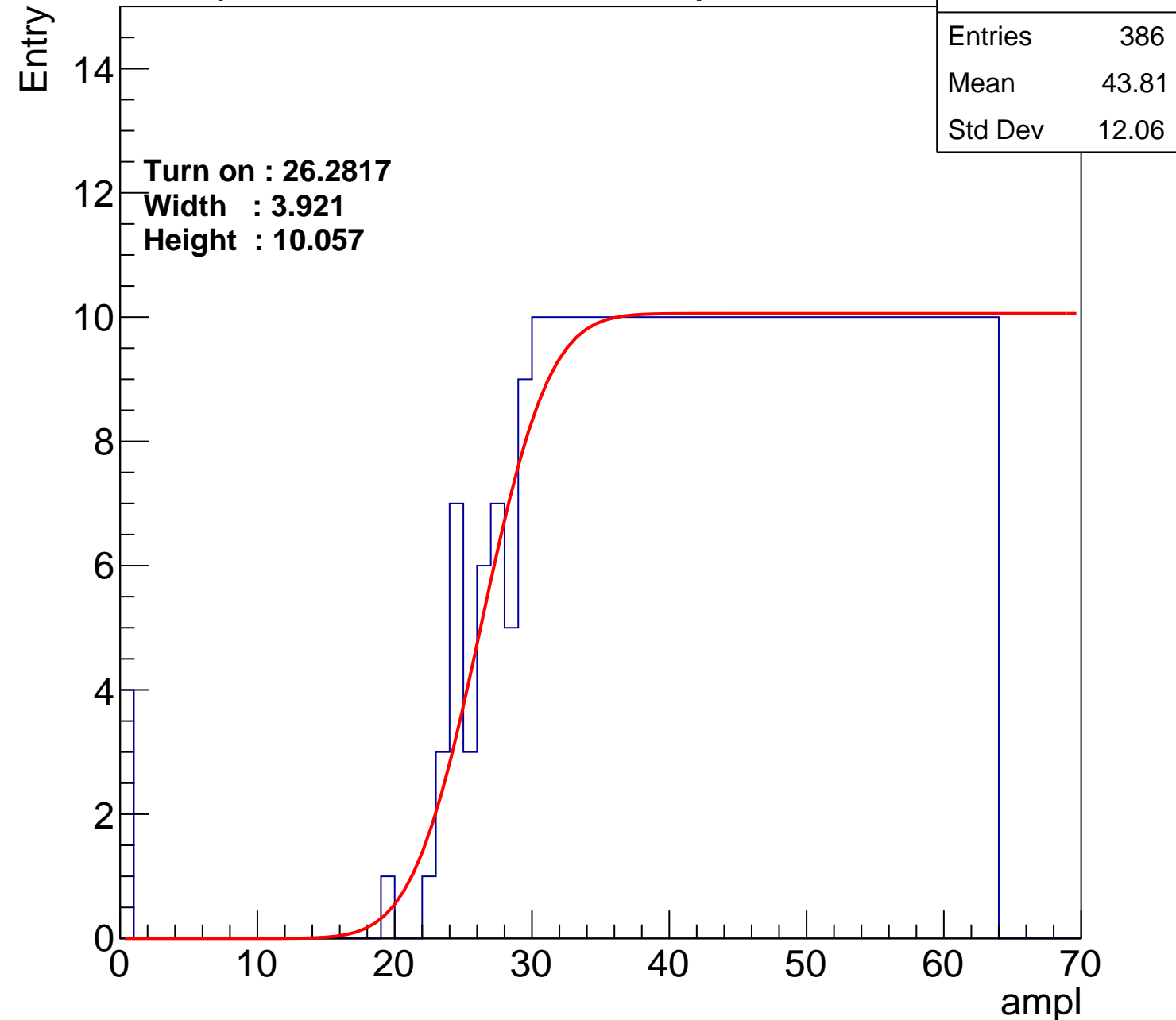
Width : 3.921

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch106

calib_packv5_042523_0143.root, FC#7, port C2

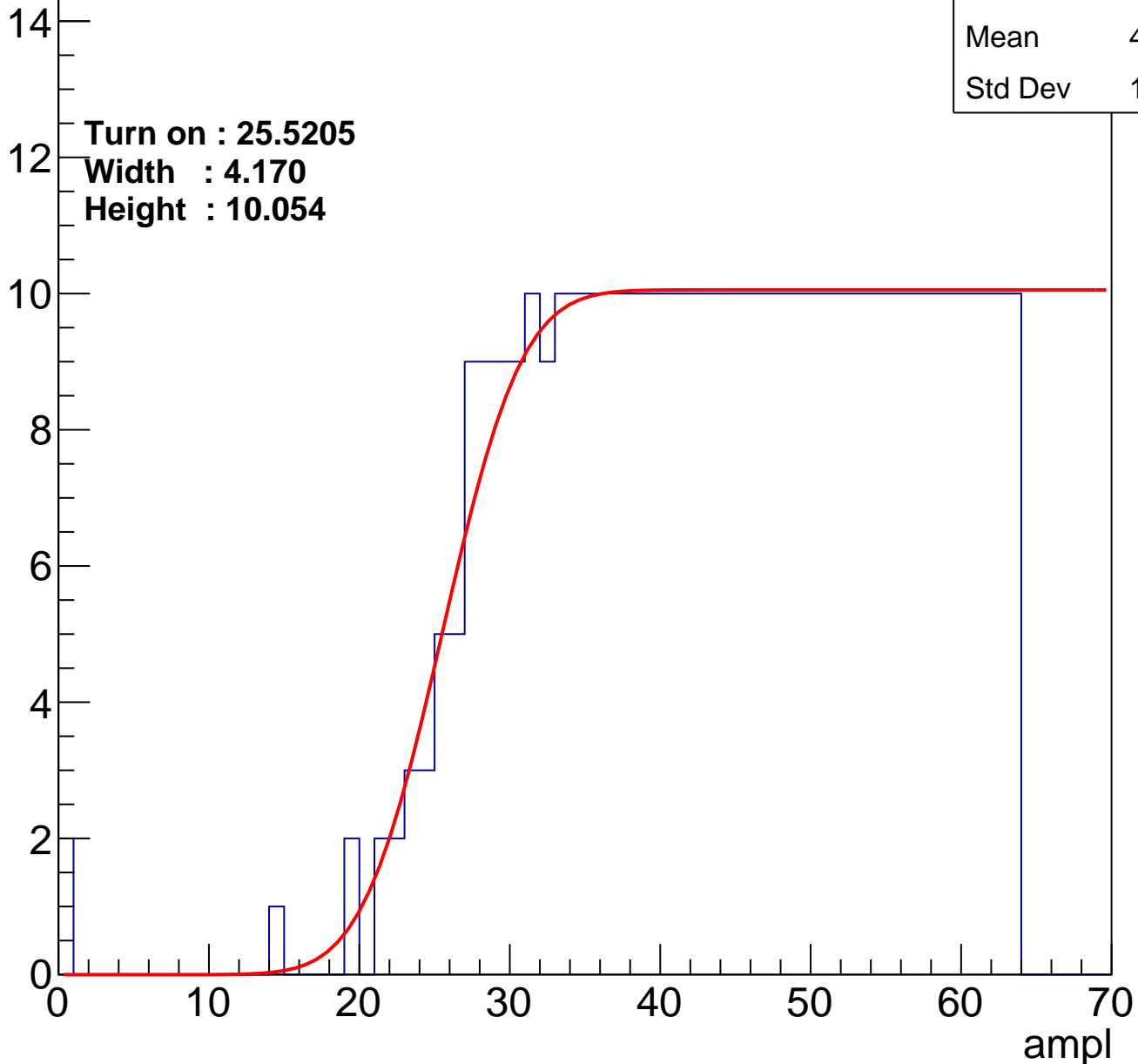
Entries	390
Mean	43.69
Std Dev	11.92

Turn on : 25.5205

Width : 4.170

Height : 10.054

Entry



B1L103S, U25-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.24
Std Dev	11.42

Turn on : 26.8436

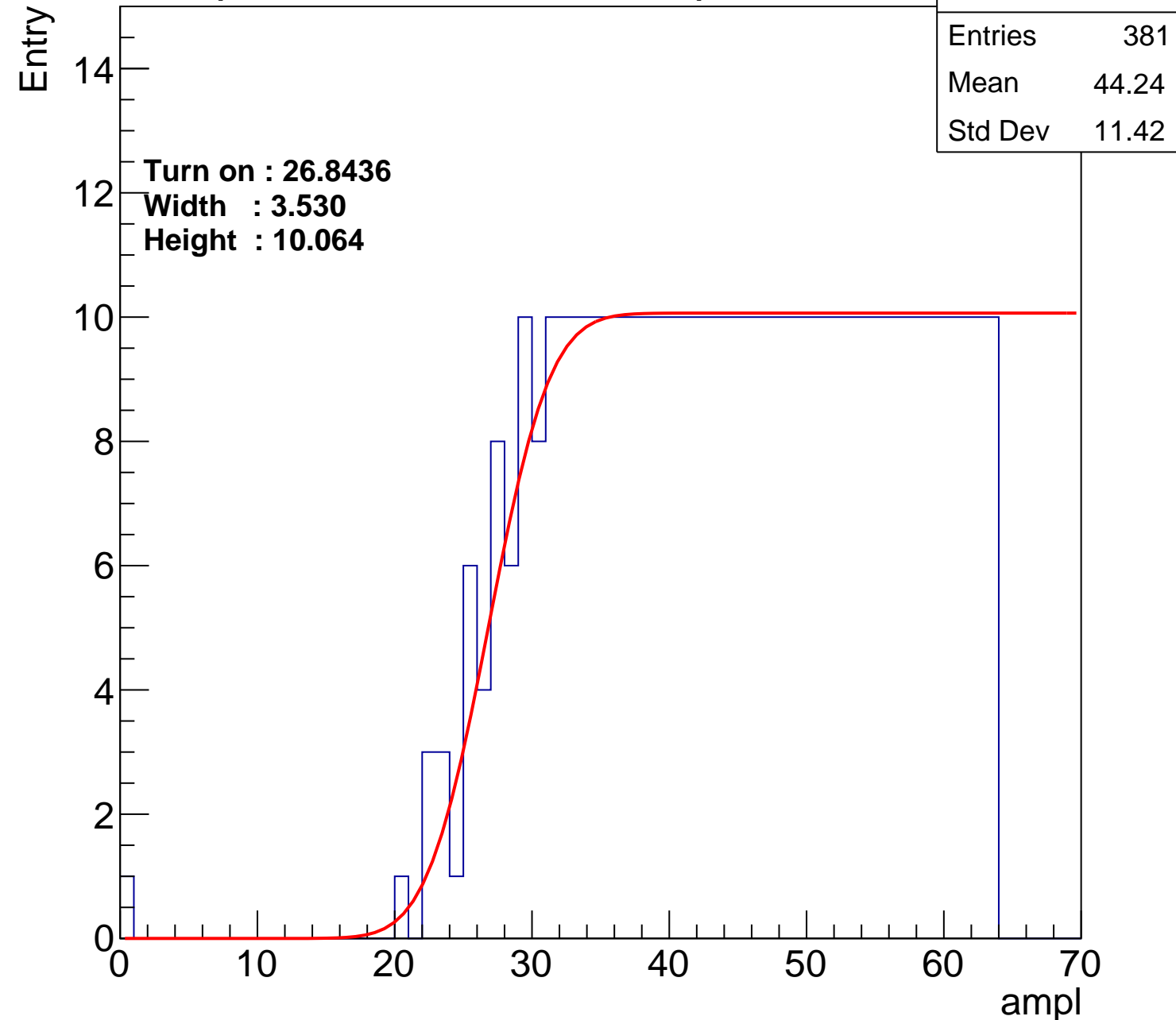
Width : 3.530

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch108

calib_packv5_042523_0143.root, FC#7, port C2

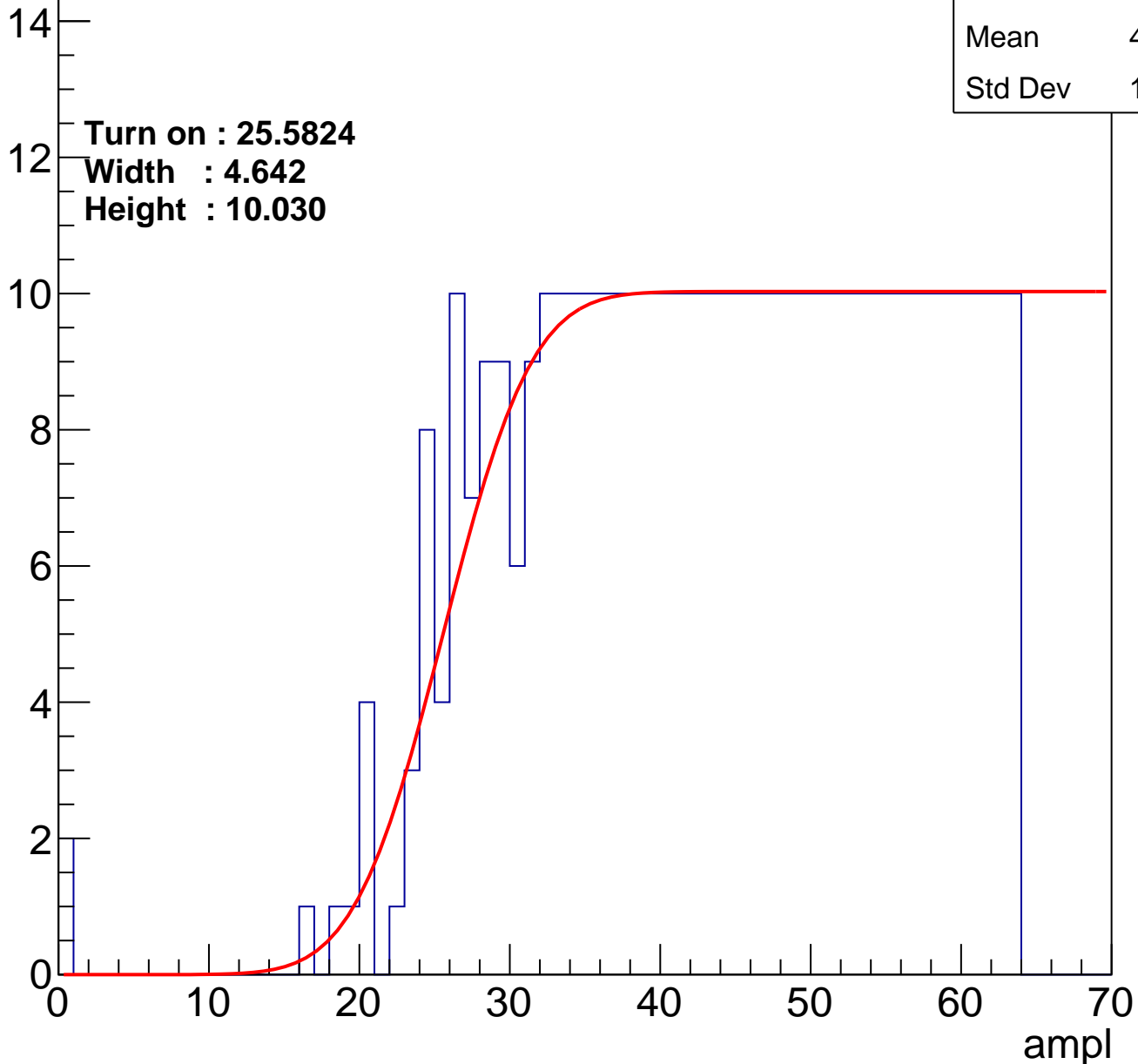
Entries	395
Mean	43.38
Std Dev	12.12

Turn on : 25.5824

Width : 4.642

Height : 10.030

Entry



B1L103S, U25-ch109

calib_packv5_042523_0143.root, FC#7, port C2

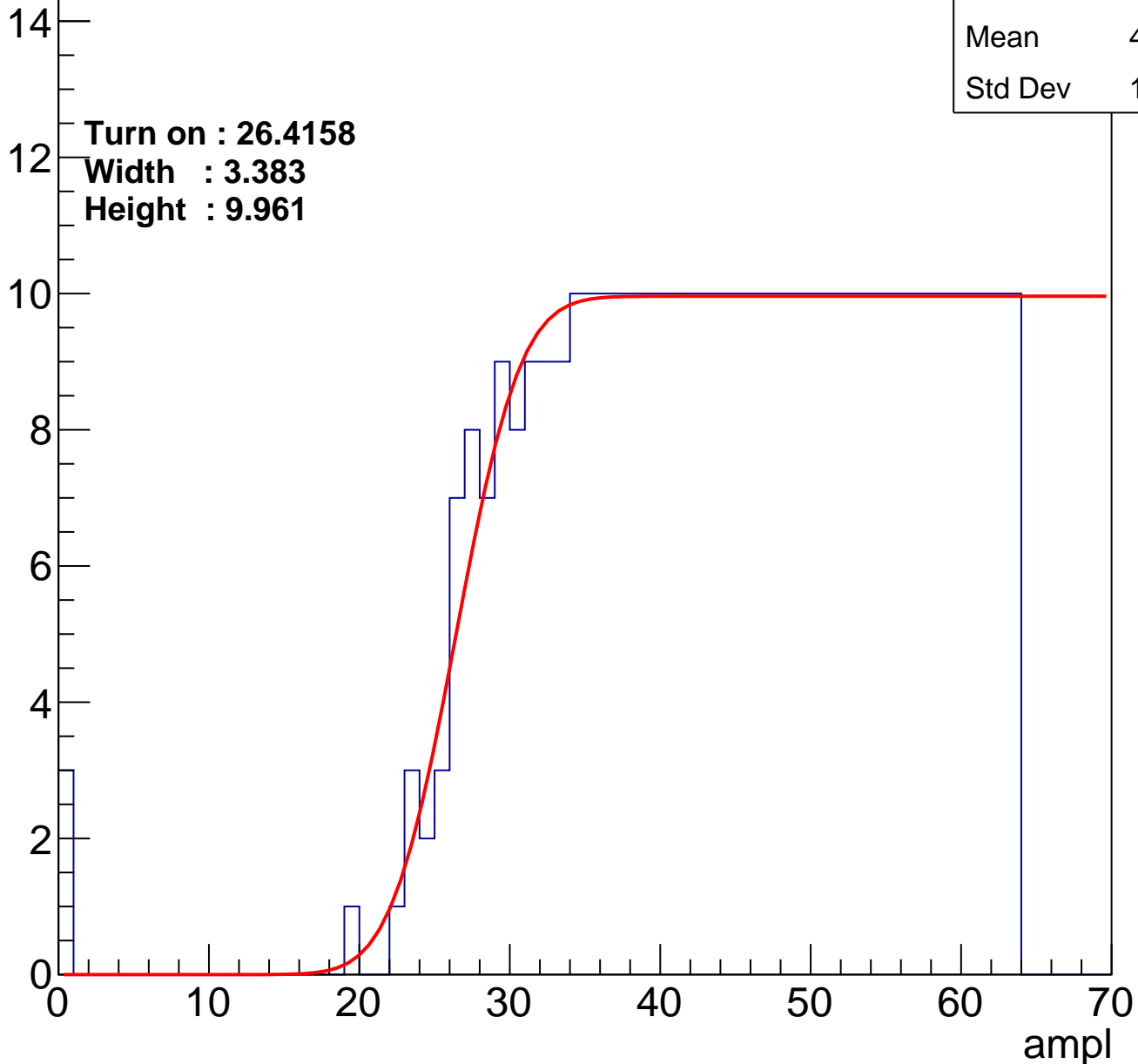
Entries	379
Mean	44.17
Std Dev	11.77

Turn on : 26.4158

Width : 3.383

Height : 9.961

Entry



B1L103S, U25-ch110

calib_packv5_042523_0143.root, FC#7, port C2

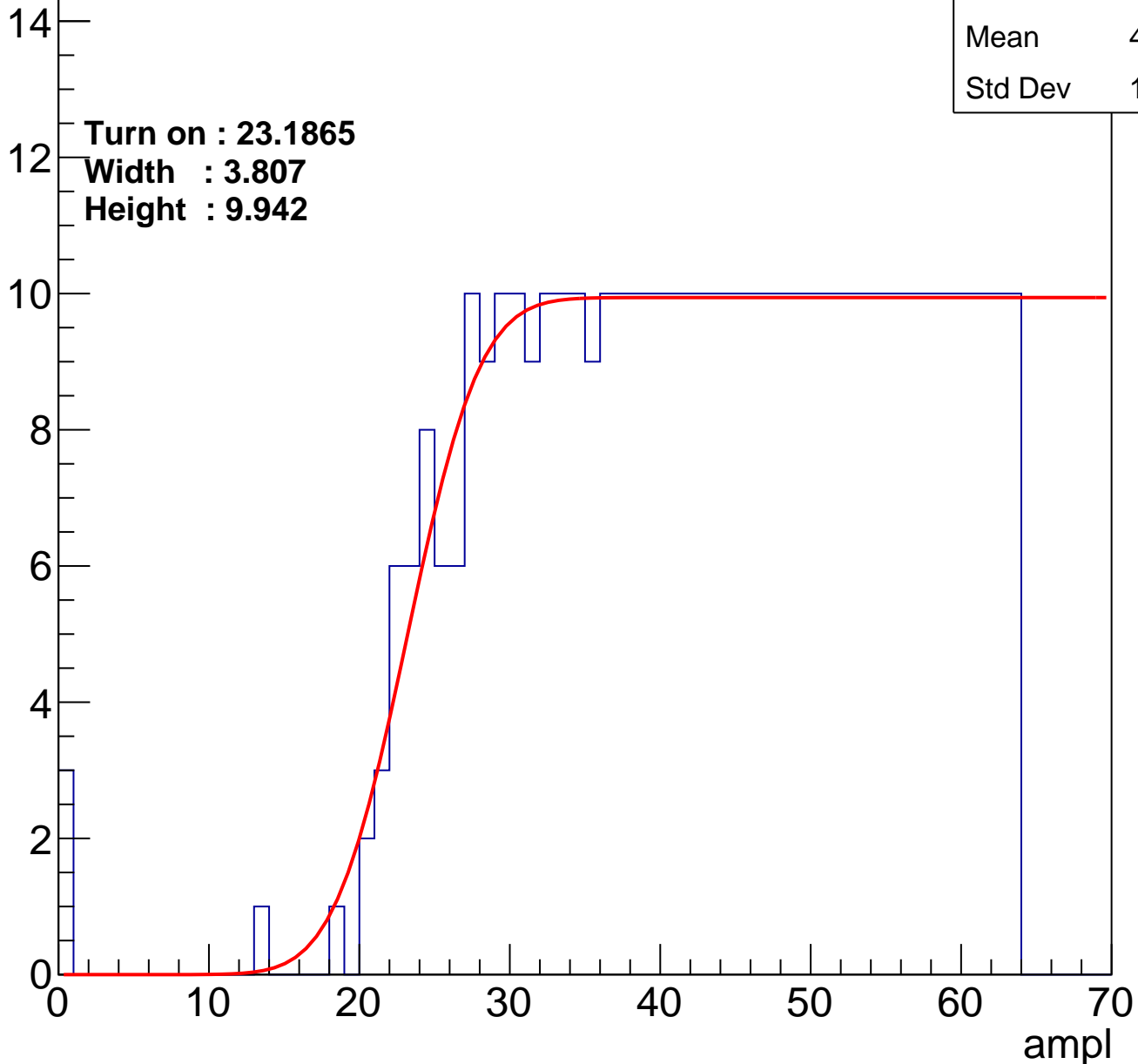
Entries	409
Mean	42.68
Std Dev	12.55

Turn on : 23.1865

Width : 3.807

Height : 9.942

Entry



B1L103S, U25-ch111

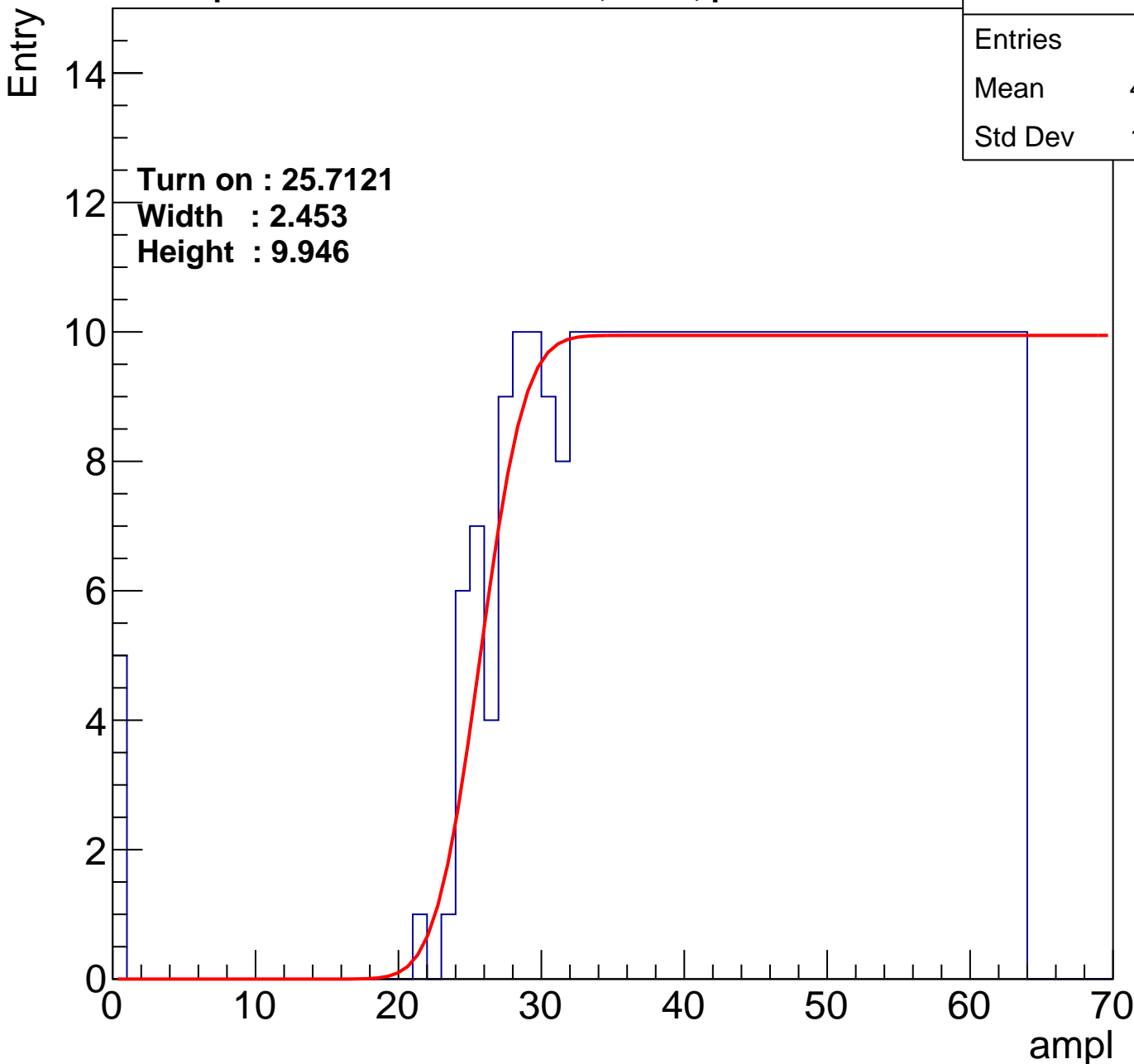
calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.58
Std Dev	12.25

Turn on : 25.7121

Width : 2.453

Height : 9.946



B1L103S, U25-ch112

calib_packv5_042523_0143.root, FC#7, port C2

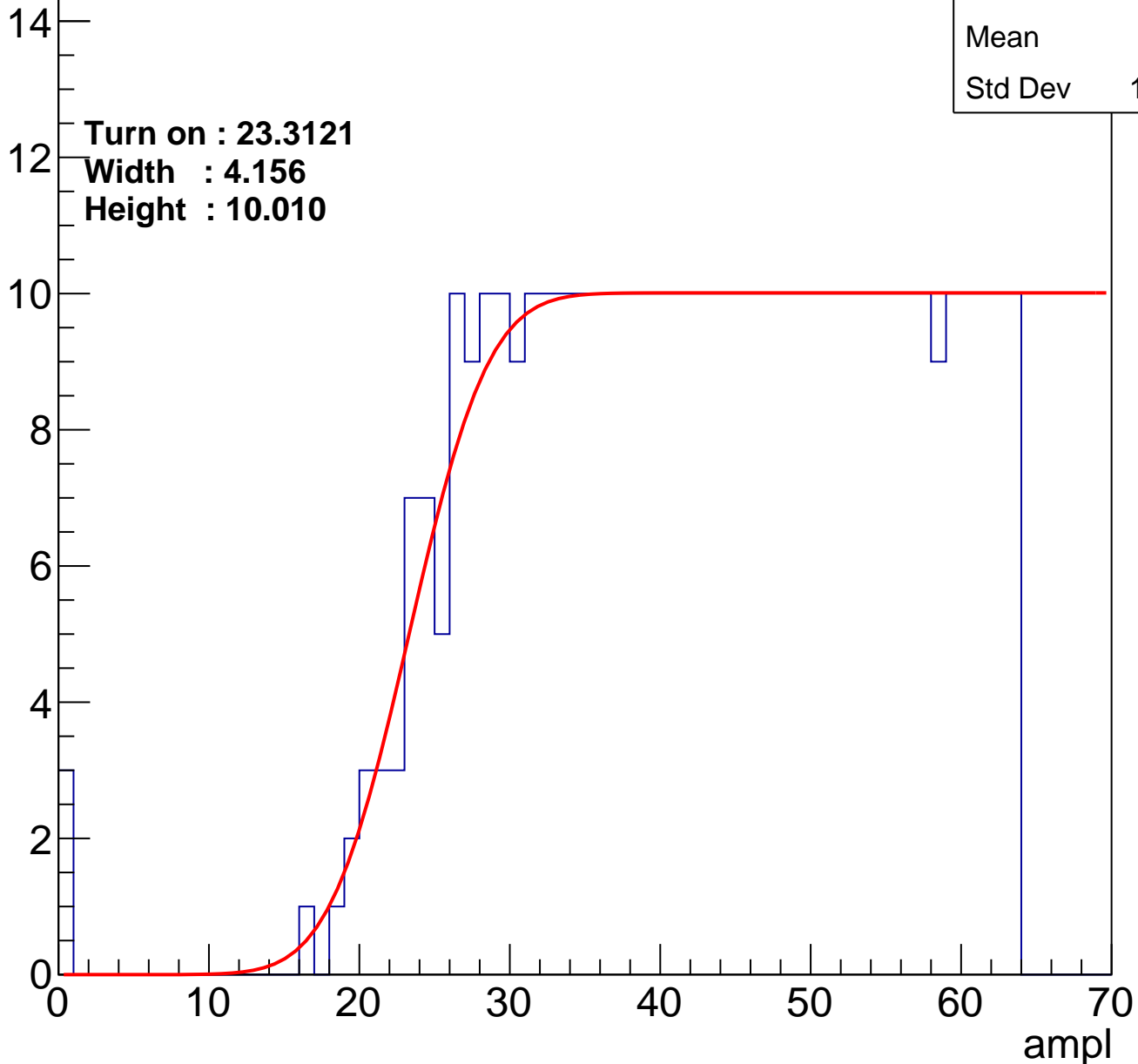
Entries	412
Mean	42.5
Std Dev	12.58

Turn on : 23.3121

Width : 4.156

Height : 10.010

Entry



B1L103S, U25-ch113

calib_packv5_042523_0143.root, FC#7, port C2

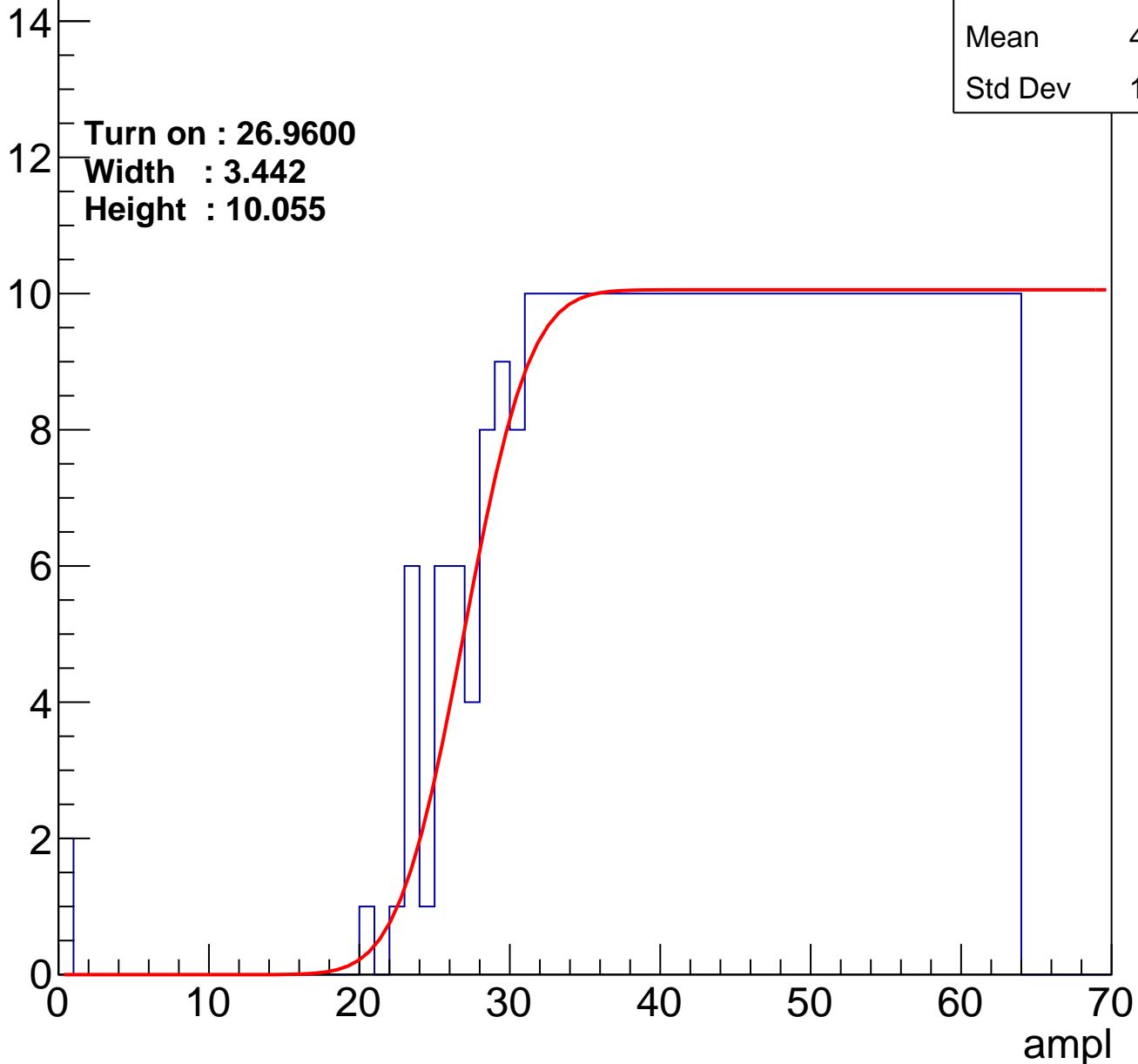
Entries	382
Mean	44.12
Std Dev	11.64

Turn on : 26.9600

Width : 3.442

Height : 10.055

Entry



B1L103S, U25-ch114

calib_packv5_042523_0143.root, FC#7, port C2

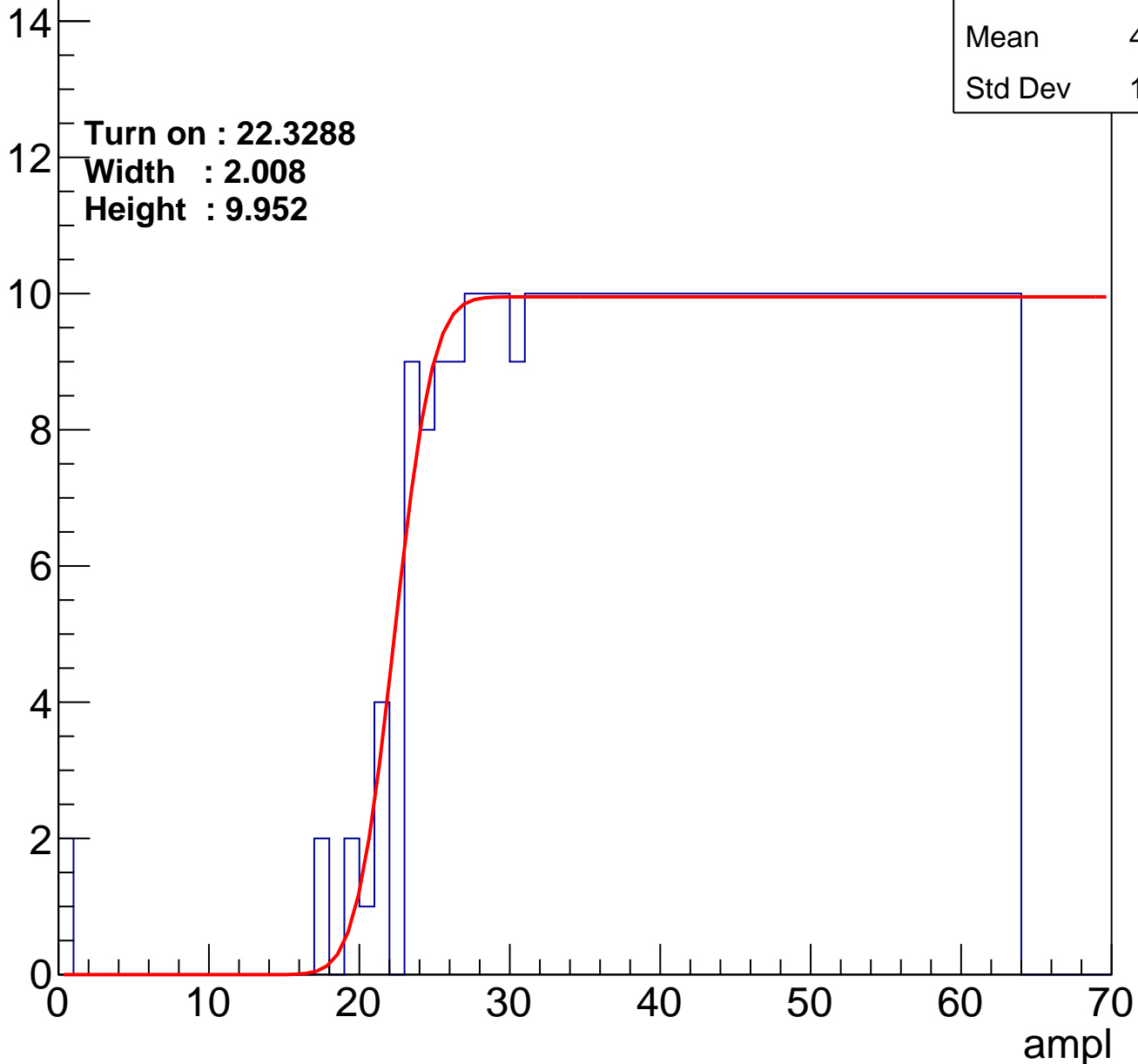
Entries	415
Mean	42.54
Std Dev	12.43

Turn on : 22.3288

Width : 2.008

Height : 9.952

Entry



B1L103S, U25-ch115

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.41
Std Dev	11.92

Turn on : 25.0031

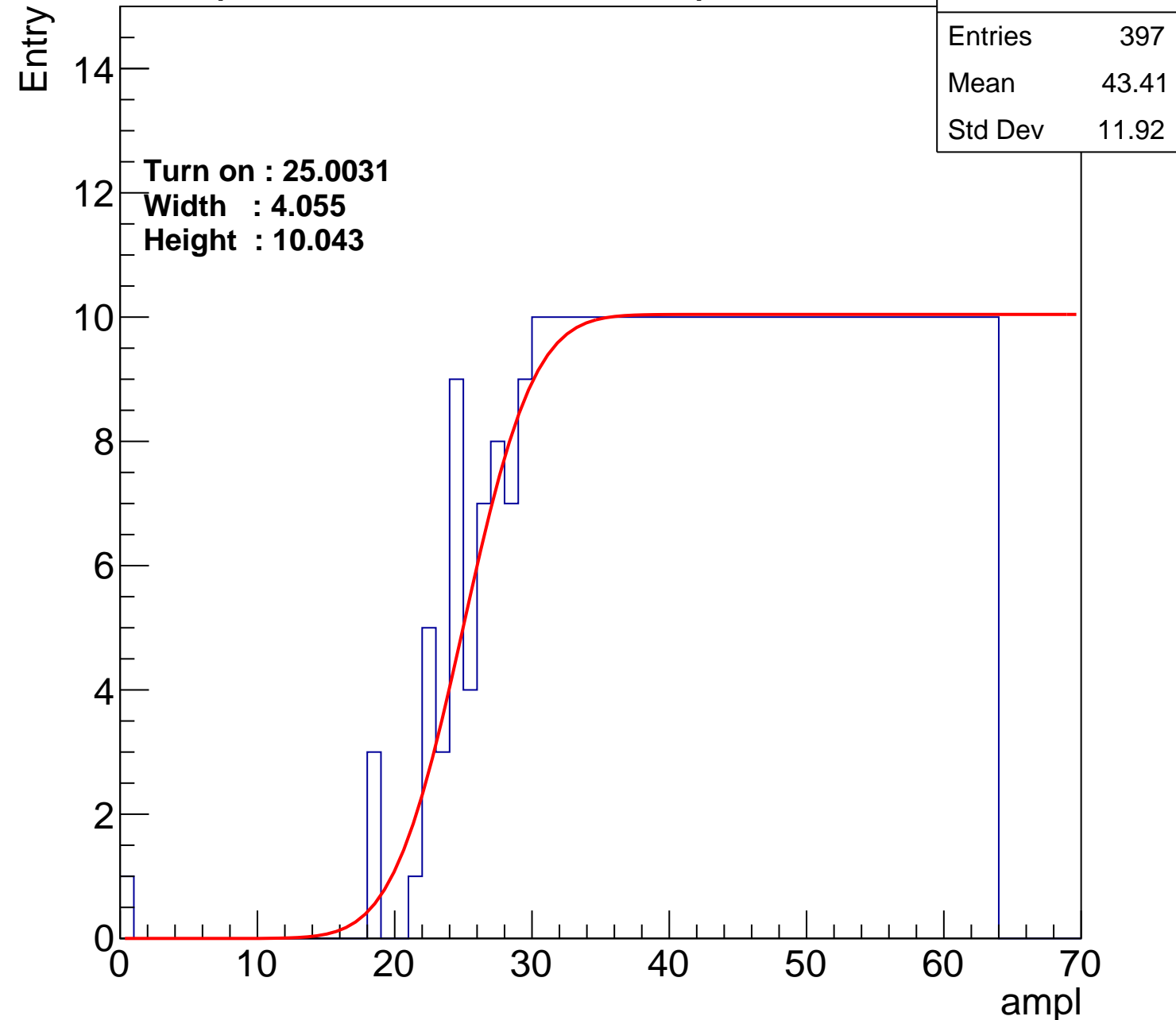
Width : 4.055

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch116

calib_packv5_042523_0143.root, FC#7, port C2

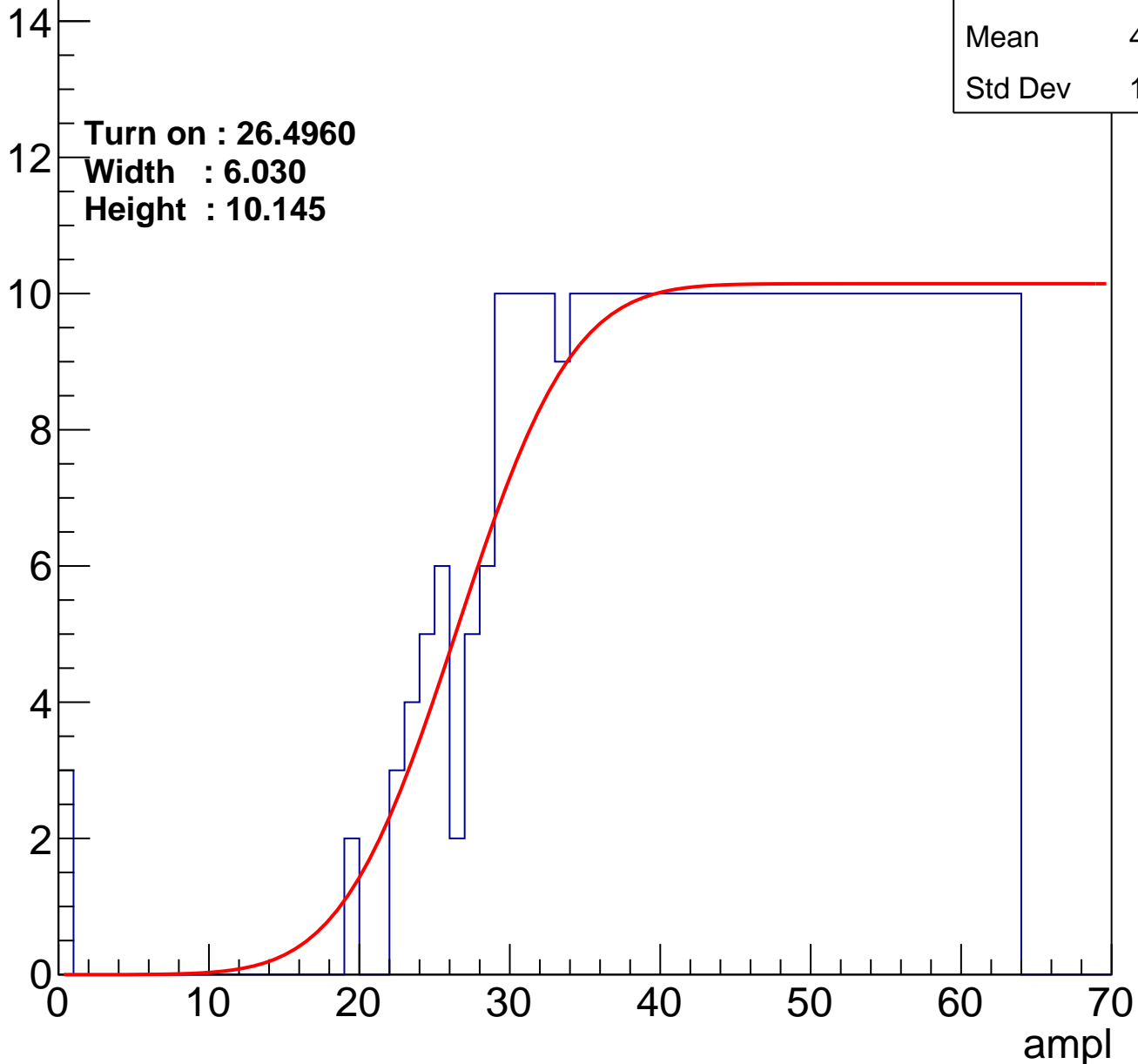
Entries	385
Mean	43.86
Std Dev	11.96

Turn on : 26.4960

Width : 6.030

Height : 10.145

Entry



B1L103S, U25-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.31
Std Dev	11.36

Turn on : 26.8339

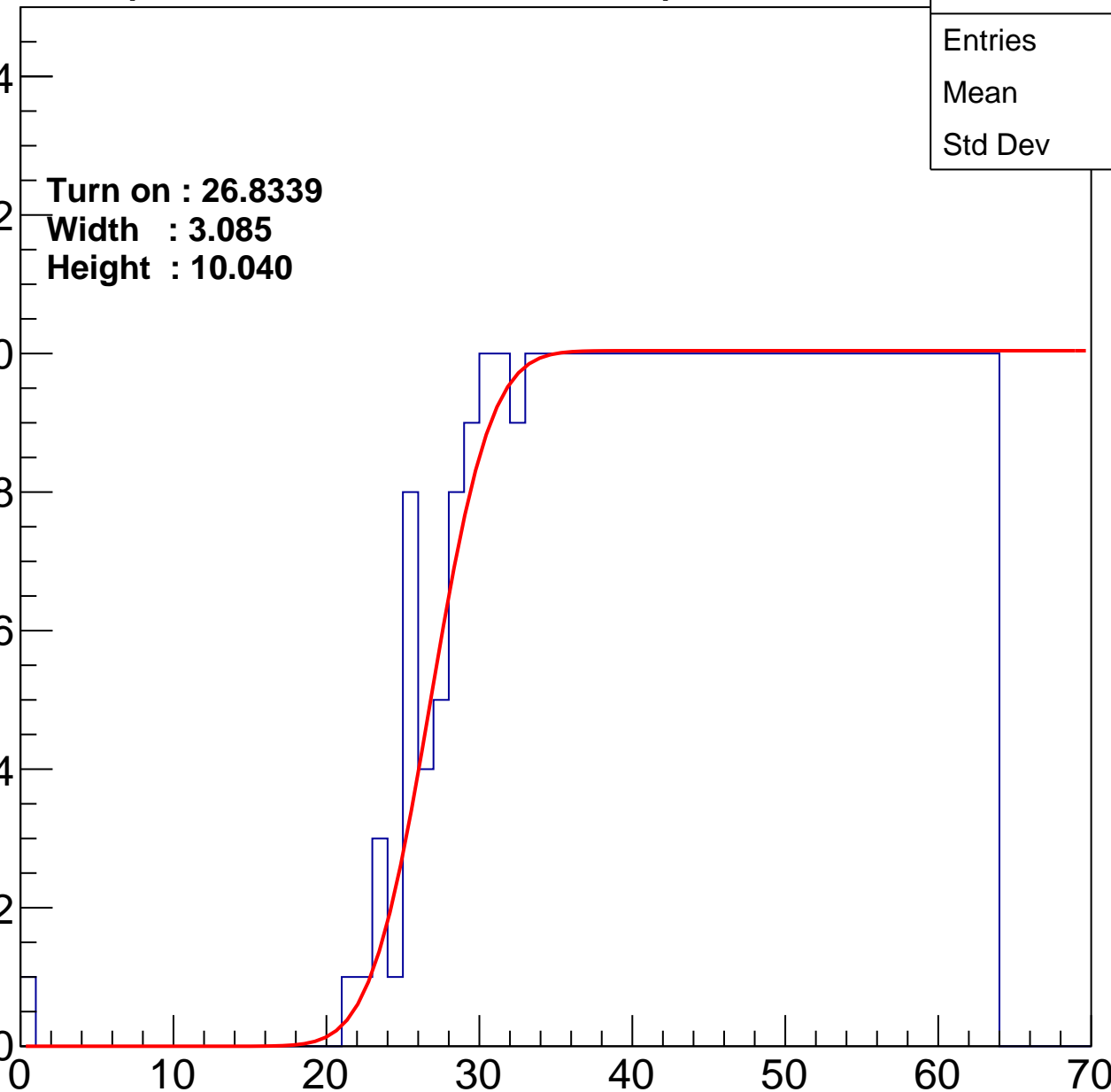
Width : 3.085

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	398
Mean	43.15
Std Dev	12.47

Turn on : 24.8075

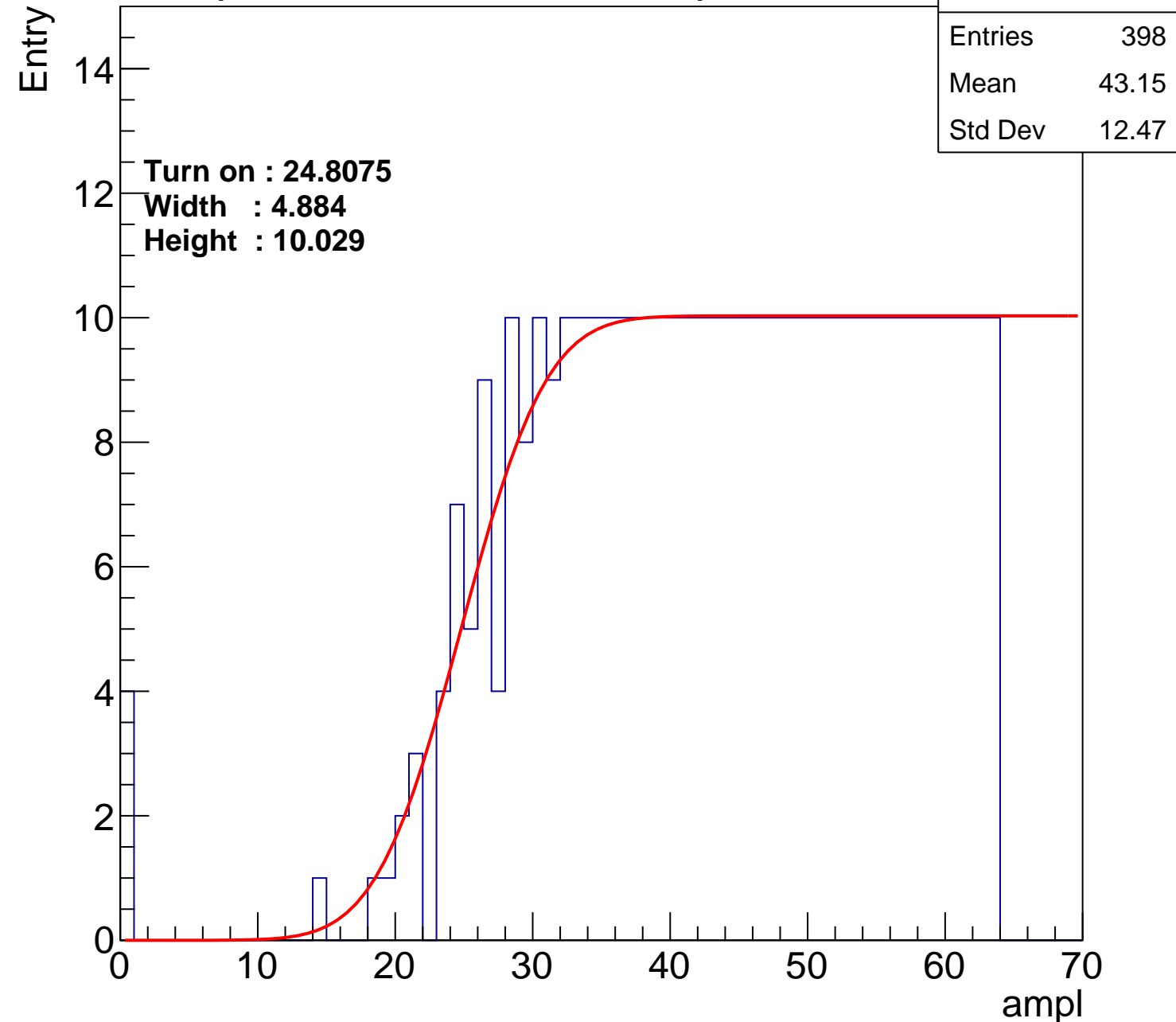
Width : 4.884

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch119

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.34
Std Dev	11.81

Turn on : 27.1355

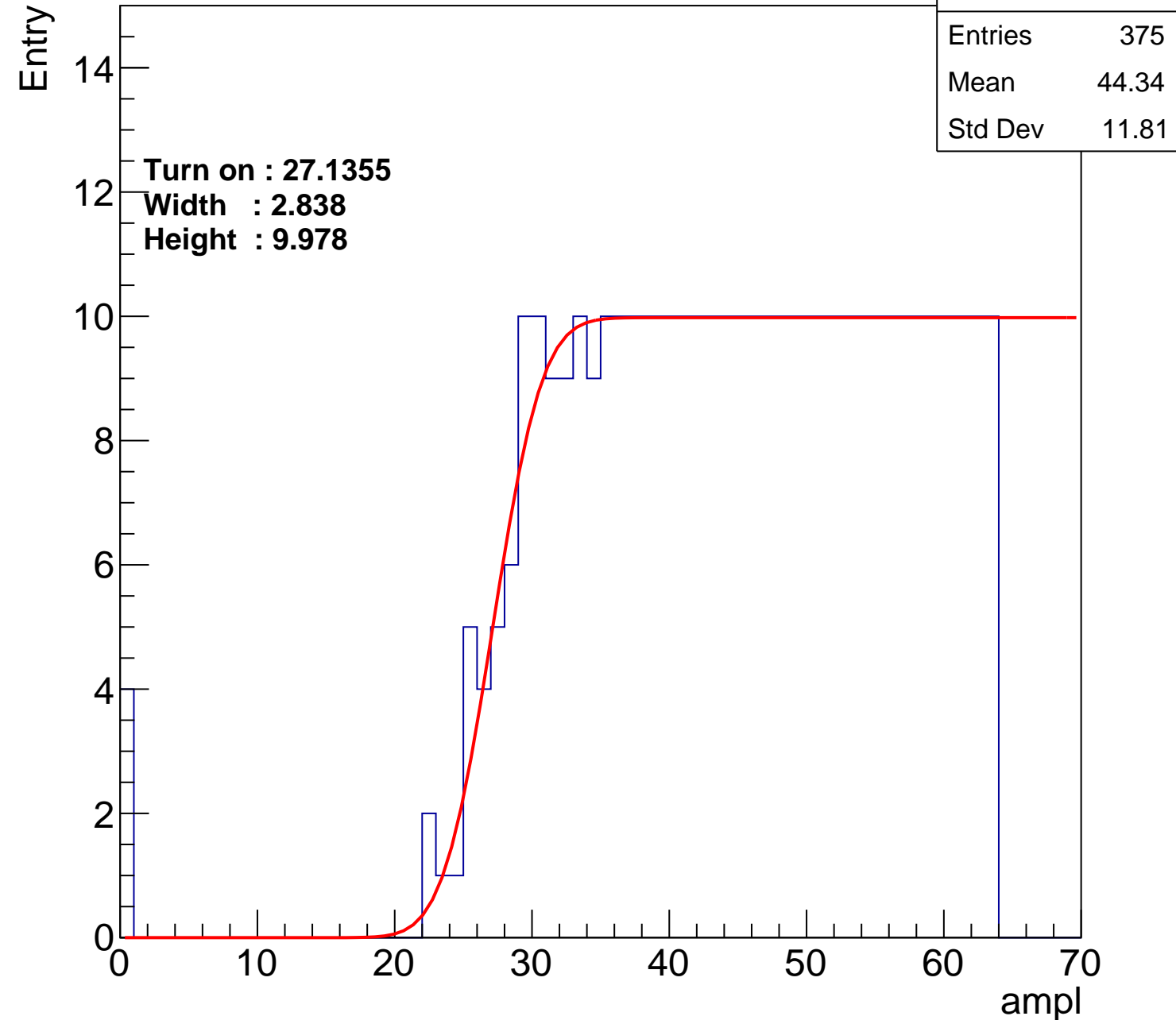
Width : 2.838

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch120

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.09
Std Dev	11.63

Turn on : 25.9718

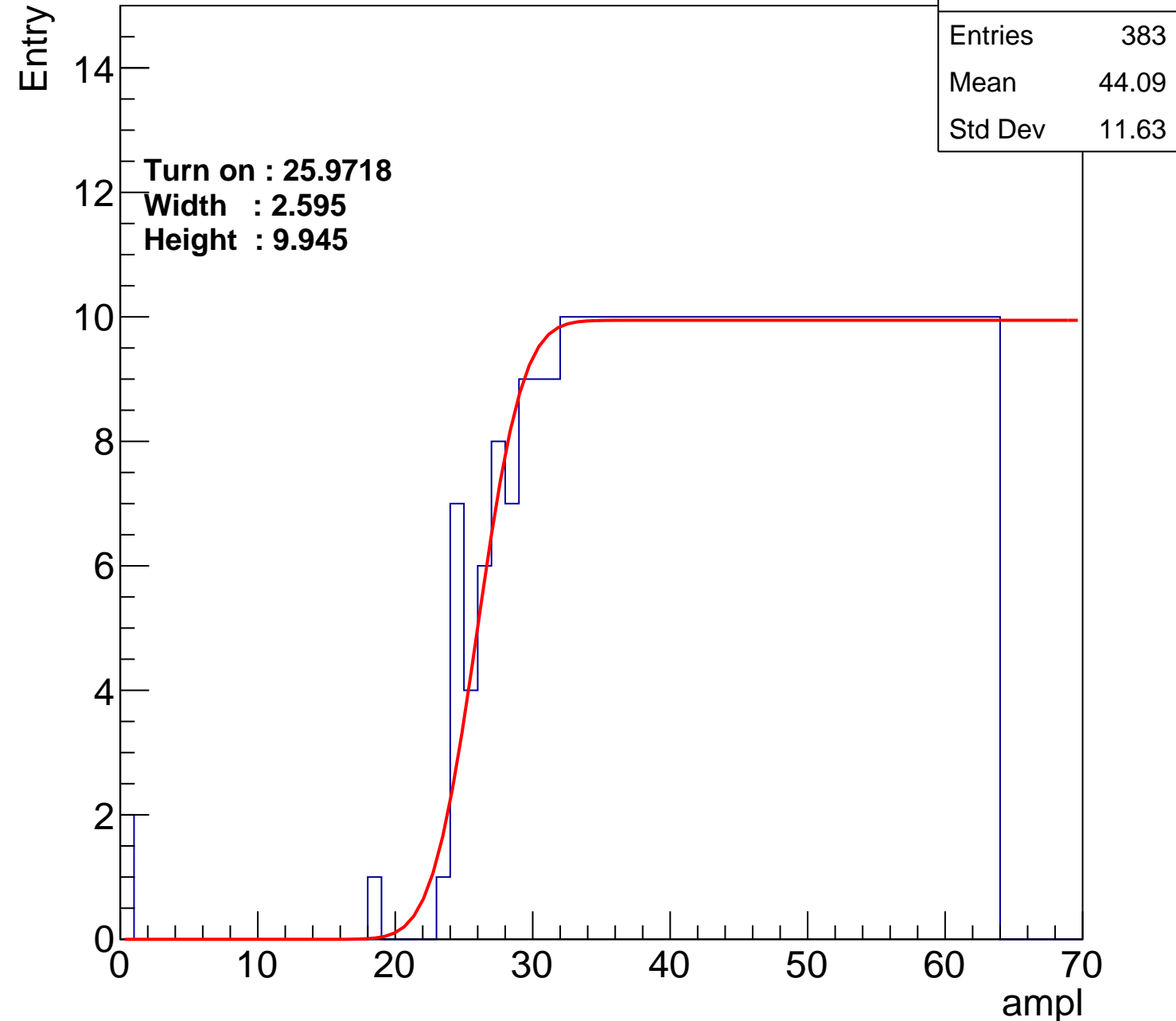
Width : 2.595

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch121

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 26.8449

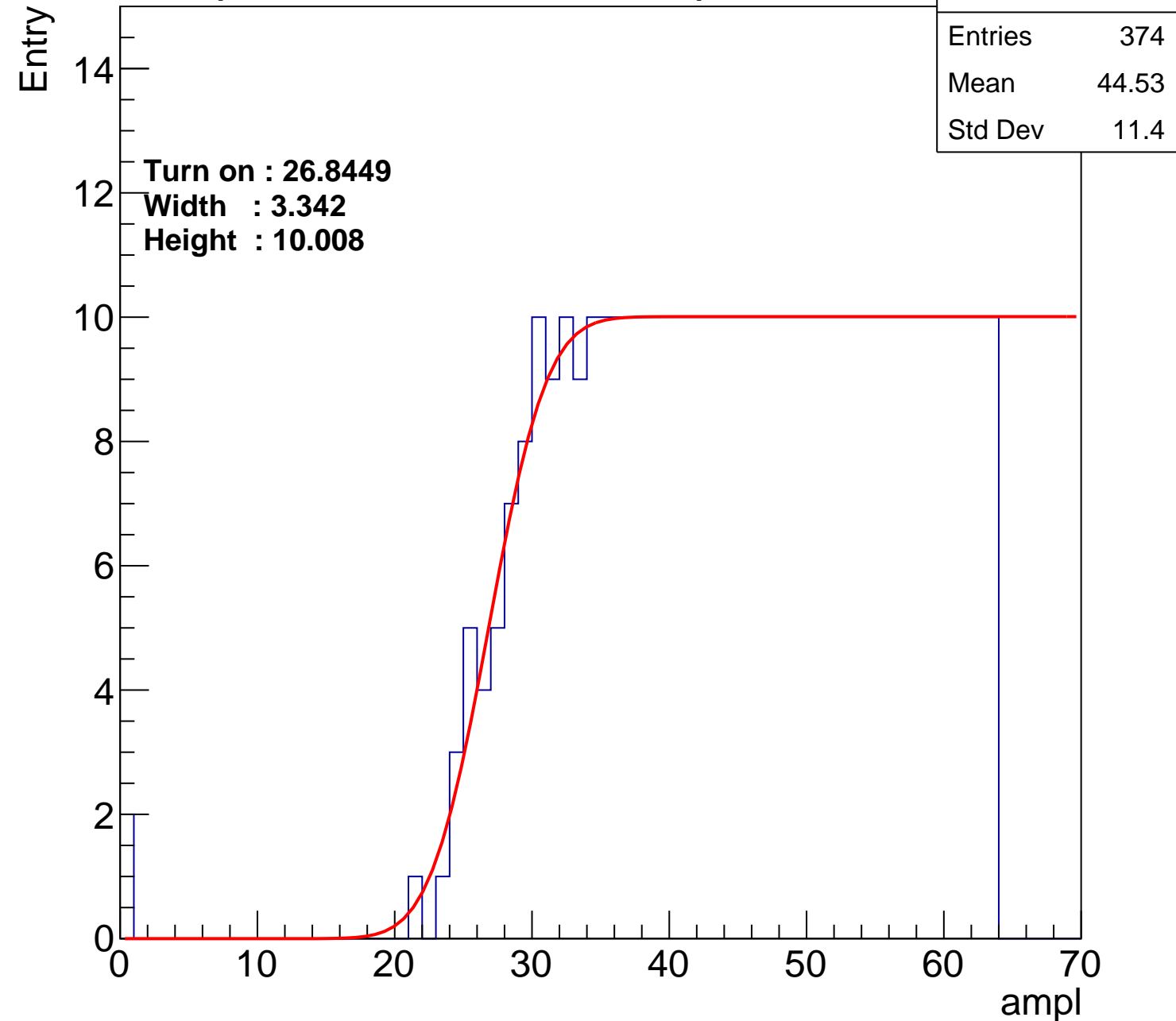
Width : 3.342

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch122

calib_packv5_042523_0143.root, FC#7, port C2

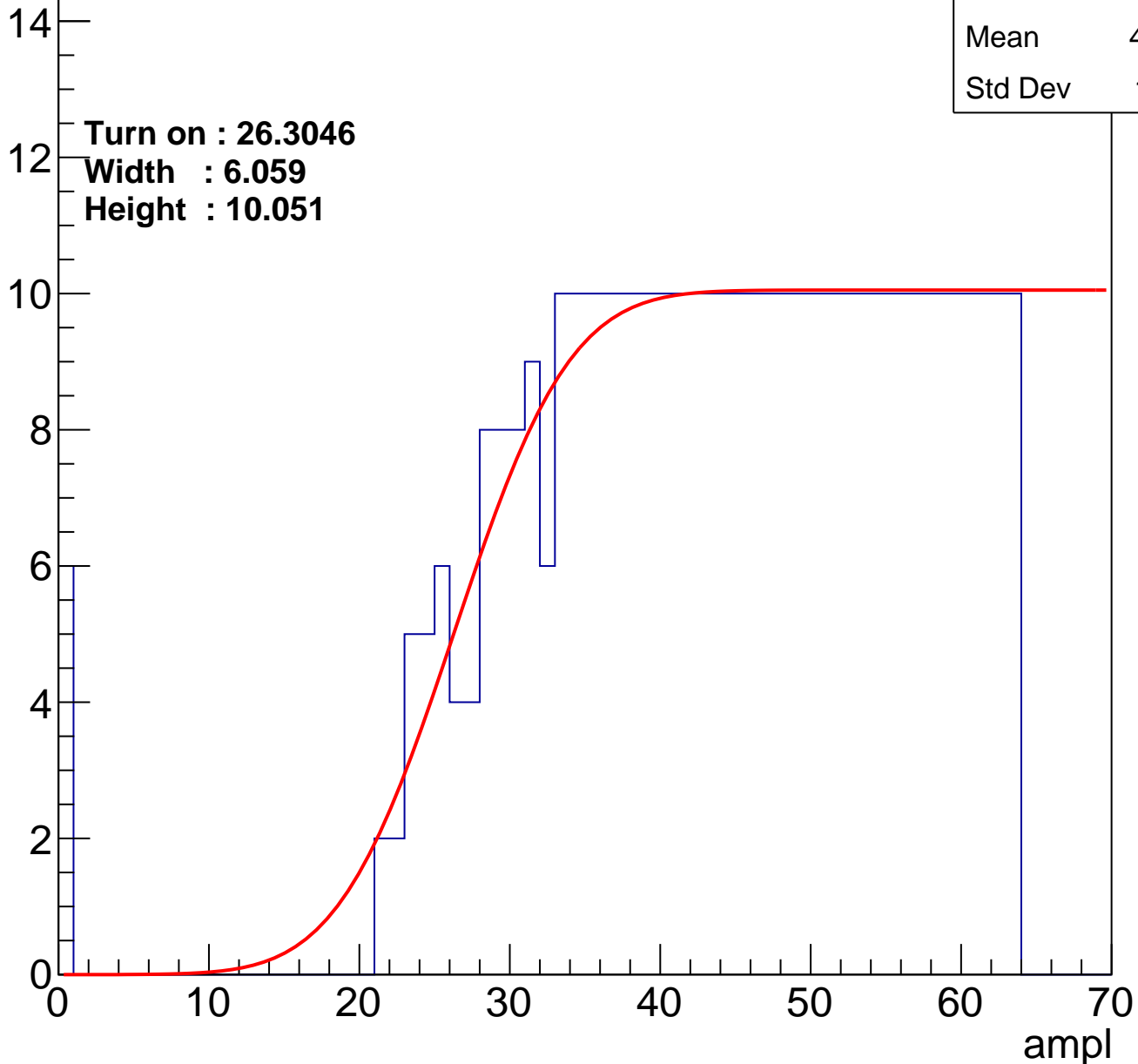
Entries	383
Mean	43.68
Std Dev	12.51

Turn on : 26.3046

Width : 6.059

Height : 10.051

Entry



B1L103S, U25-ch123

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.23
Std Dev	10.93

Turn on : 28.2252

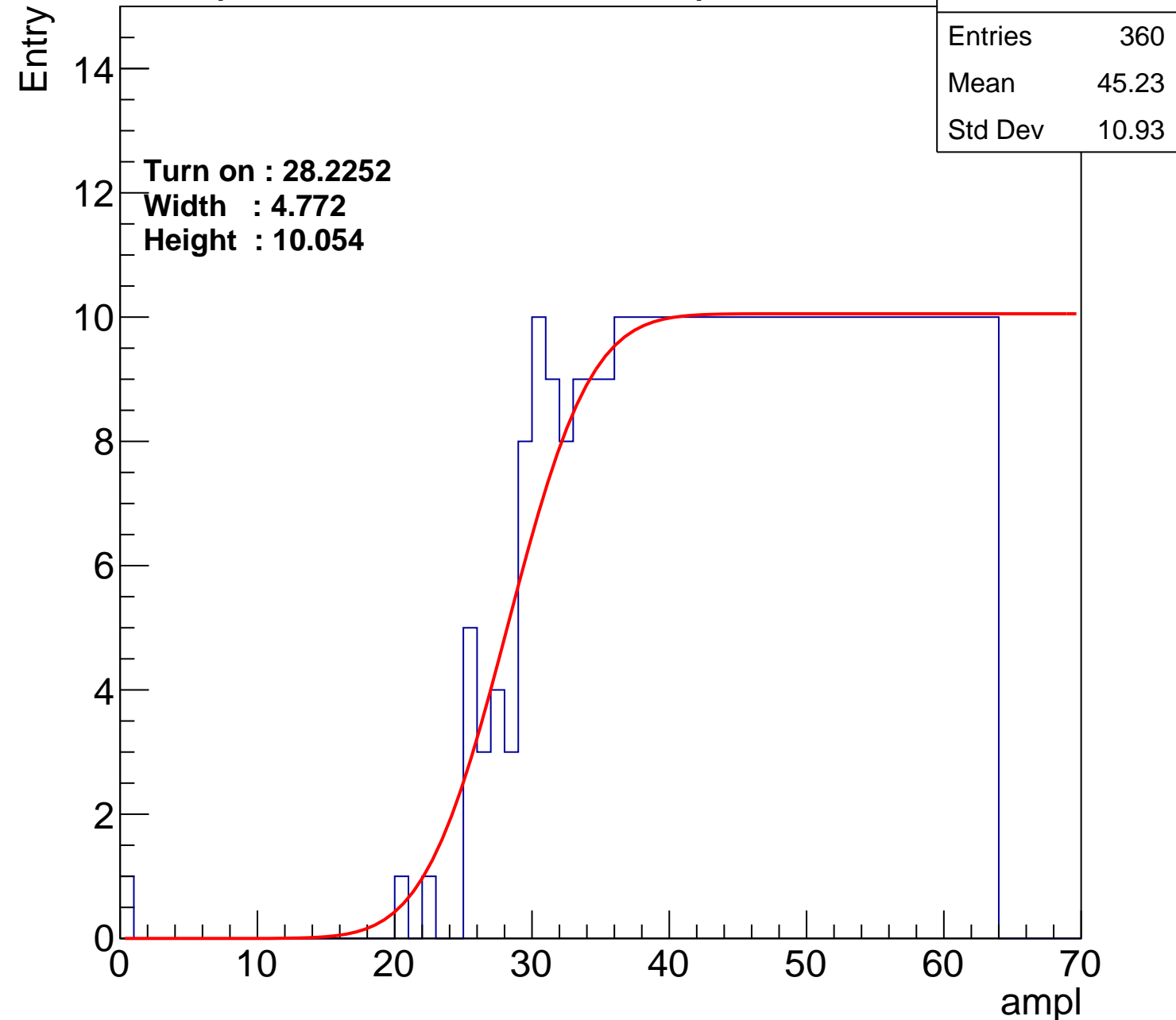
Width : 4.772

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch124

calib_packv5_042523_0143.root, FC#7, port C2

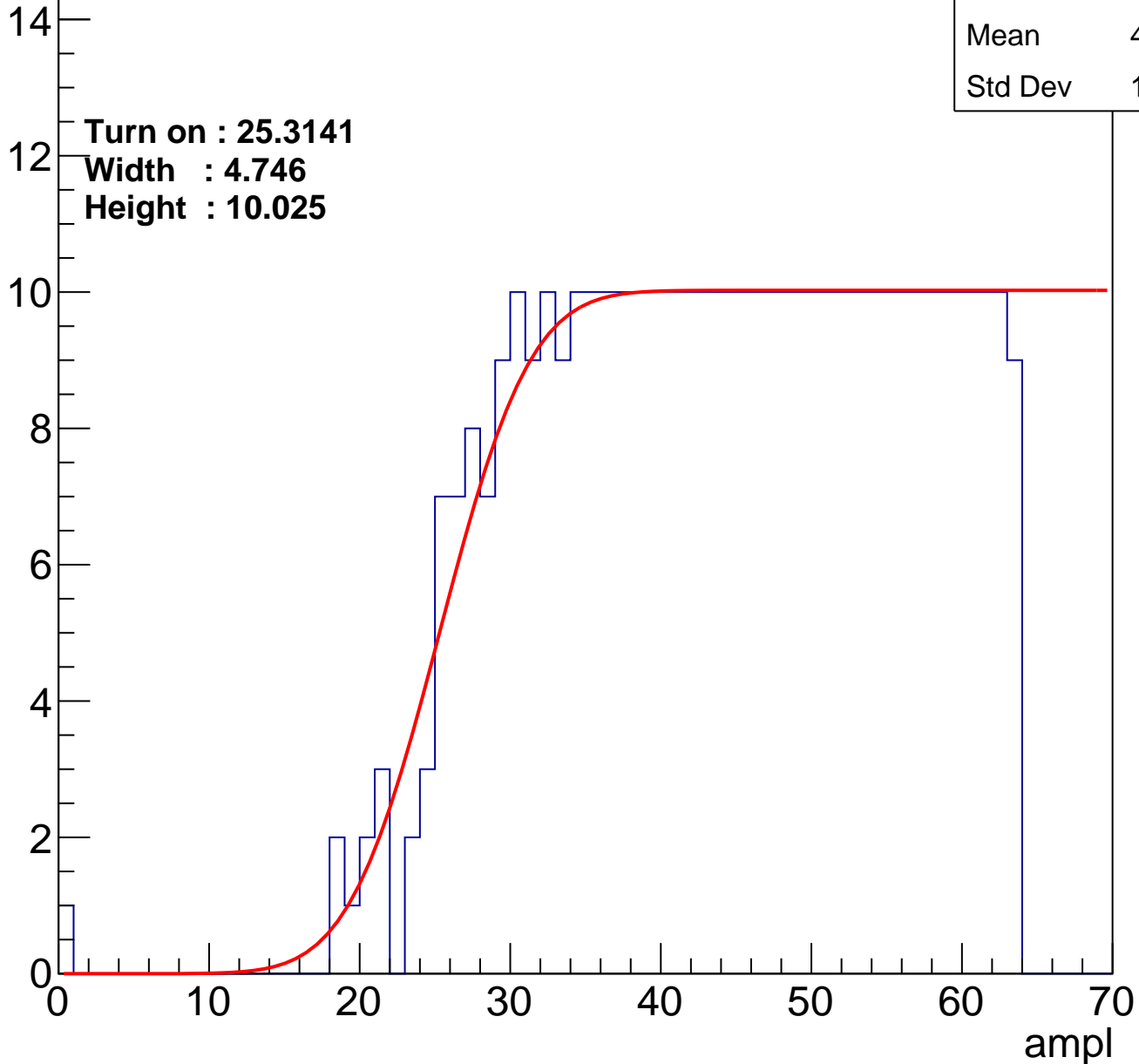
Entries	389
Mean	43.67
Std Dev	11.76

Turn on : 25.3141

Width : 4.746

Height : 10.025

Entry



B1L103S, U25-ch125

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.27
Std Dev	11.03

Turn on : 28.0826

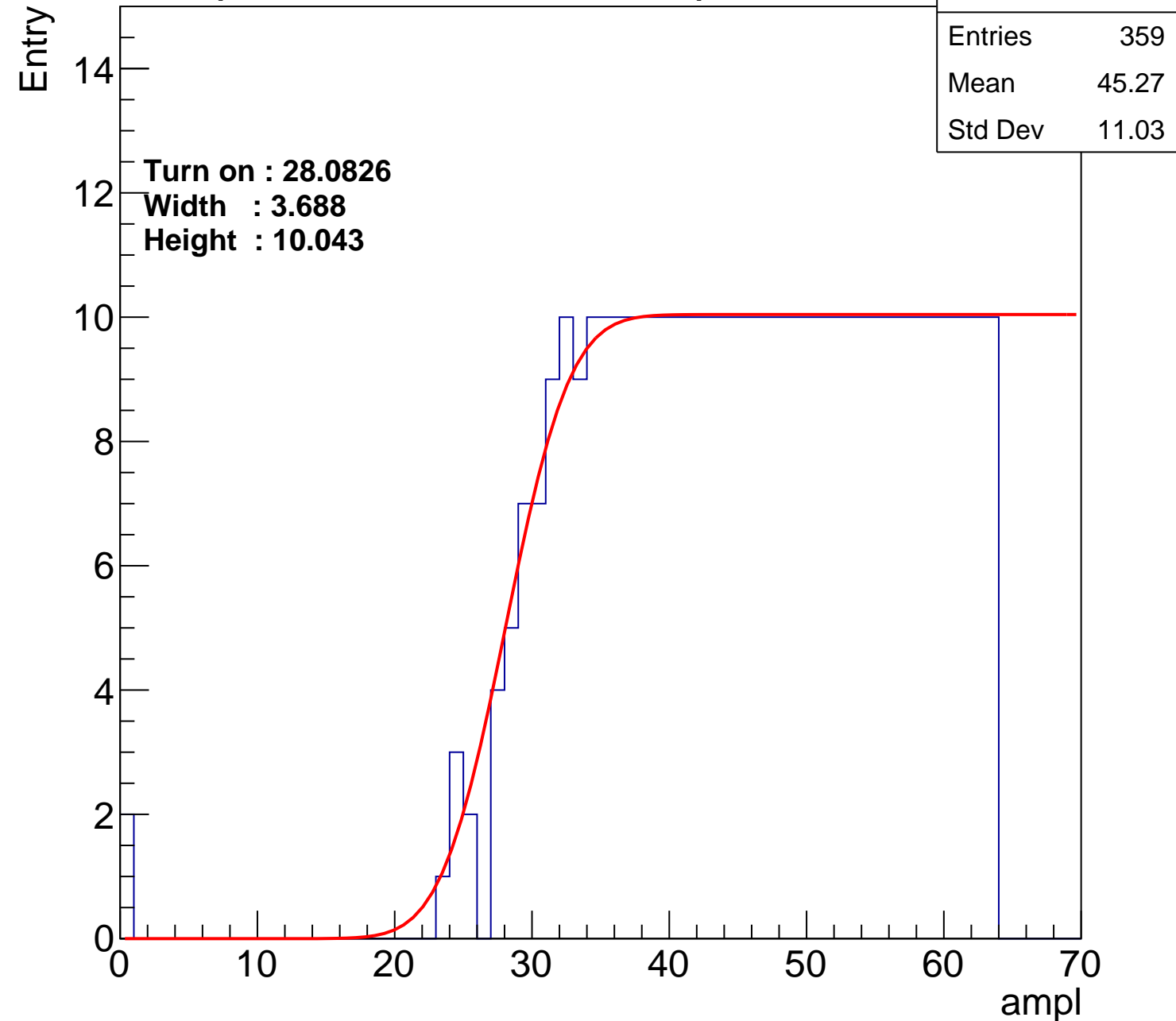
Width : 3.688

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U25-ch126

calib_packv5_042523_0143.root, FC#7, port C2

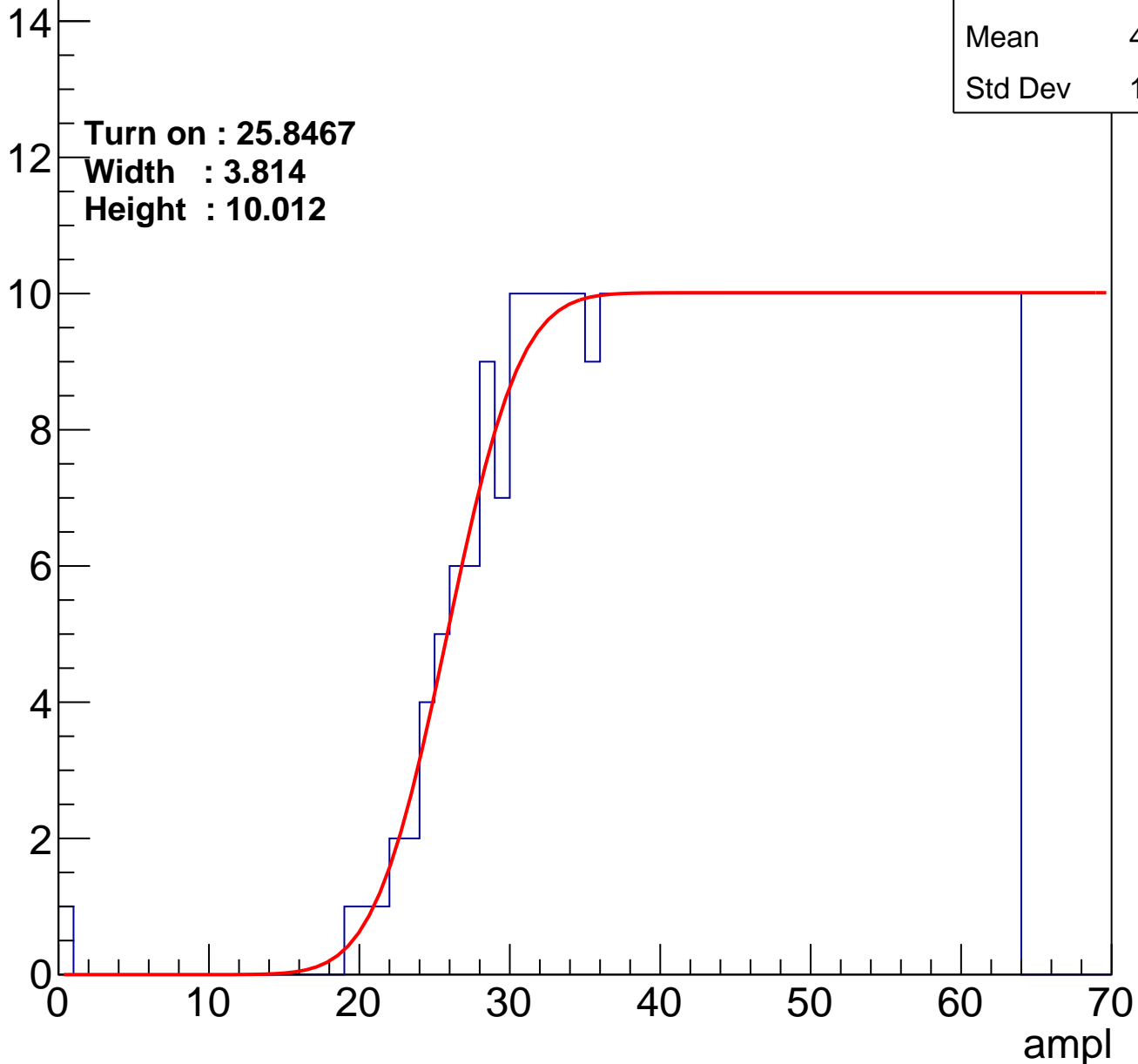
Entries	384
Mean	44.06
Std Dev	11.55

Turn on : 25.8467

Width : 3.814

Height : 10.012

Entry



B1L103S, U25-ch127

calib_packv5_042523_0143.root, FC#7, port C2

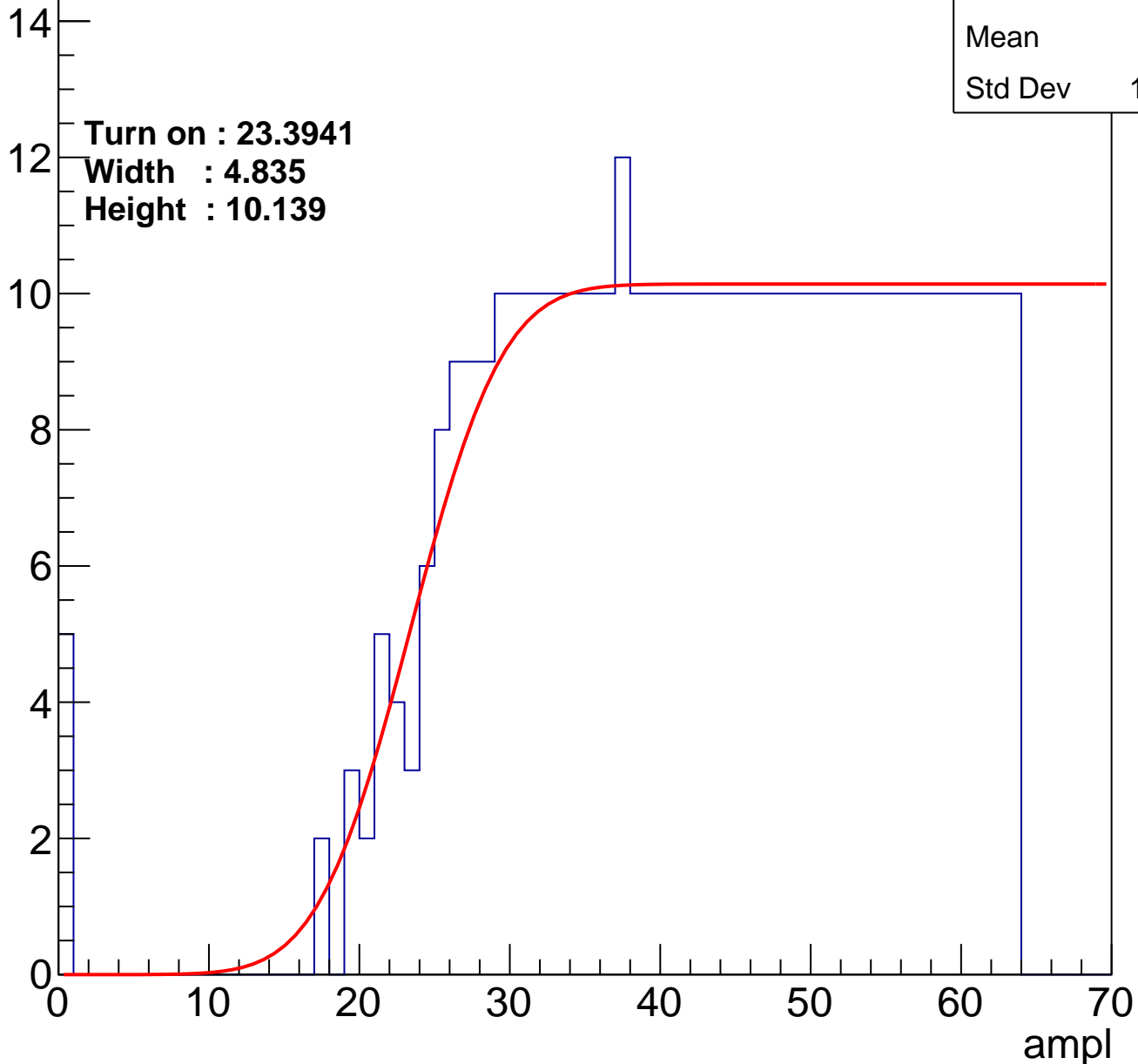
Entries	417
Mean	42.3
Std Dev	12.88

Turn on : 23.3941

Width : 4.835

Height : 10.139

Entry



B1L103S, U25-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	417
Mean	42.3
Std Dev	12.88

Turn on : 23.3941

Width : 4.835

Height : 10.139

Entry

