

B1L001S, U25-ch0, adc0

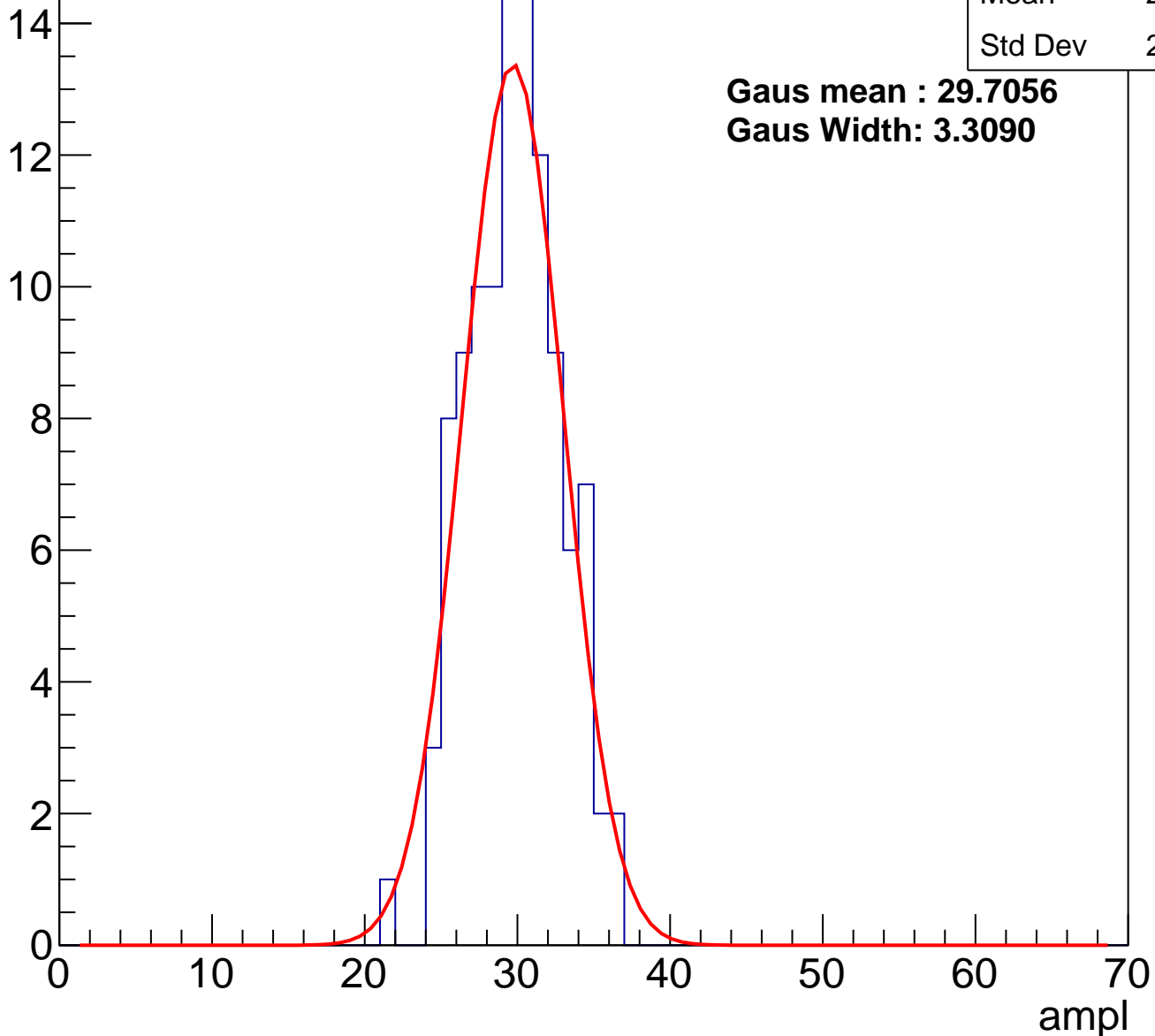
calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	29.36
Std Dev	2.982

Gaus mean : 29.7056

Gaus Width: 3.3090

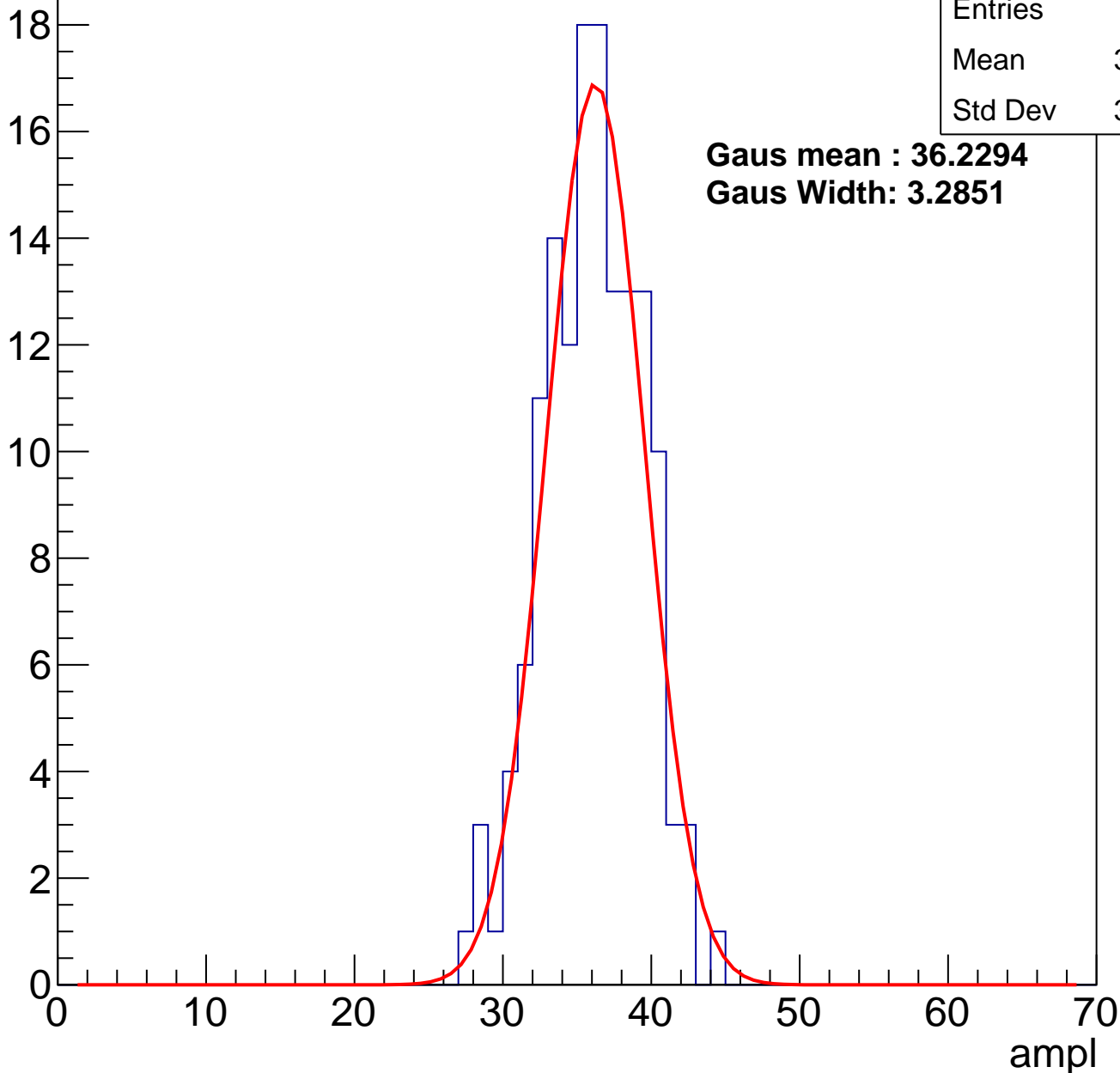
Entry



B1L001S, U25-ch0, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch0, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	43.01
Std Dev	3.32

Gaus mean : 43.3247

Gaus Width: 3.3385

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

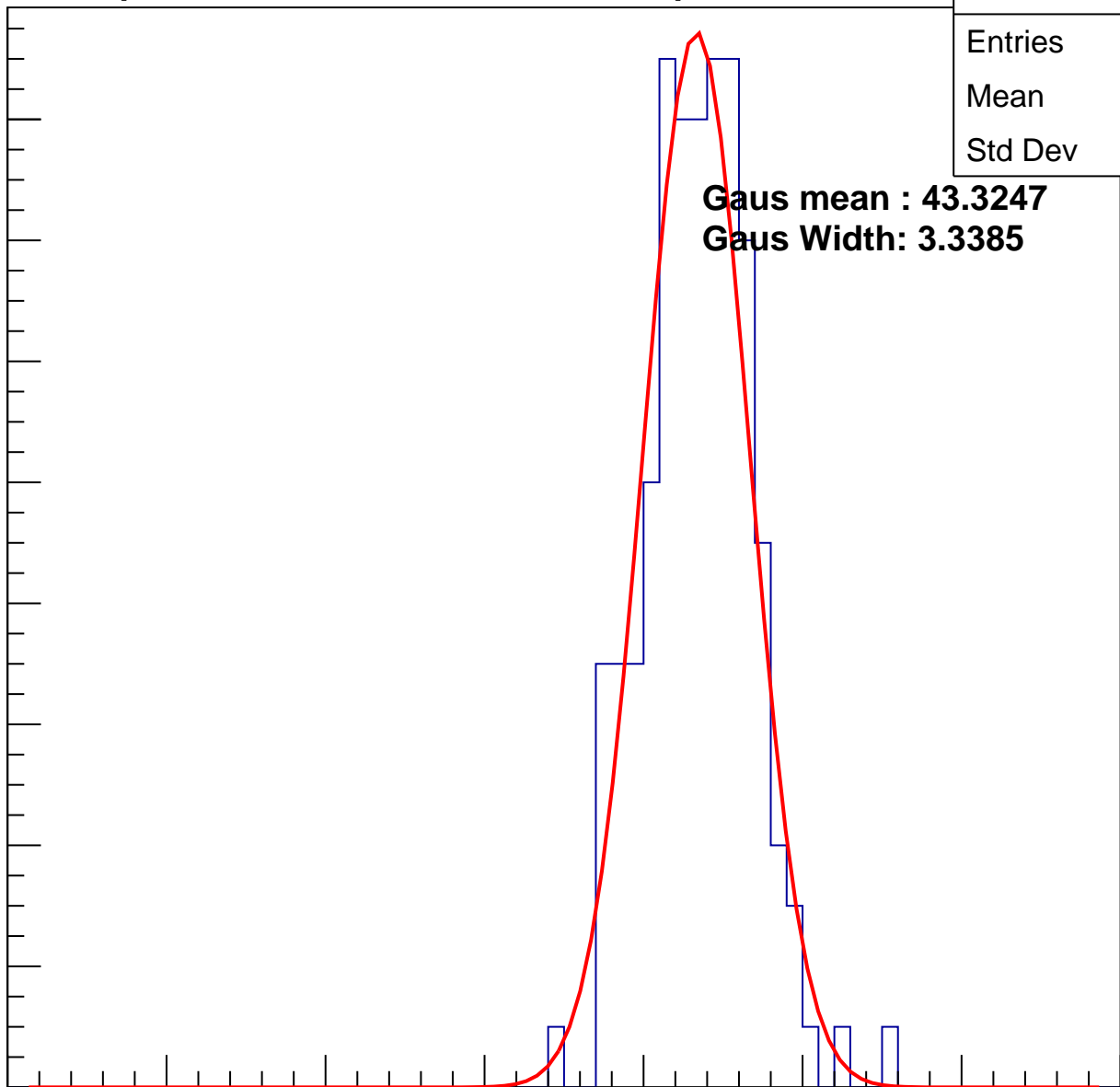
40

50

60

70

ampl



B1L001S, U25-ch0, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	50.04
Std Dev	3.219

Entry

16

14

12

10

8

6

4

2

0

0

10

20

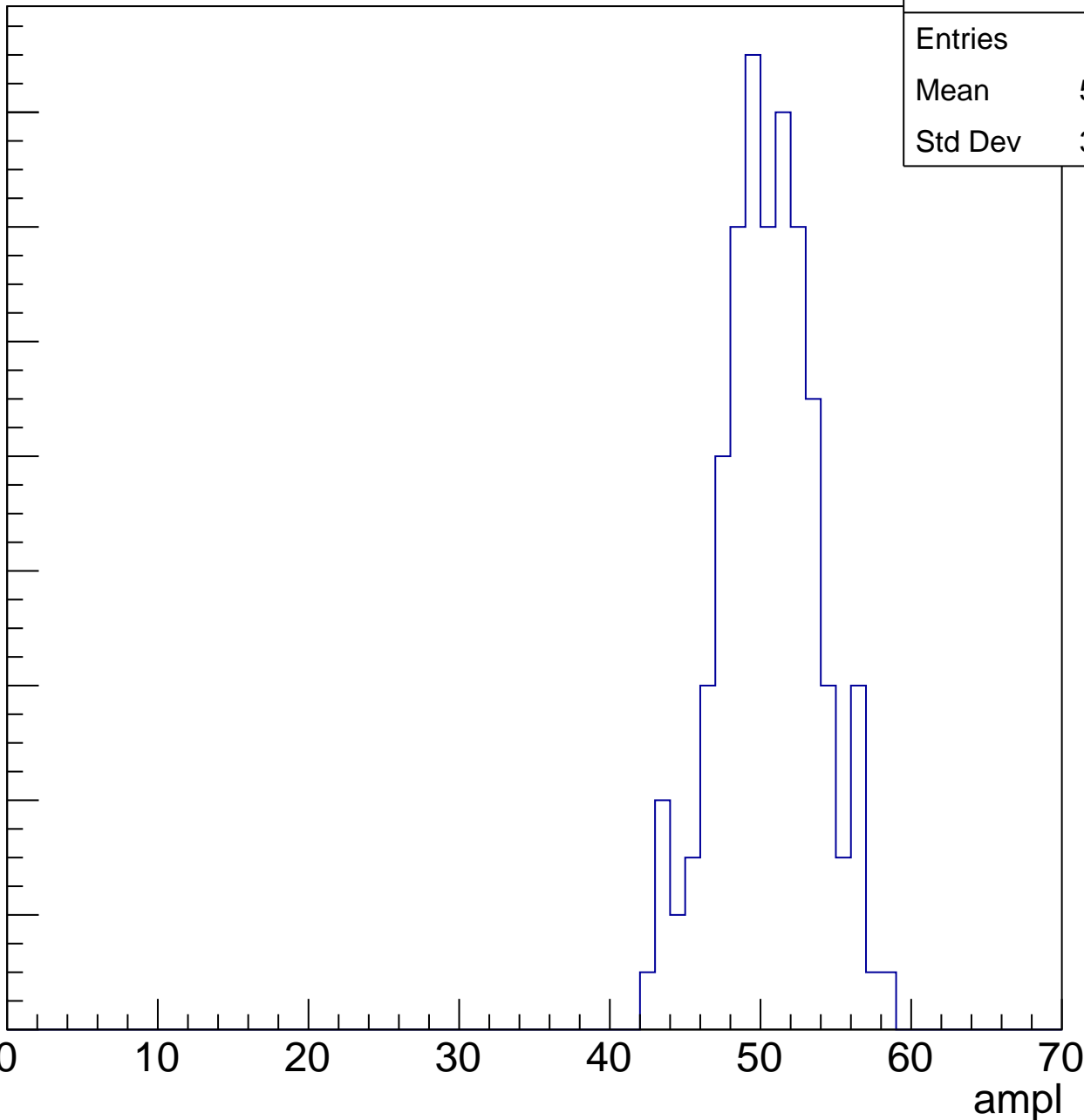
30

40

50

60

ampl

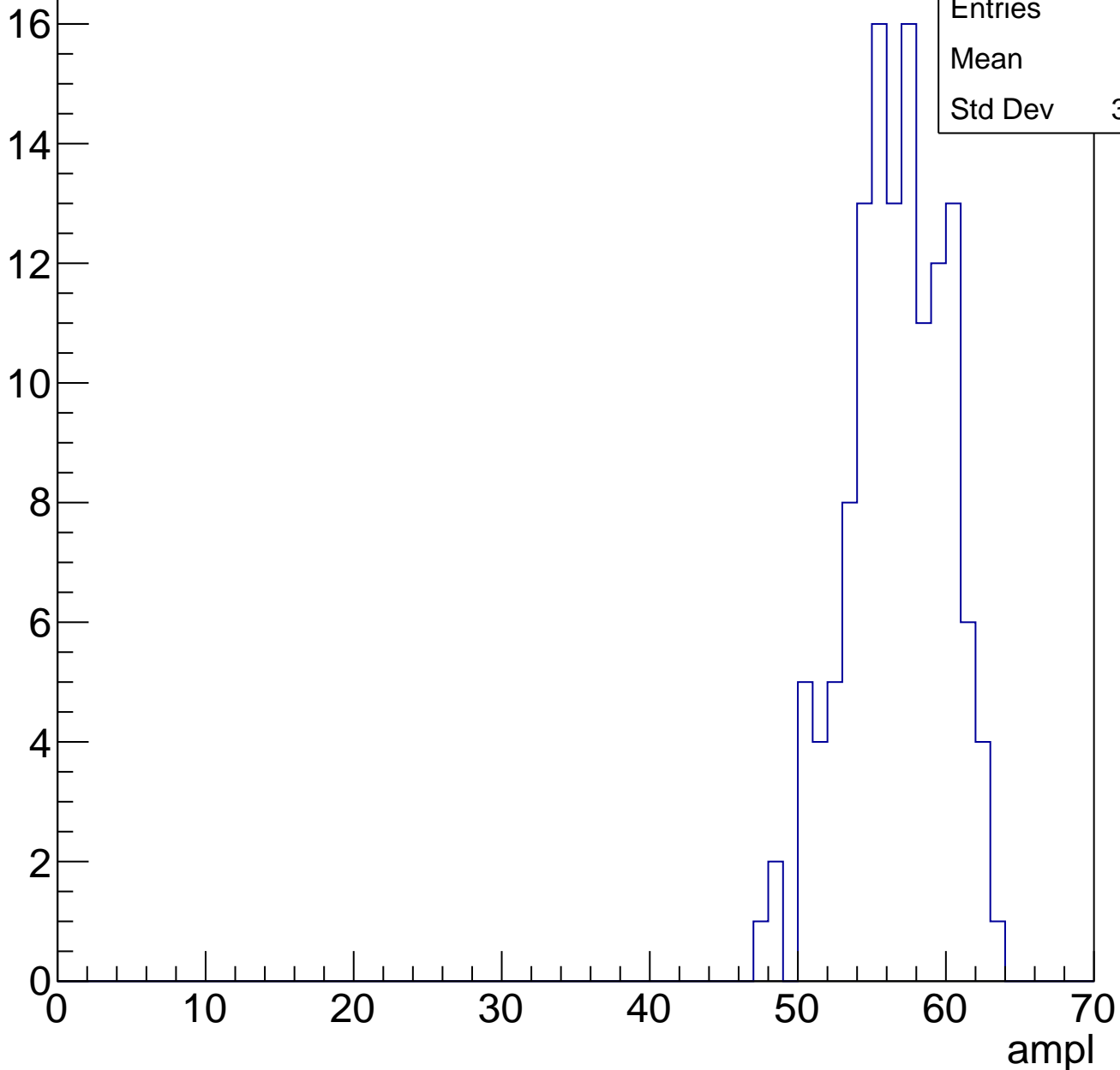


B1L001S, U25-ch0, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	56.2
Std Dev	3.308

Entry



B1L001S, U25-ch0, adc5

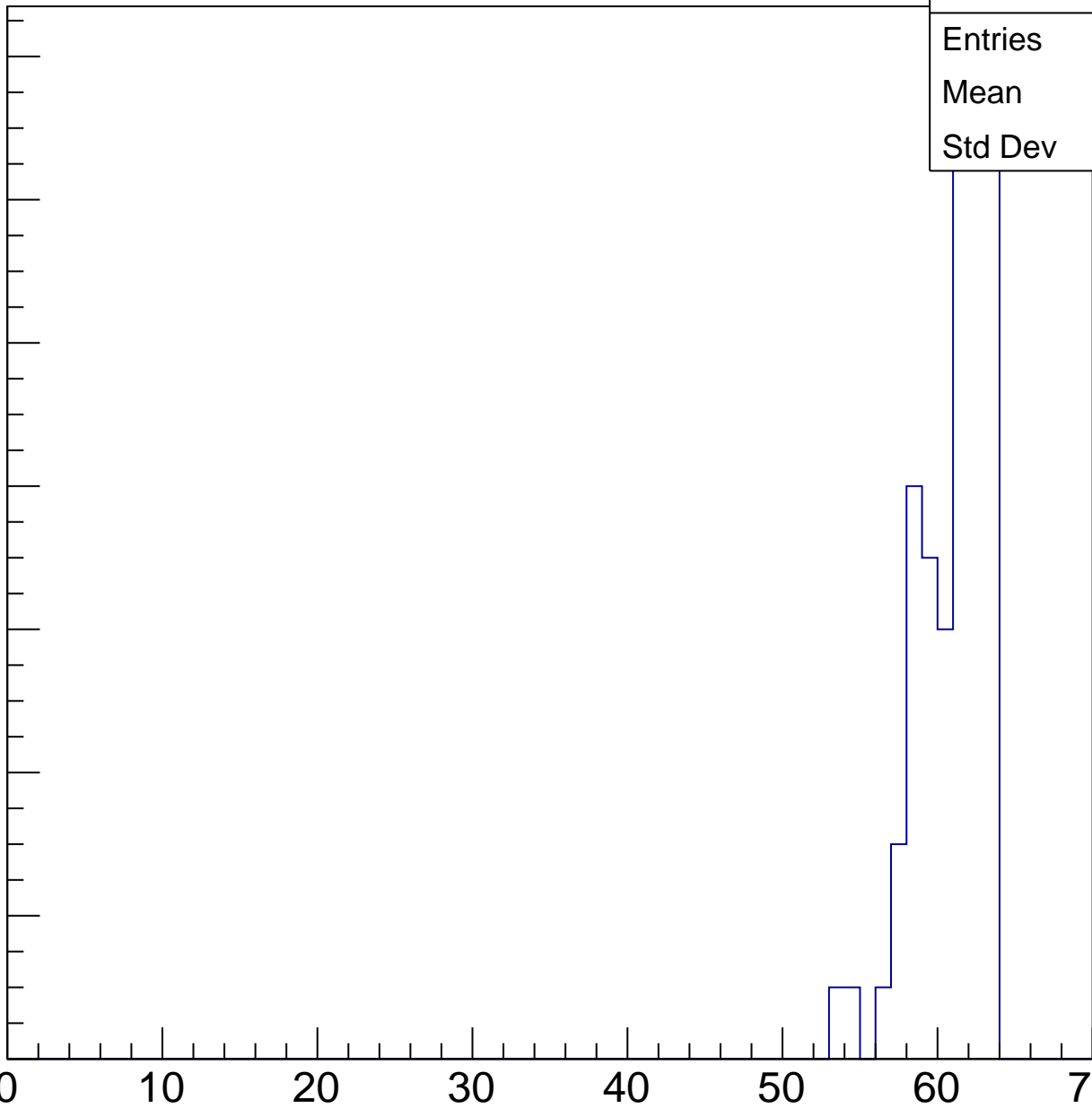
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	67
Mean	60.46
Std Dev	2.248

ampl

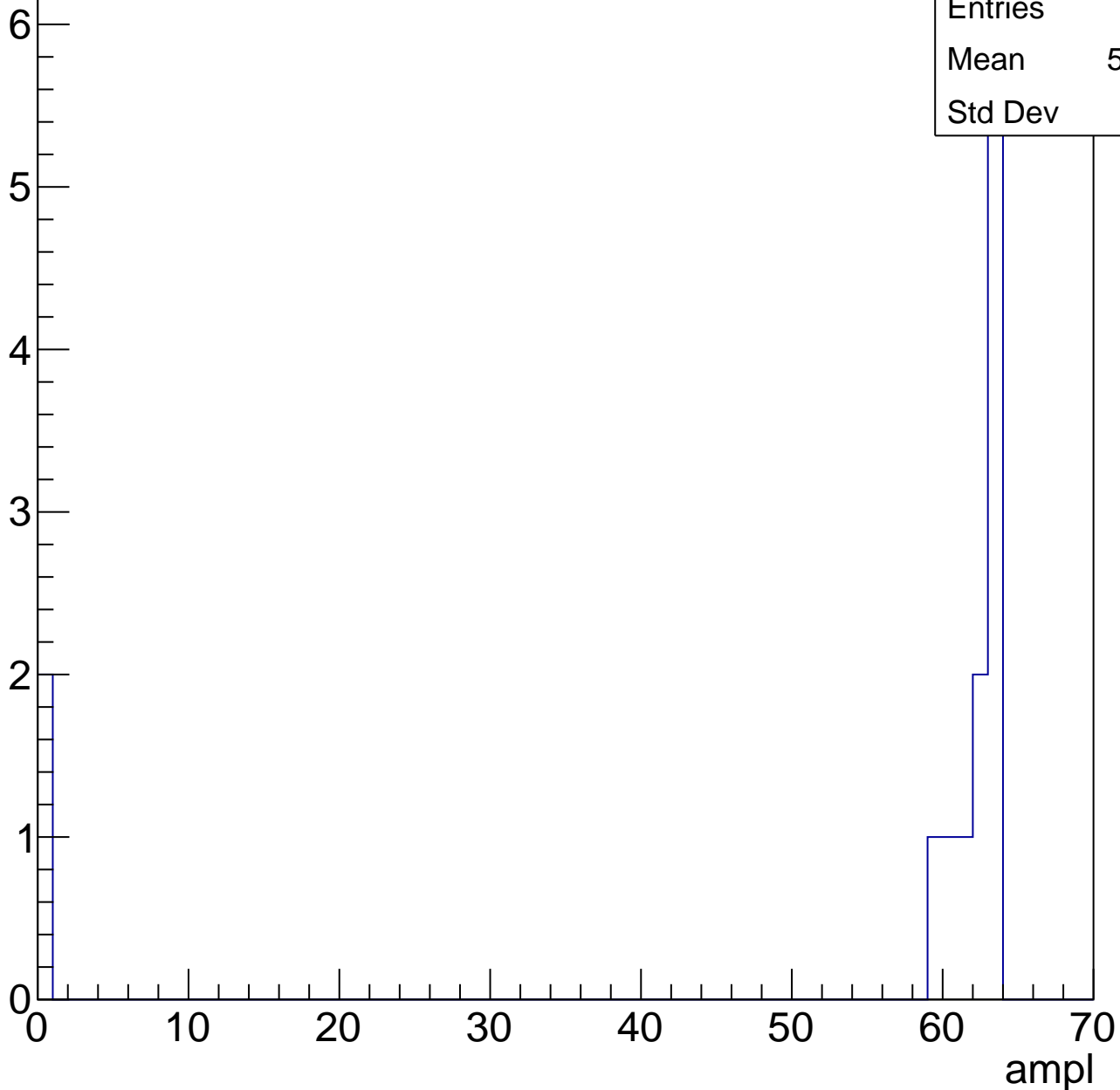


B1L001S, U25-ch0, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	52.46
Std Dev	22.4



B1L001S, U25-ch0, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U25-ch1, adc0

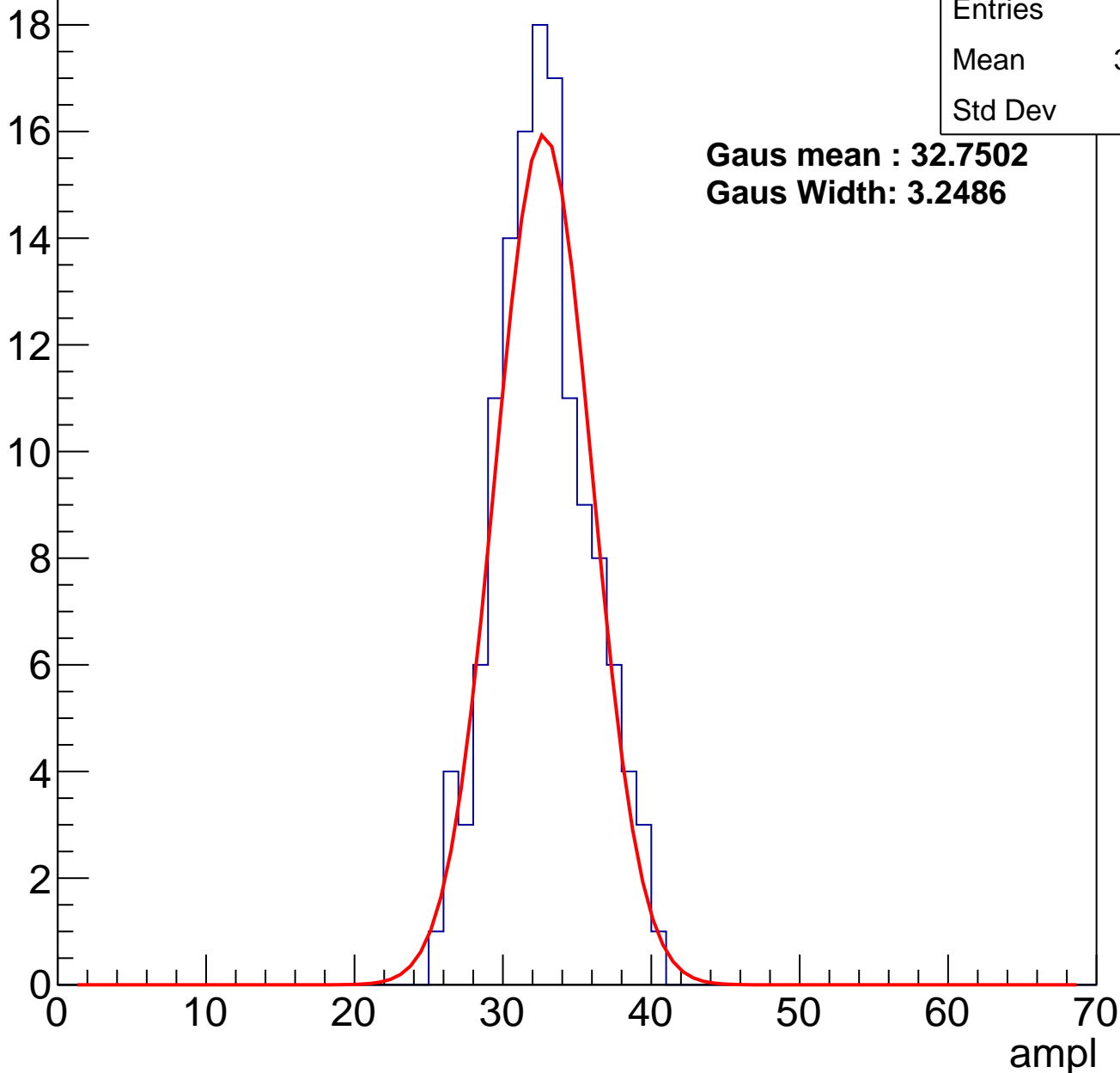
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	32.26
Std Dev	3.14

Gaus mean : 32.7502

Gaus Width: 3.2486

Entry



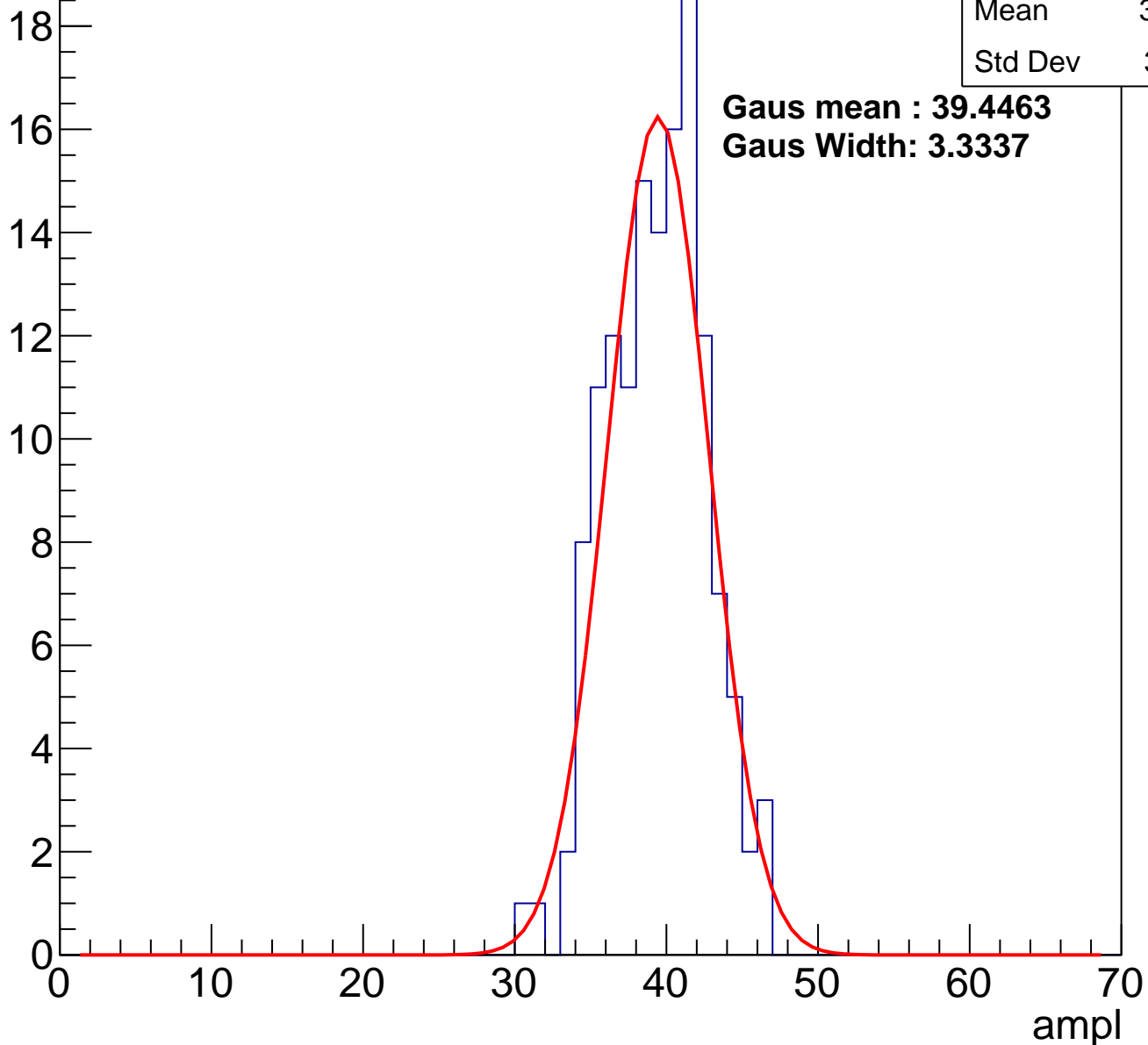
B1L001S, U25-ch1, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	38.93
Std Dev	3.191

Gaus mean : 39.4463
Gaus Width: 3.3337

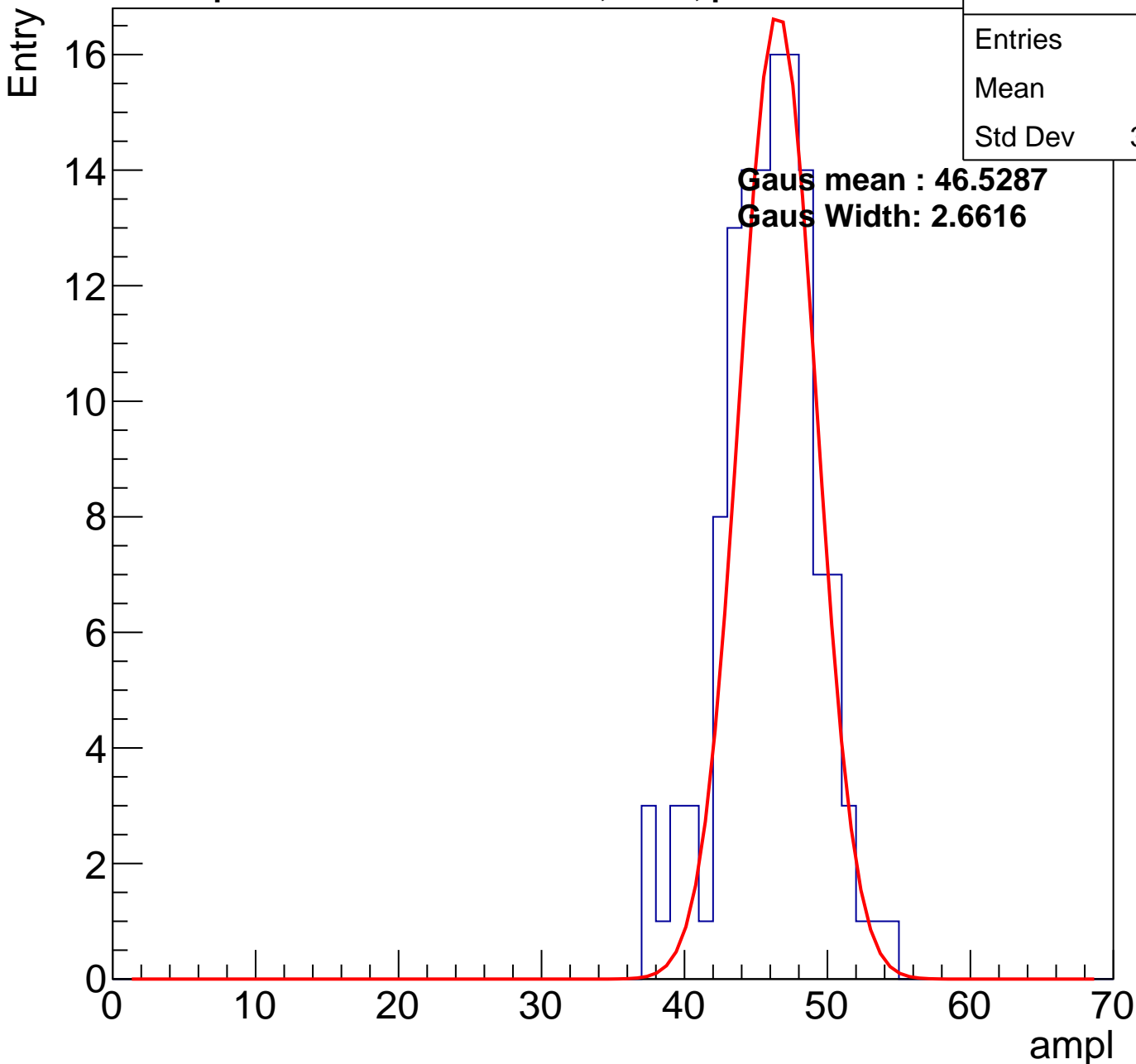
Entry



B1L001S, U25-ch1, adc2

calib_packv5_042523_0143.root, FC#2, port C2

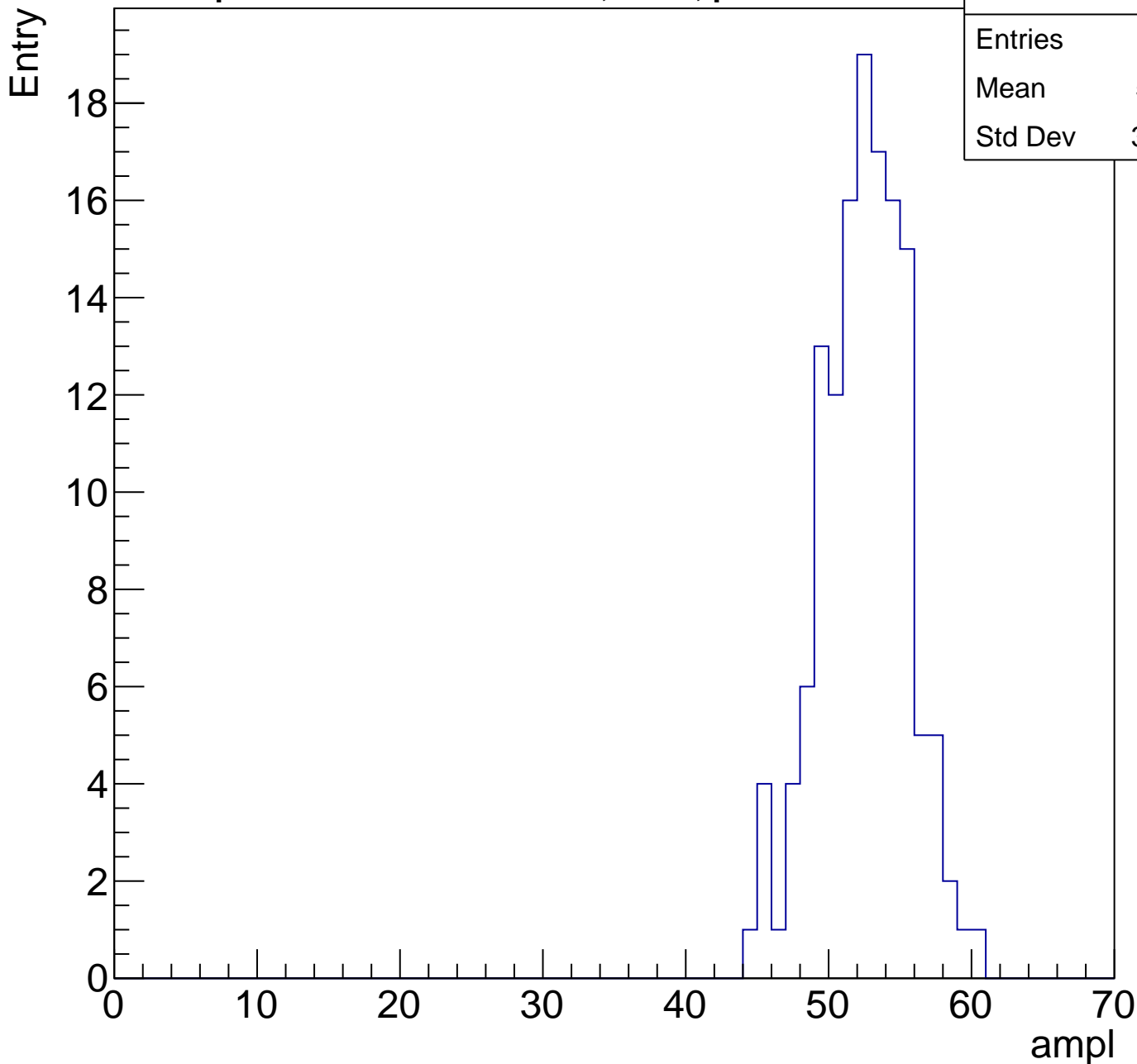
Entries	126
Mean	45.5
Std Dev	3.263



B1L001S, U25-ch1, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	52.01
Std Dev	3.044



B1L001S, U25-ch1, adc4

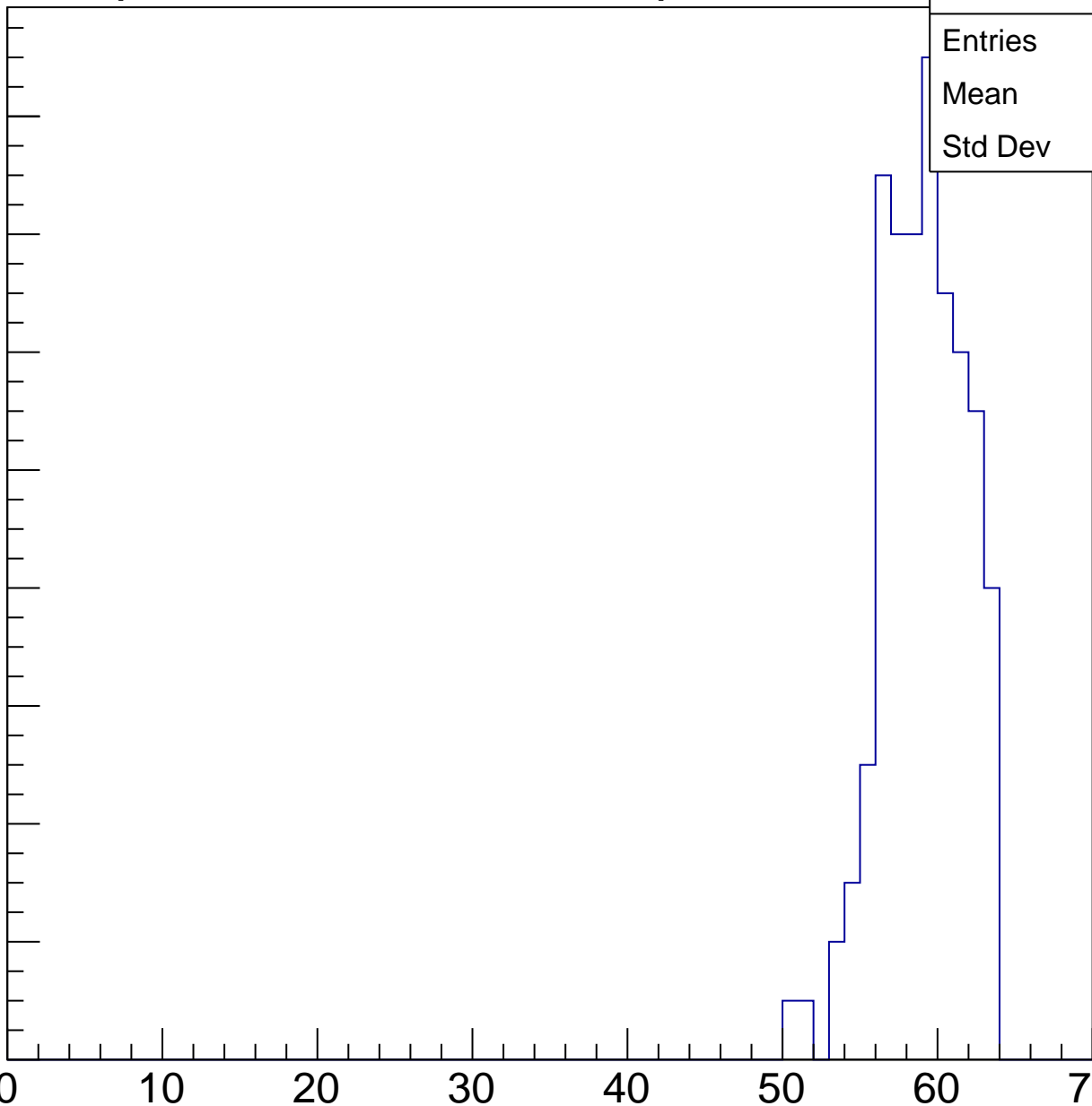
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	58.58
Std Dev	2.698

ampl



B1L001S, U25-ch1, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	44
Mean	58.39
Std Dev	12.85

Entries

44

Mean

58.39

Std Dev

12.85

B1L001S, U25-ch1, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch1, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch2, adc0

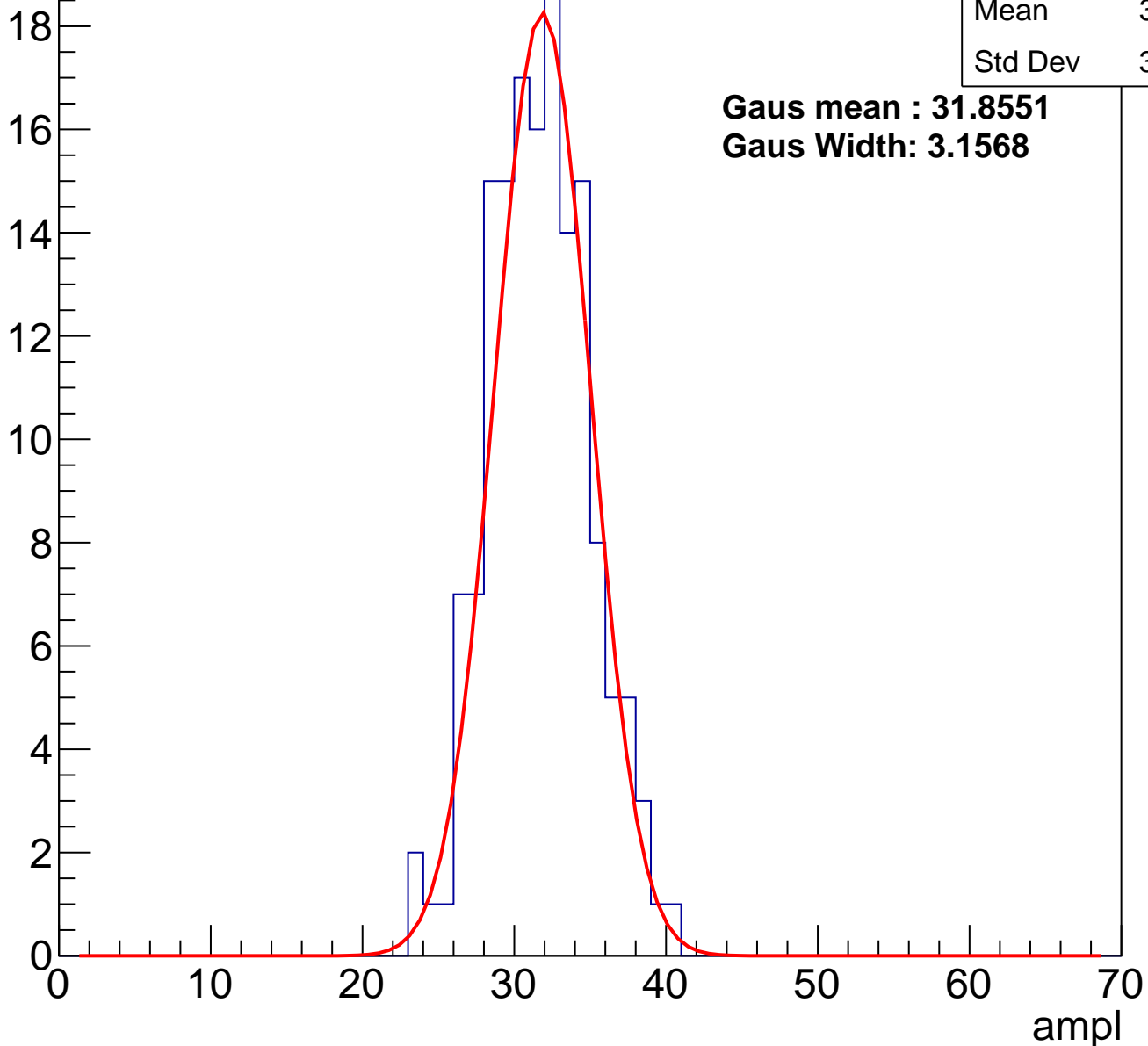
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	31.22
Std Dev	3.285

Gaus mean : 31.8551

Gaus Width: 3.1568

Entry



B1L001S, U25-ch2, adc1

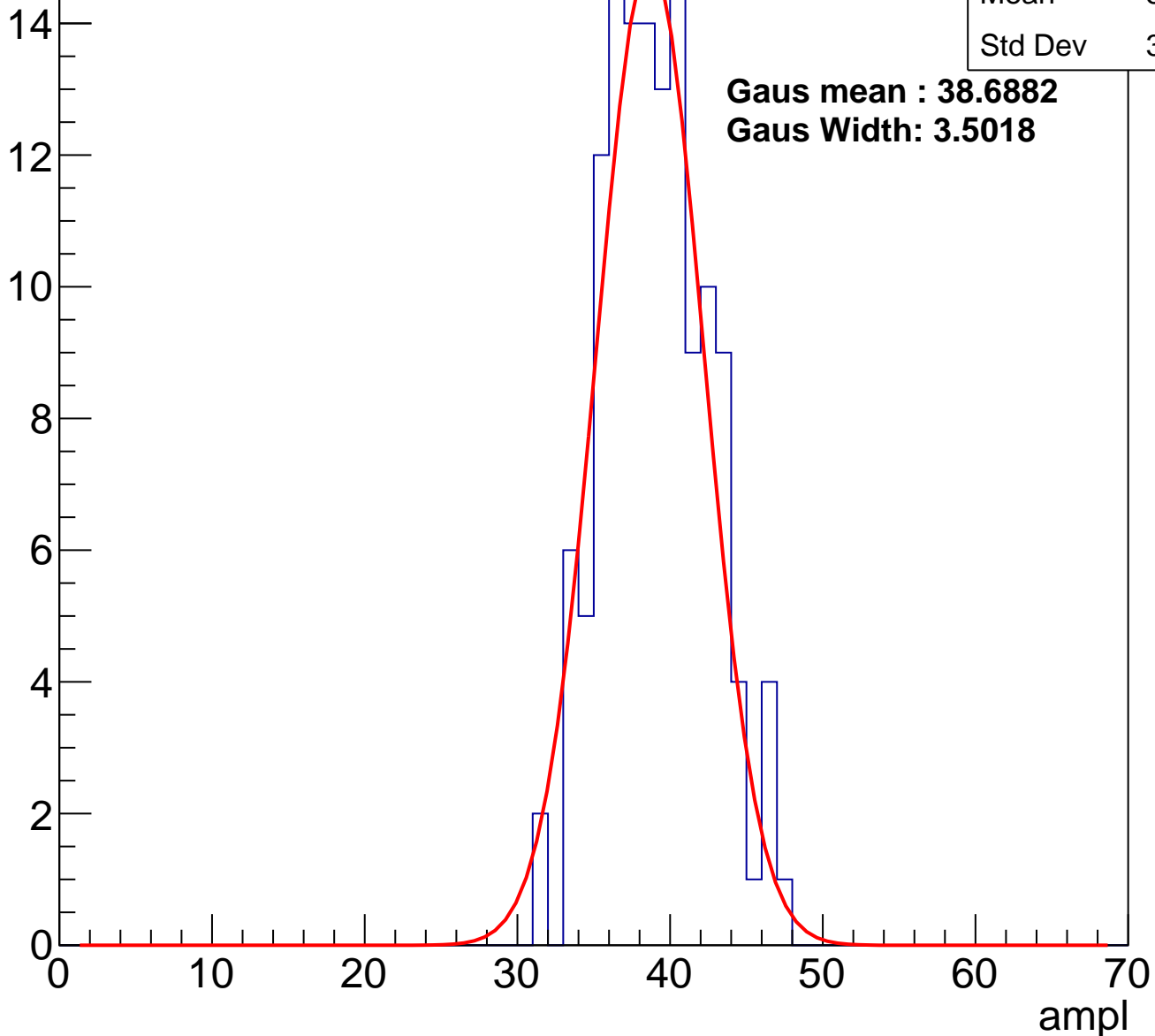
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	38.62
Std Dev	3.376

Gaus mean : 38.6882

Gaus Width: 3.5018

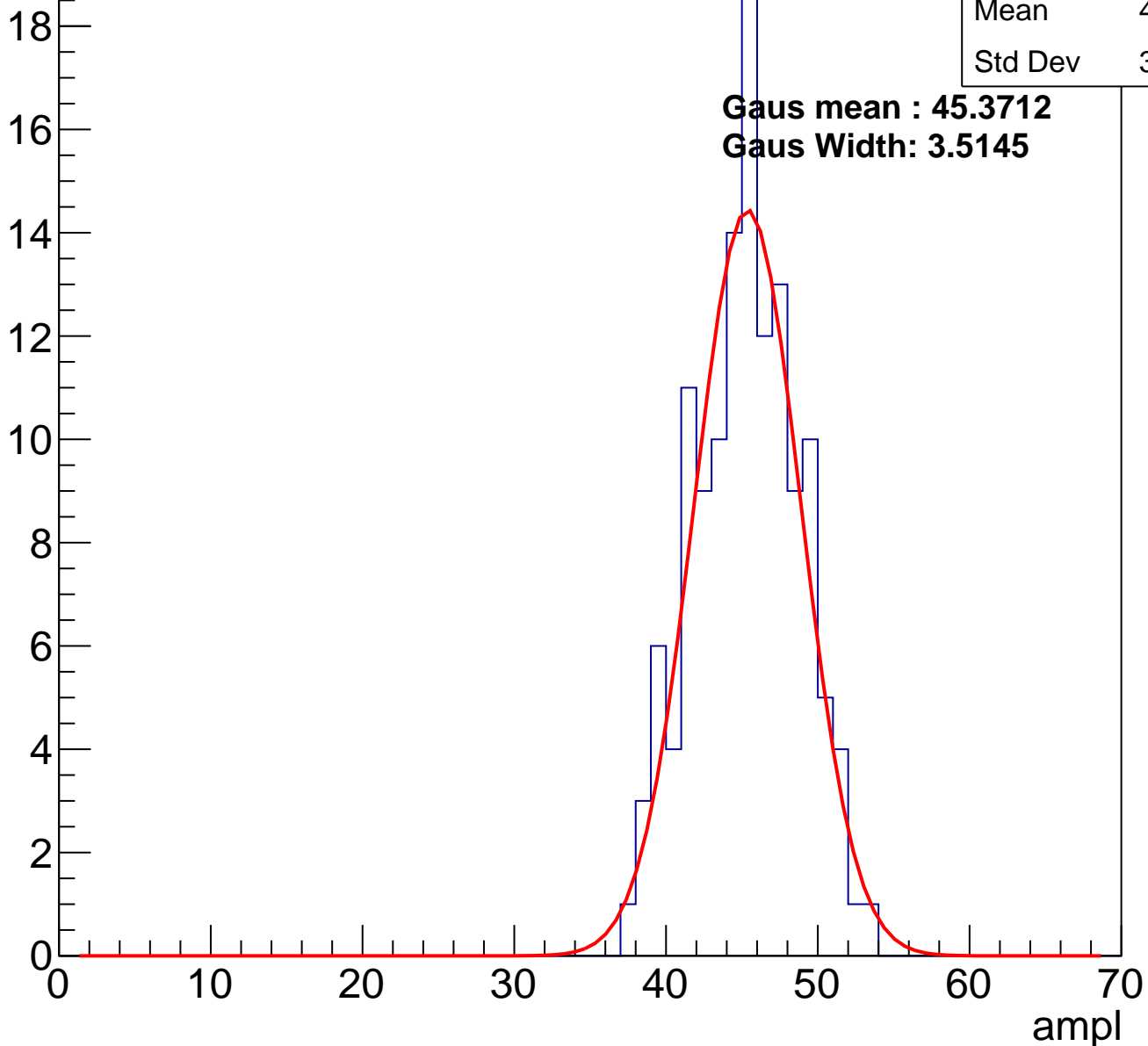
Entry



B1L001S, U25-ch2, adc2

calib_packv5_042523_0143.root, FC#2, port C2

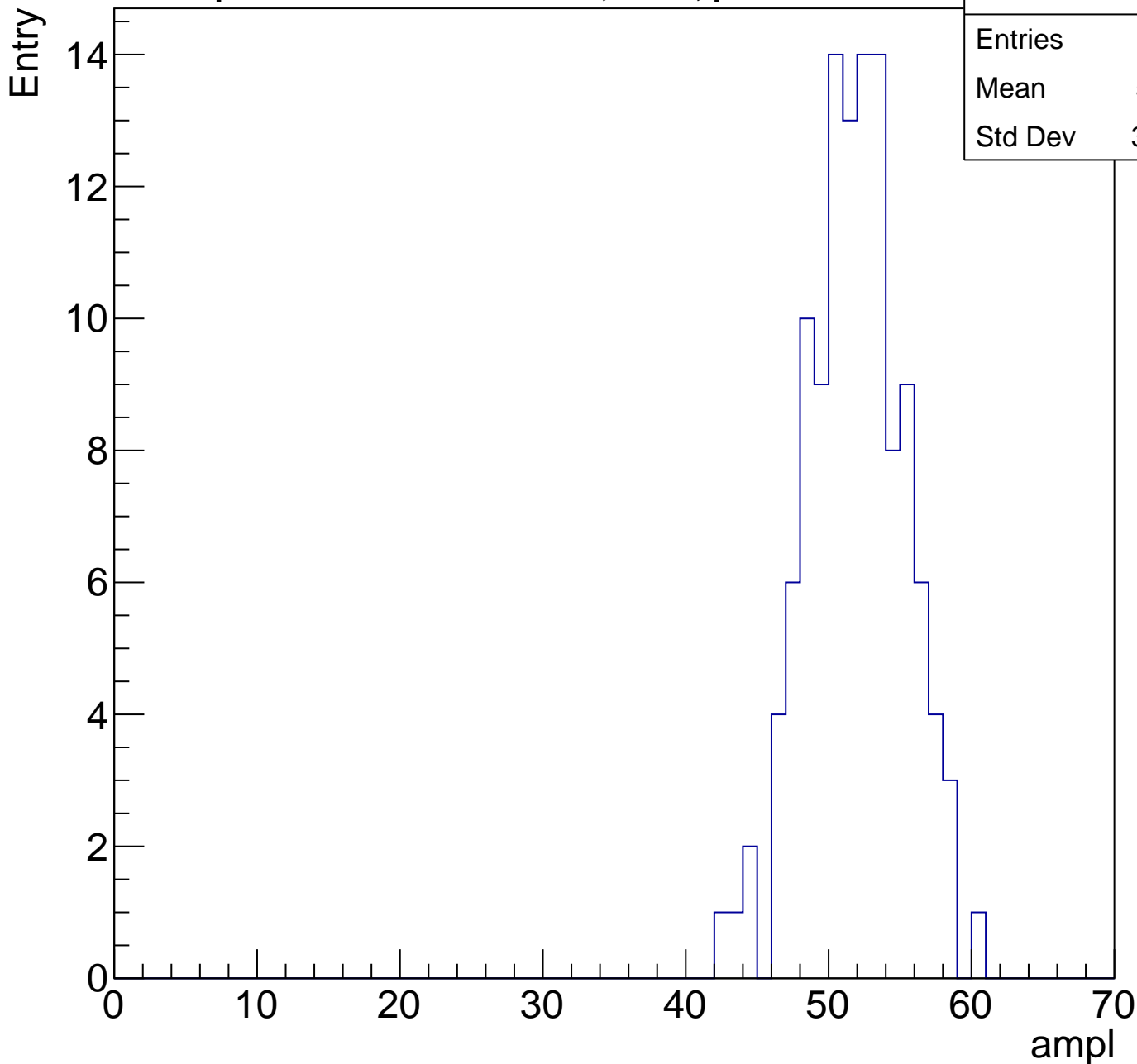
Entry



B1L001S, U25-ch2, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	51.41
Std Dev	3.389



B1L001S, U25-ch2, adc4

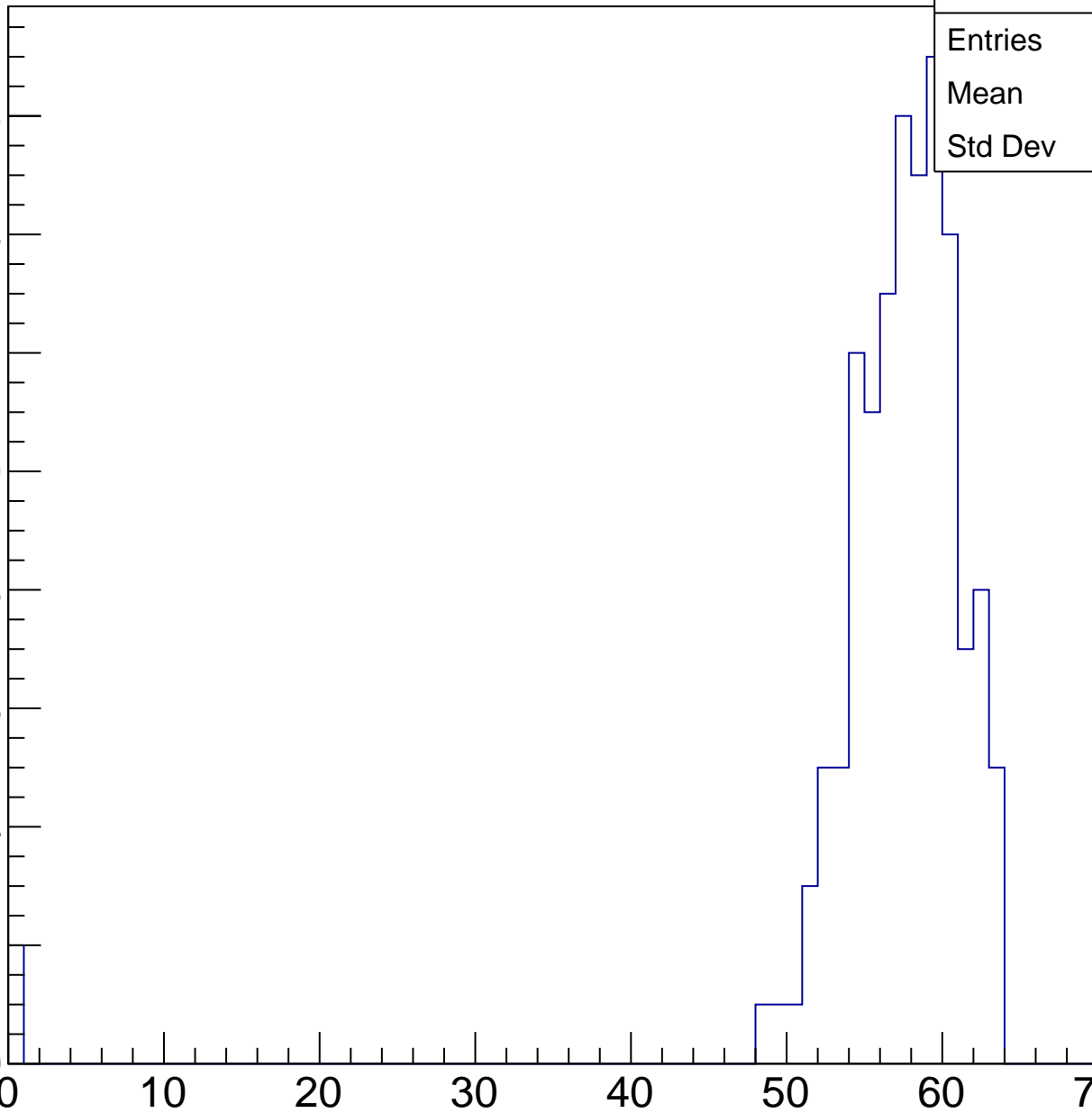
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	56.39
Std Dev	7.583

ampl



B1L001S, U25-ch2, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	48
Mean	61.33
Std Dev	1.586

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch2, adc6

calib_packv5_042523_0143.root, FC#2, port C2

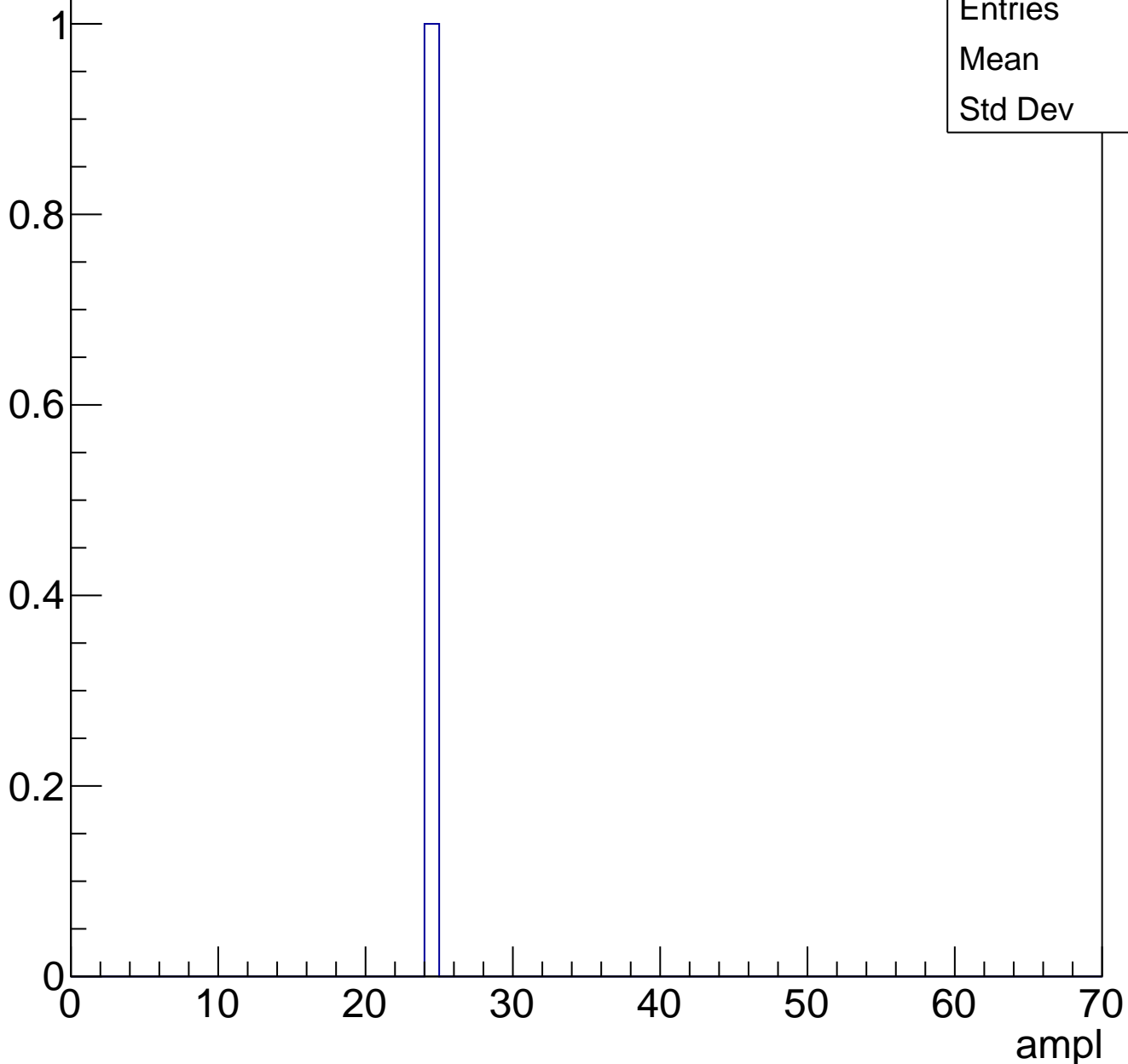
Entry



B1L001S, U25-ch2, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch3, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	31.54
Std Dev	4.329

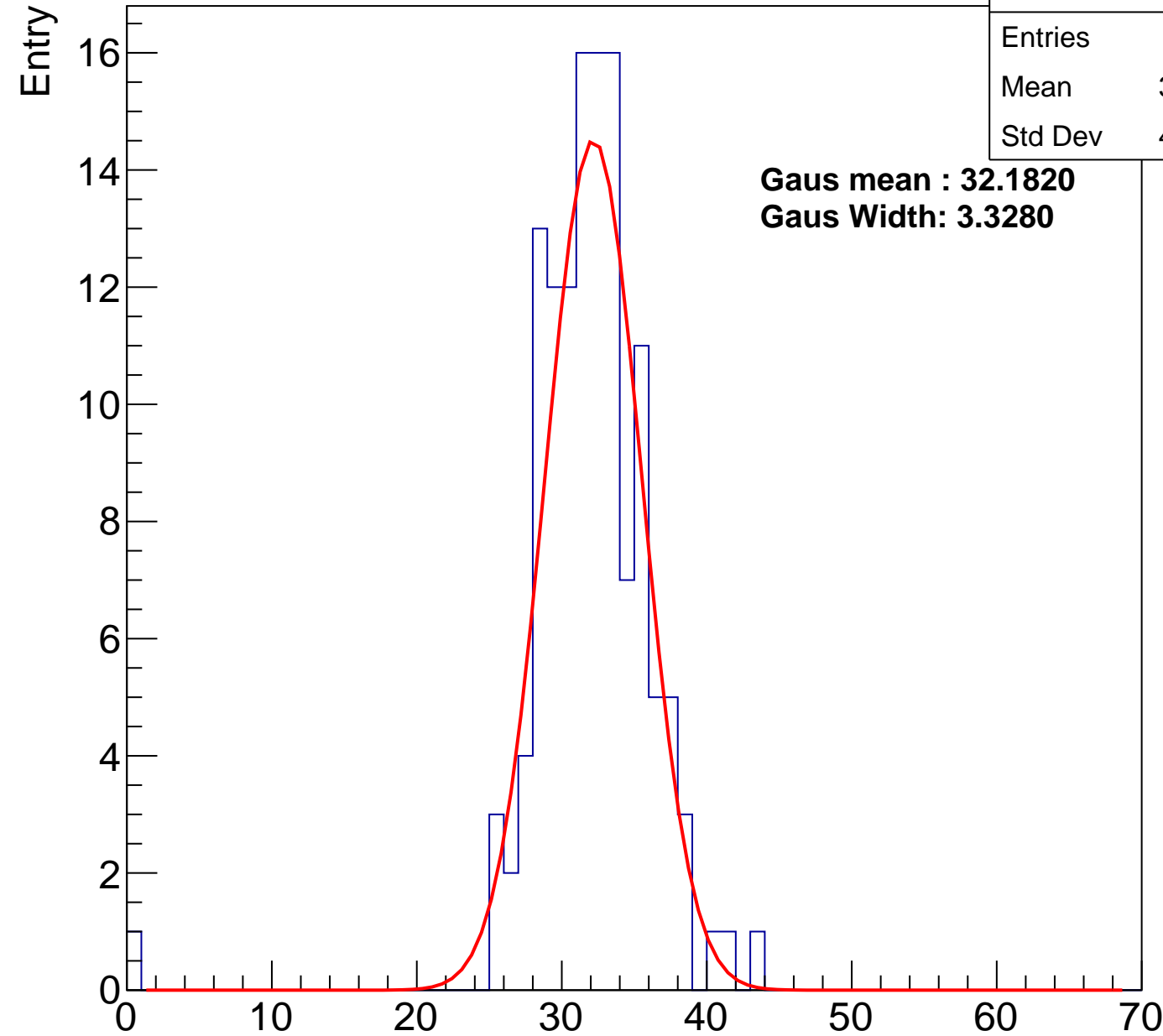
Gaus mean : 32.1820

Gaus Width: 3.3280

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch3, adc1

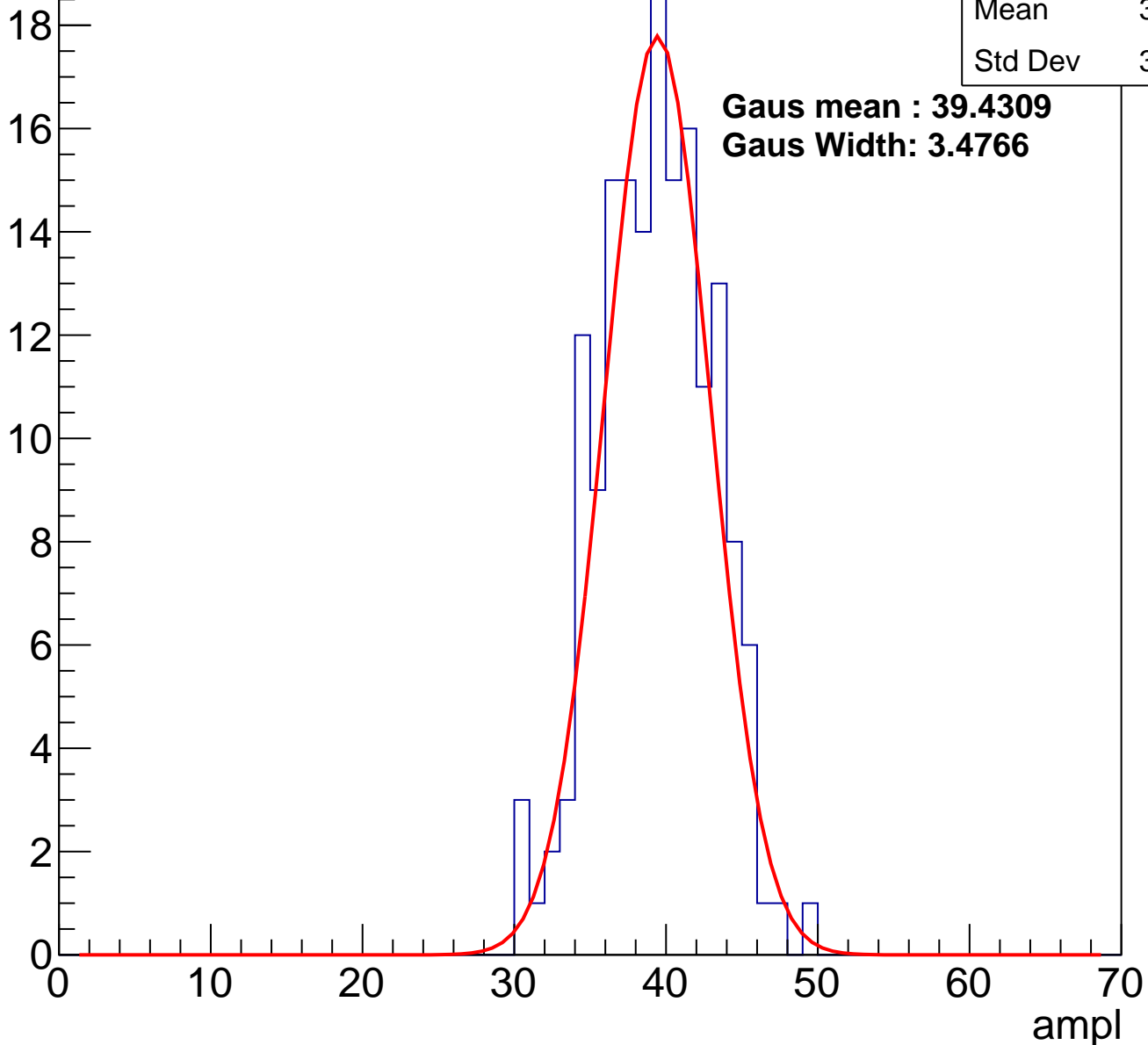
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	38.88
Std Dev	3.634

Gaus mean : 39.4309

Gaus Width: 3.4766

Entry



B1L001S, U25-ch3, adc2

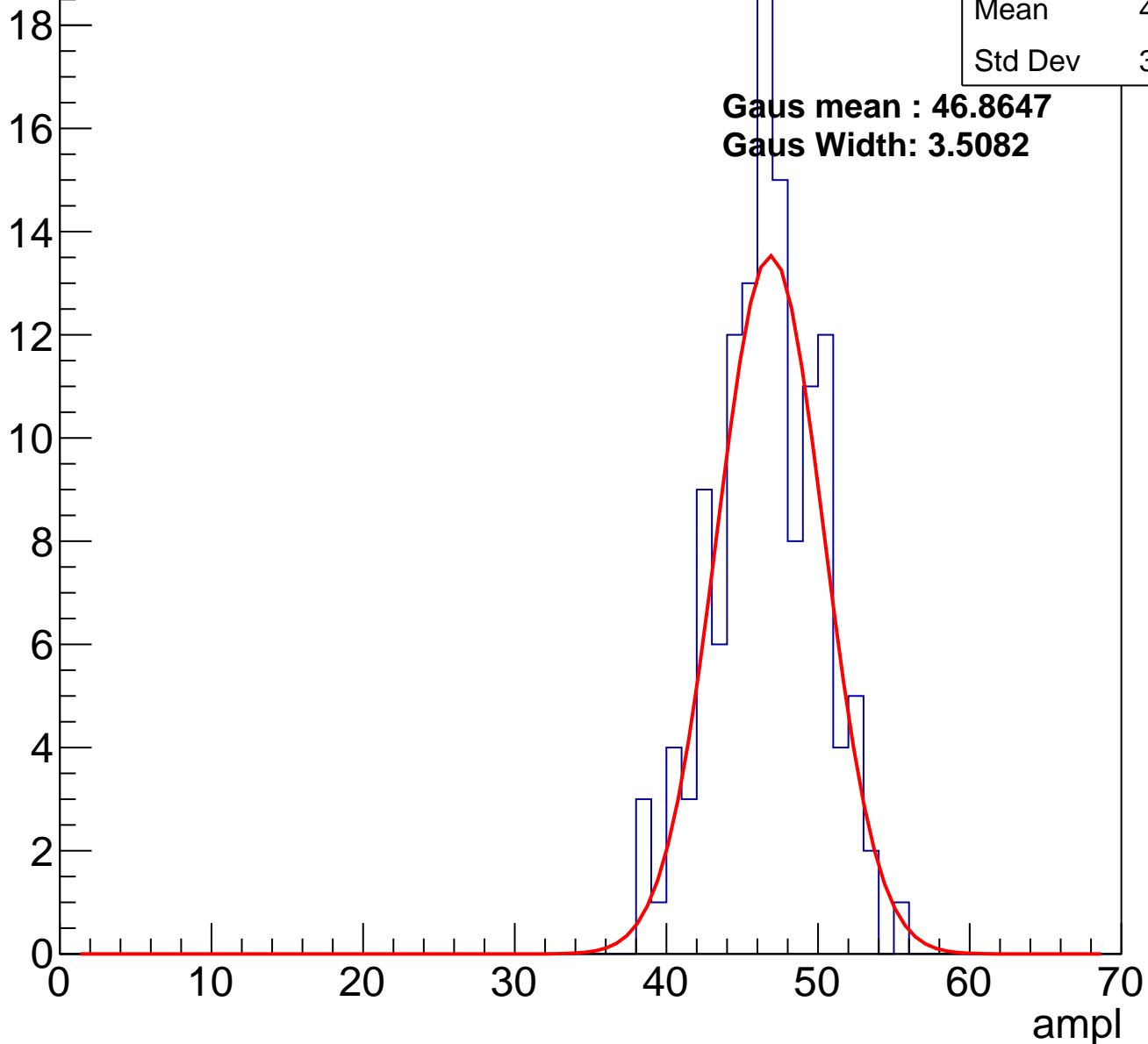
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	46.19
Std Dev	3.436

Gaus mean : 46.8647

Gaus Width: 3.5082

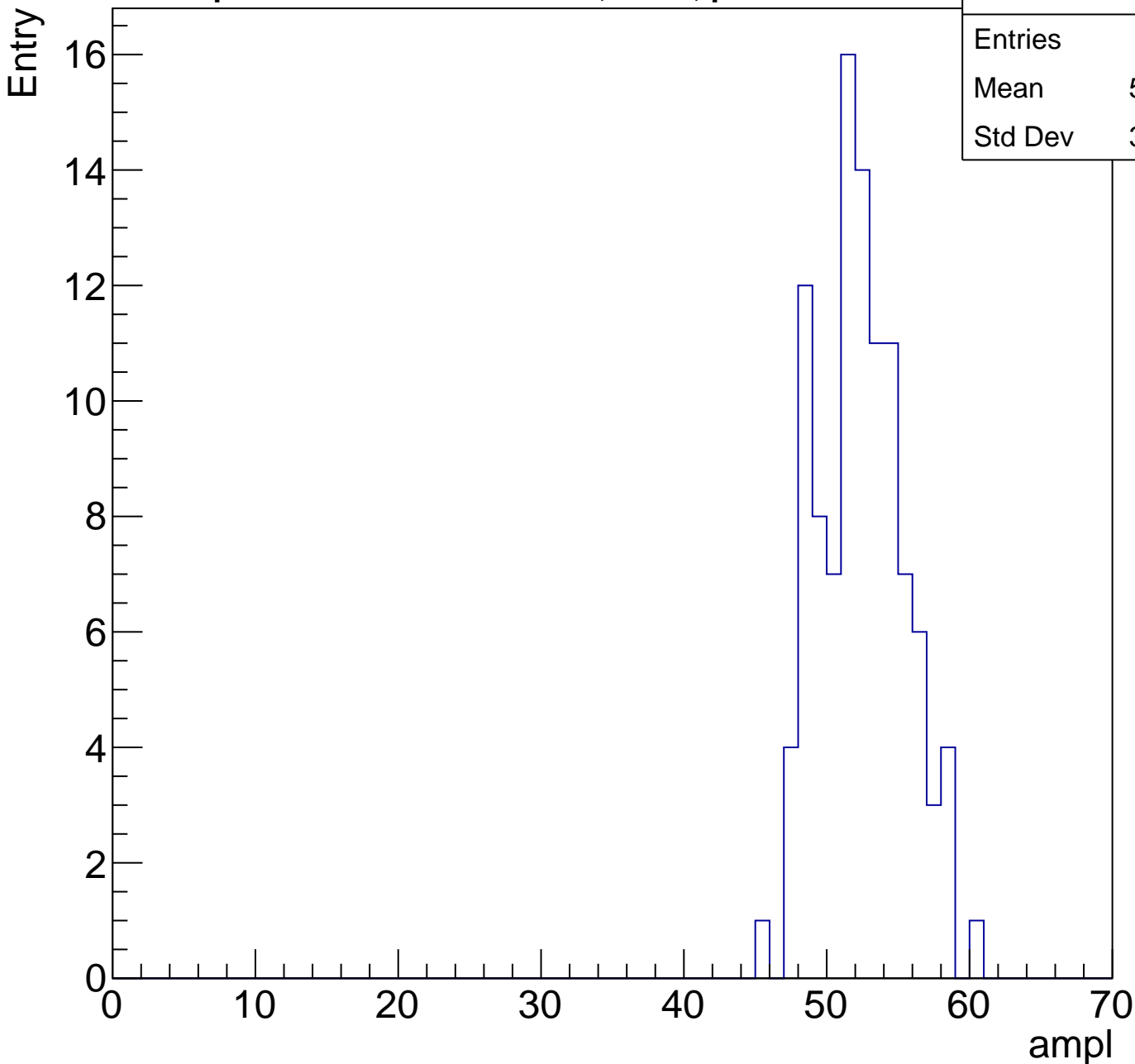
Entry



B1L001S, U25-ch3, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	51.96
Std Dev	3.014



B1L001S, U25-ch3, adc4

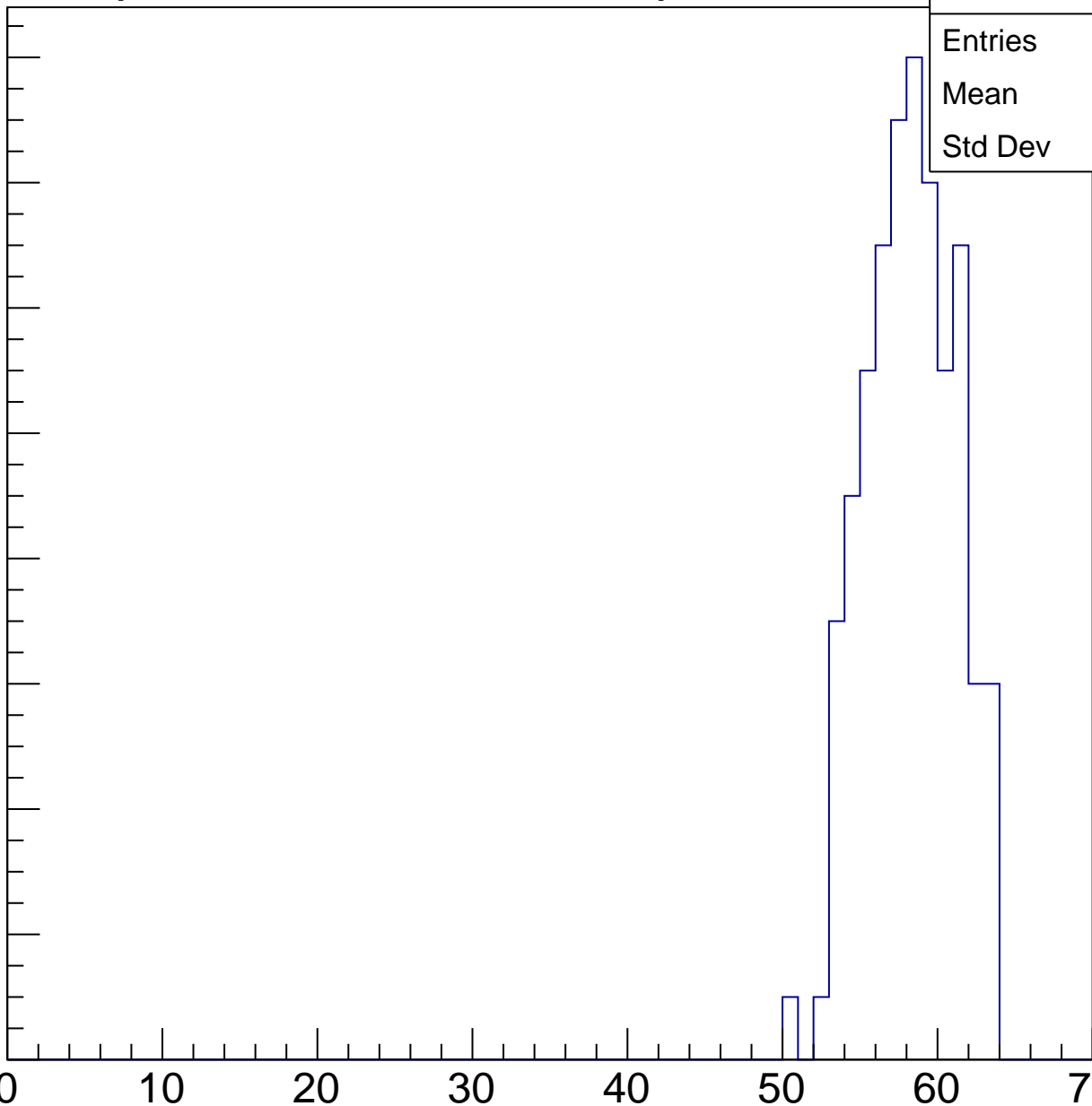
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	123
Mean	57.76
Std Dev	2.849

ampl



B1L001S, U25-ch3, adc5

calib_packv5_042523_0143.root, FC#2, port C2

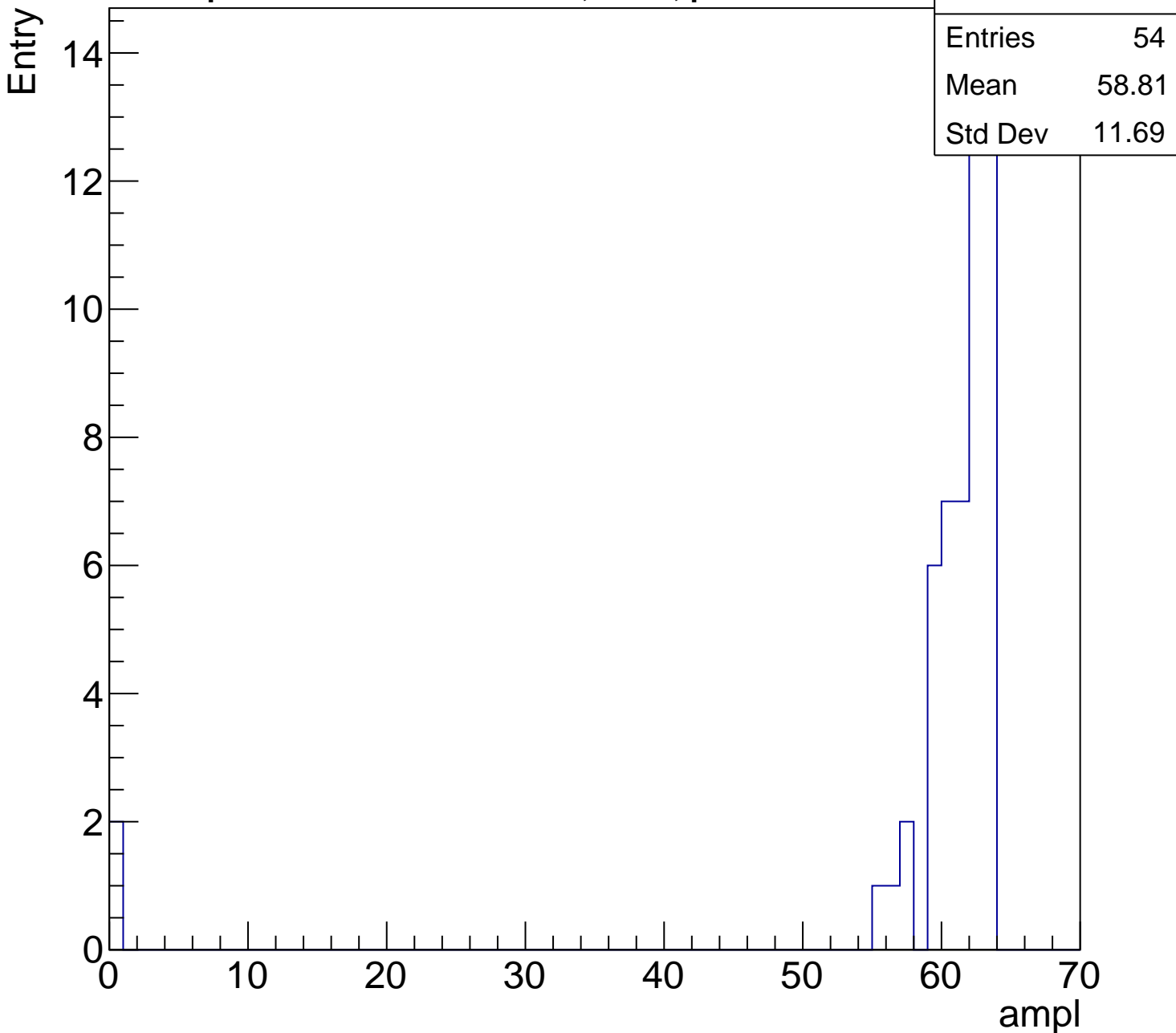
Entries	54
Mean	58.81
Std Dev	11.69

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch3, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch3, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch4, adc0

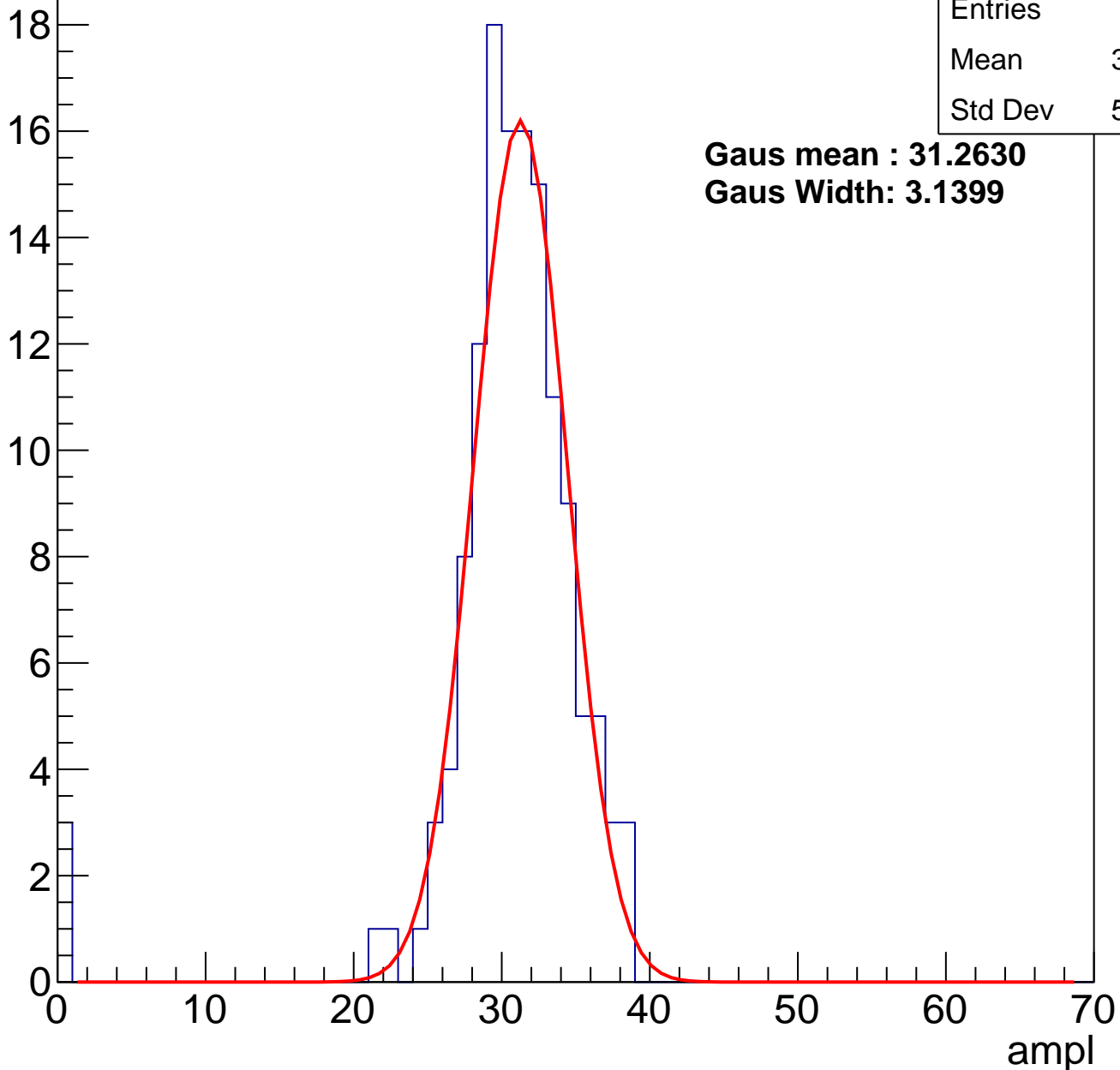
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	30.04
Std Dev	5.545

Gaus mean : 31.2630

Gaus Width: 3.1399

Entry

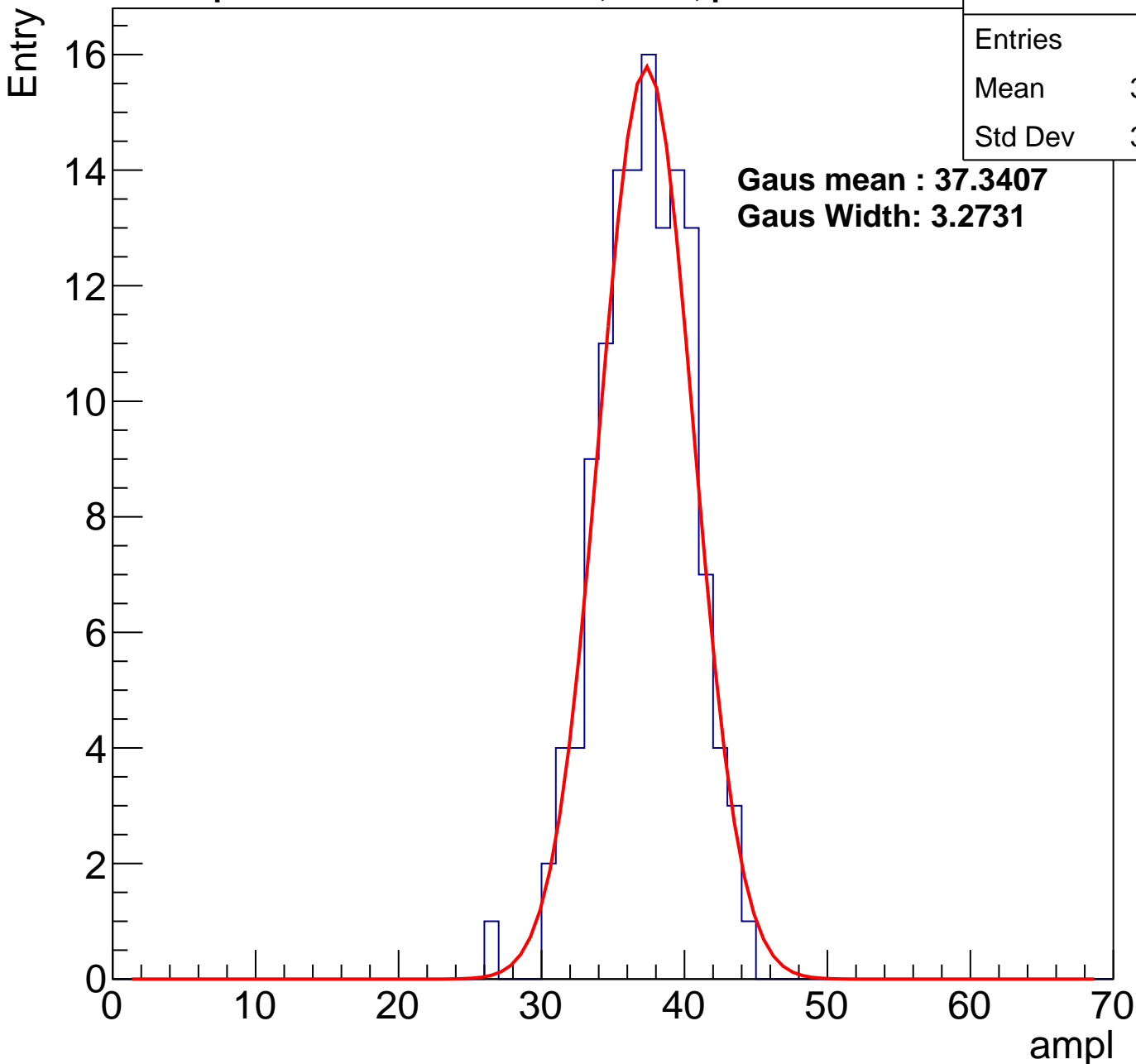


B1L001S, U25-ch4, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	36.79
Std Dev	3.193

Gaus mean : 37.3407
Gaus Width: 3.2731



B1L001S, U25-ch4, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

134

Mean

43.76

Std Dev

3.17

Gaus mean : 44.4460

Gaus Width: 3.2825

0

10

20

30

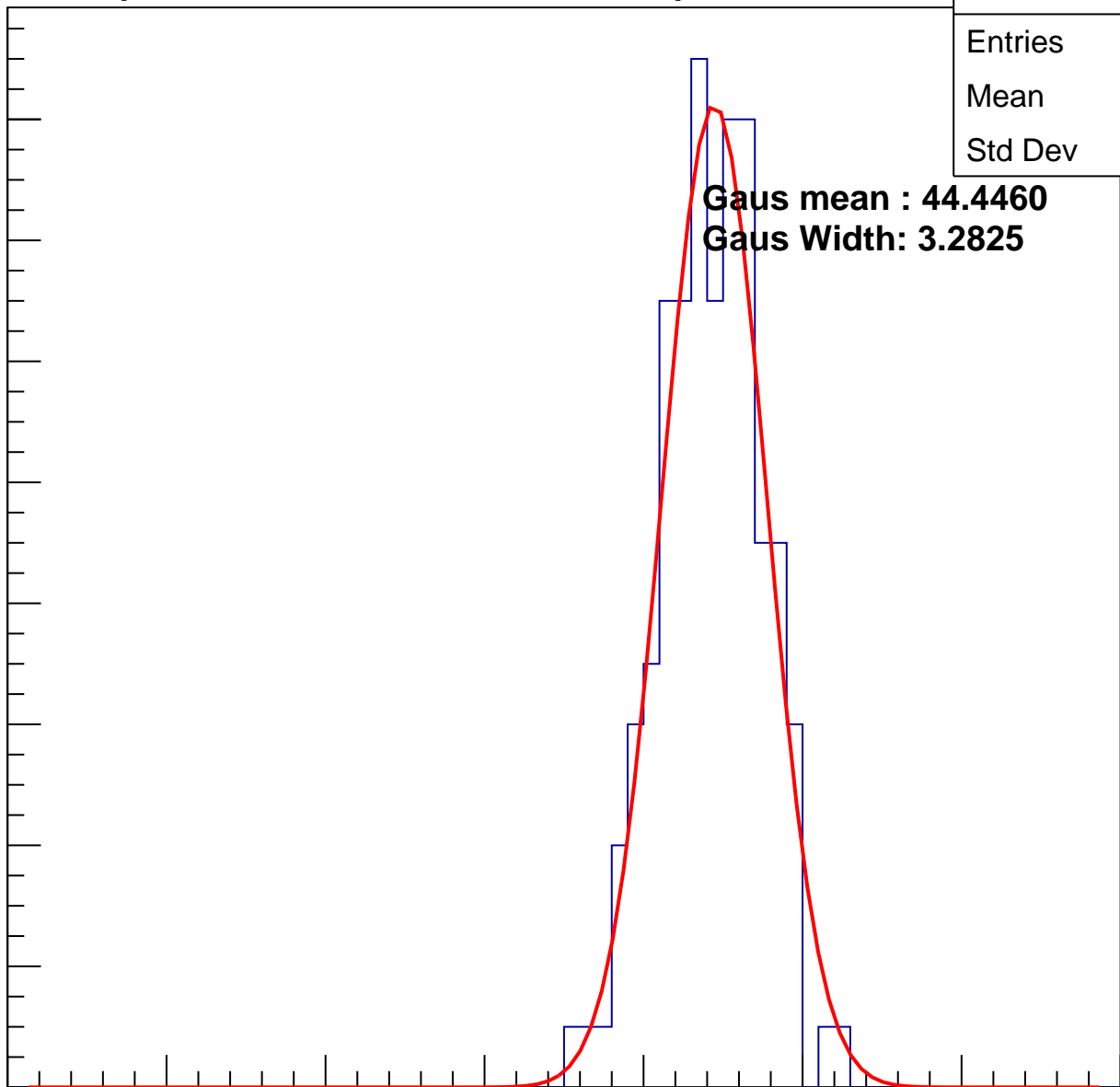
40

50

60

70

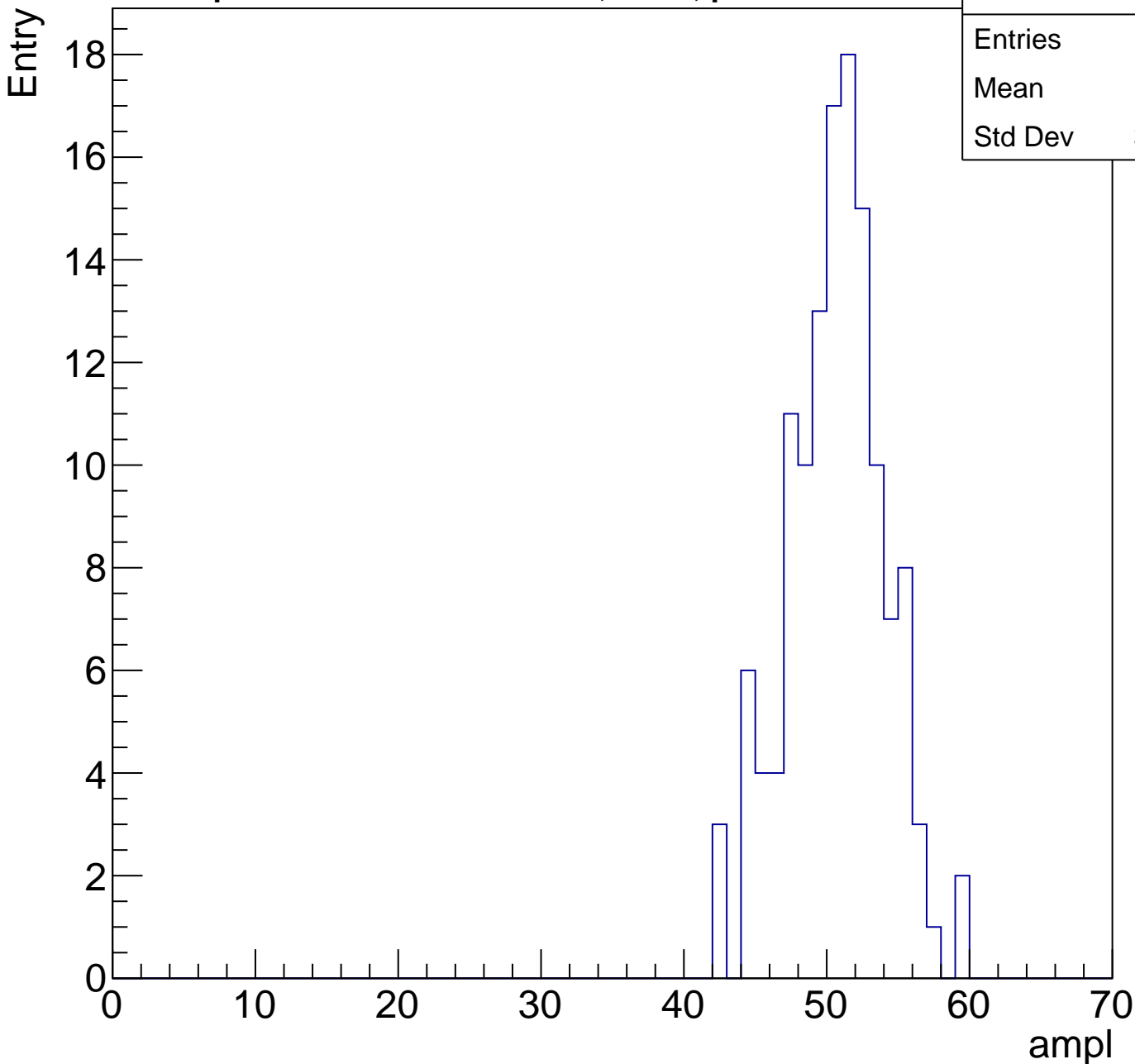
ampl



B1L001S, U25-ch4, adc3

calib_packv5_042523_0143.root, FC#2, port C2

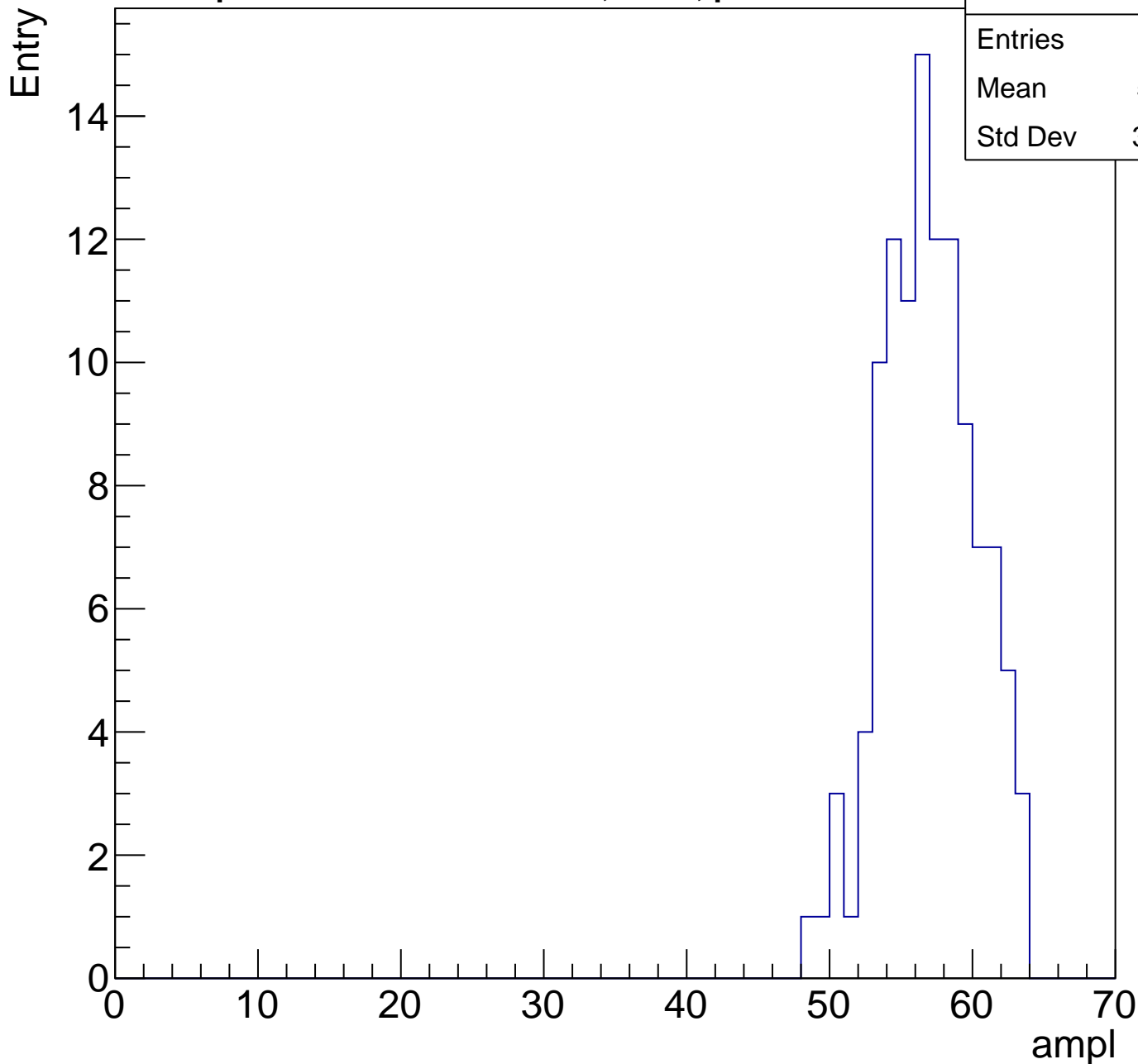
Entries	132
Mean	50.2
Std Dev	3.391



B1L001S, U25-ch4, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	56.51
Std Dev	3.237



B1L001S, U25-ch4, adc5

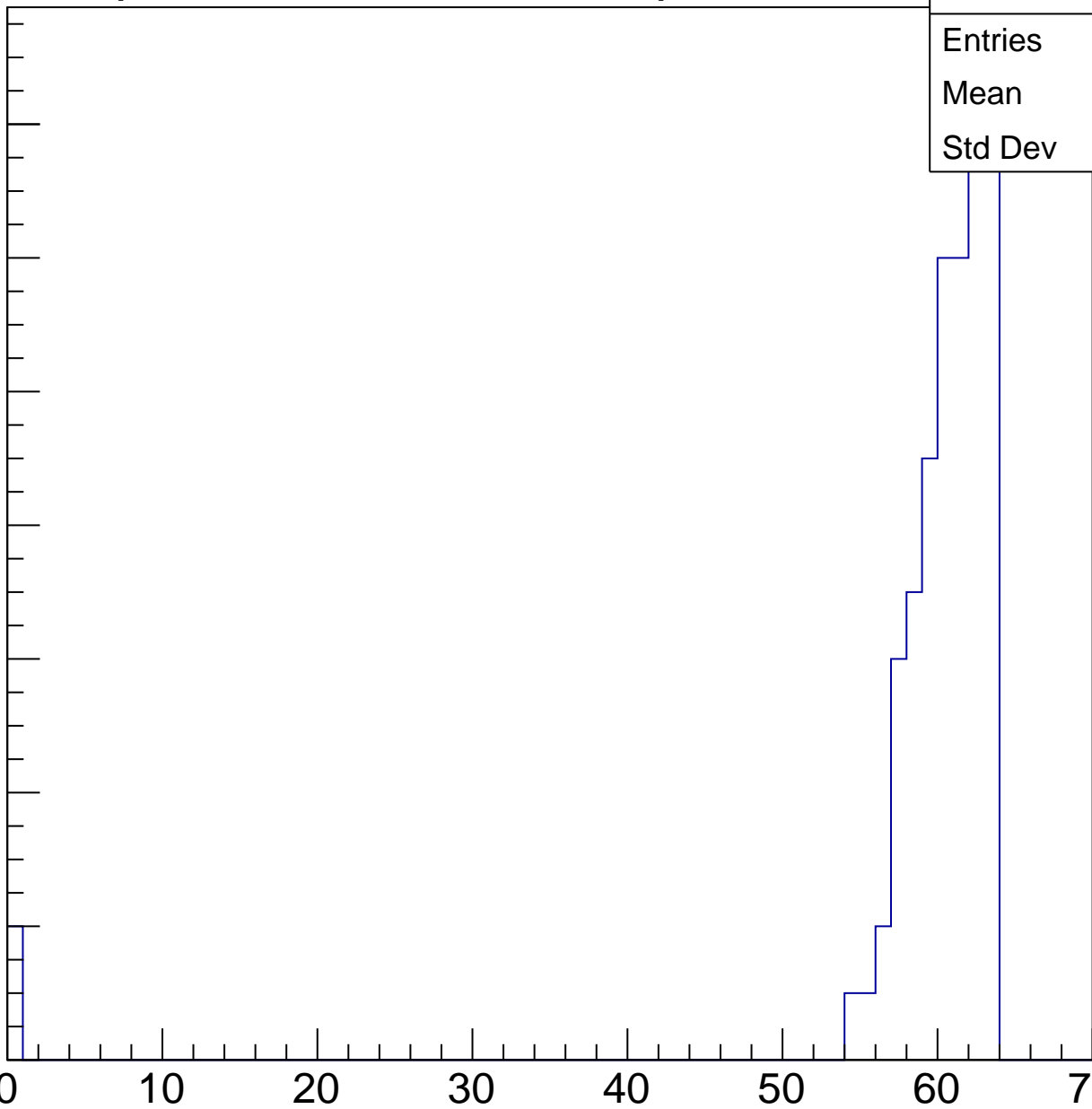
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	58.83
Std Dev	9.607

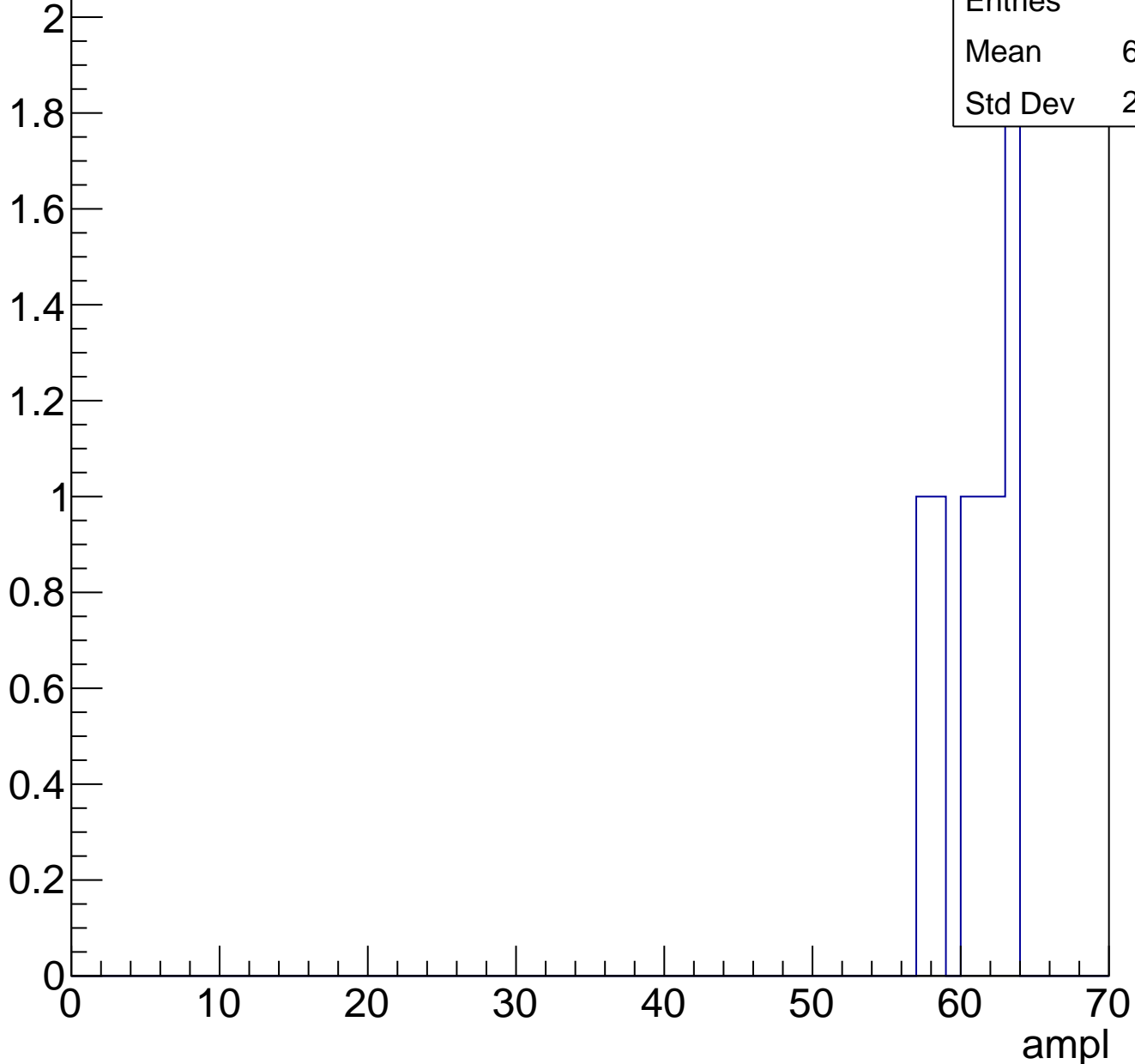
ampl



B1L001S, U25-ch4, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	7
Mean	60.57
Std Dev	2.195

B1L001S, U25-ch4, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch5, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	31.54
Std Dev	3.105

Gaus mean : 31.7503

Gaus Width: 3.0538

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch5, adc1

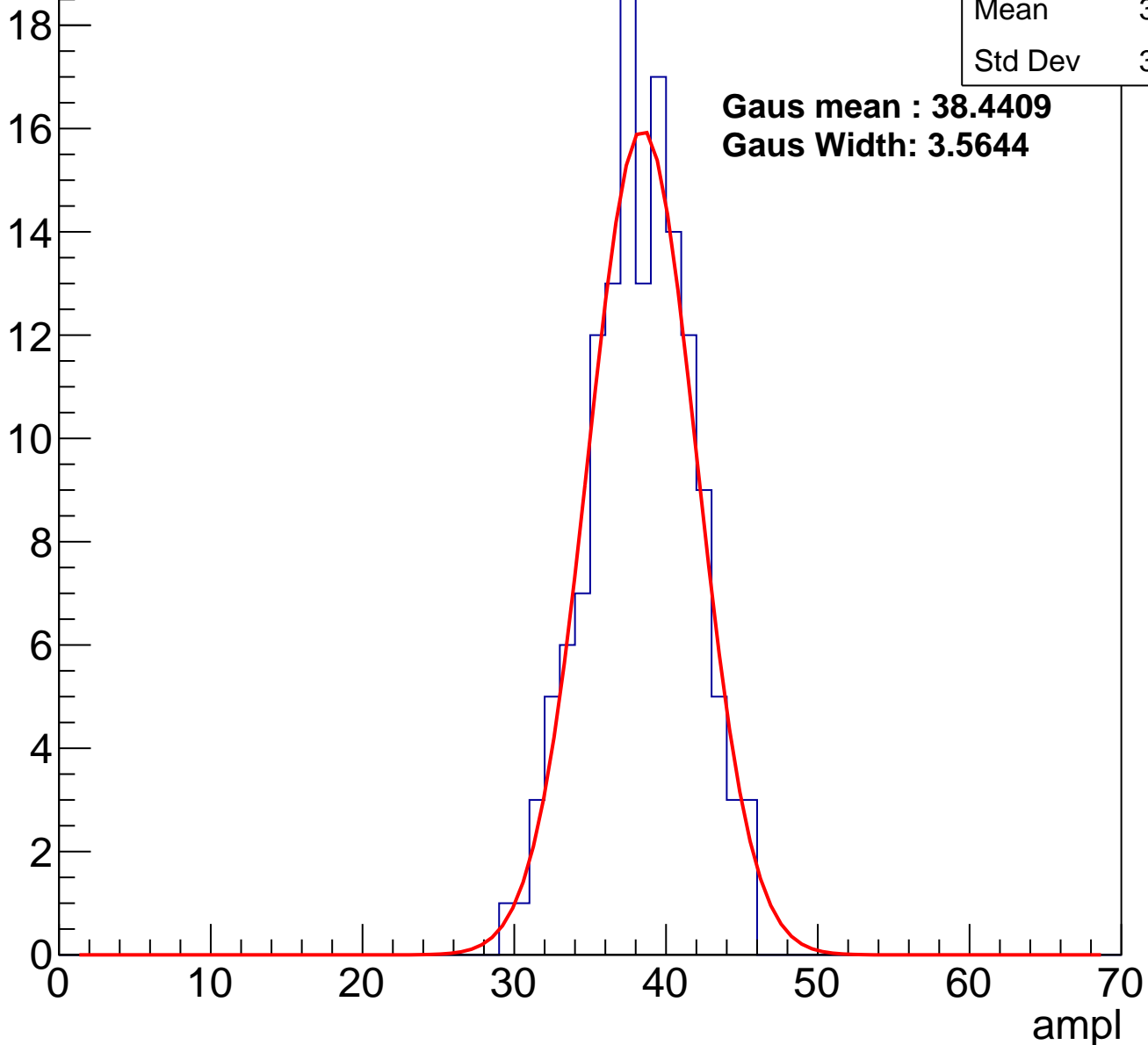
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	37.82
Std Dev	3.359

Gaus mean : 38.4409

Gaus Width: 3.5644

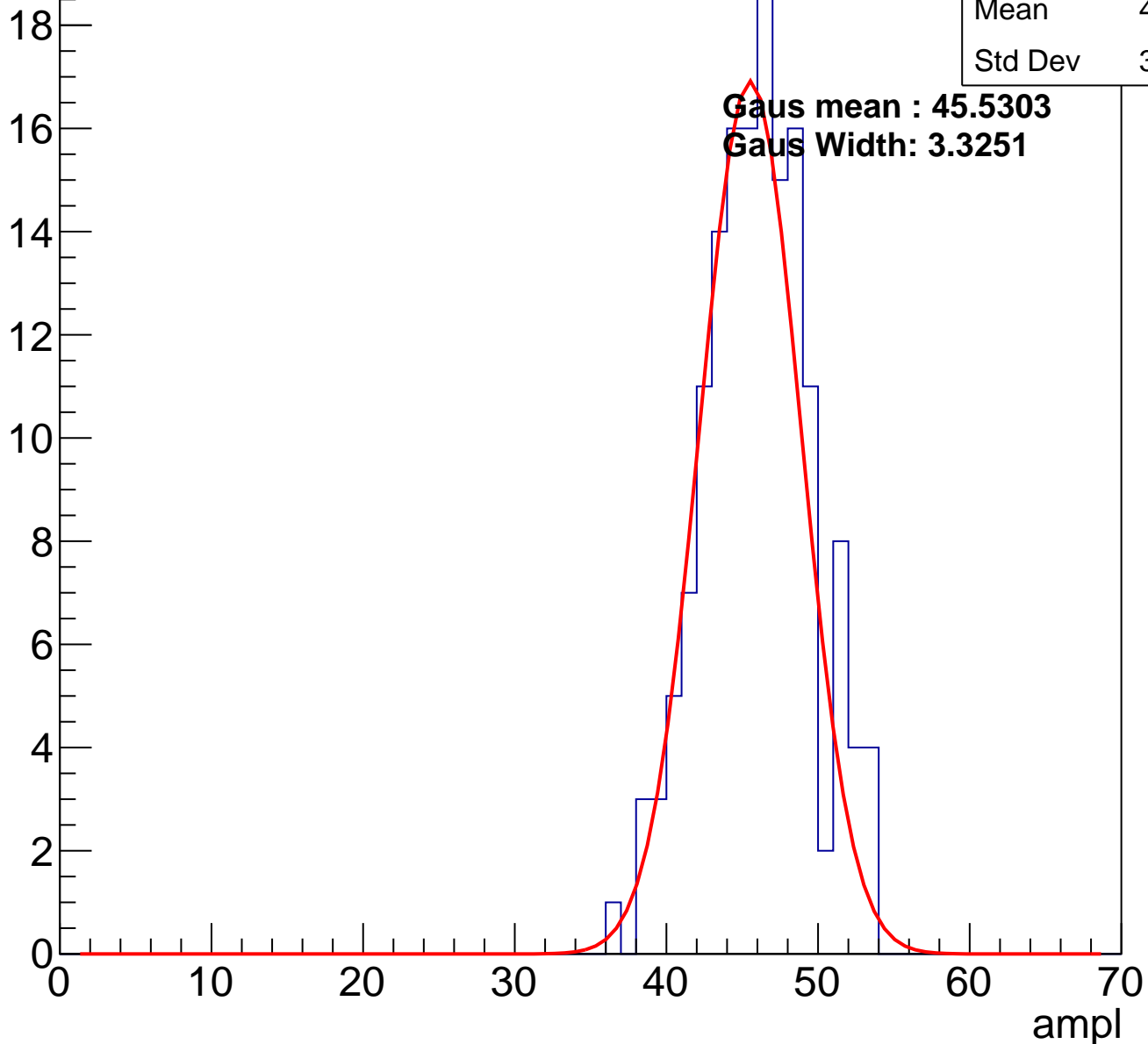
Entry



B1L001S, U25-ch5, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



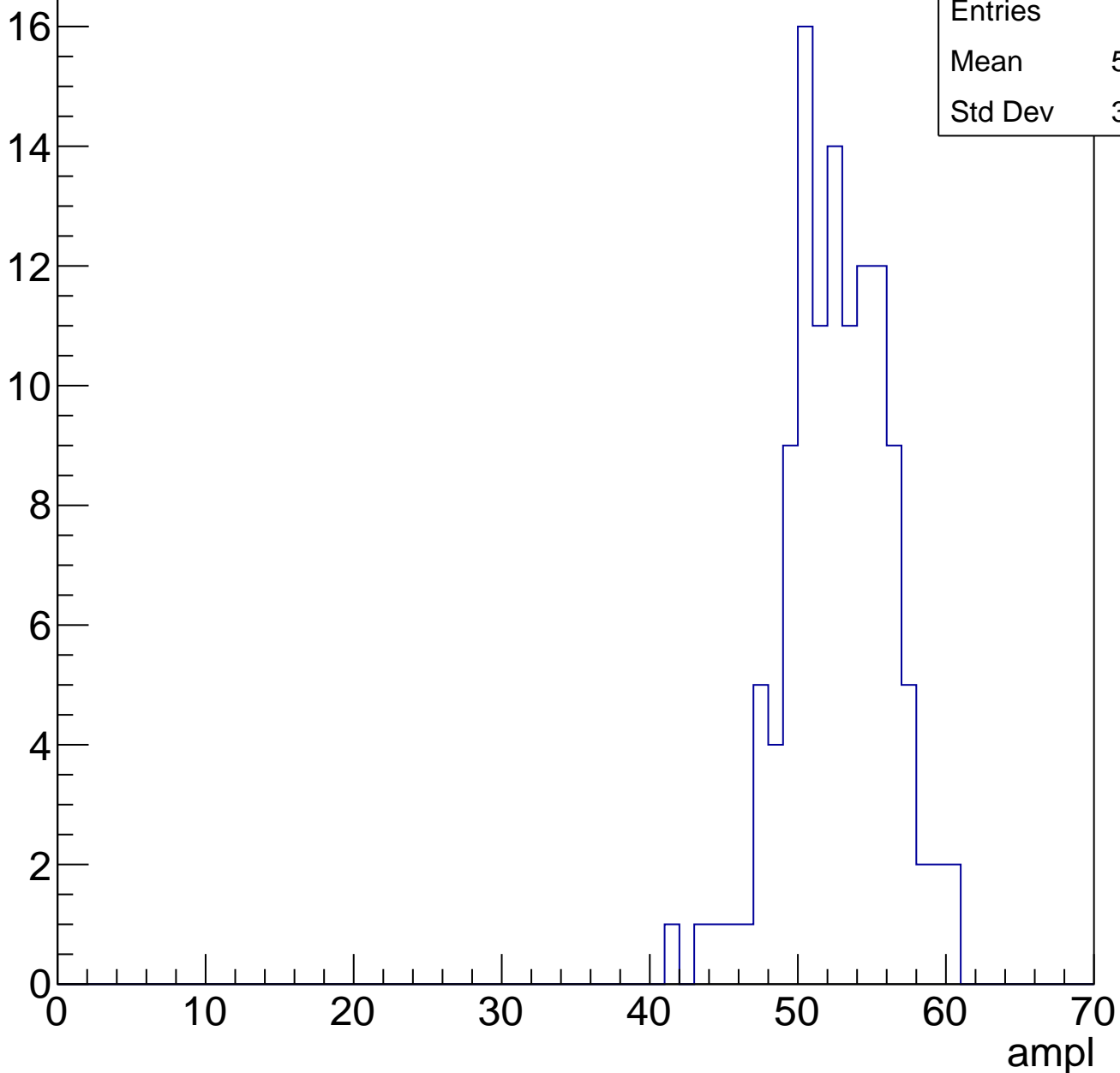
Entries	155
Mean	45.52
Std Dev	3.493

Gaus mean : 45.5303
Gaus Width: 3.3251

B1L001S, U25-ch5, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	119
Mean	52.18
Std Dev	3.462

B1L001S, U25-ch5, adc4

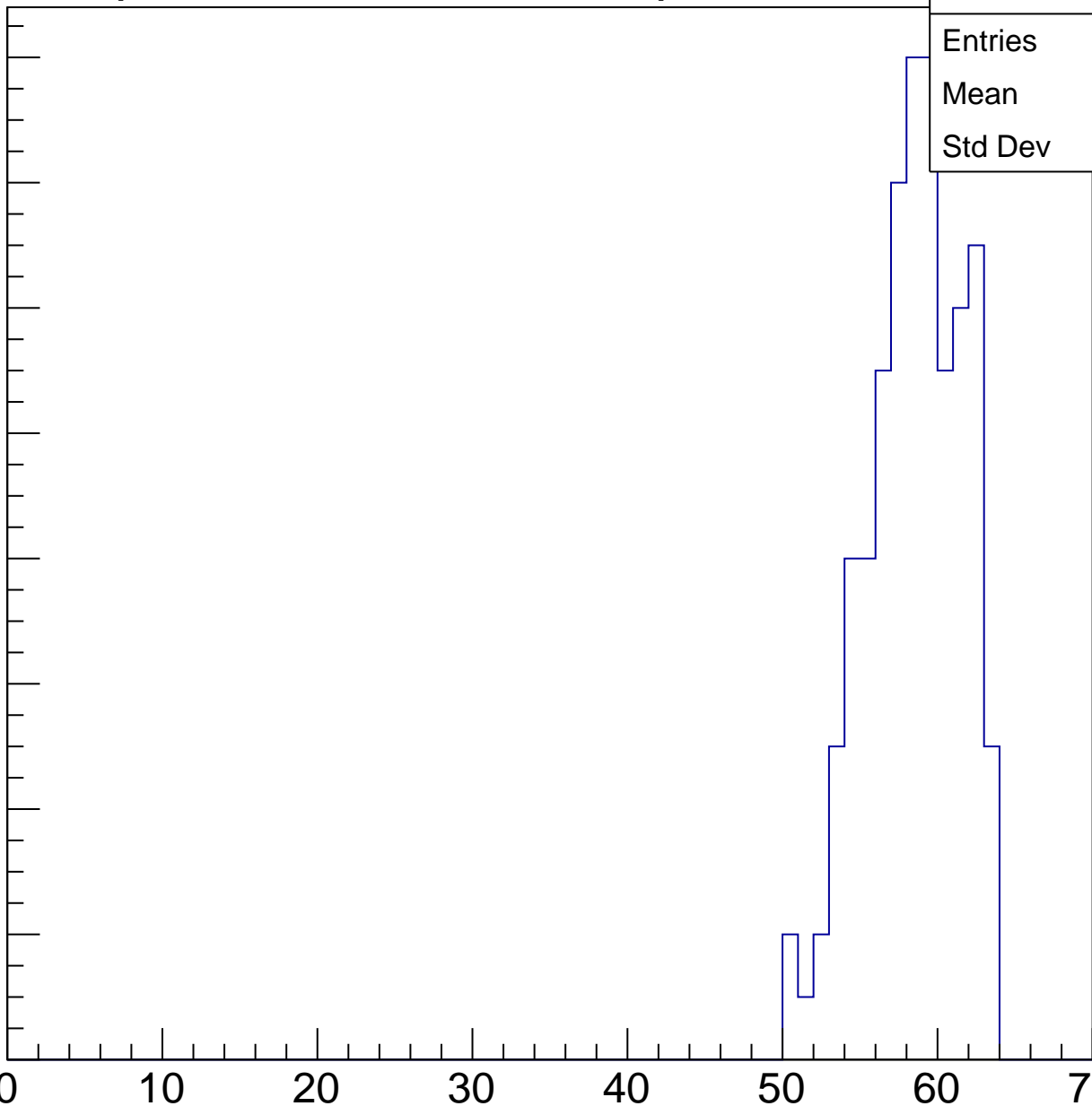
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	57.99
Std Dev	3.023

ampl



B1L001S, U25-ch5, adc5

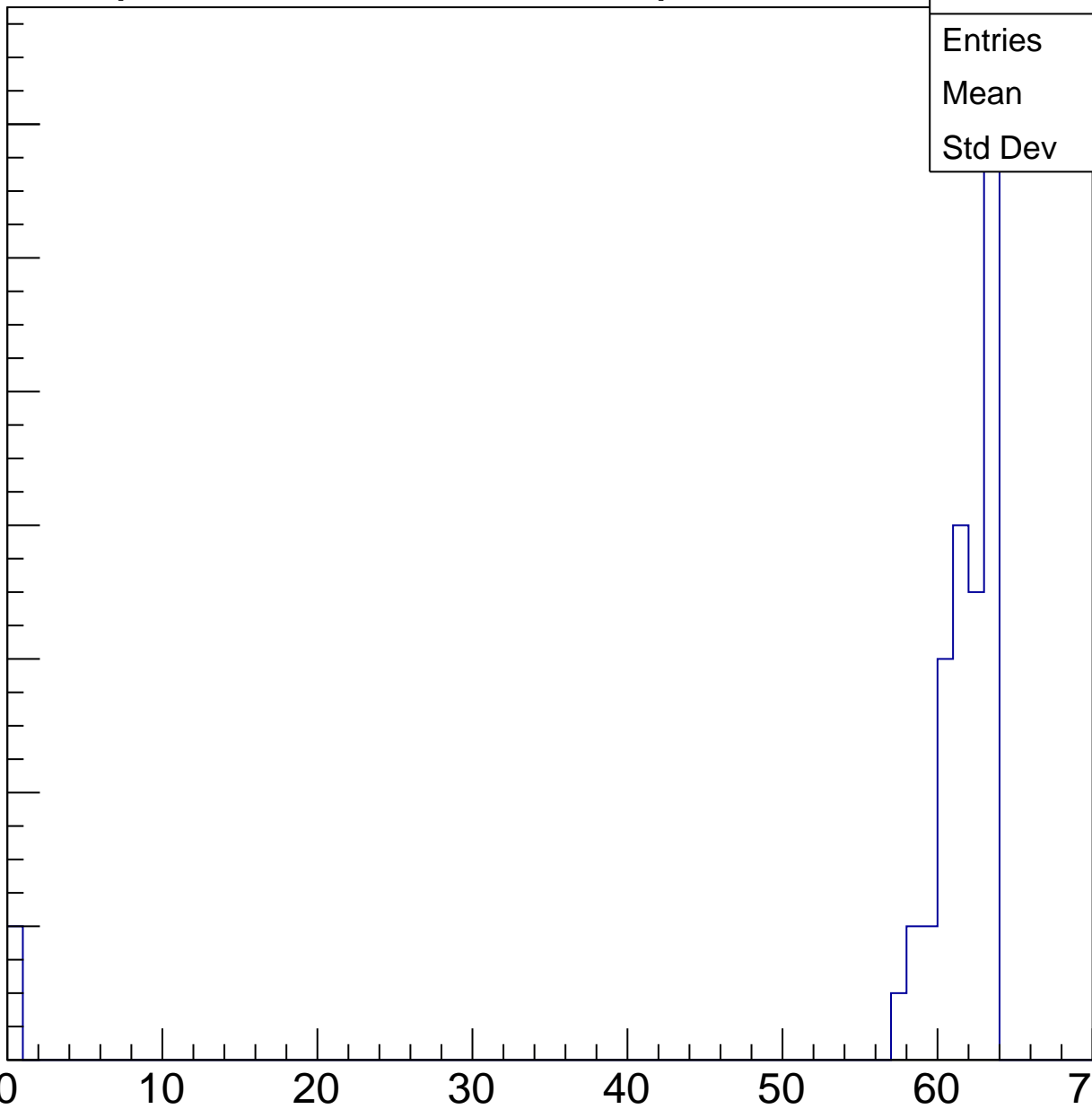
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	43
Mean	58.56
Std Dev	13.03

ampl



B1L001S, U25-ch5, adc6

calib_packv5_042523_0143.root, FC#2, port C2

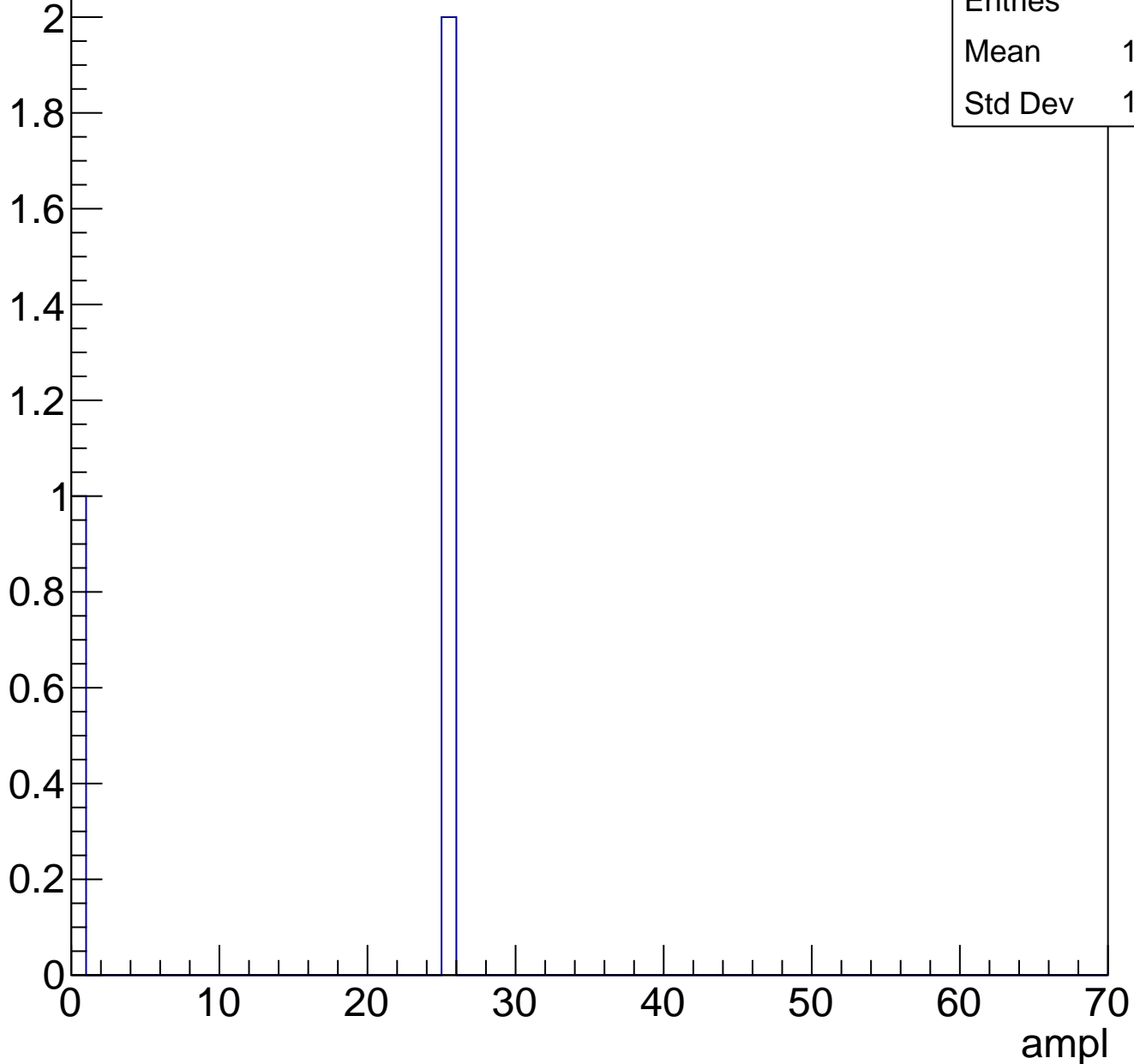
Entry



B1L001S, U25-ch5, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	16.67
Std Dev	11.79

B1L001S, U25-ch6, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	30.63
Std Dev	3.266

Gaus mean : 31.1705

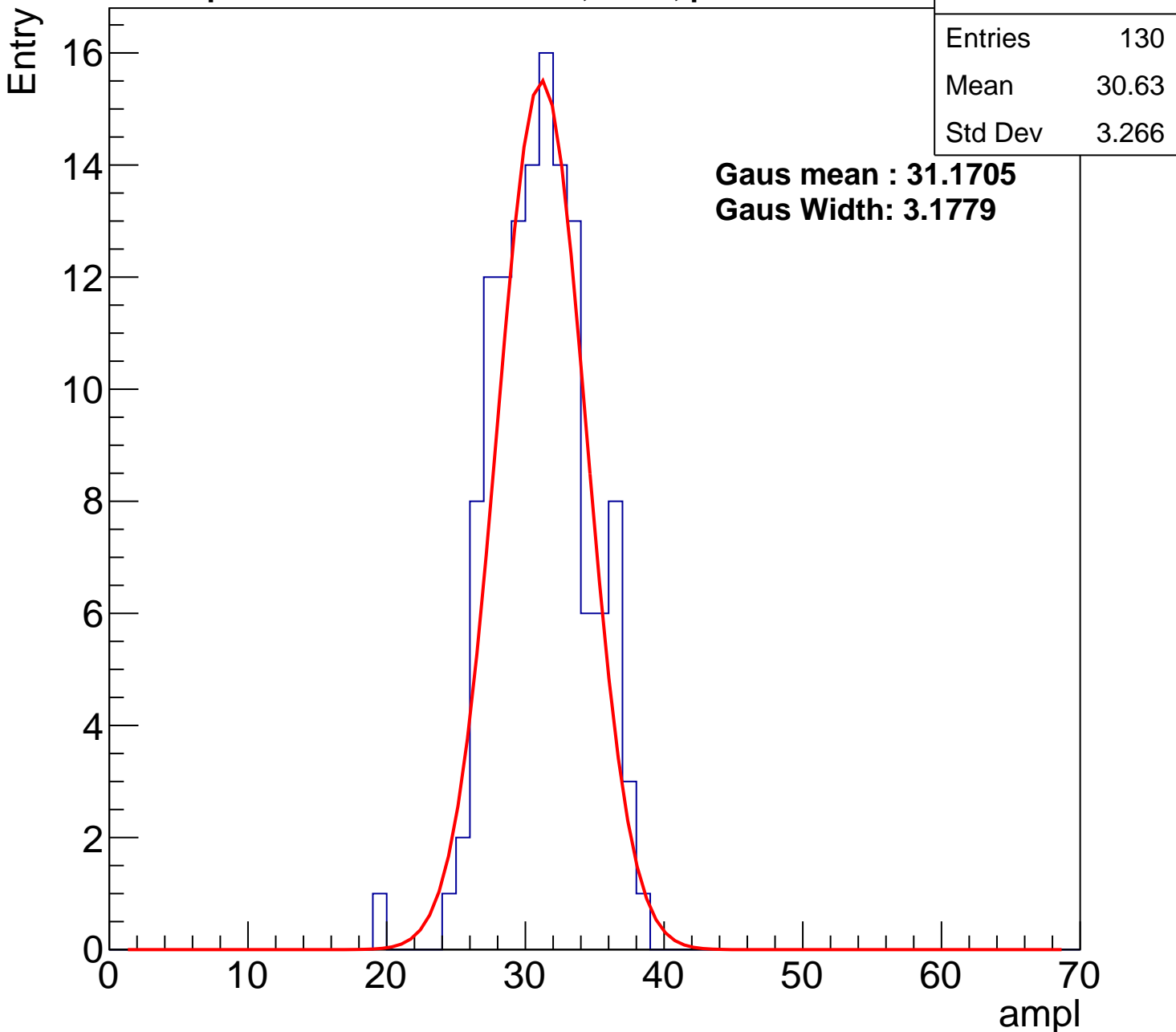
Gaus Width: 3.1779

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch6, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.92
Std Dev	3.174

Gaus mean : 37.3866

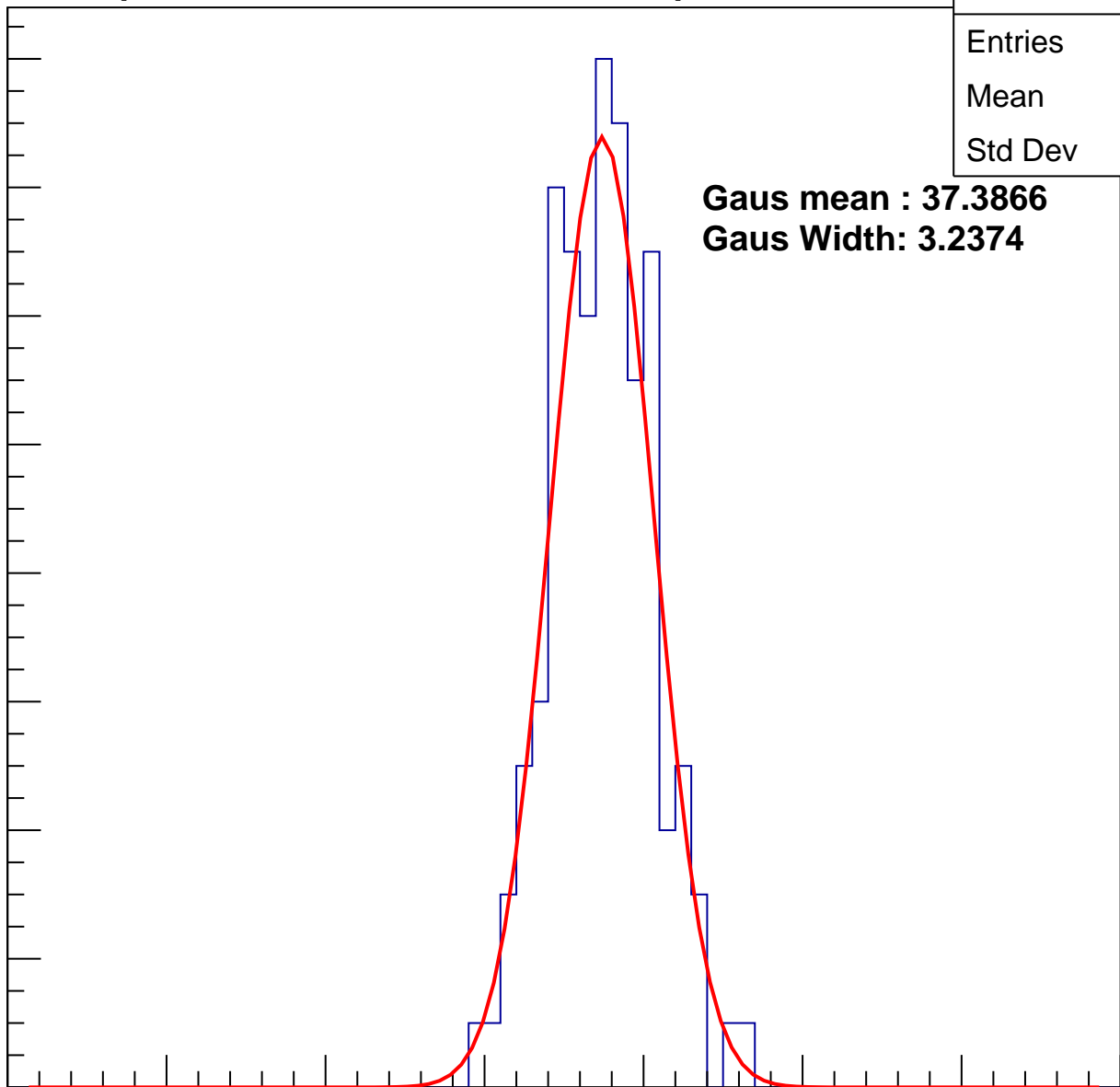
Gaus Width: 3.2374

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch6, adc2

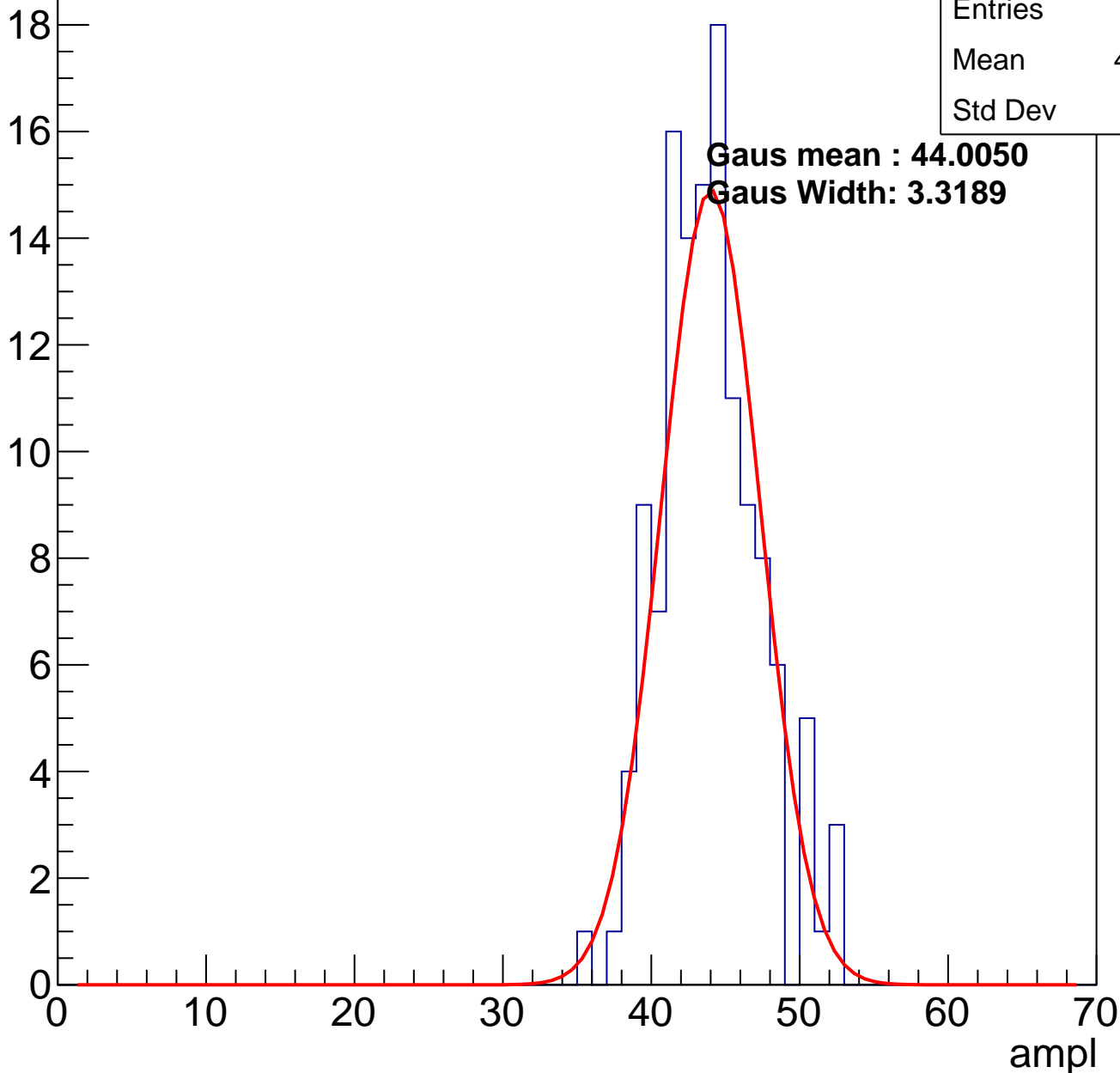
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	43.48
Std Dev	3.34

Gaus mean : 44.0050

Gaus Width: 3.3189

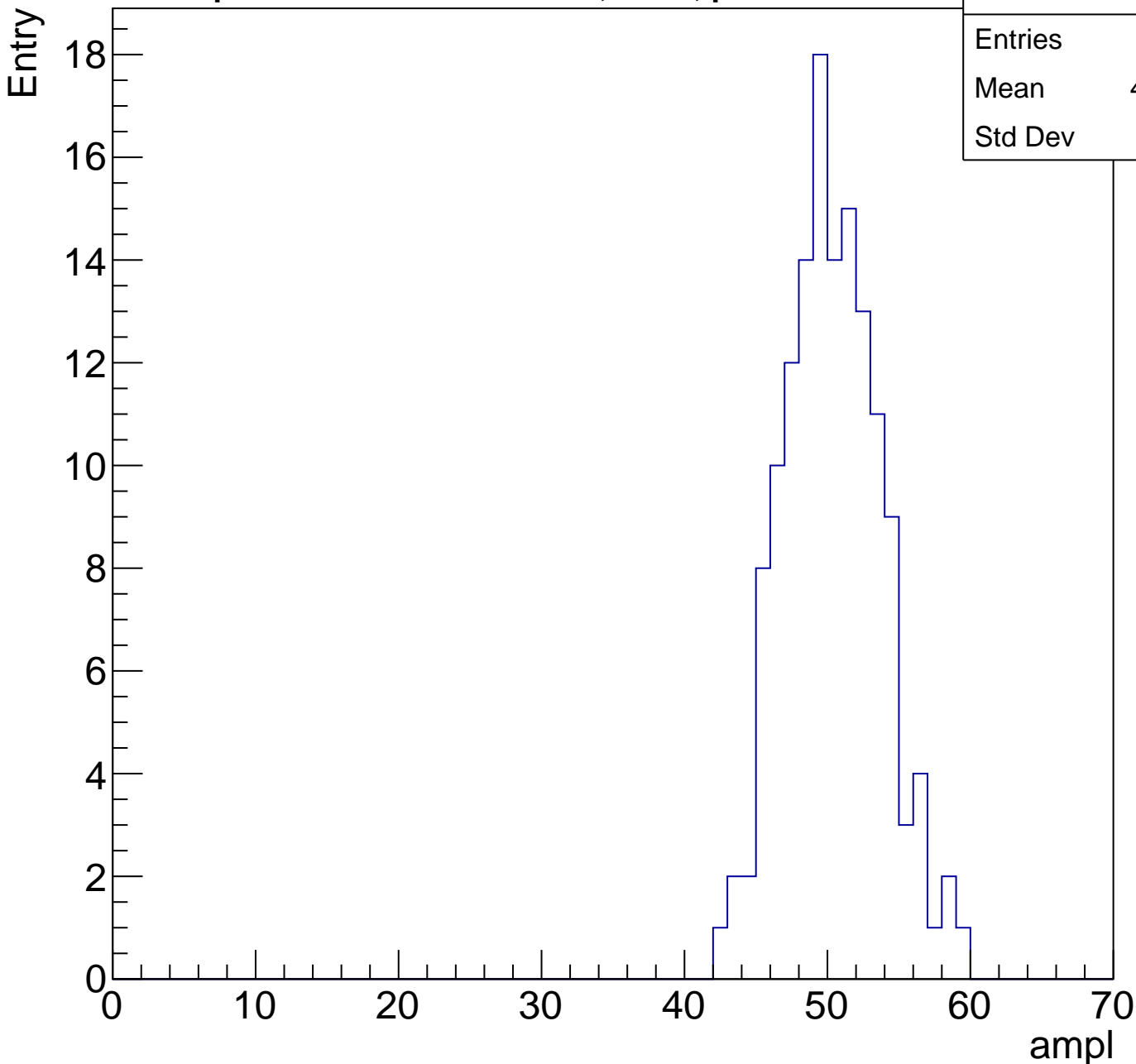
Entry



B1L001S, U25-ch6, adc3

calib_packv5_042523_0143.root, FC#2, port C2

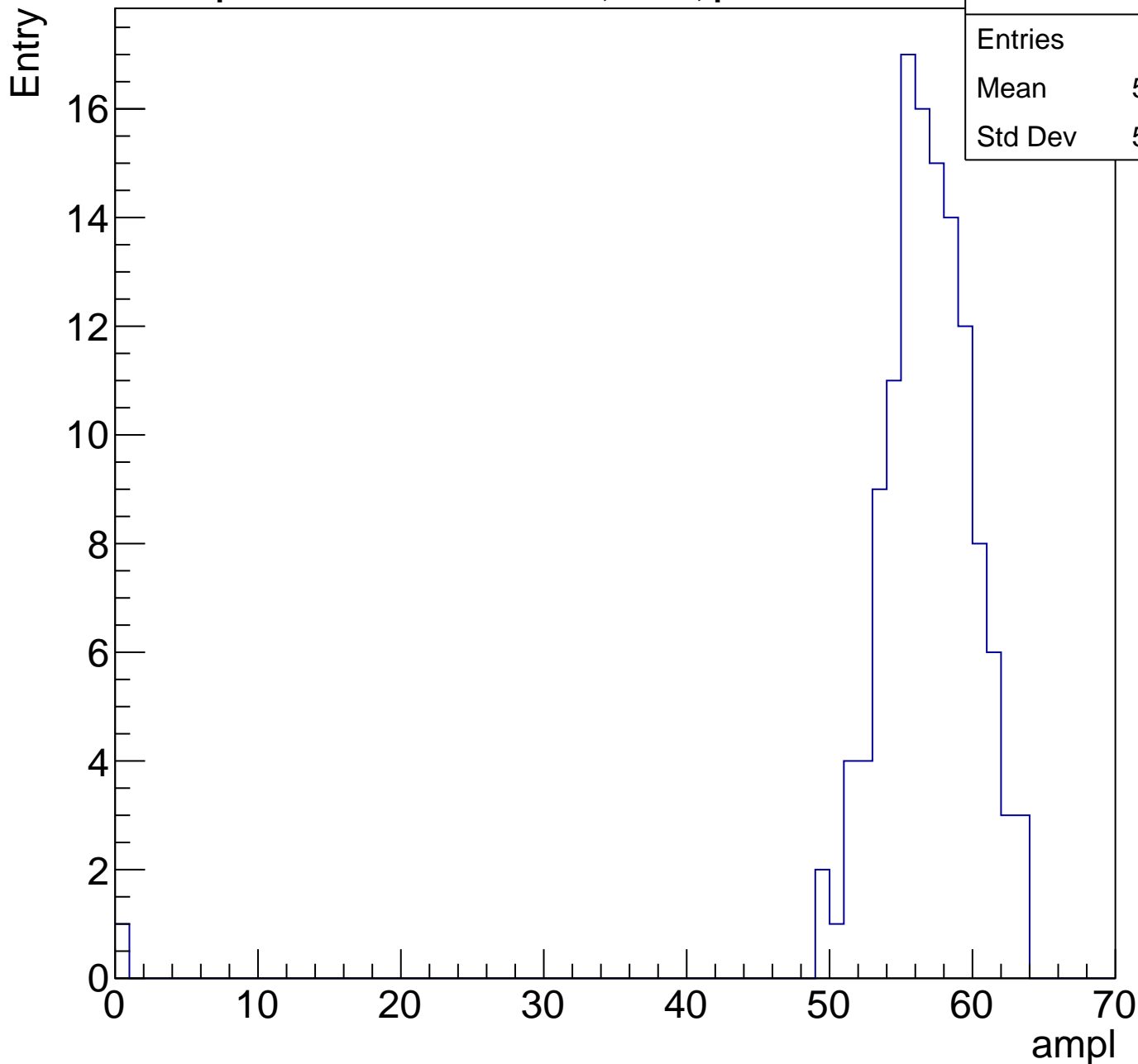
Entries	140
Mean	49.89
Std Dev	3.34



B1L001S, U25-ch6, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	56.02
Std Dev	5.834



B1L001S, U25-ch6, adc5

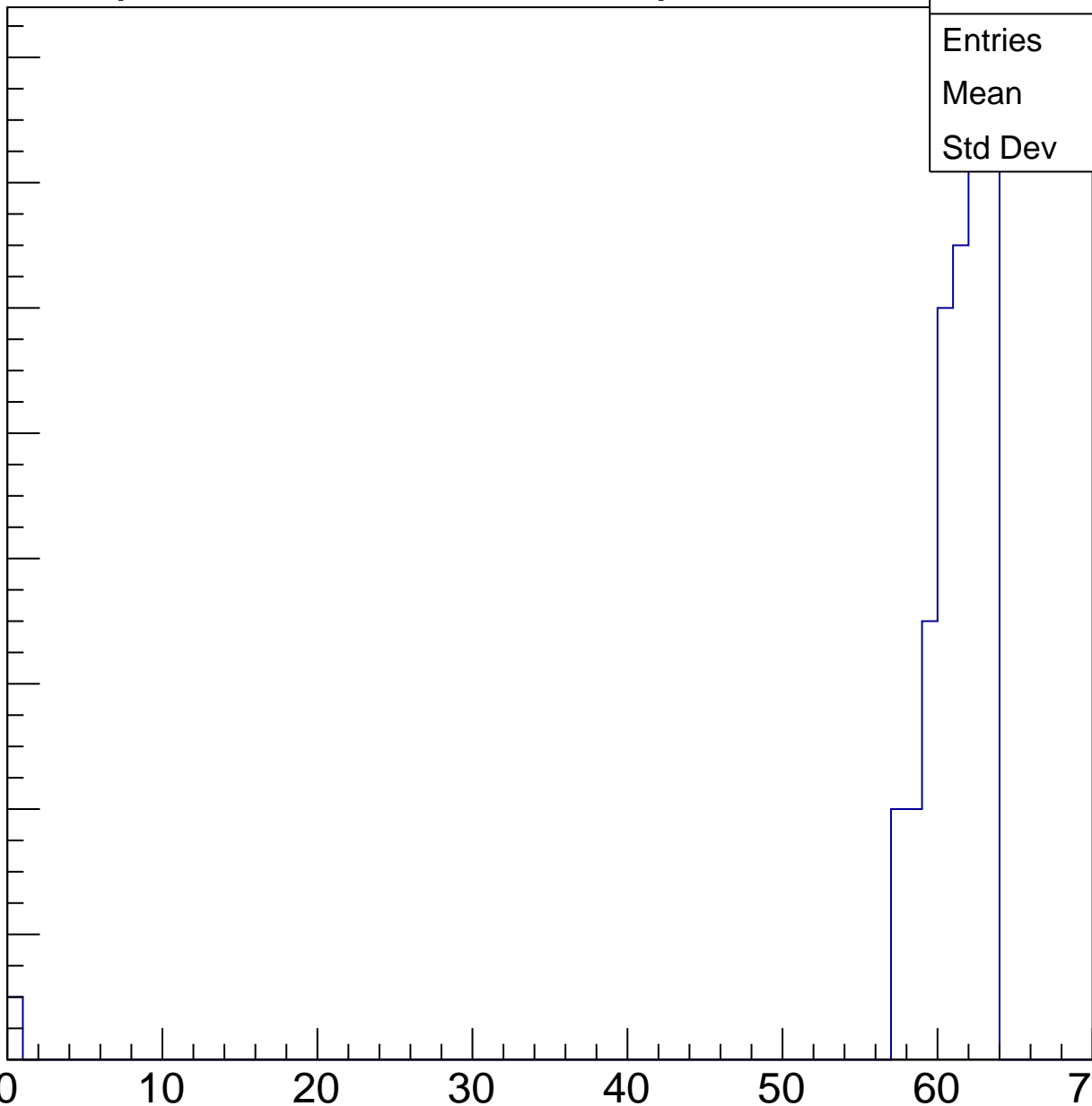
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	73
Mean	60.08
Std Dev	7.29

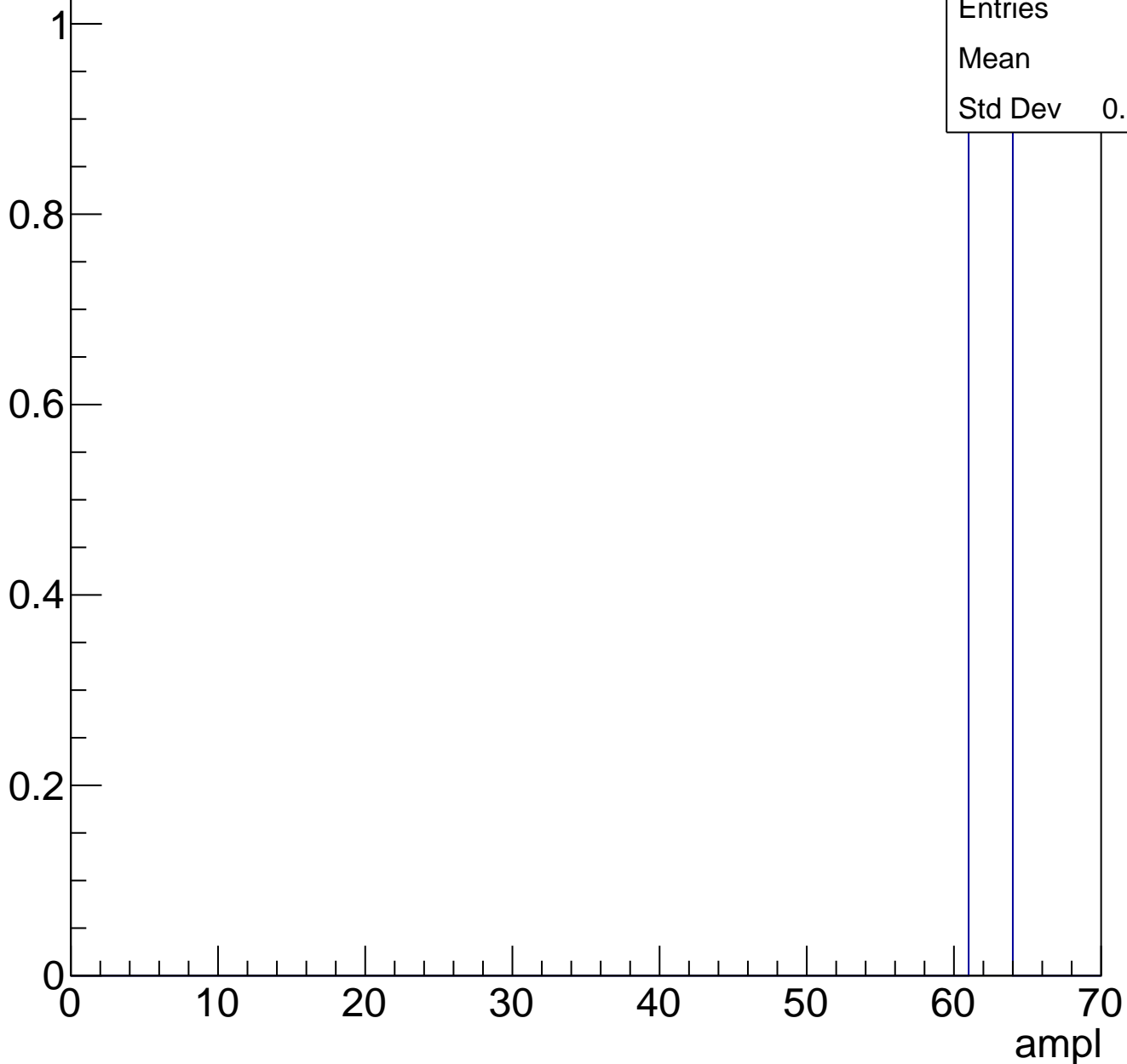
ampl



B1L001S, U25-ch6, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch6, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L001S, U25-ch7, adc0

calib_packv5_042523_0143.root, FC#2, port C2

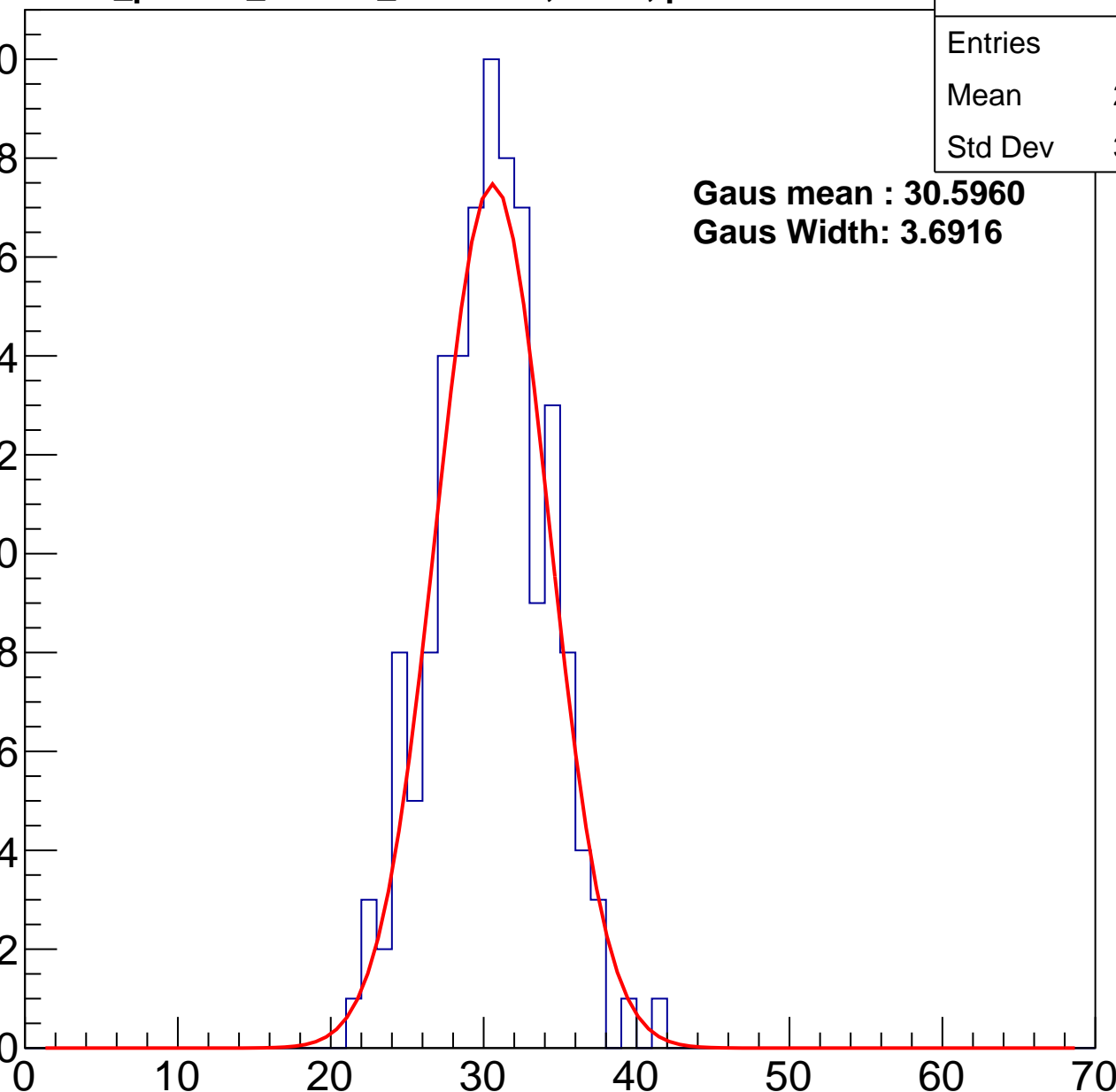
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	166
Mean	29.98
Std Dev	3.623

Gaus mean : 30.5960

Gaus Width: 3.6916



ampl

B1L001S, U25-ch7, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	37.5
Std Dev	3.135

Gaus mean : 38.1090

Gaus Width: 3.3509

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

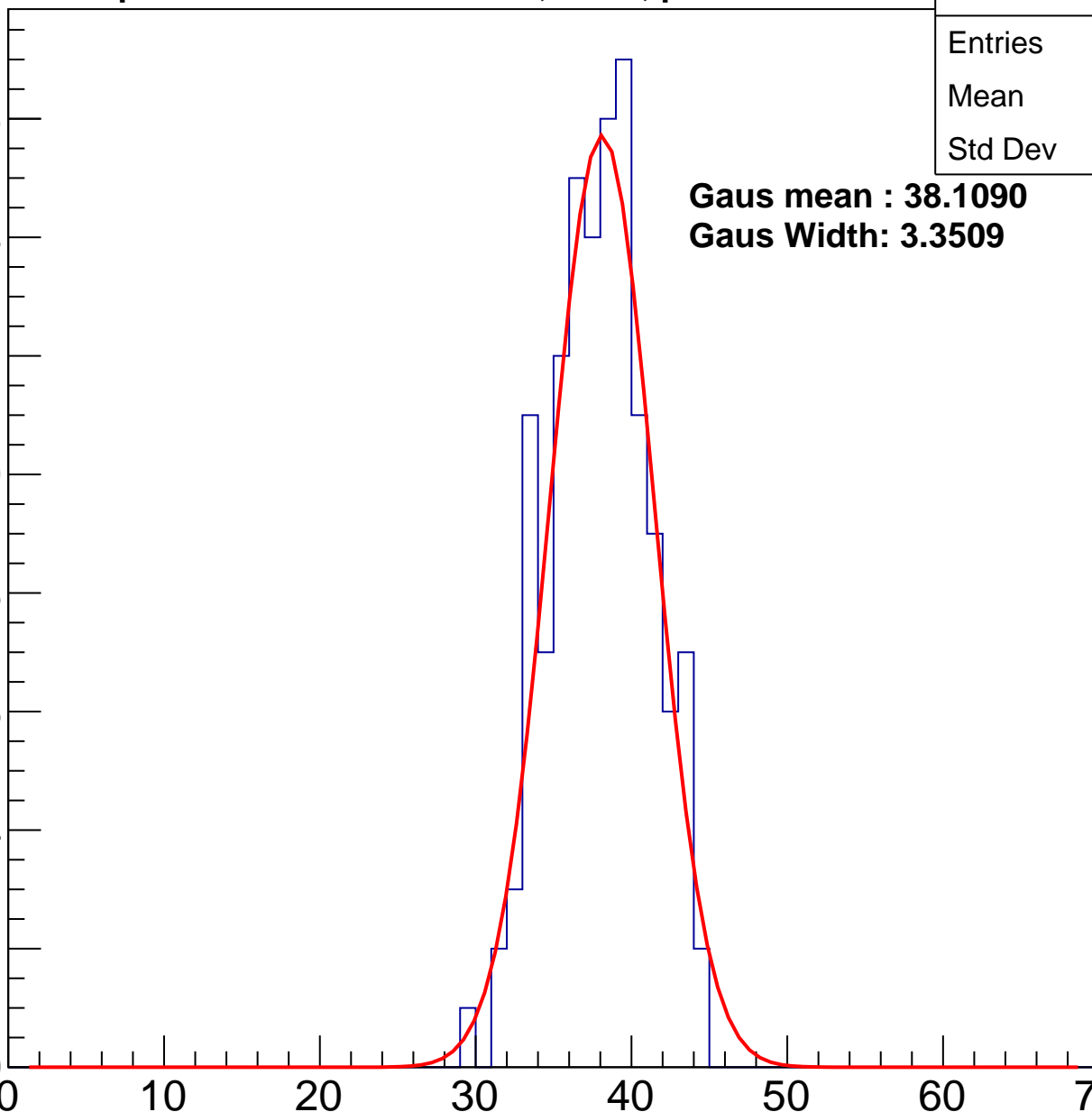
40

50

60

70

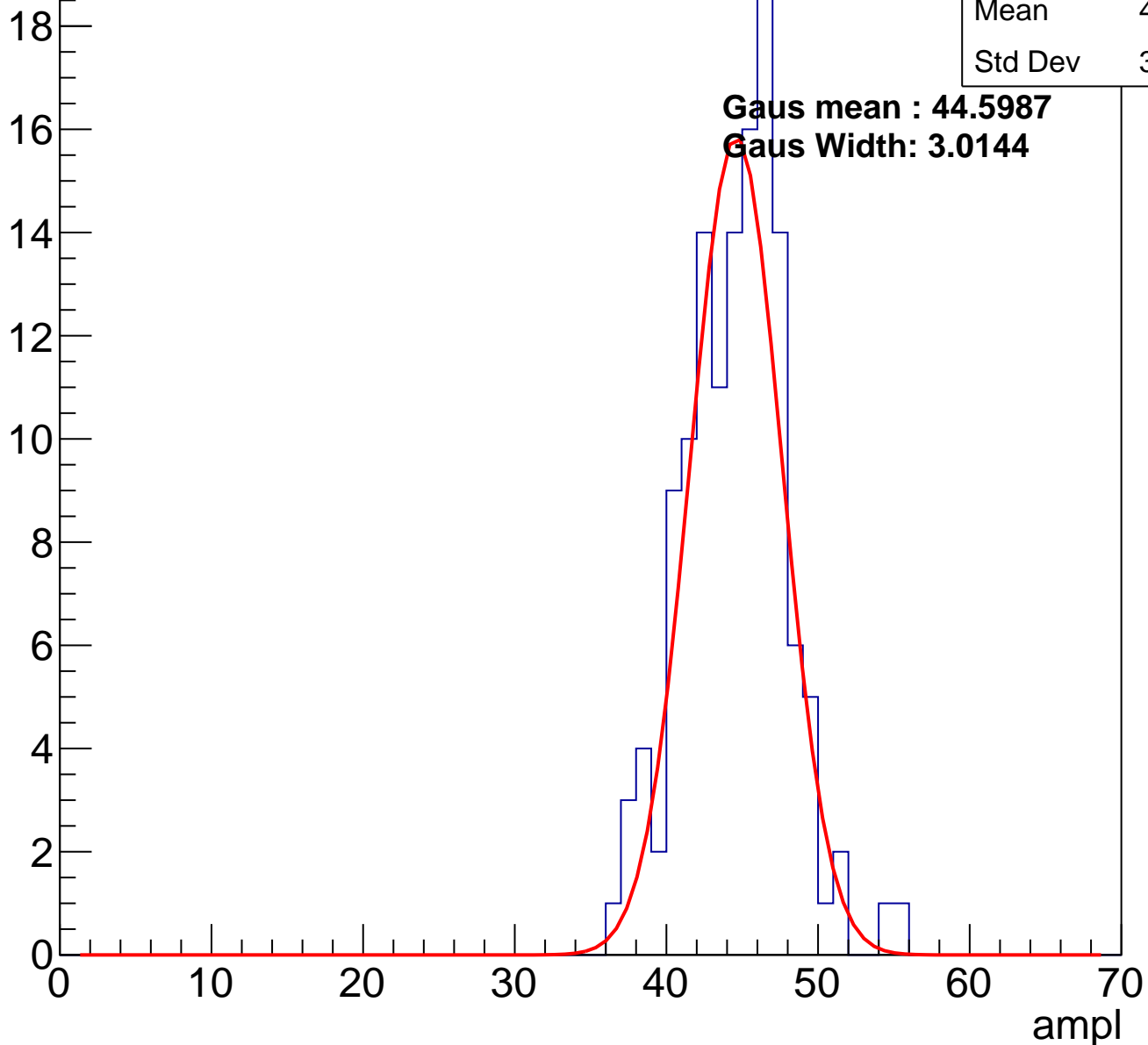
ampl



B1L001S, U25-ch7, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

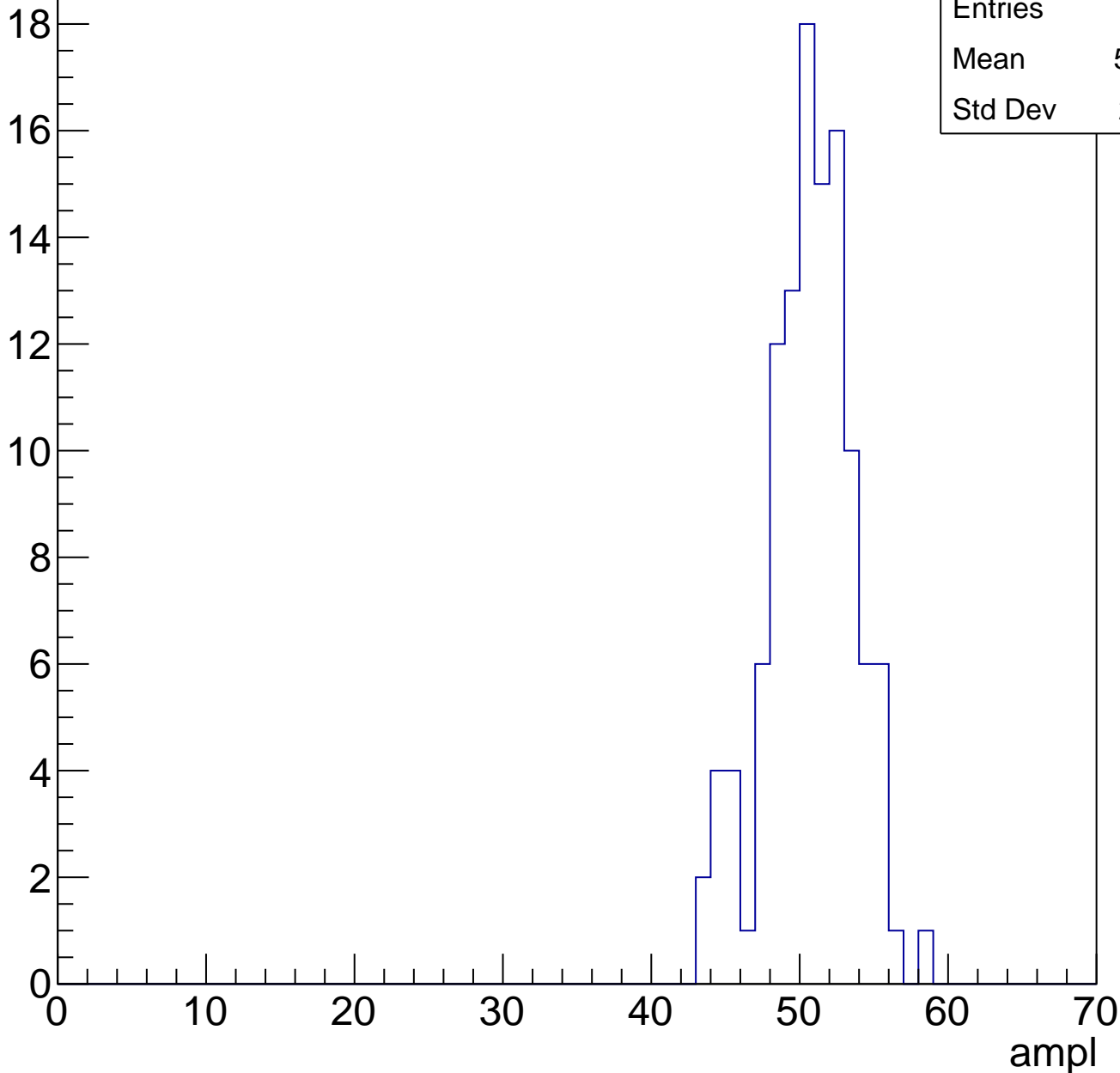


B1L001S, U25-ch7, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	50.24
Std Dev	2.921

Entry



B1L001S, U25-ch7, adc4

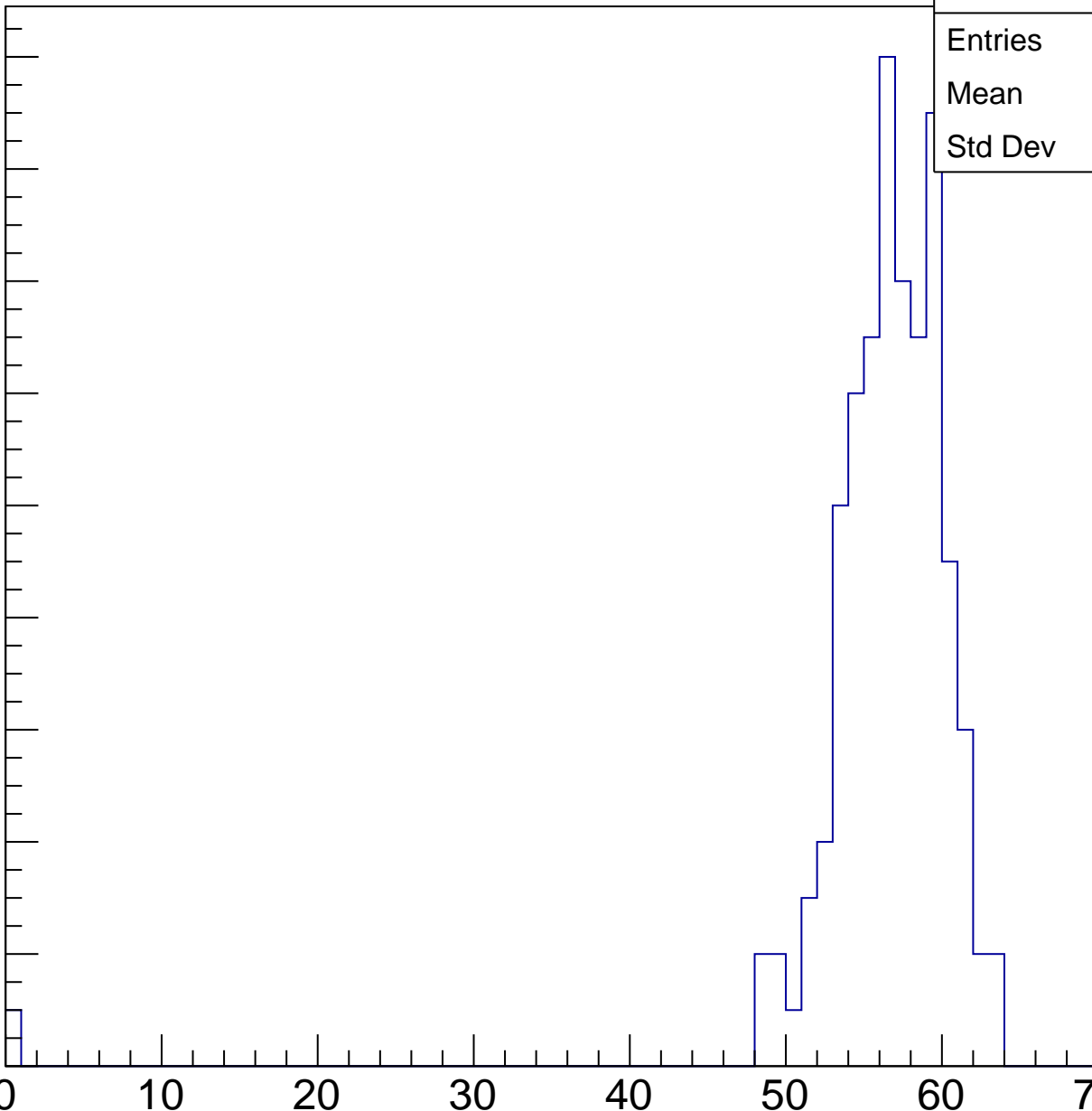
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	129
Mean	55.95
Std Dev	5.821

ampl



B1L001S, U25-ch7, adc5

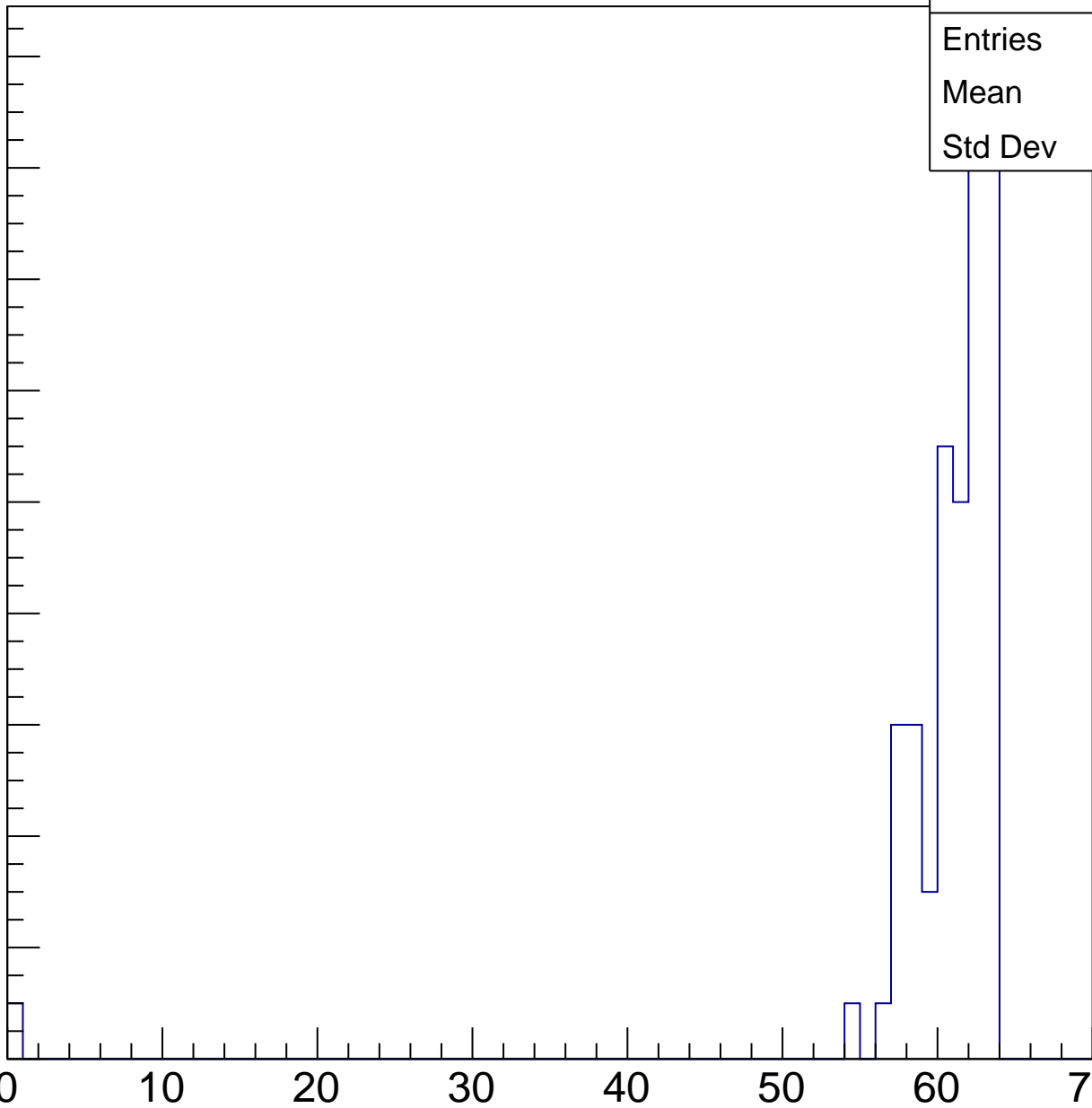
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	73
Mean	59.88
Std Dev	7.363

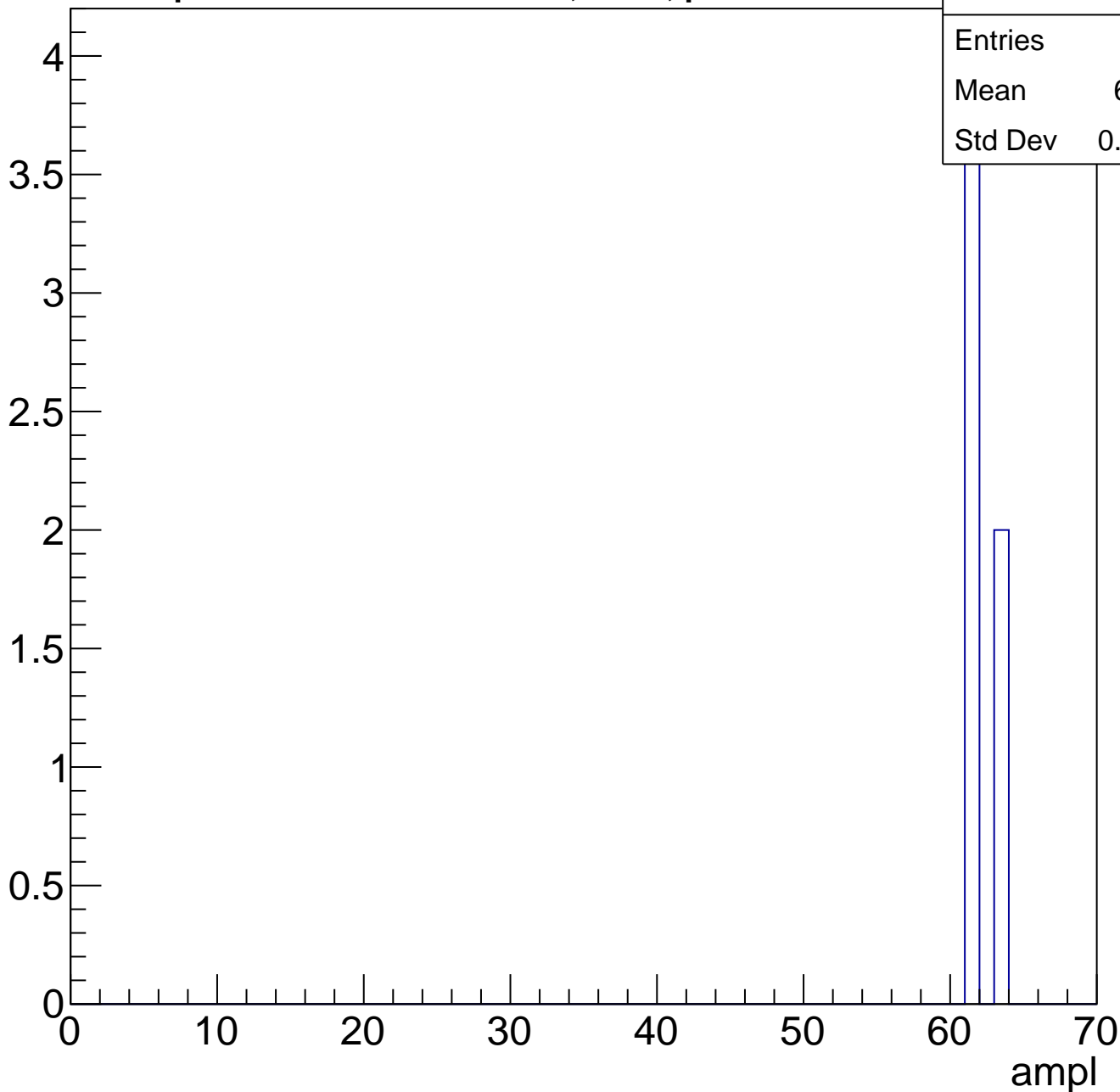
ampl



B1L001S, U25-ch7, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch7, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U25-ch8, adc0

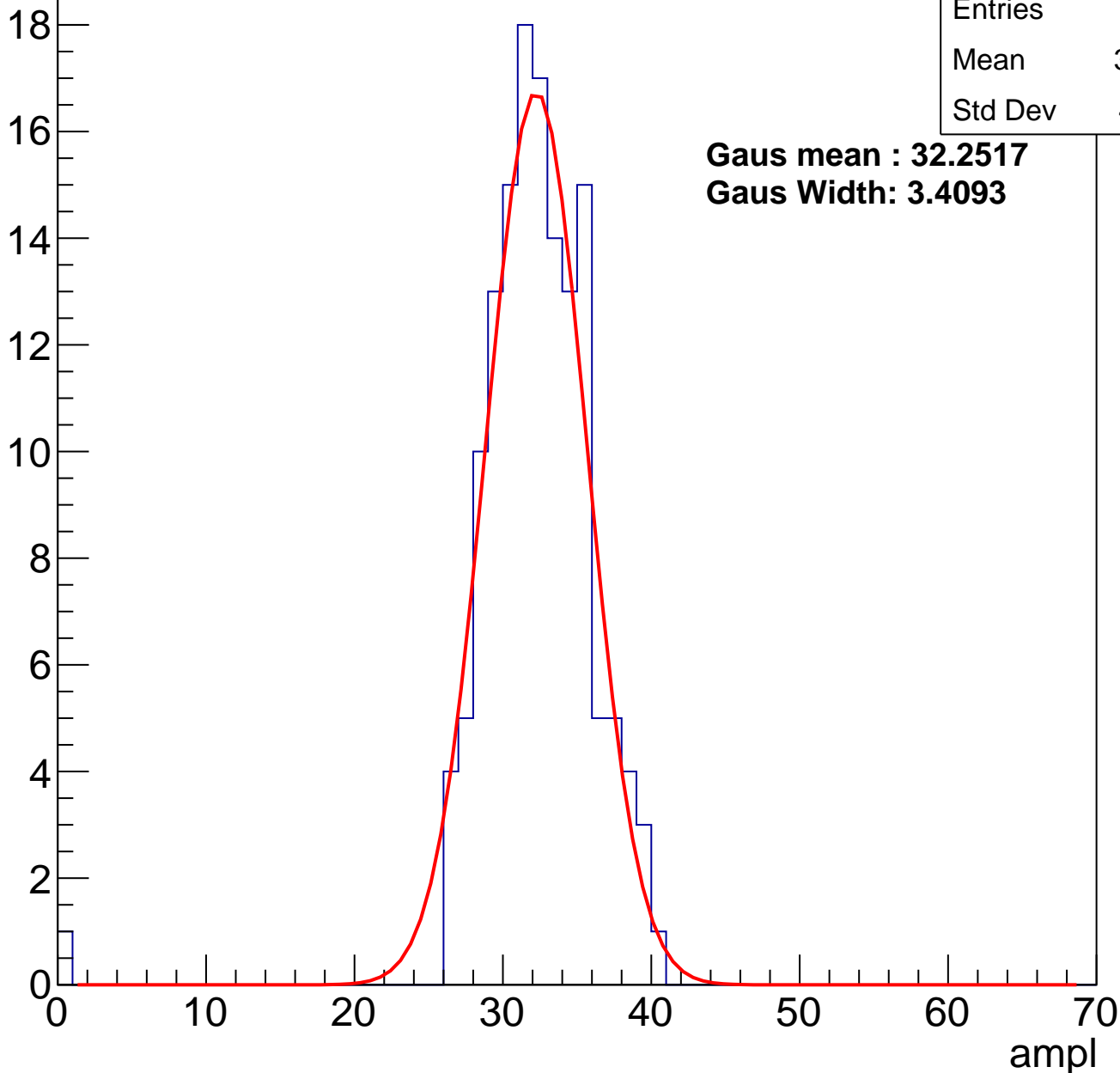
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	31.83
Std Dev	4.101

Gaus mean : 32.2517

Gaus Width: 3.4093

Entry



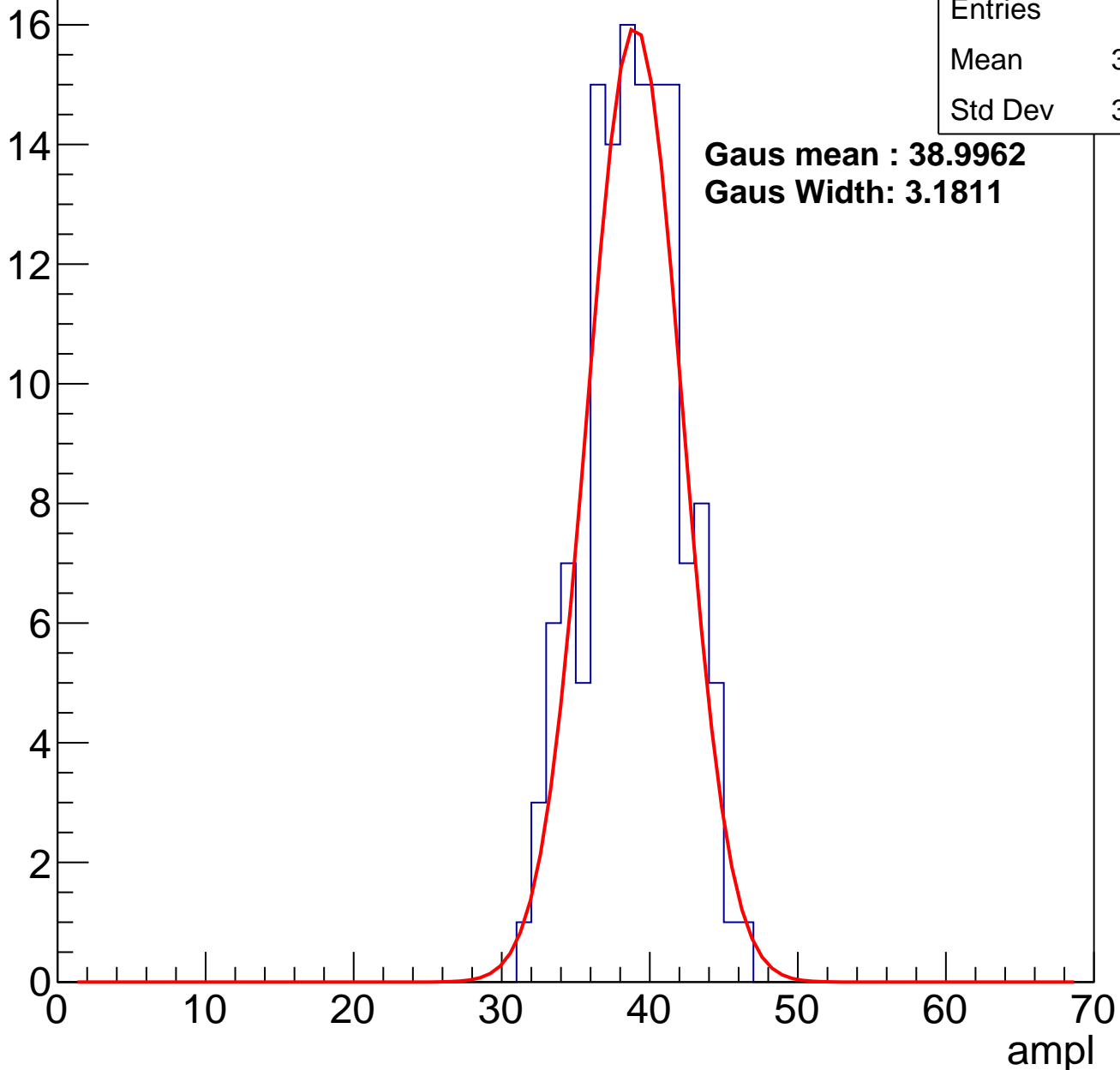
B1L001S, U25-ch8, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	38.46
Std Dev	3.145

Gaus mean : 38.9962
Gaus Width: 3.1811

Entry



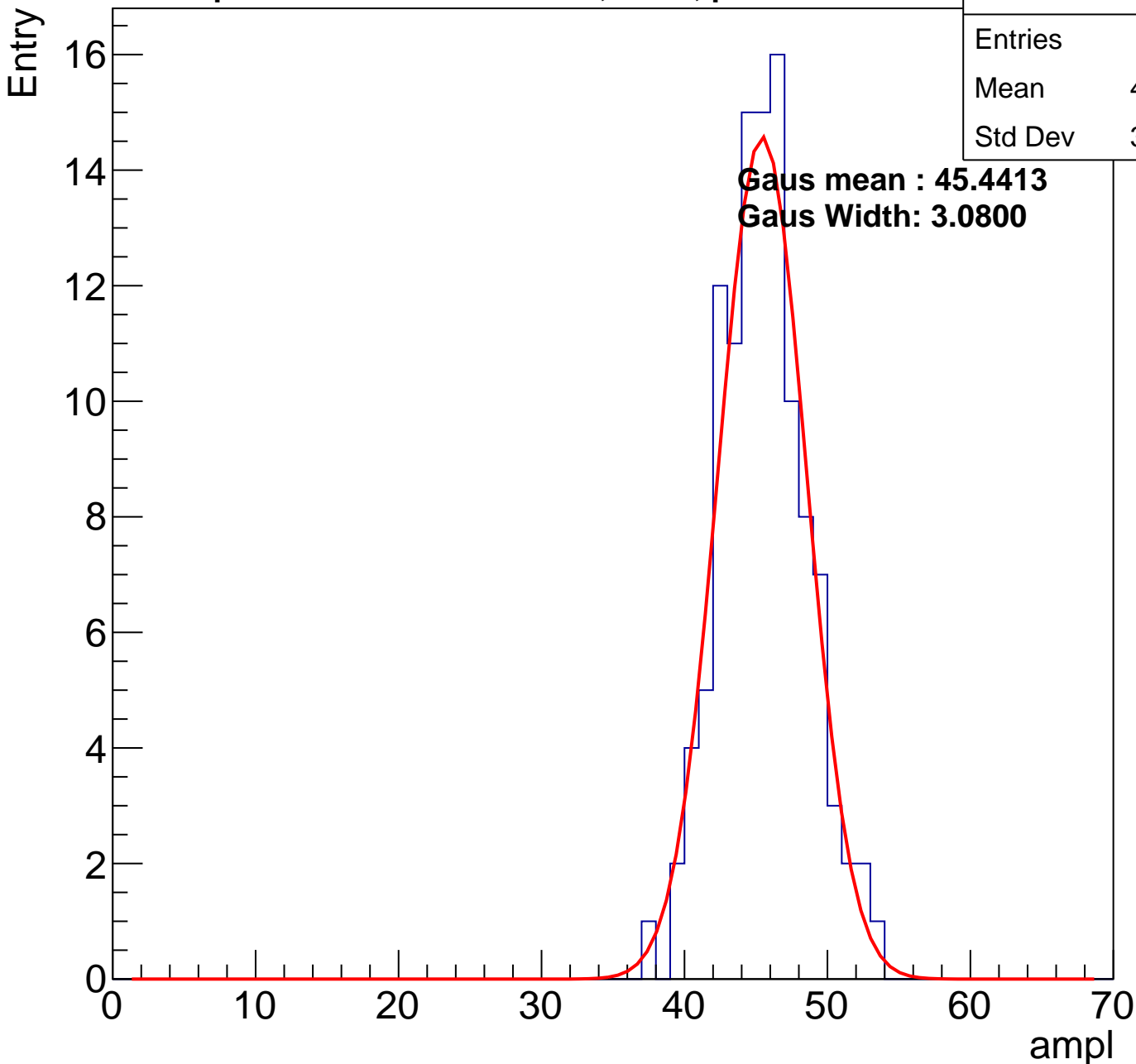
B1L001S, U25-ch8, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	45.04
Std Dev	3.009

Gaus mean : 45.4413

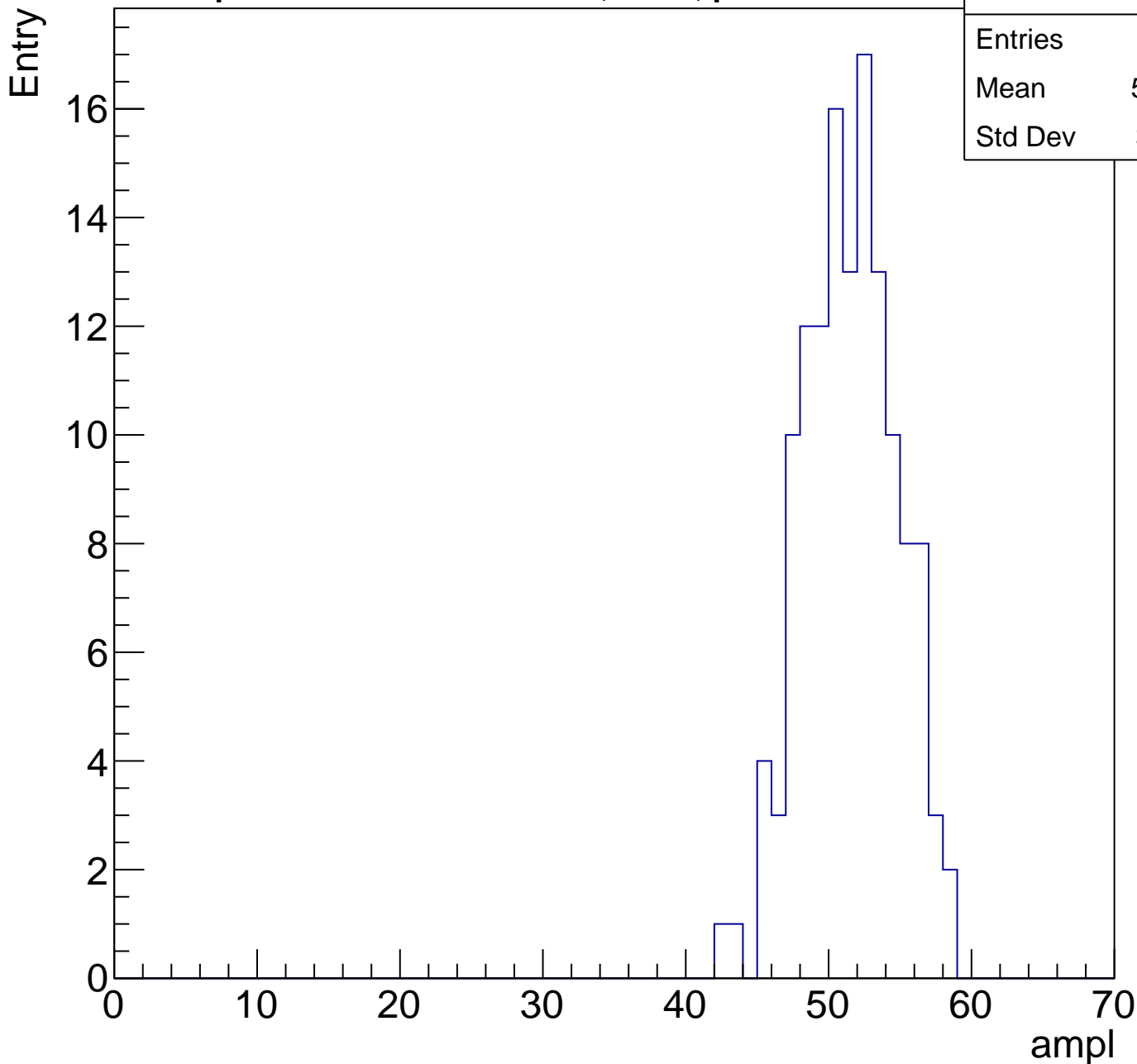
Gaus Width: 3.0800



B1L001S, U25-ch8, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	51.04
Std Dev	3.231



B1L001S, U25-ch8, adc4

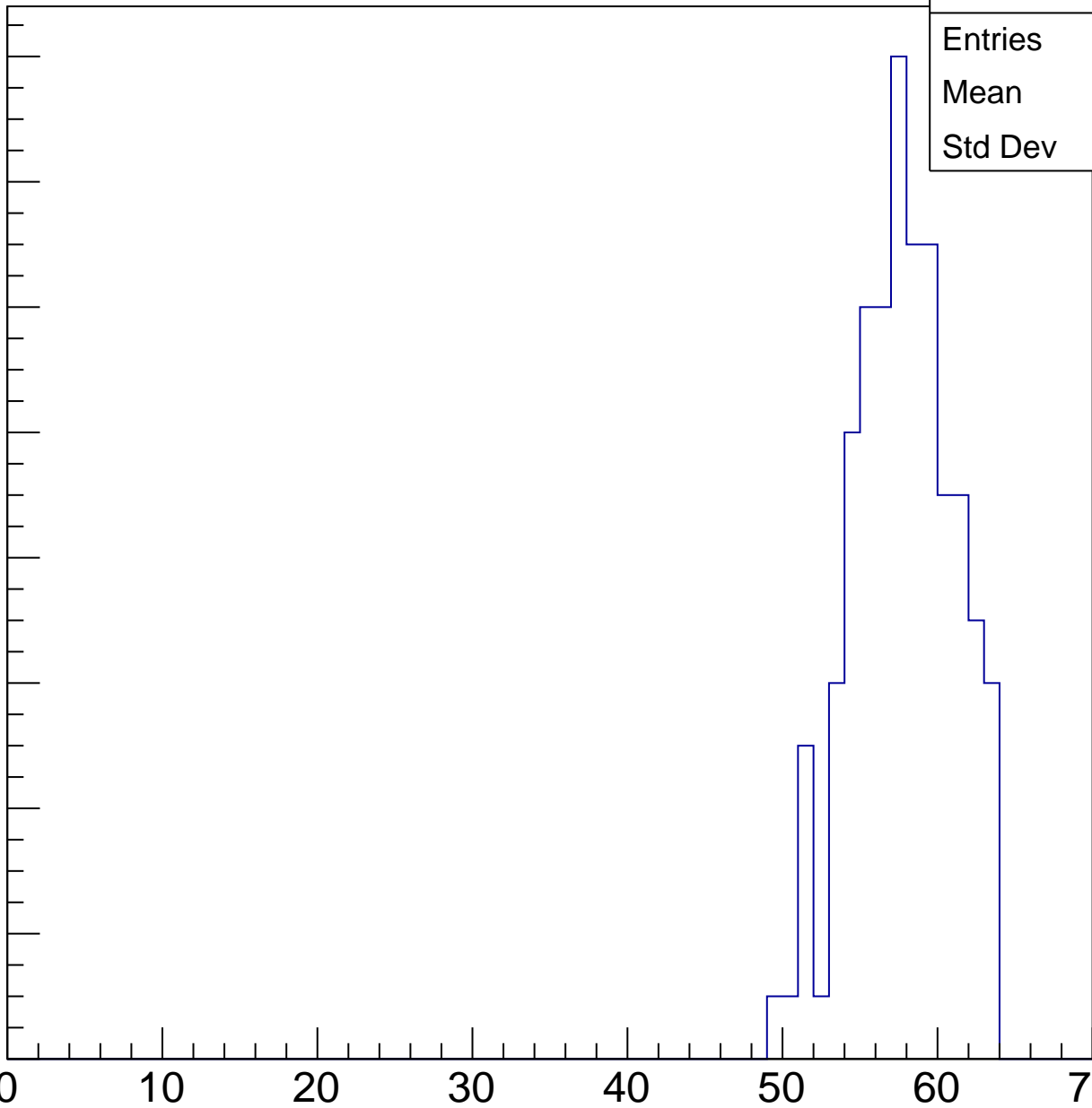
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	57.27
Std Dev	3.196

ampl



B1L001S, U25-ch8, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

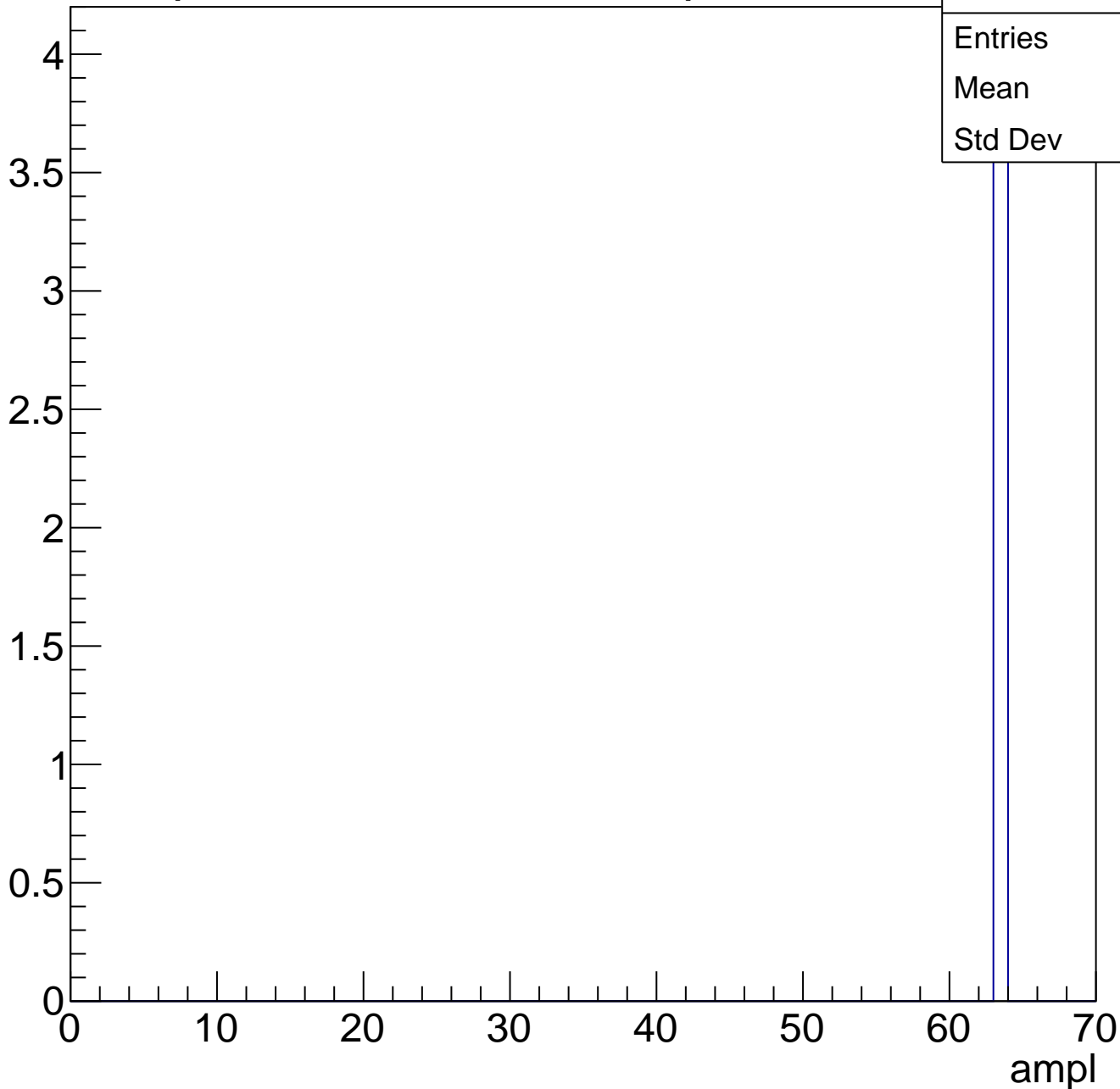
Entries	60
Mean	58.78
Std Dev	11.03



B1L001S, U25-ch8, adc6

calib_packv5_042523_0143.root, FC#2, port C2

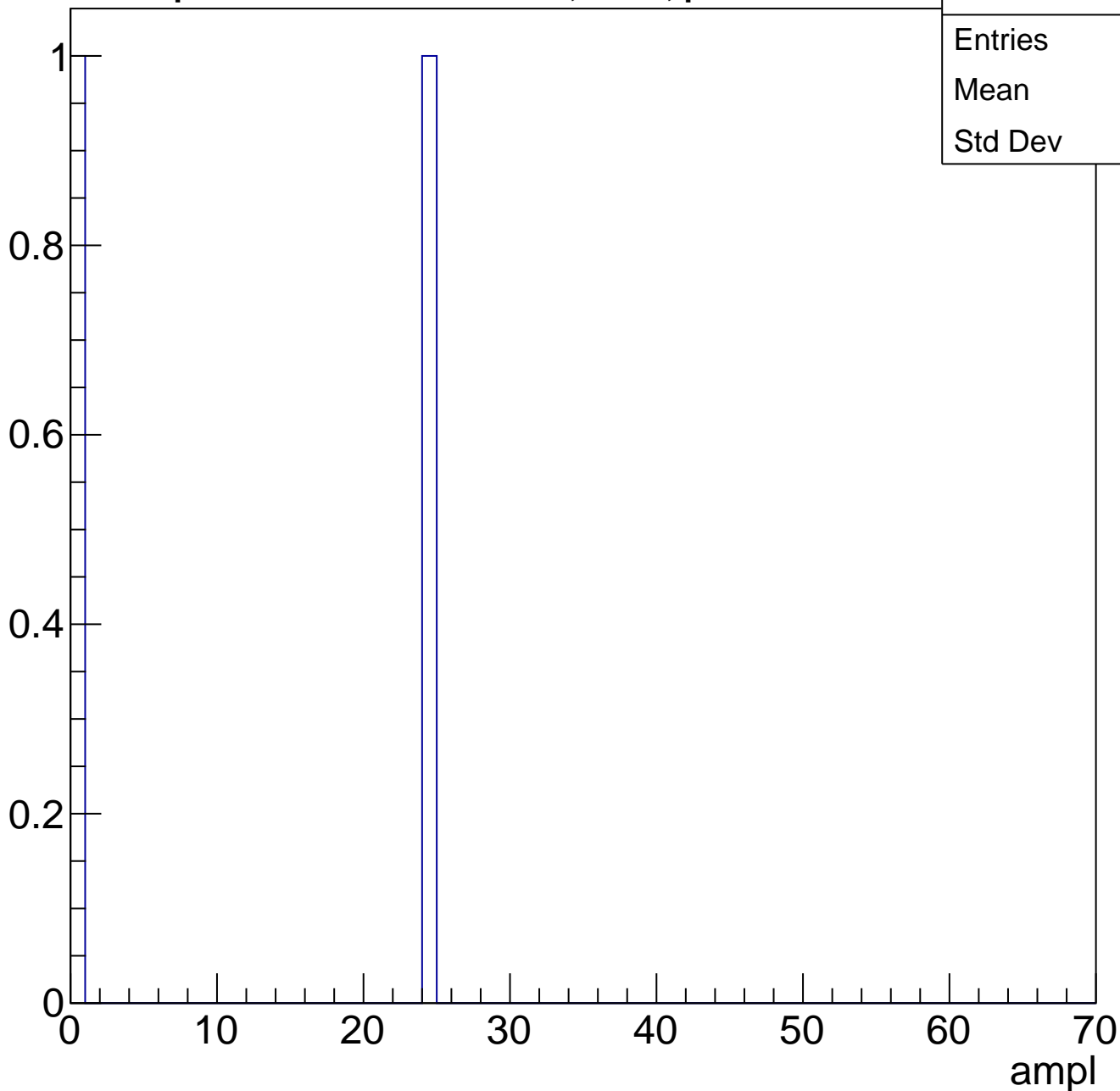
Entry



B1L001S, U25-ch8, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch9, adc0

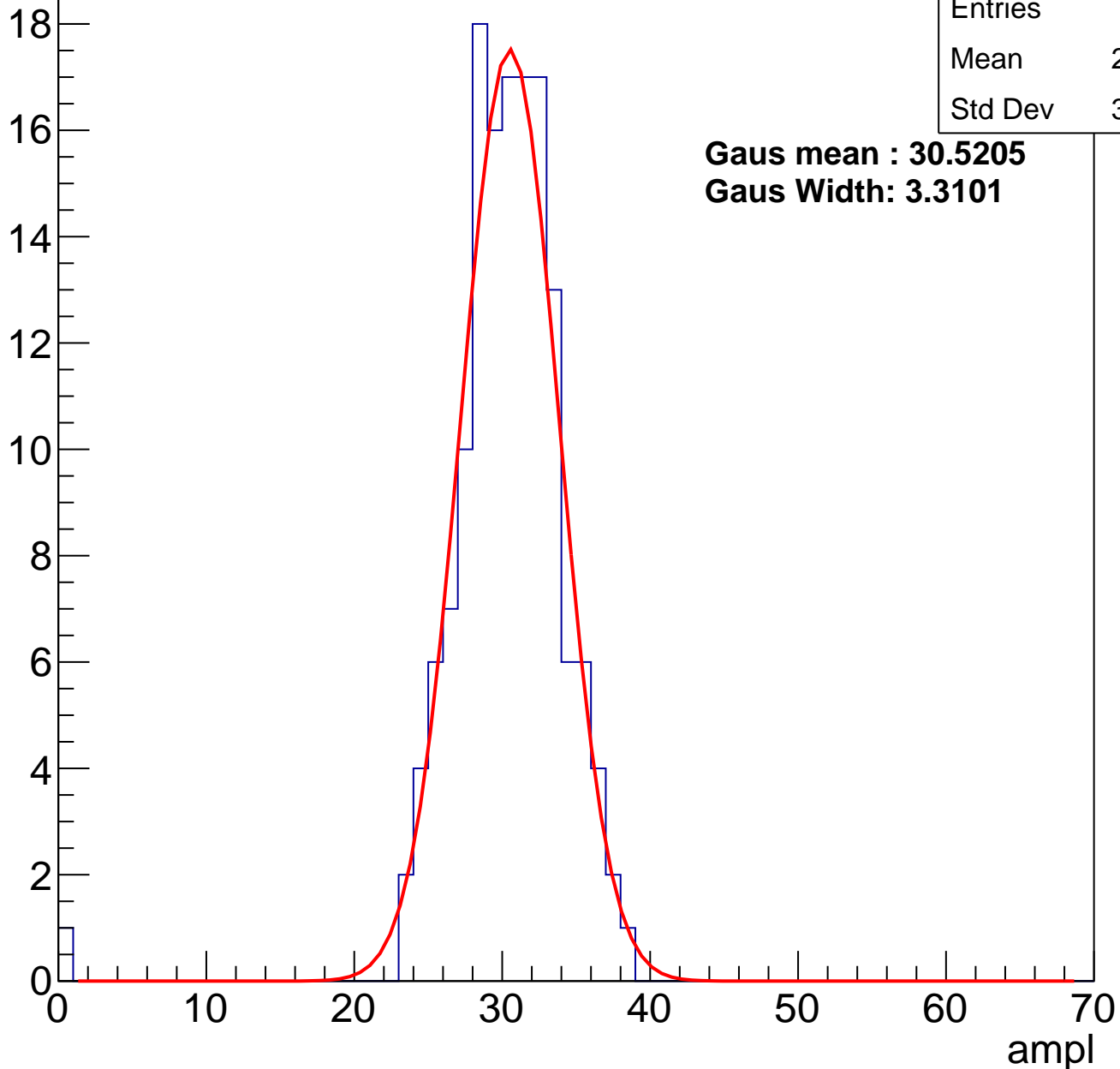
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	29.88
Std Dev	3.984

Gaus mean : 30.5205

Gaus Width: 3.3101

Entry



B1L001S, U25-ch9, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	37.09
Std Dev	3.132

Gaus mean : 37.4315

Gaus Width: 3.0688

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

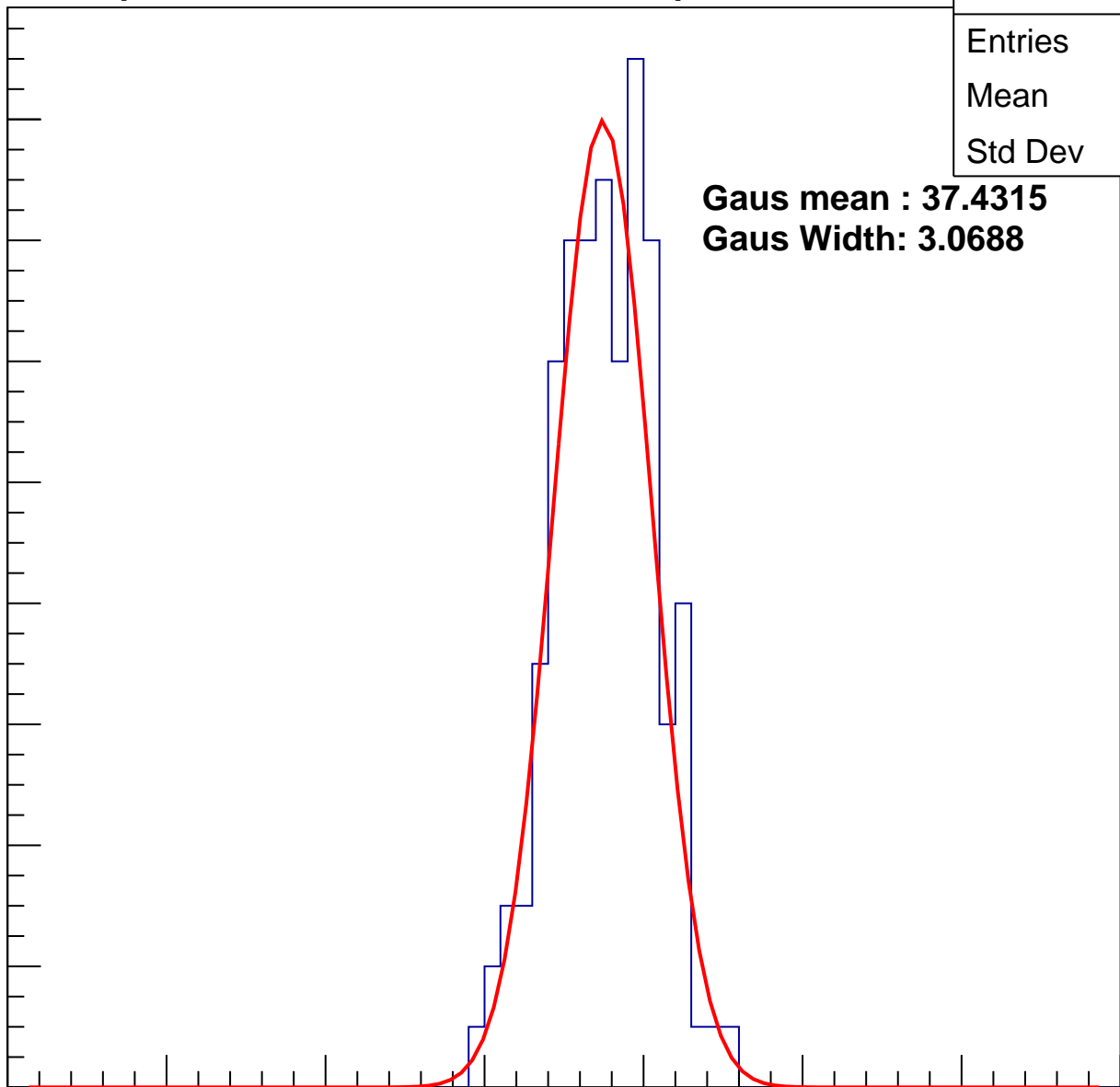
40

50

60

70

ampl



B1L001S, U25-ch9, adc2

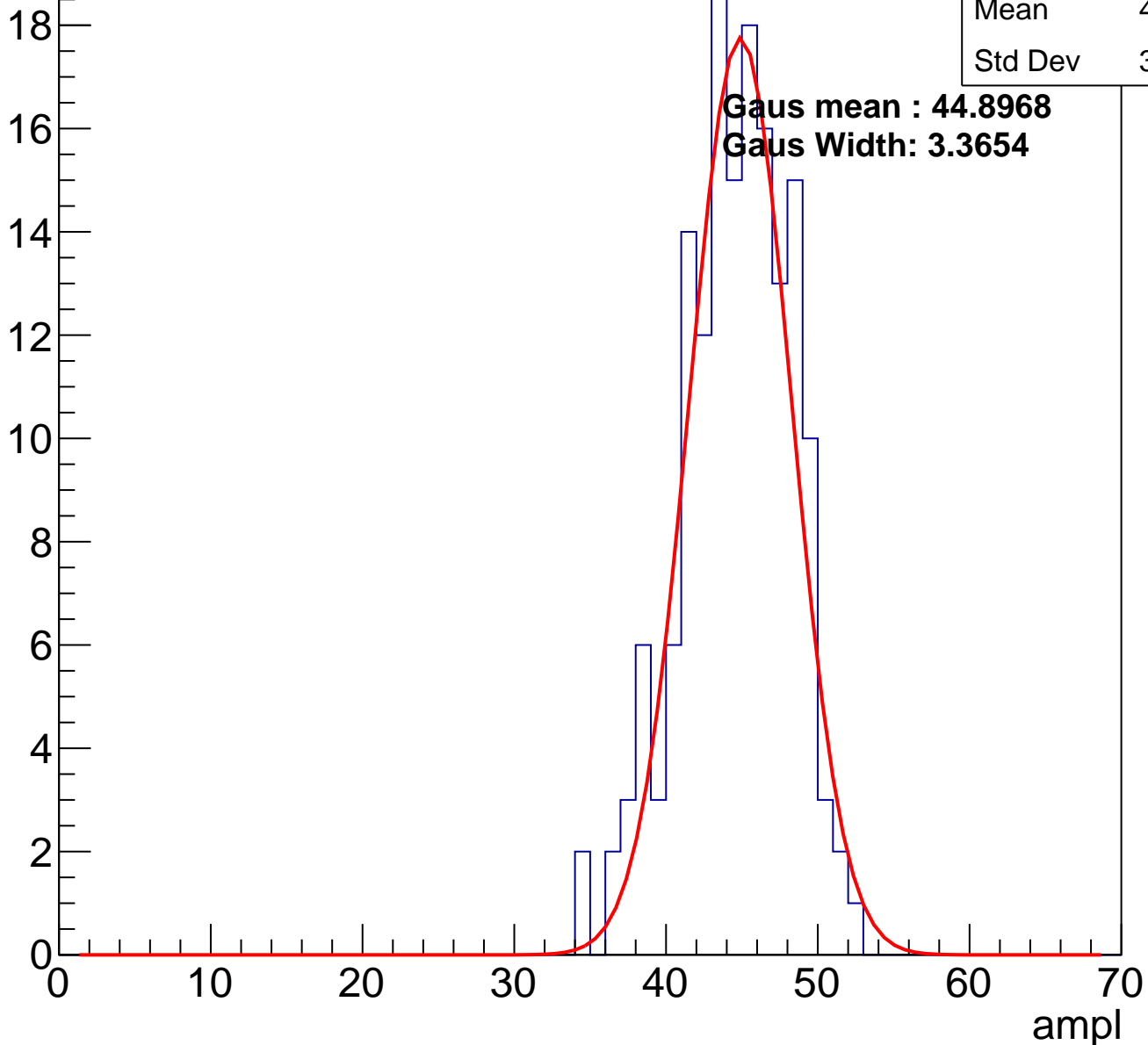
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	44.14
Std Dev	3.545

Gaus mean : 44.8968

Gaus Width: 3.3654

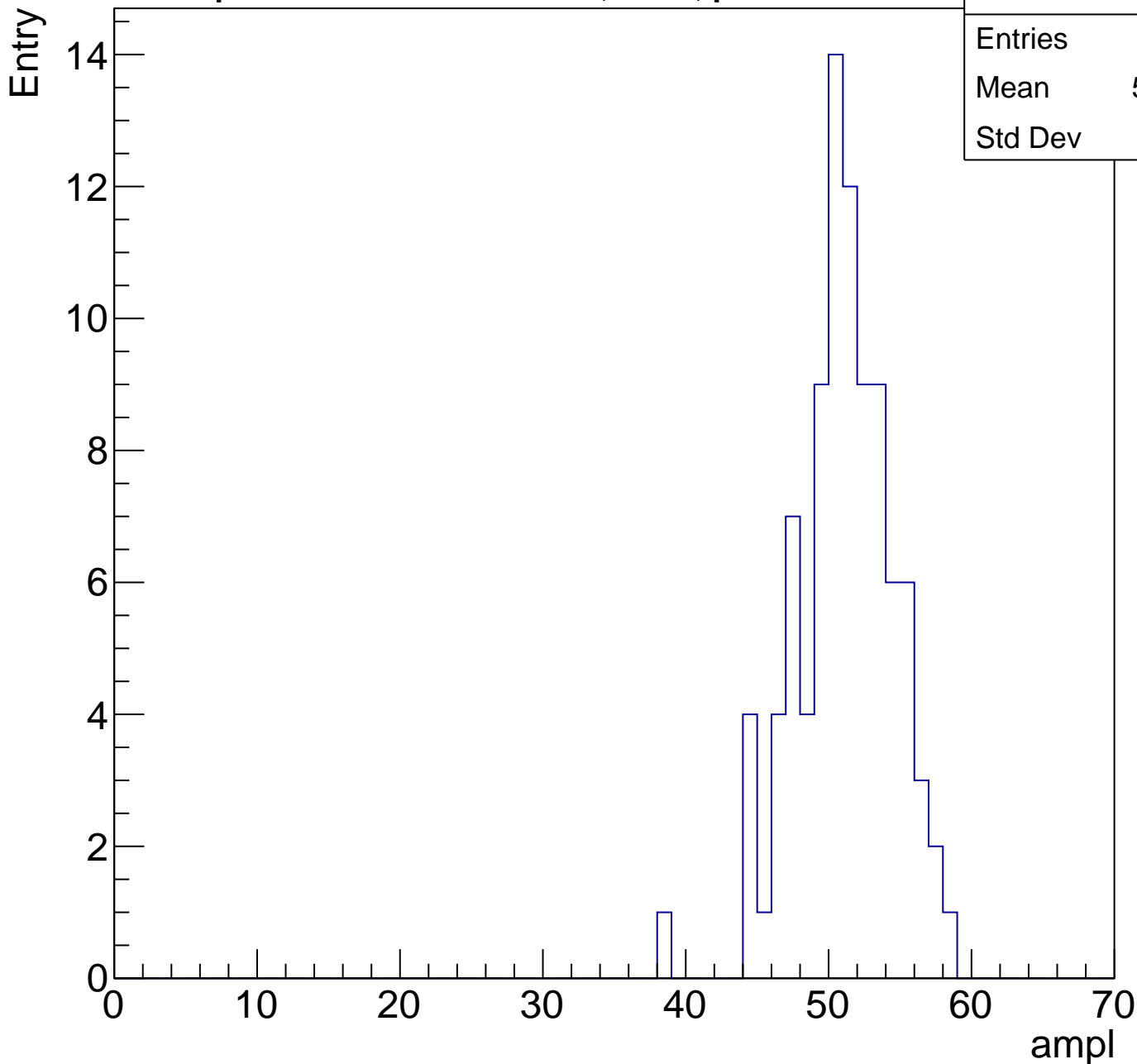
Entry



B1L001S, U25-ch9, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	92
Mean	50.61
Std Dev	3.42



B1L001S, U25-ch9, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	55.9
Std Dev	3.146

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

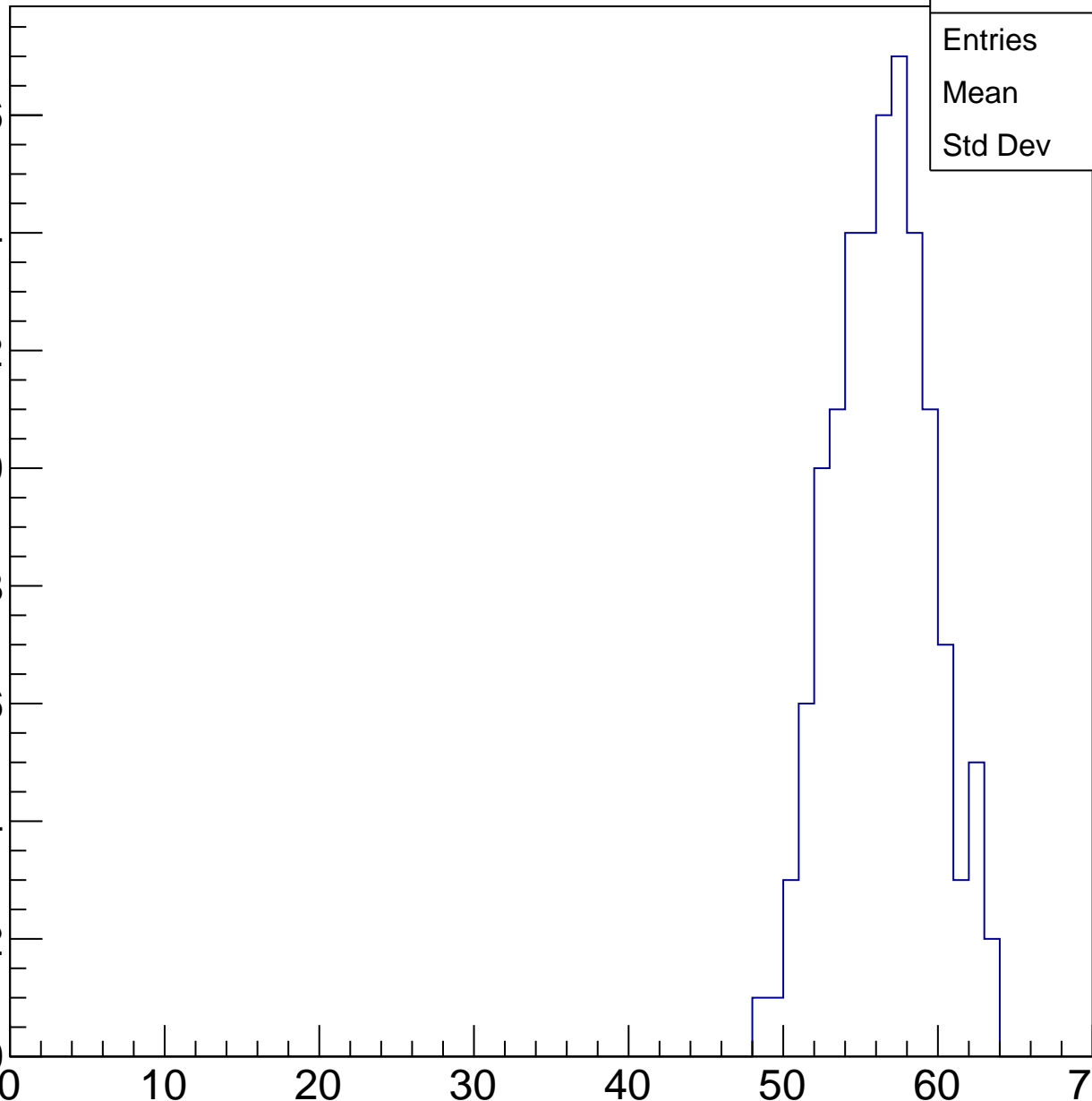
40

50

60

70

ampl



B1L001S, U25-ch9, adc5

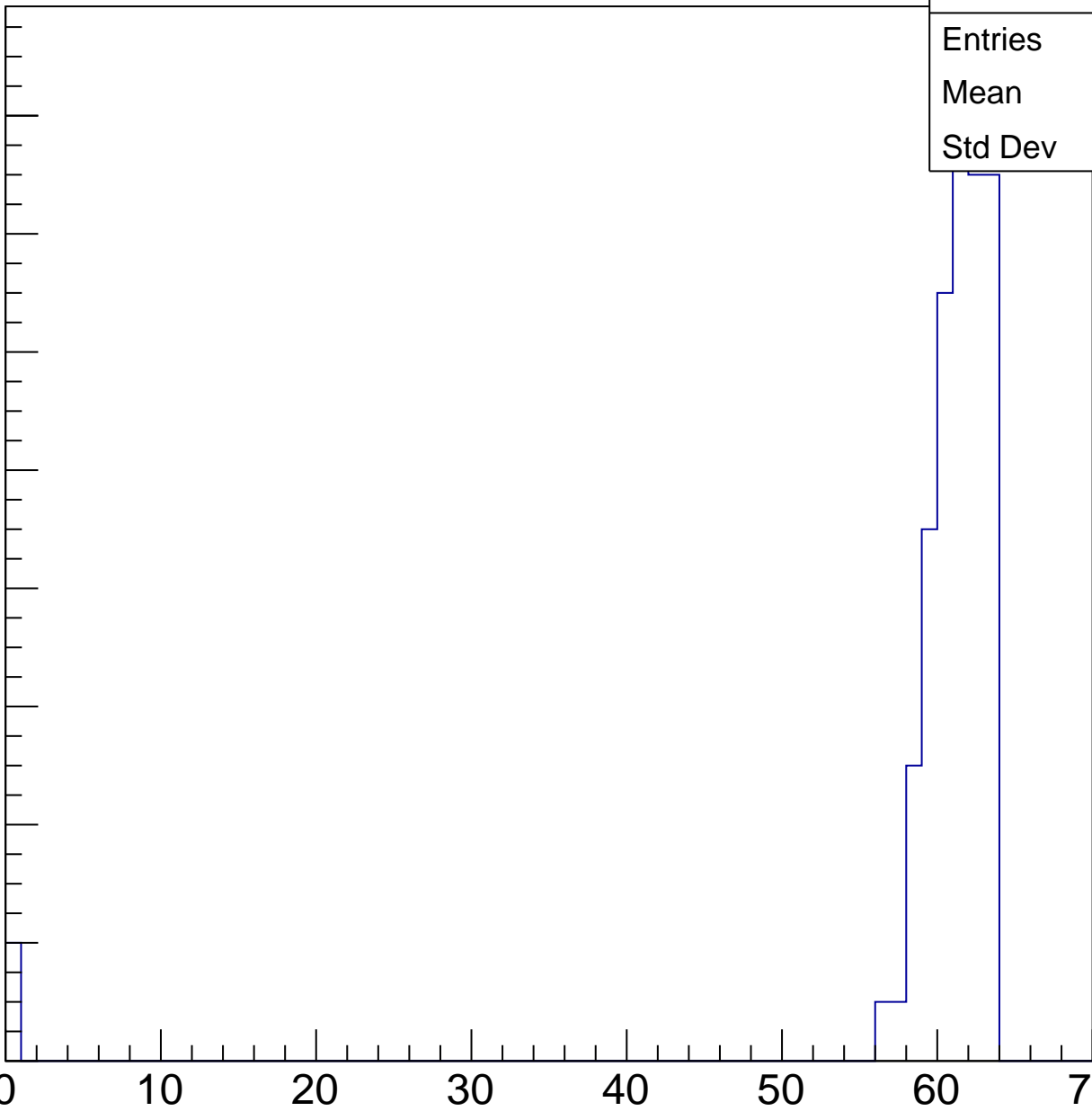
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.31
Std Dev	9.759

ampl



B1L001S, U25-ch9, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch9, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L001S, U25-ch10, adc0

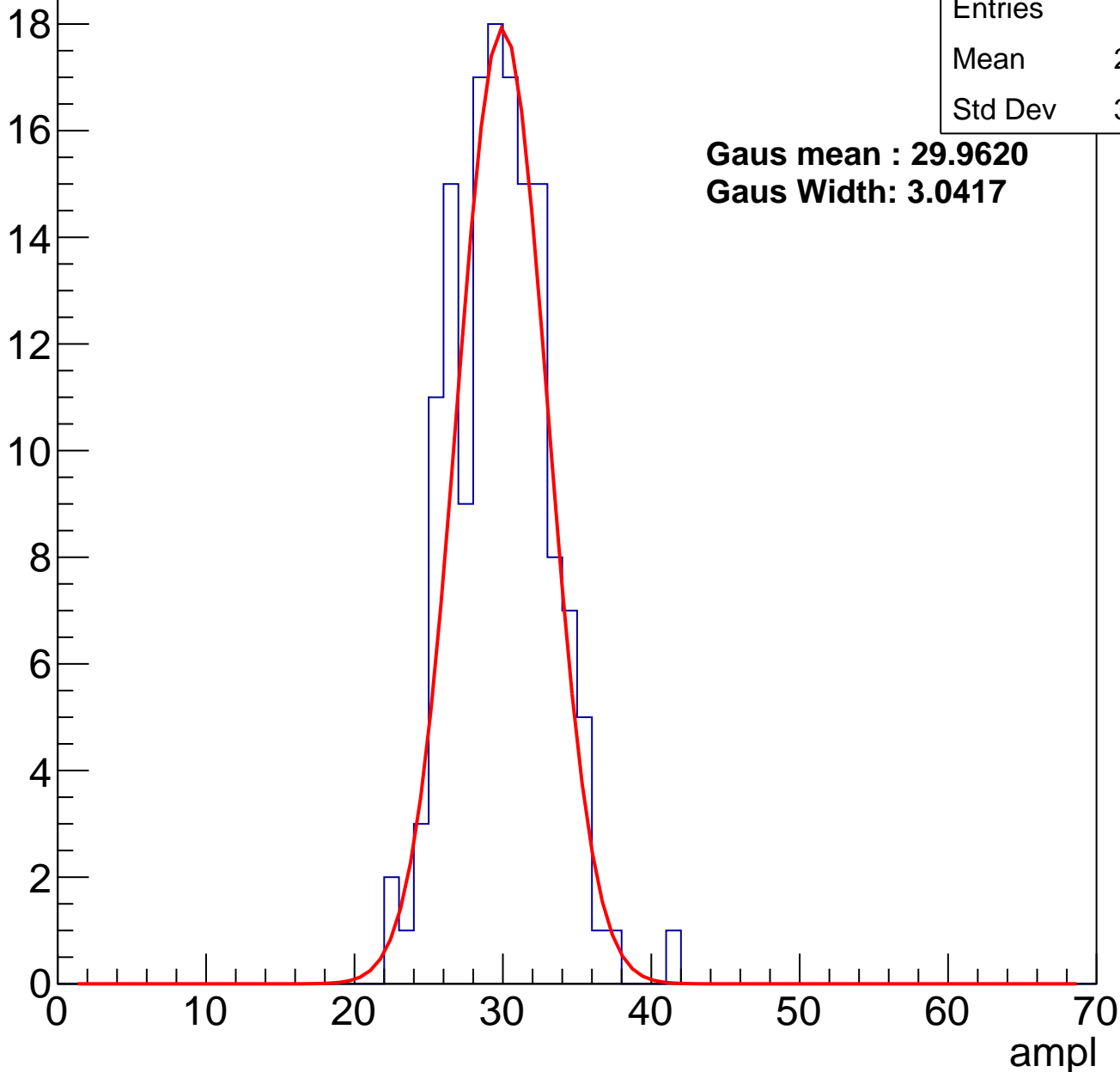
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.39
Std Dev	3.208

Gaus mean : 29.9620

Gaus Width: 3.0417

Entry



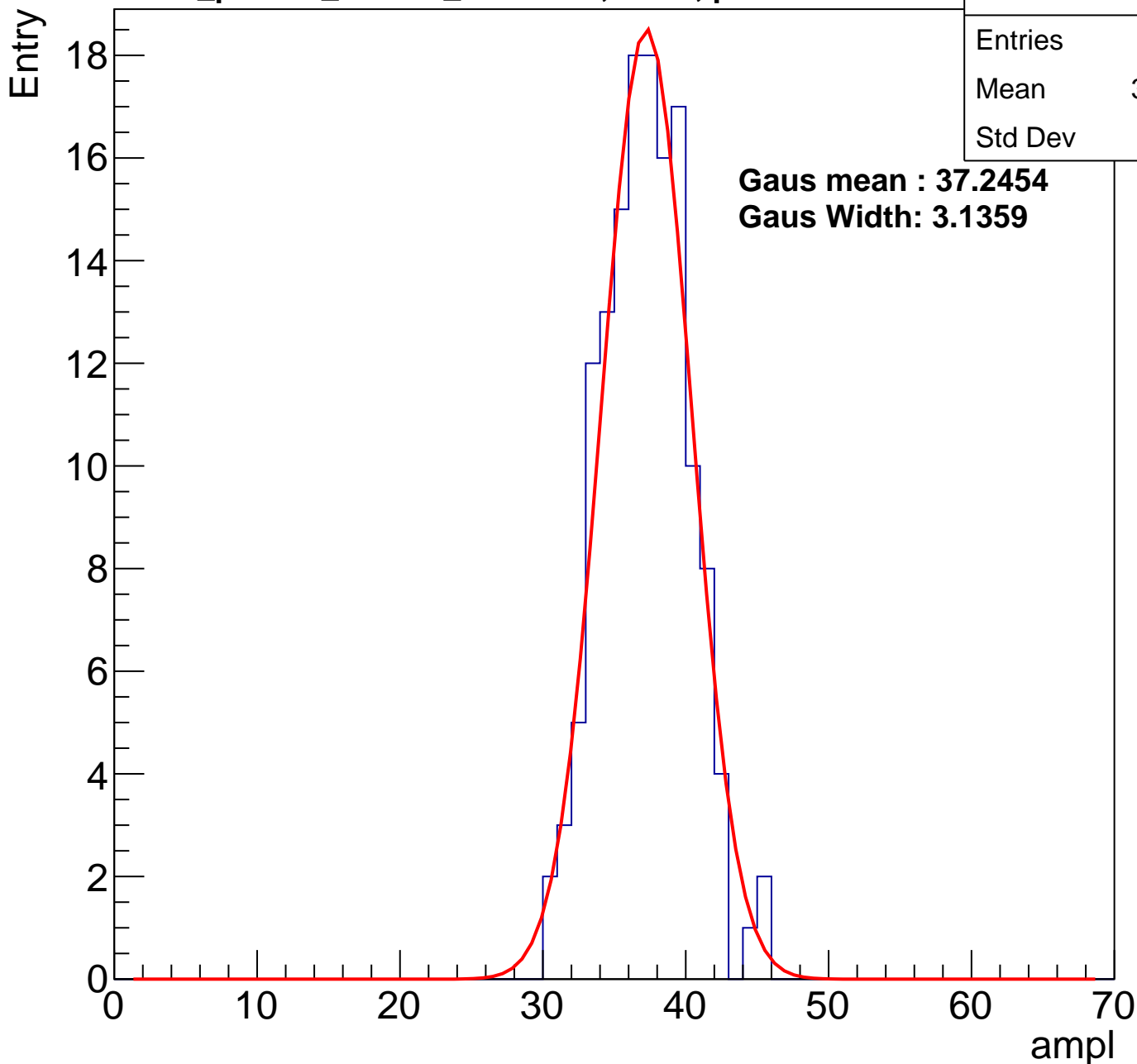
B1L001S, U25-ch10, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	36.74
Std Dev	2.99

Gaus mean : 37.2454

Gaus Width: 3.1359



B1L001S, U25-ch10, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	43.32
Std Dev	2.822

Gaus mean : 43.6146

Gaus Width: 2.6002

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

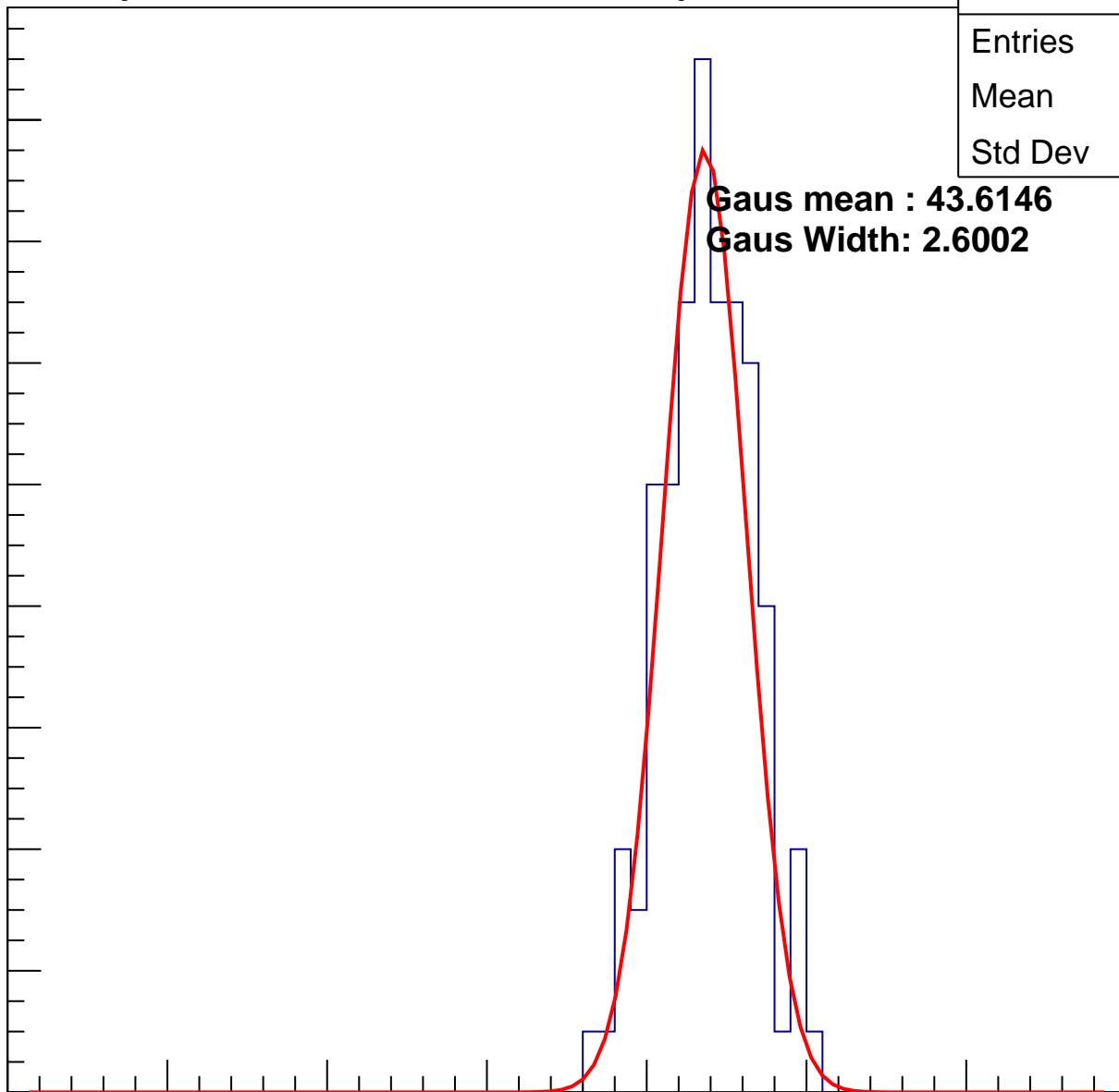
40

50

60

70

ampl

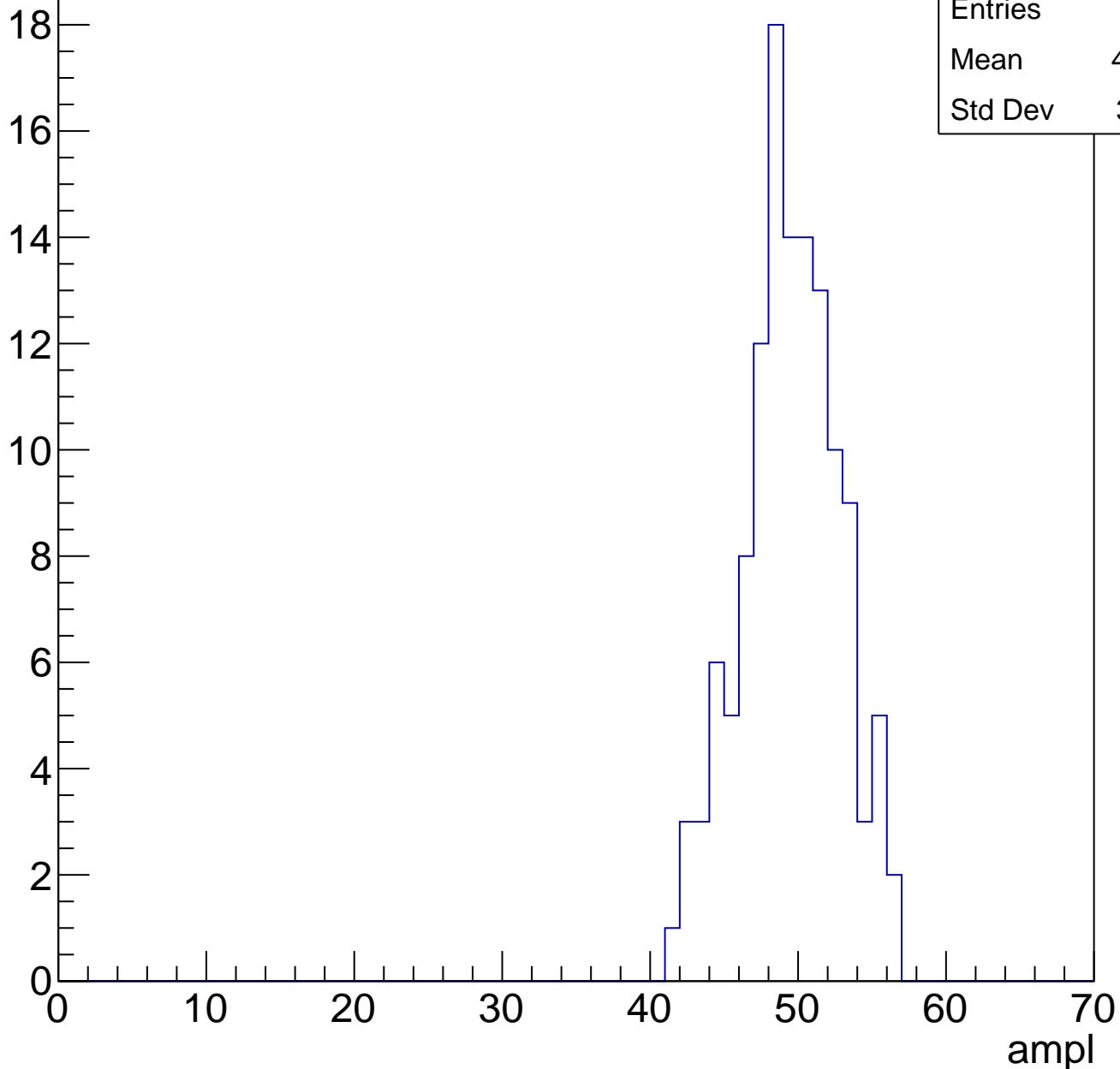


B1L001S, U25-ch10, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	49.02
Std Dev	3.261

Entry



B1L001S, U25-ch10, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	55.52
Std Dev	3.185

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

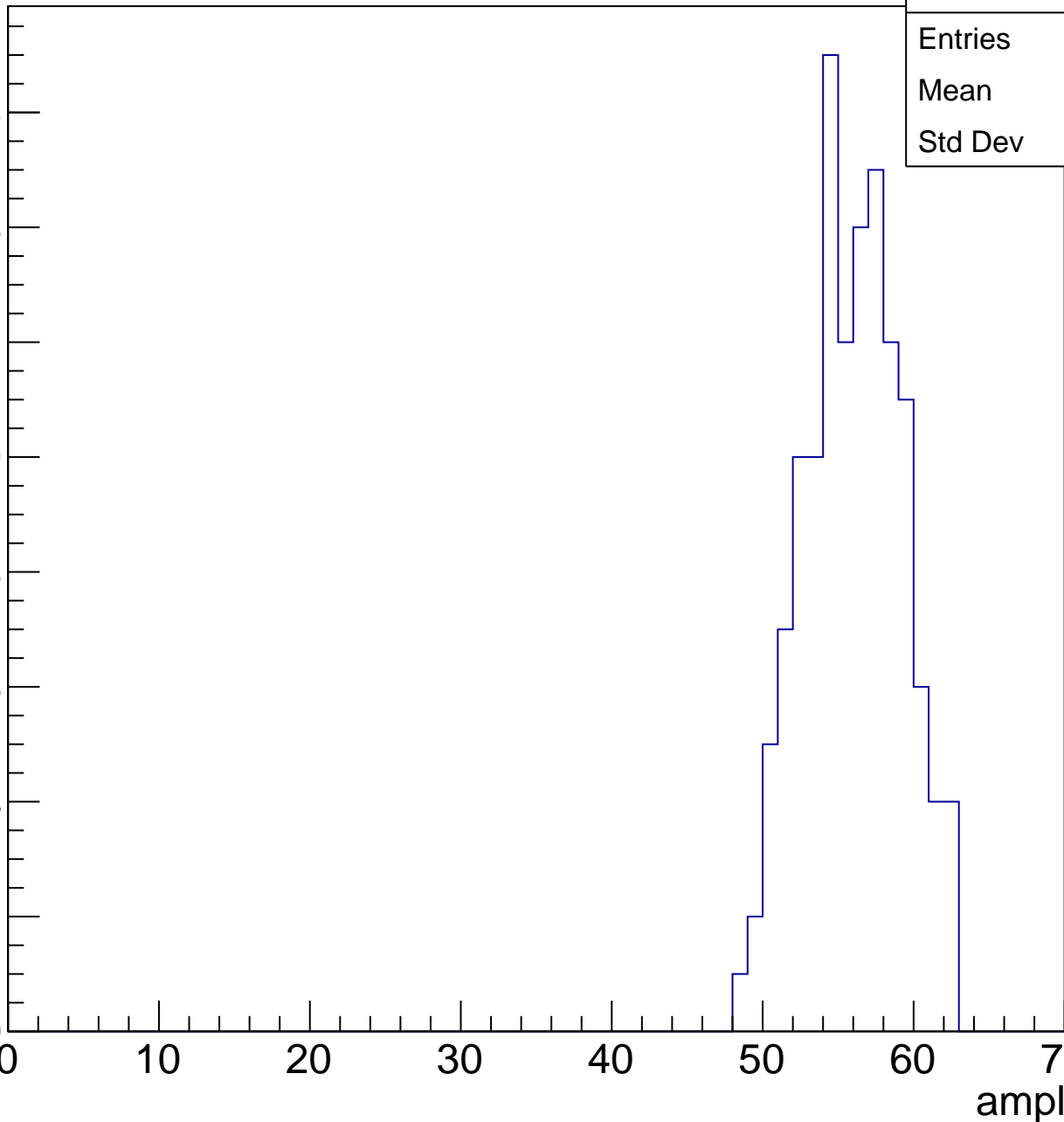
40

50

60

70

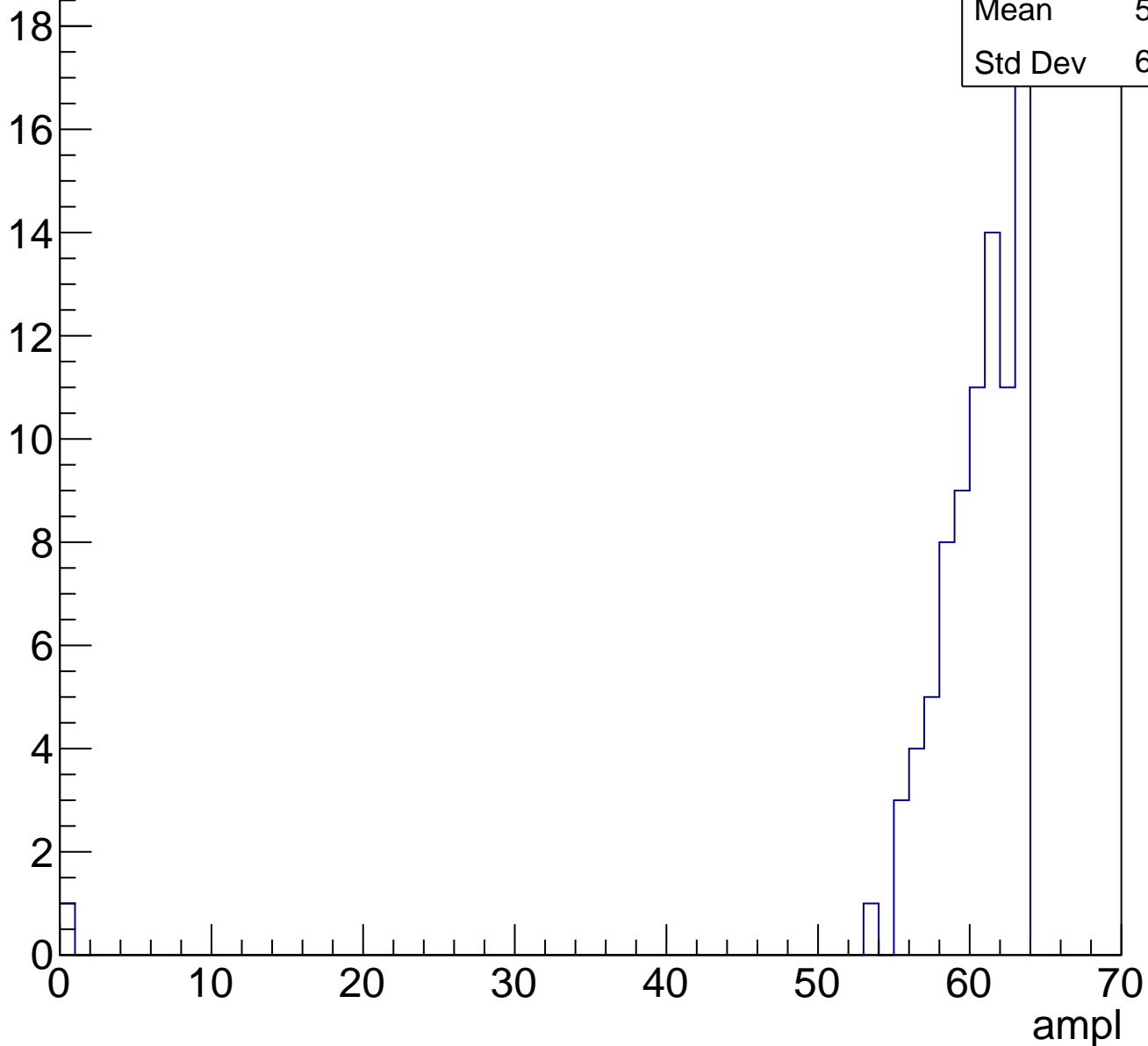
ampl



B1L001S, U25-ch10, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

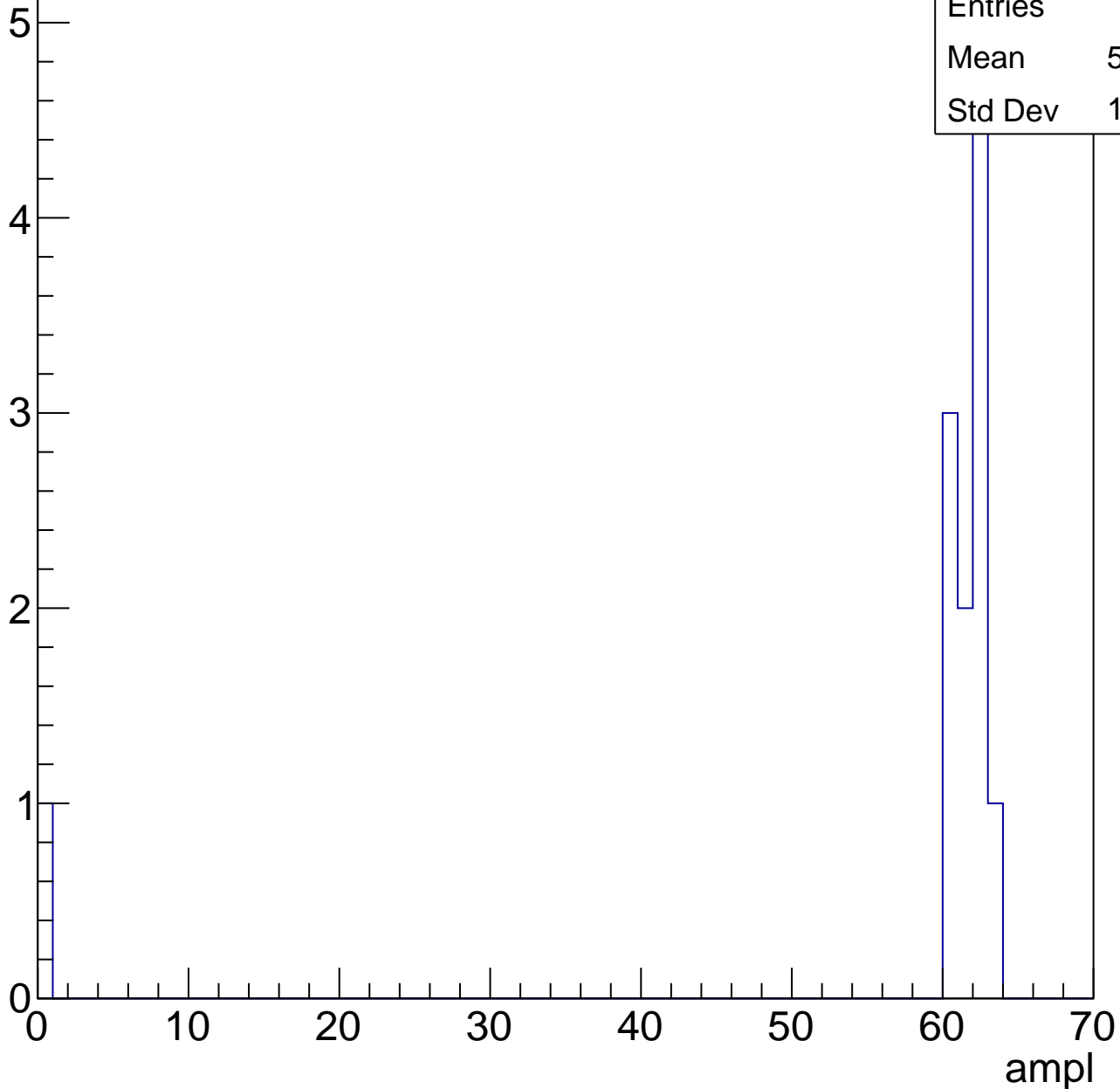


B1L001S, U25-ch10, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

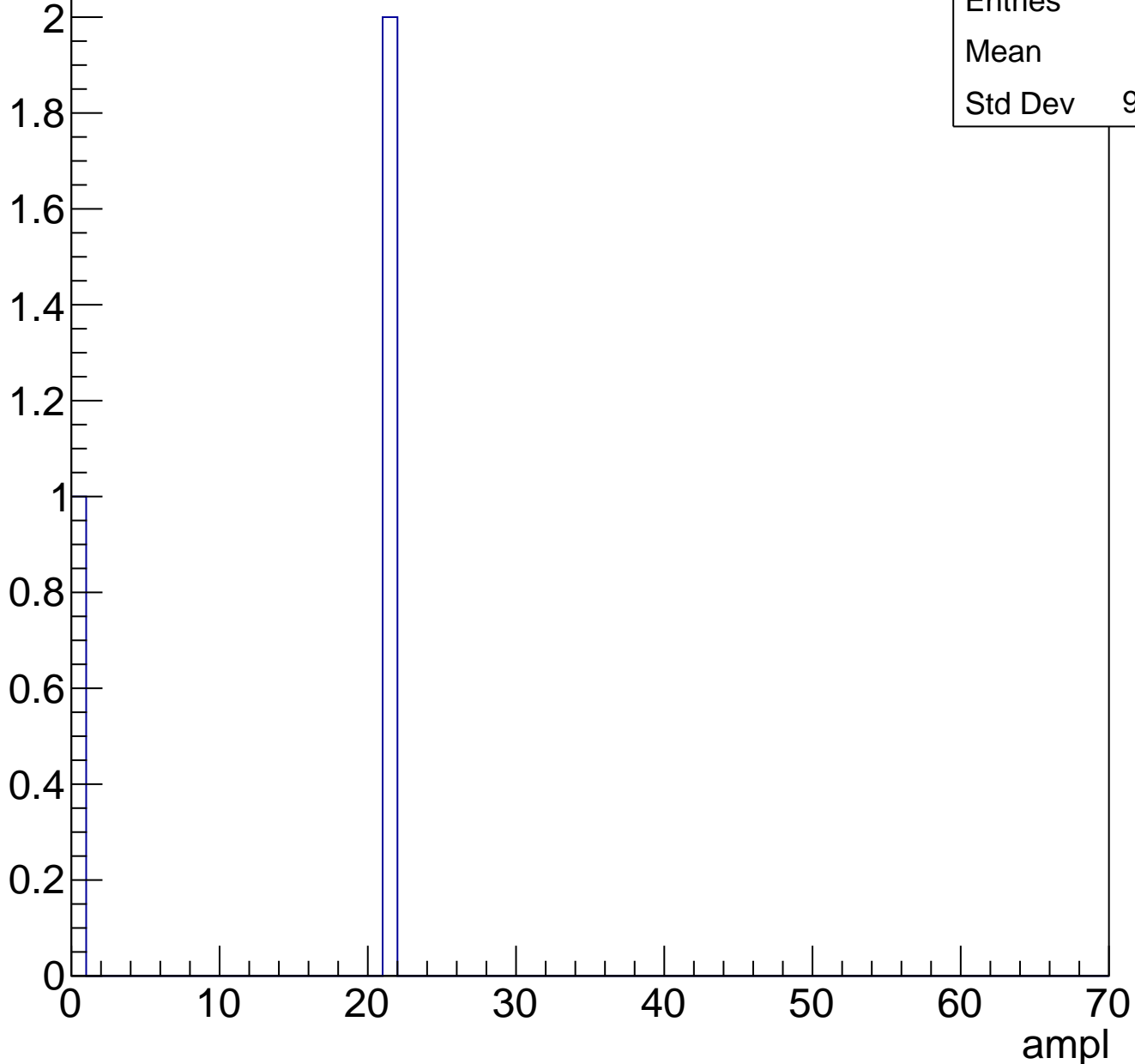
Entries	12
Mean	56.25
Std Dev	16.99



B1L001S, U25-ch10, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	14
Std Dev	9.899

B1L001S, U25-ch11, adc0

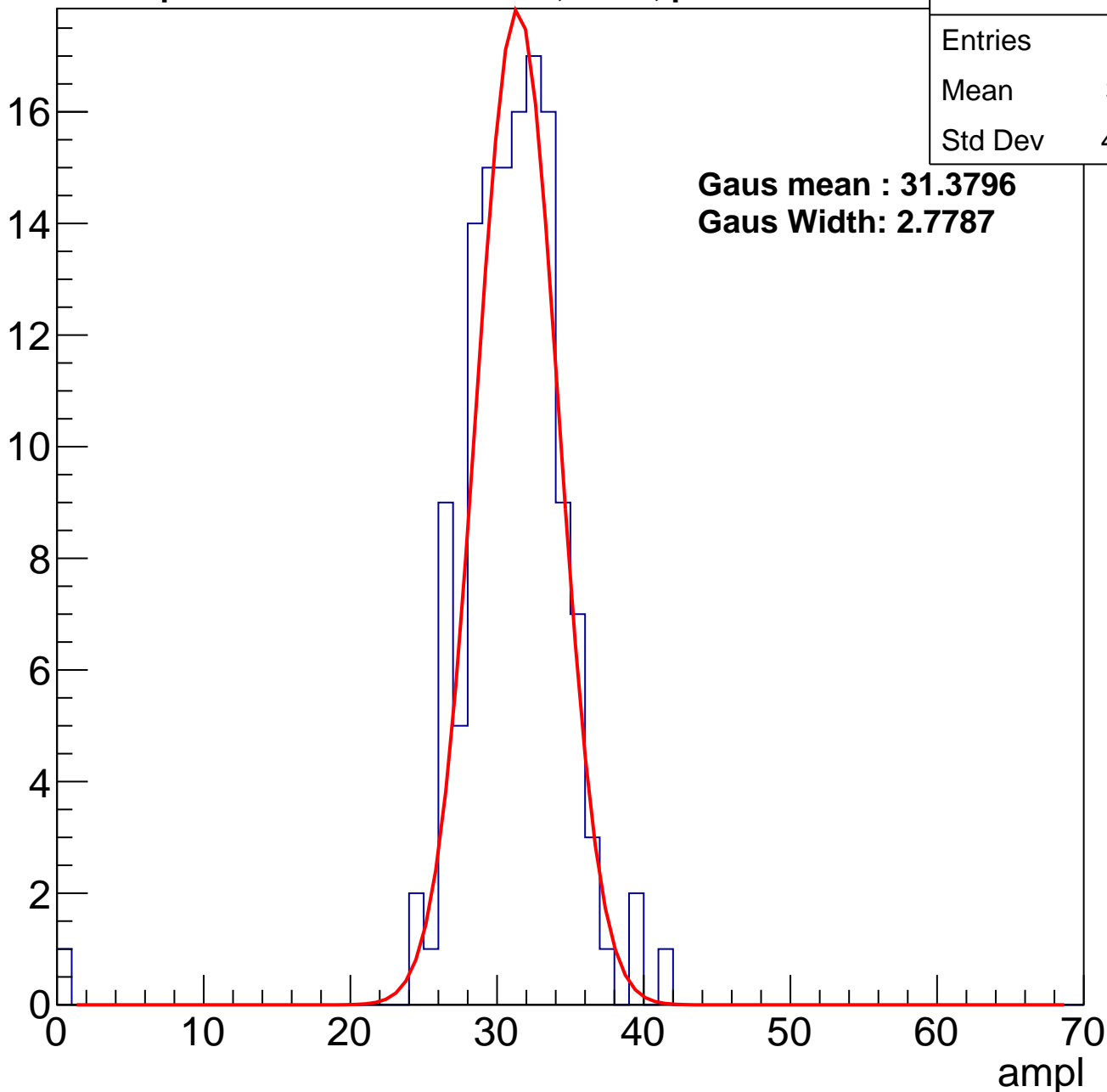
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	30.61
Std Dev	4.046

Gaus mean : 31.3796

Gaus Width: 2.7787

Entry



B1L001S, U25-ch11, adc1

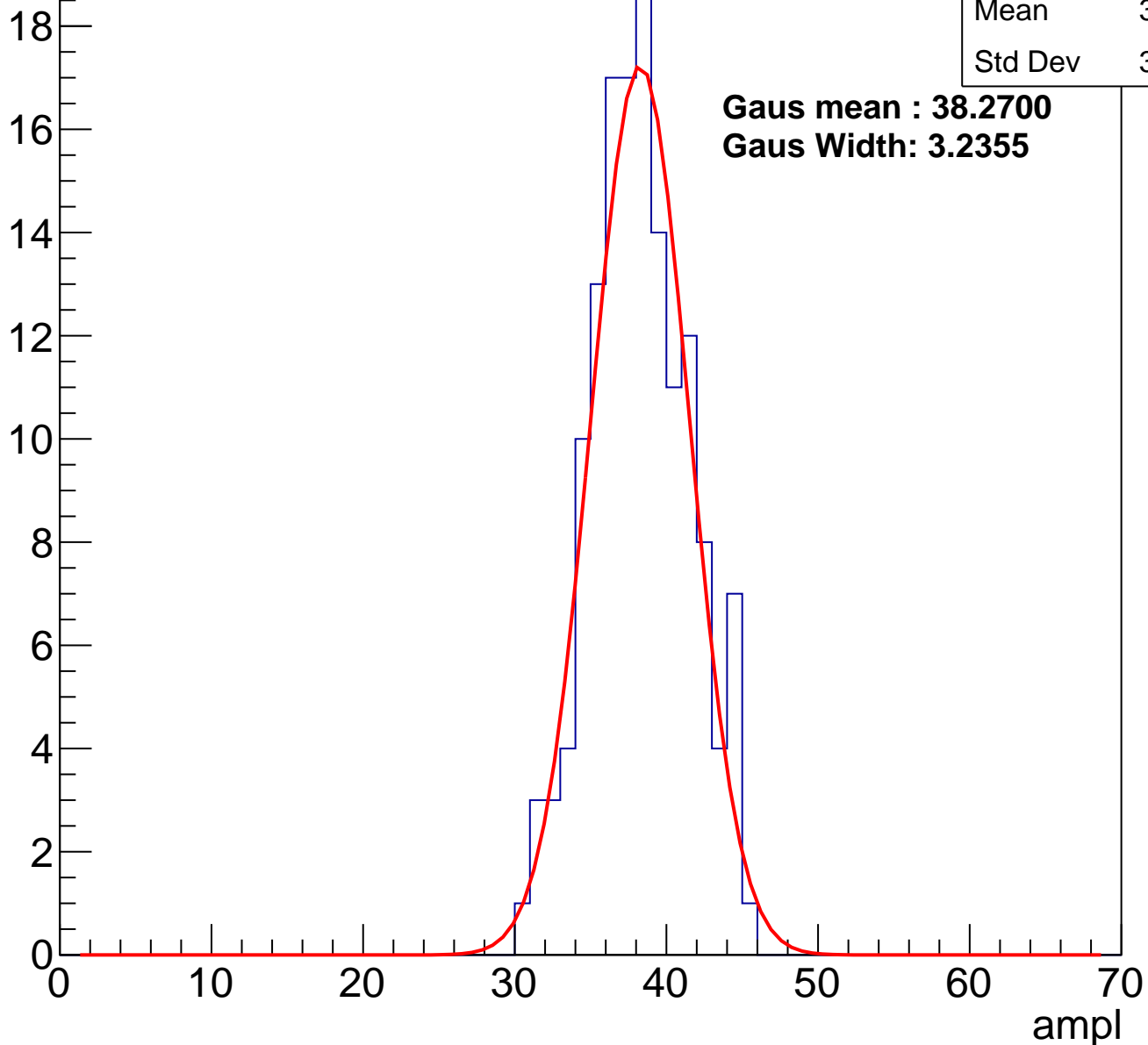
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	37.83
Std Dev	3.197

Gaus mean : 38.2700

Gaus Width: 3.2355

Entry



B1L001S, U25-ch11, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries 118

Mean 44.07

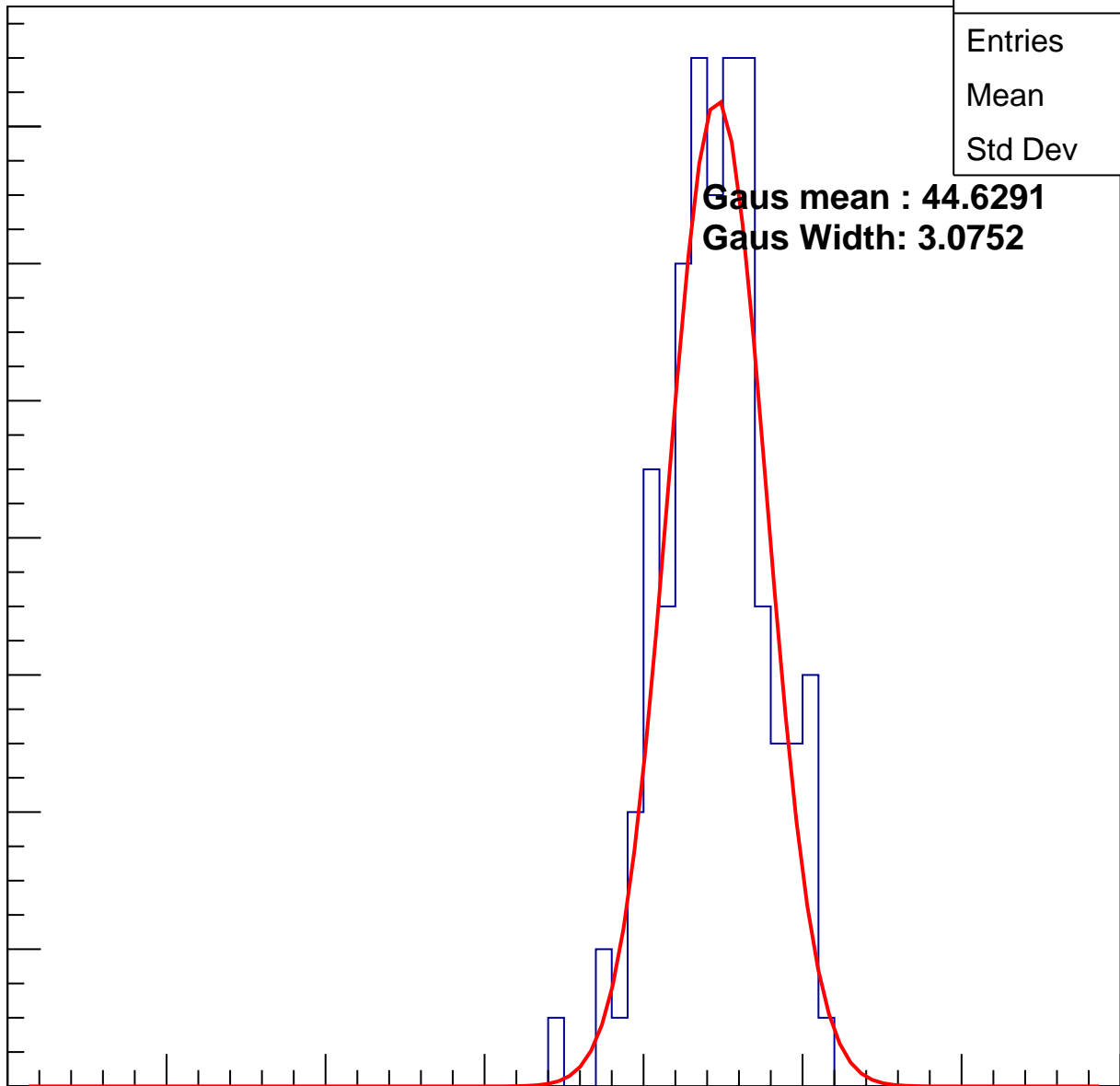
Std Dev 3.204

Gaus mean : 44.6291

Gaus Width: 3.0752

ampl

0 10 20 30 40 50 60 70

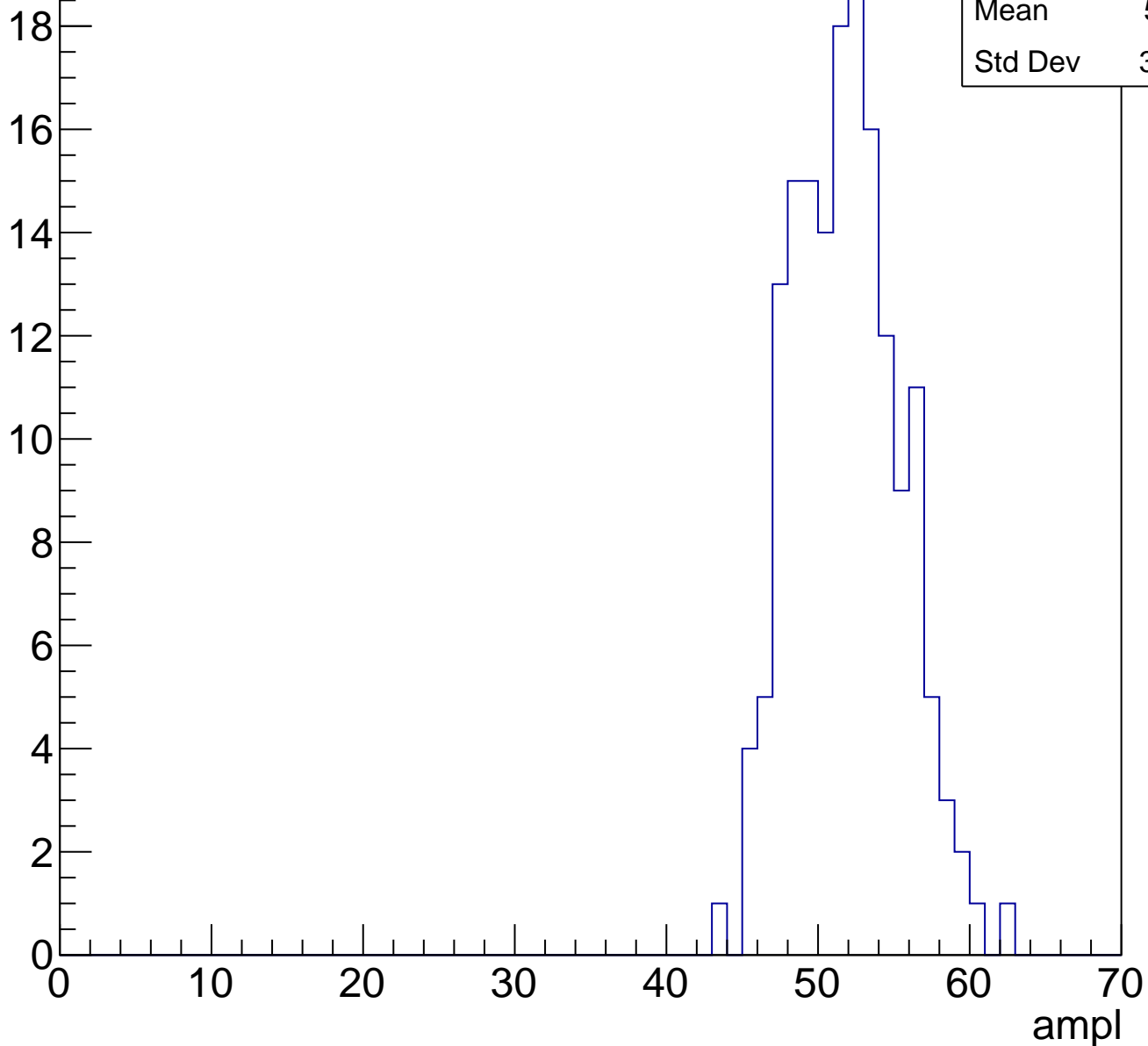


B1L001S, U25-ch11, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	51.41
Std Dev	3.483

Entry



B1L001S, U25-ch11, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

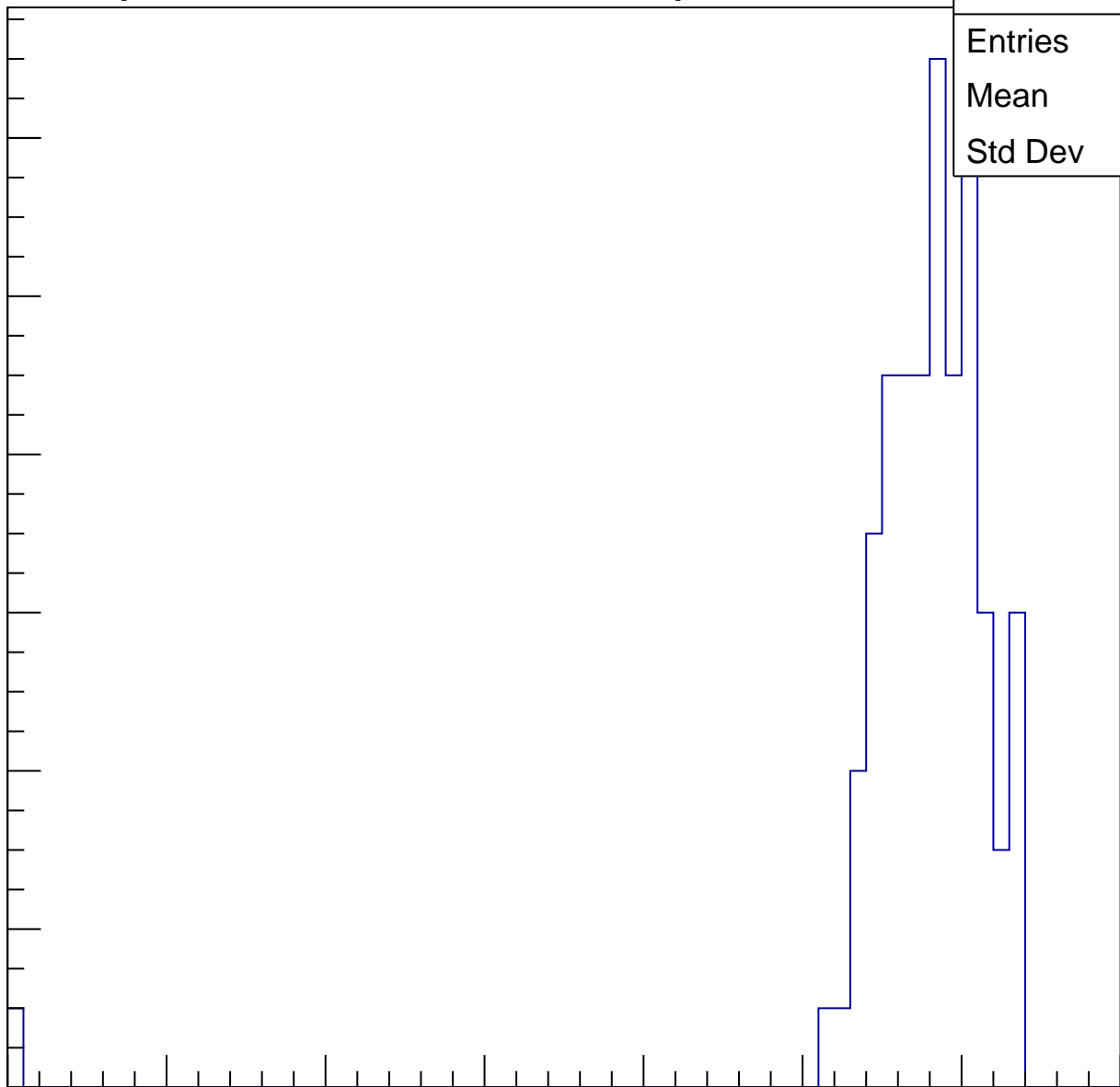
2

0

0 10 20 30 40 50 60 70

ampl

Entries	90
Mean	57.11
Std Dev	6.687



B1L001S, U25-ch11, adc5

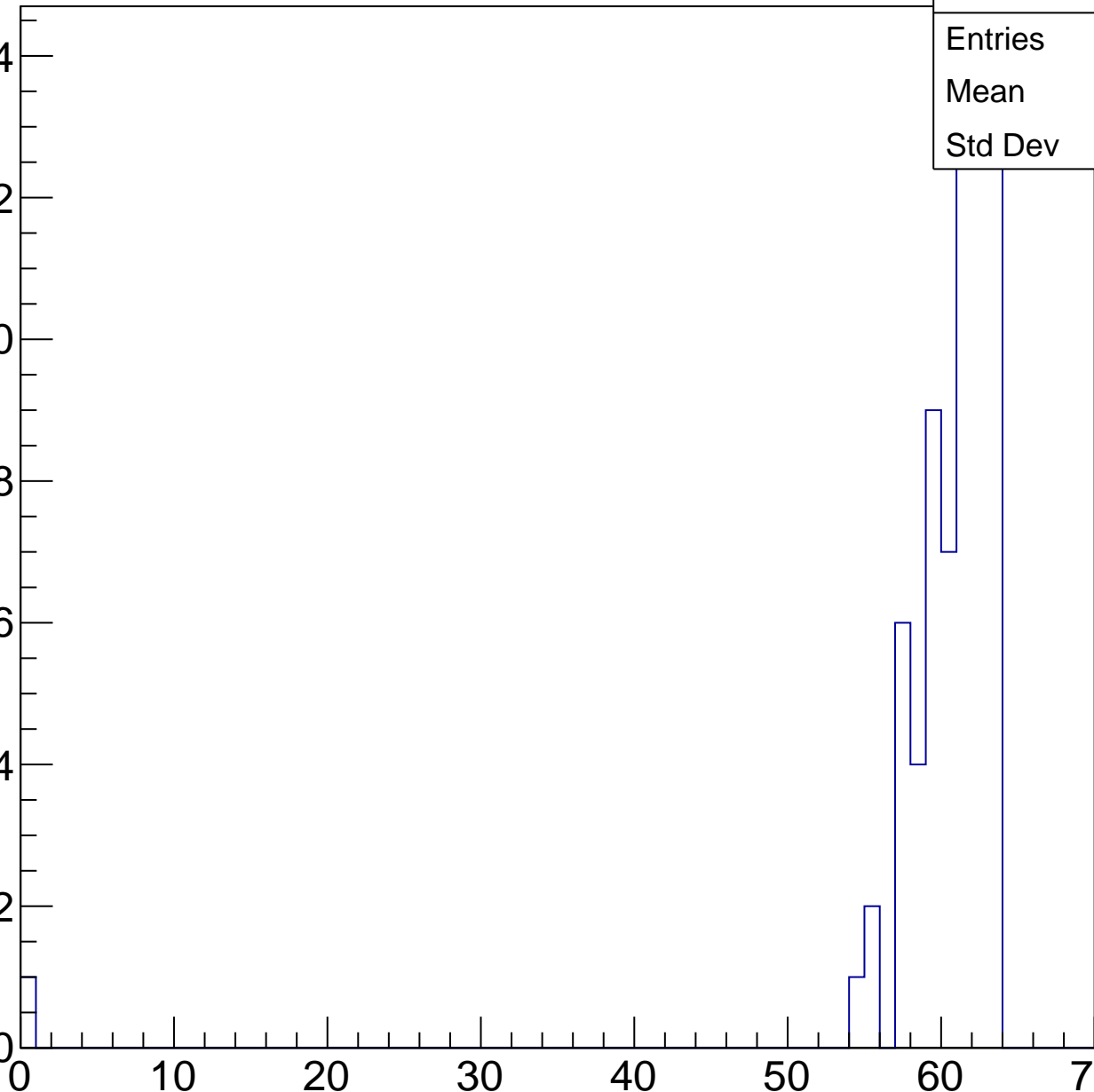
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	59.58
Std Dev	7.449

ampl



B1L001S, U25-ch11, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch11, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L001S, U25-ch12, adc0

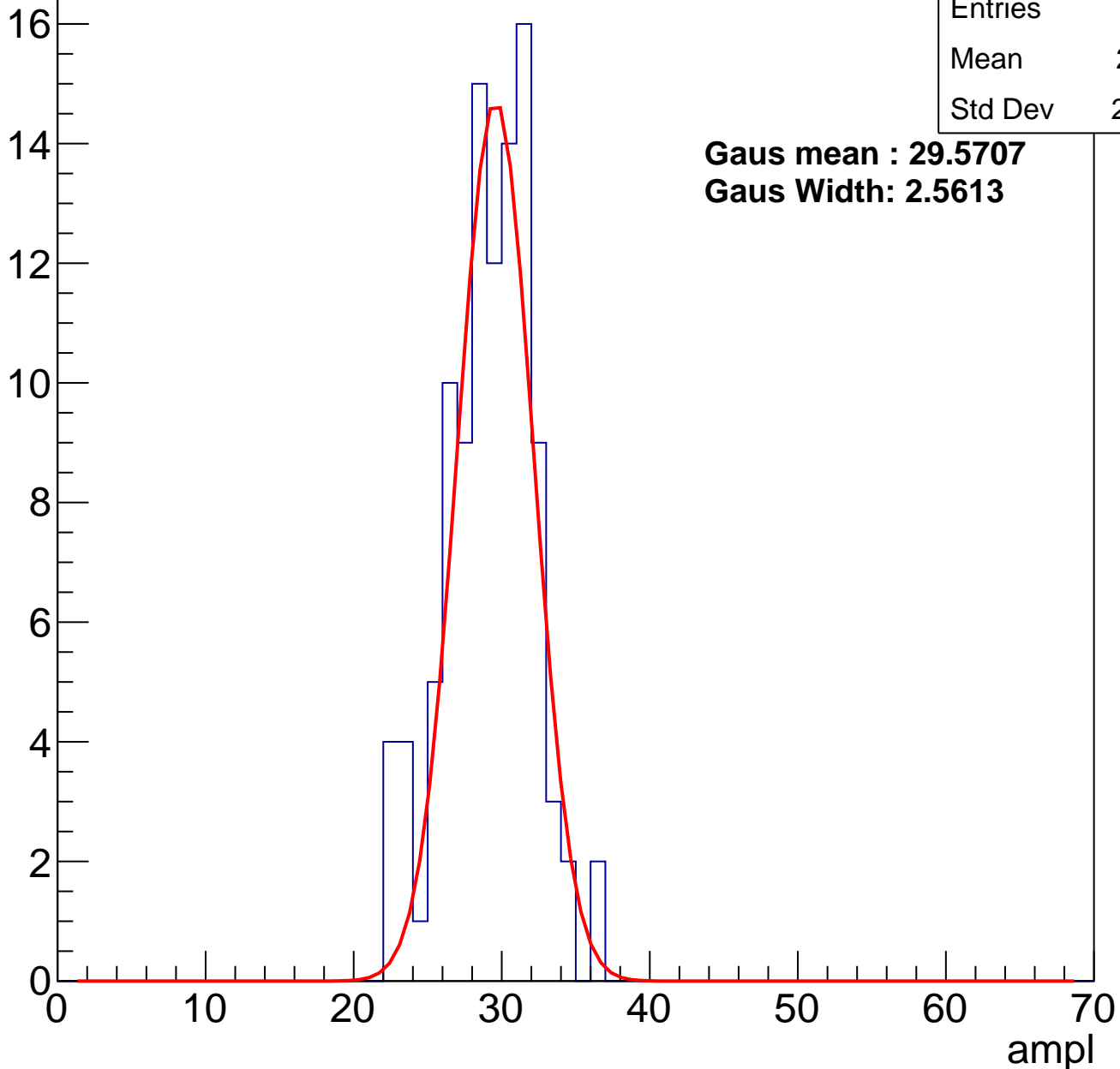
calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	28.71
Std Dev	2.965

Gaus mean : 29.5707

Gaus Width: 2.5613

Entry



B1L001S, U25-ch12, adc1

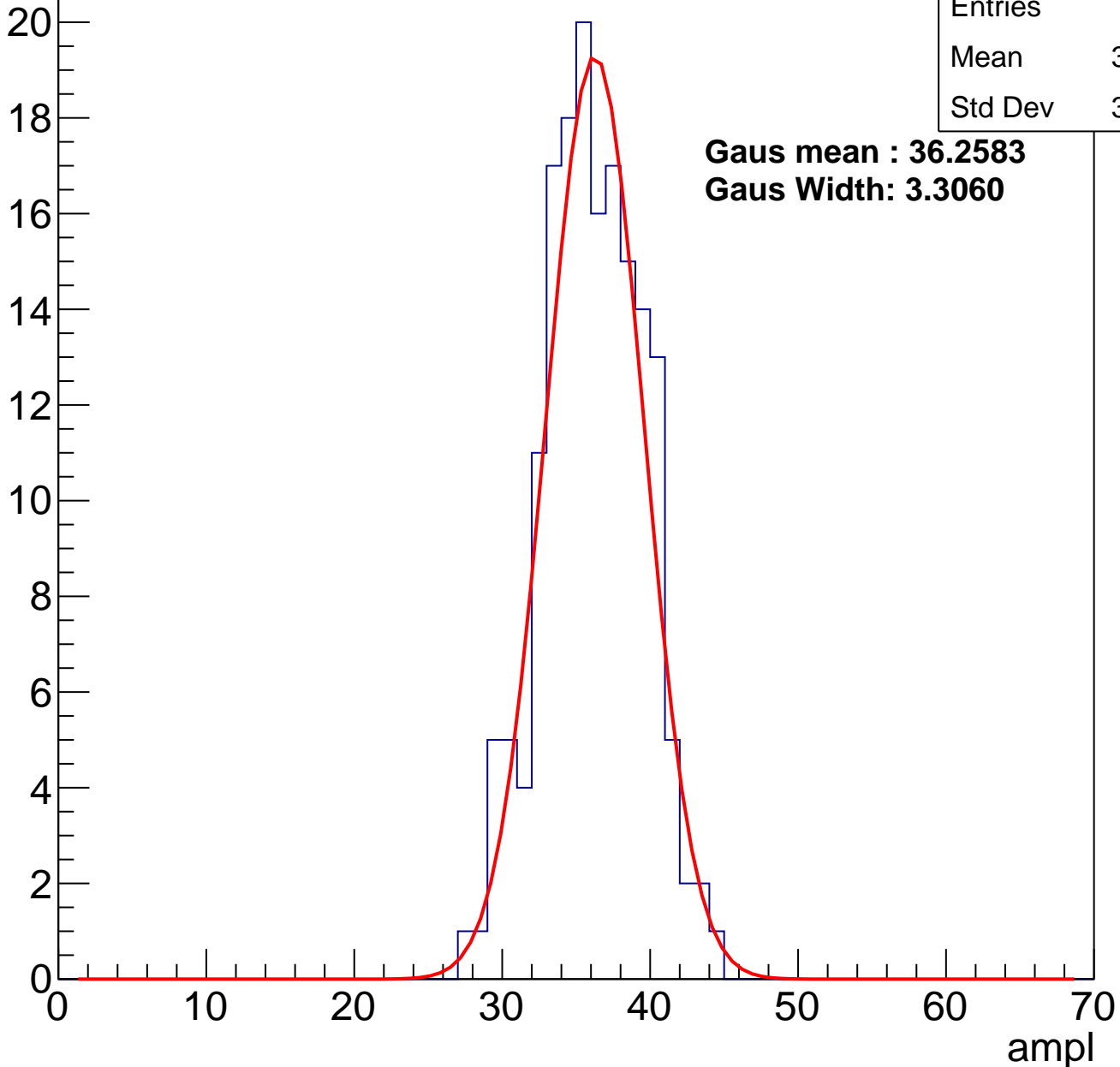
calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	35.68
Std Dev	3.323

Gaus mean : 36.2583

Gaus Width: 3.3060

Entry



B1L001S, U25-ch12, adc2

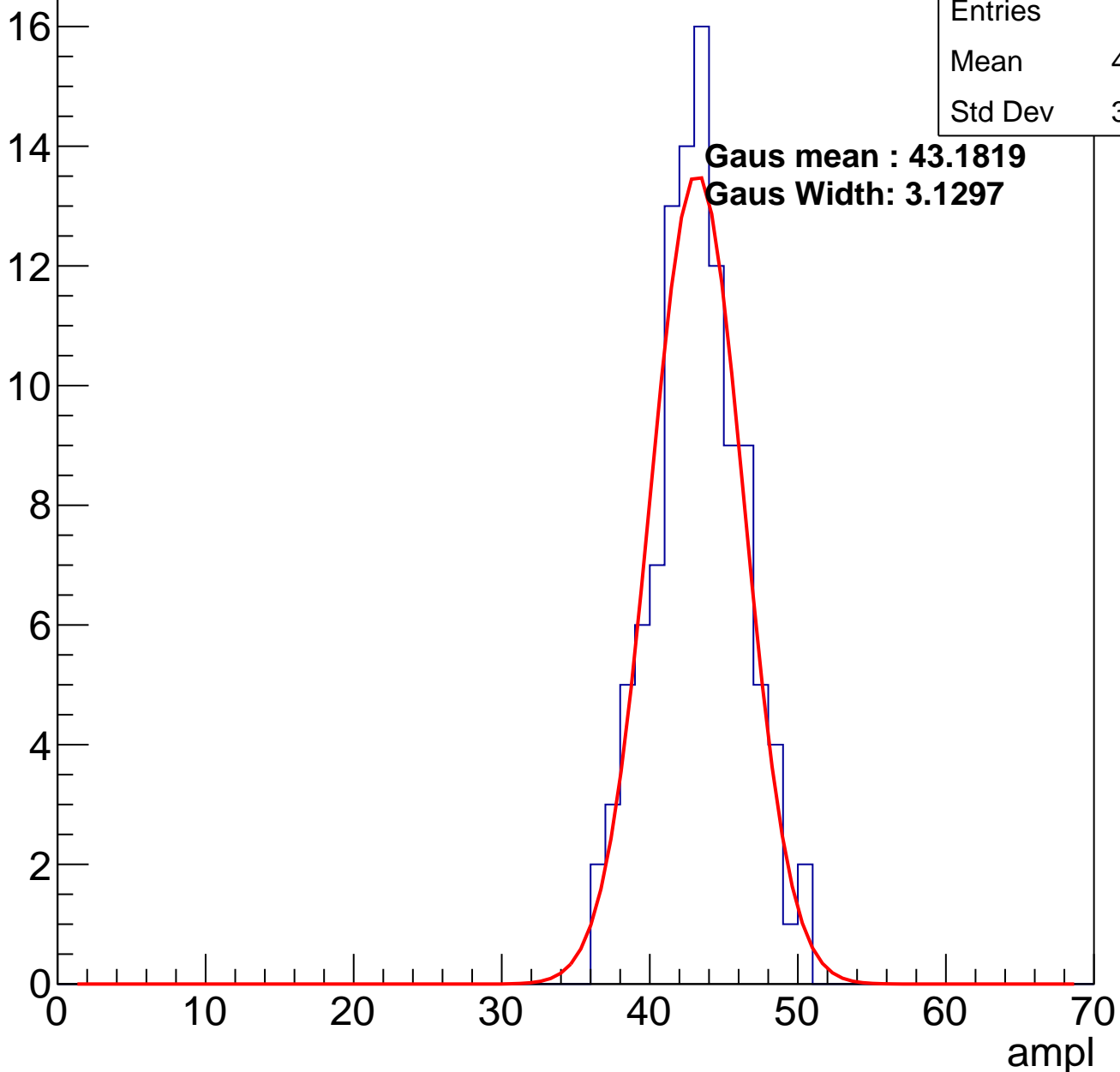
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	42.77
Std Dev	3.033

Gaus mean : 43.1819

Gaus Width: 3.1297

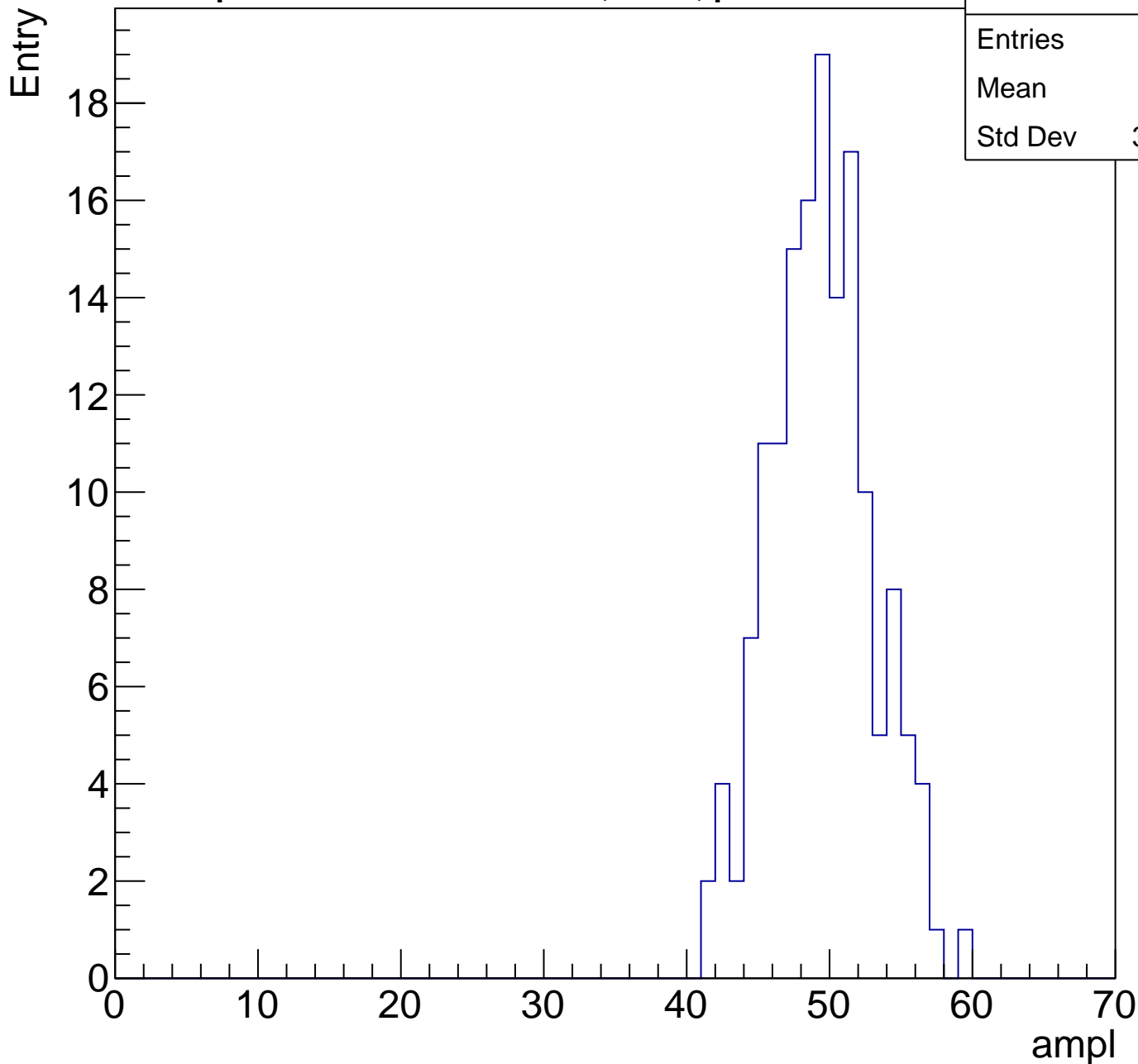
Entry



B1L001S, U25-ch12, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	49
Std Dev	3.549



B1L001S, U25-ch12, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	56.02
Std Dev	2.987

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

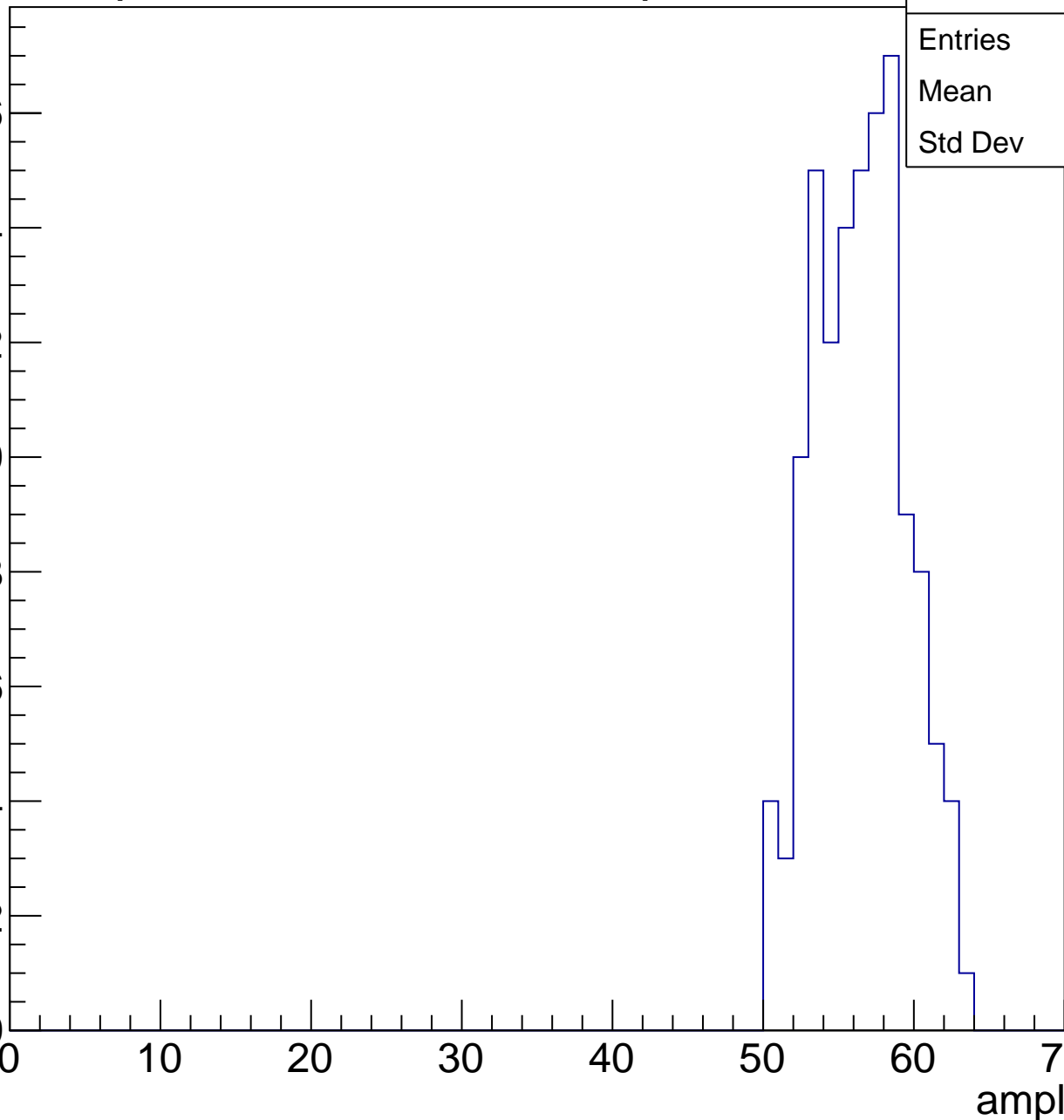
40

50

60

70

ampl



B1L001S, U25-ch12, adc5

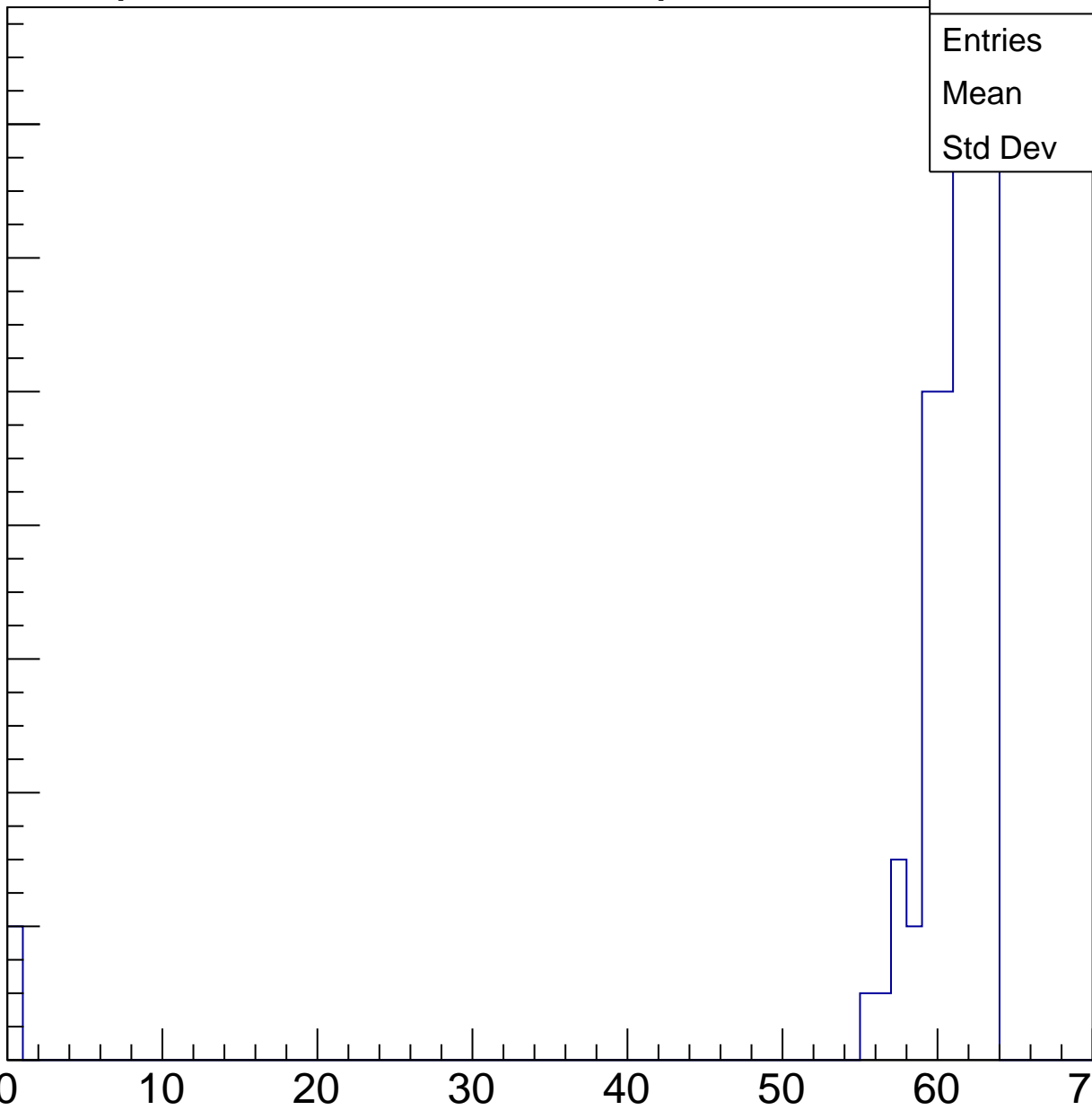
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	59.12
Std Dev	10.09

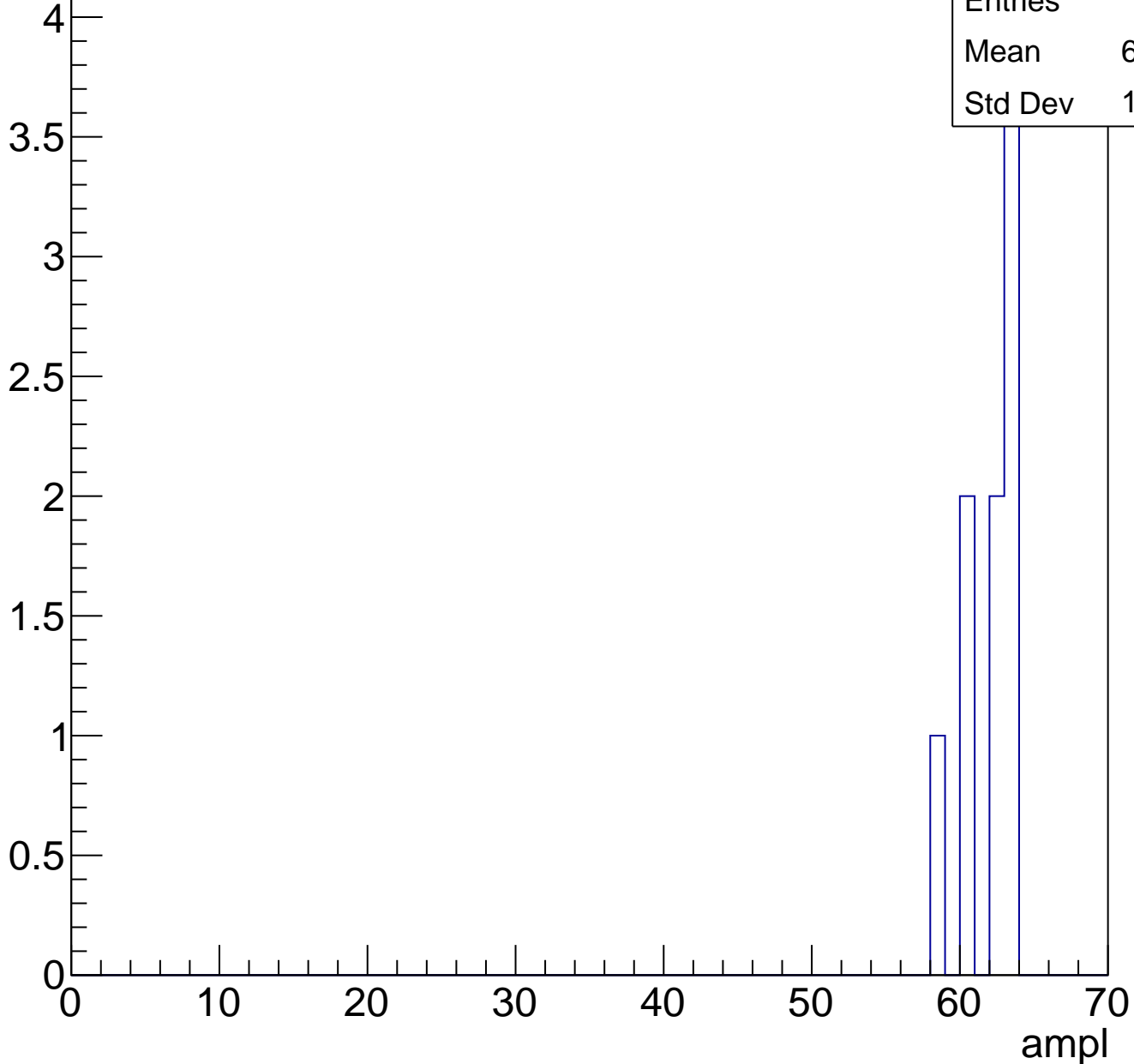
ampl



B1L001S, U25-ch12, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch12, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U25-ch13, adc0

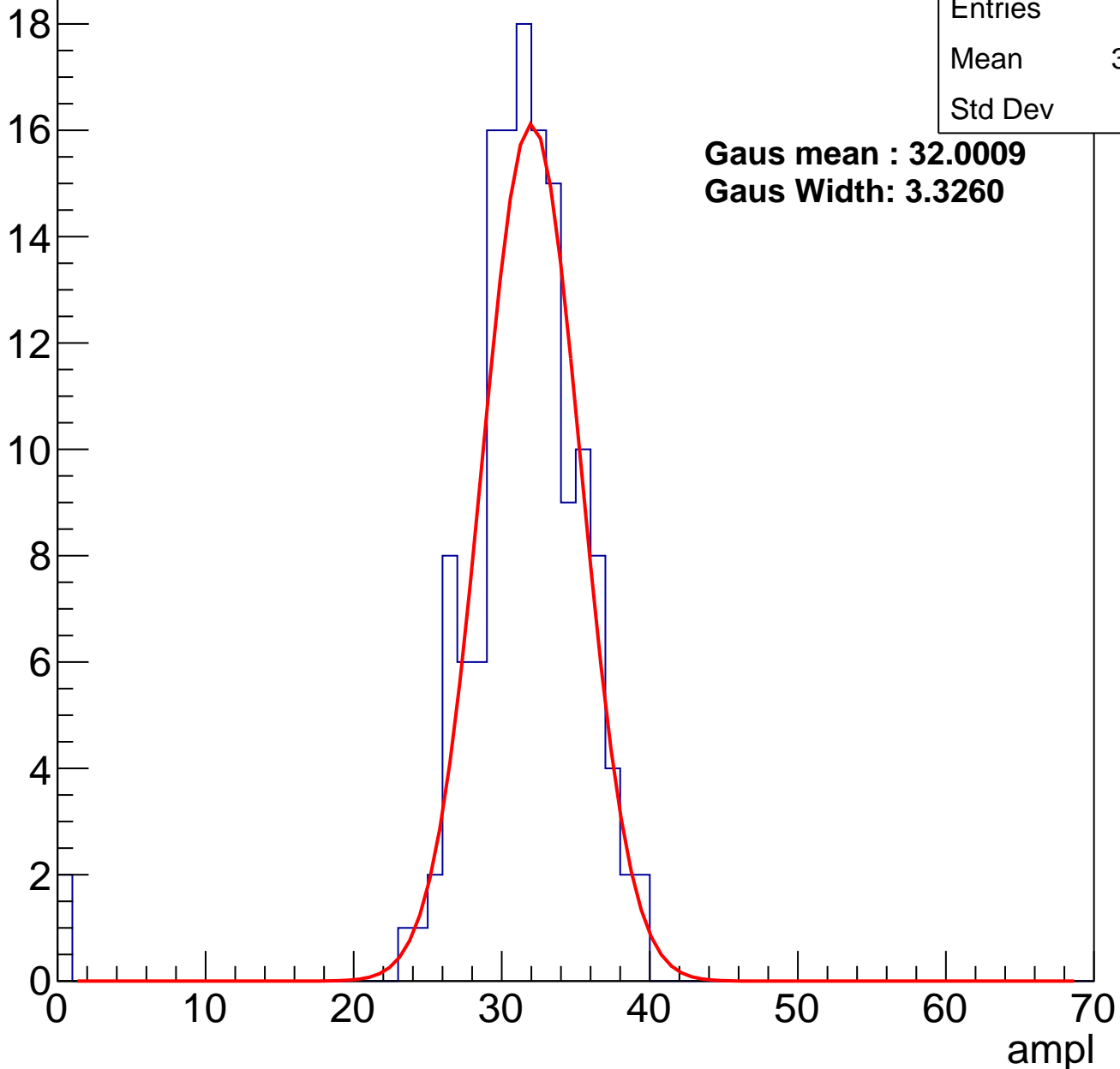
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	30.92
Std Dev	4.91

Gaus mean : 32.0009

Gaus Width: 3.3260

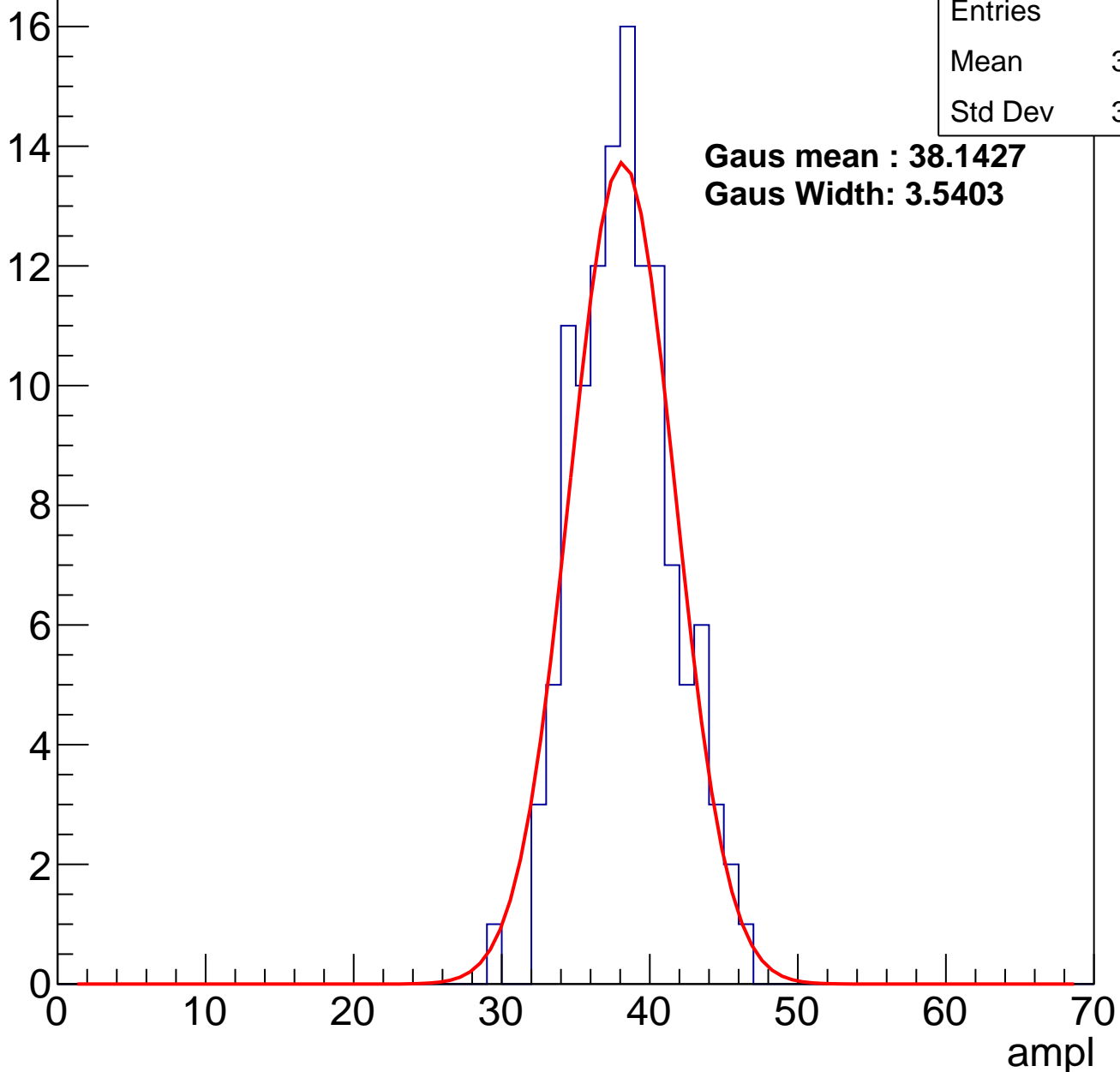
Entry



B1L001S, U25-ch13, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch13, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

142

Mean

44.32

Std Dev

3.364

Gaus mean : 44.6693

Gaus Width: 3.4416

0

10

20

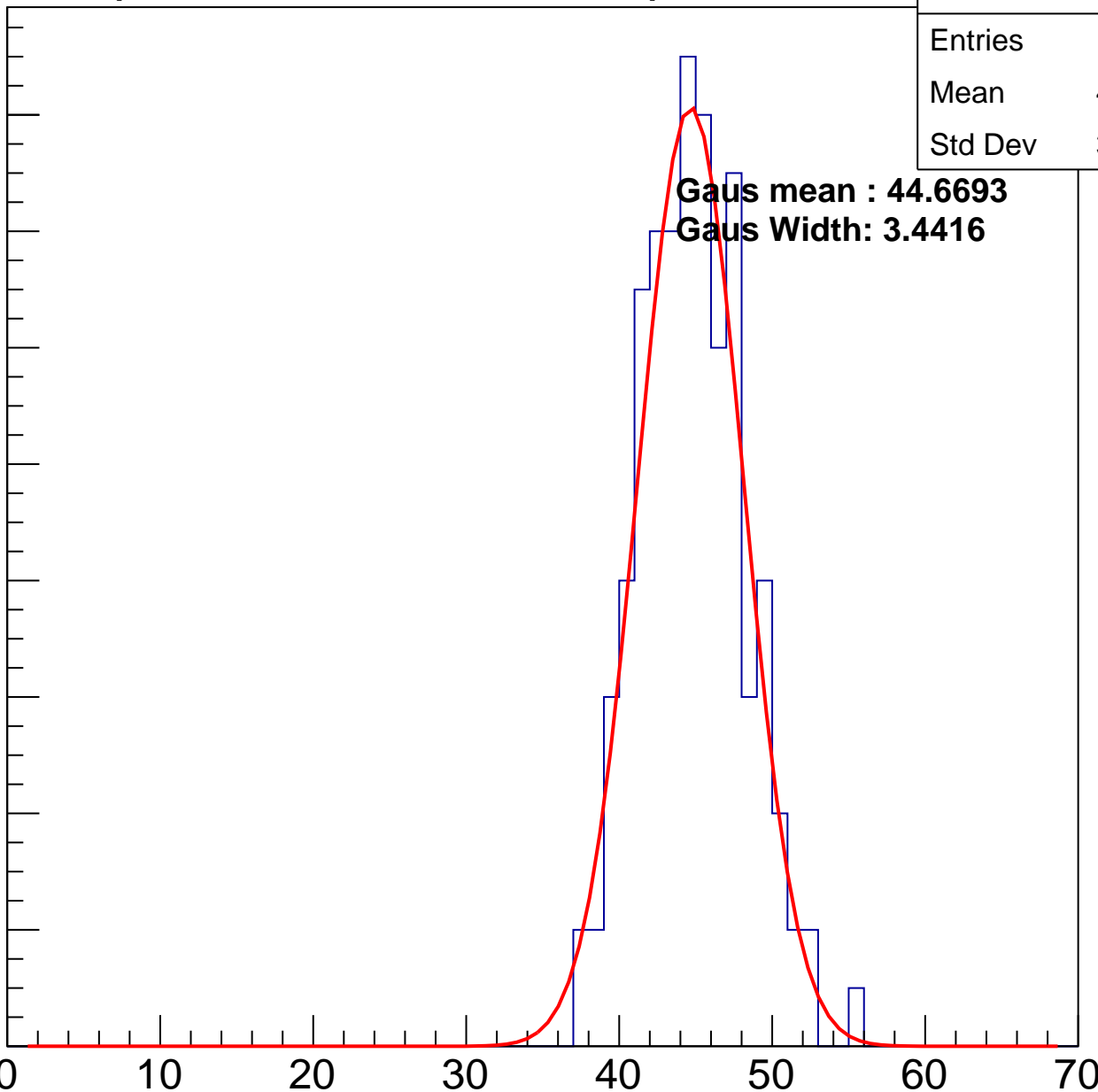
30

40

50

60

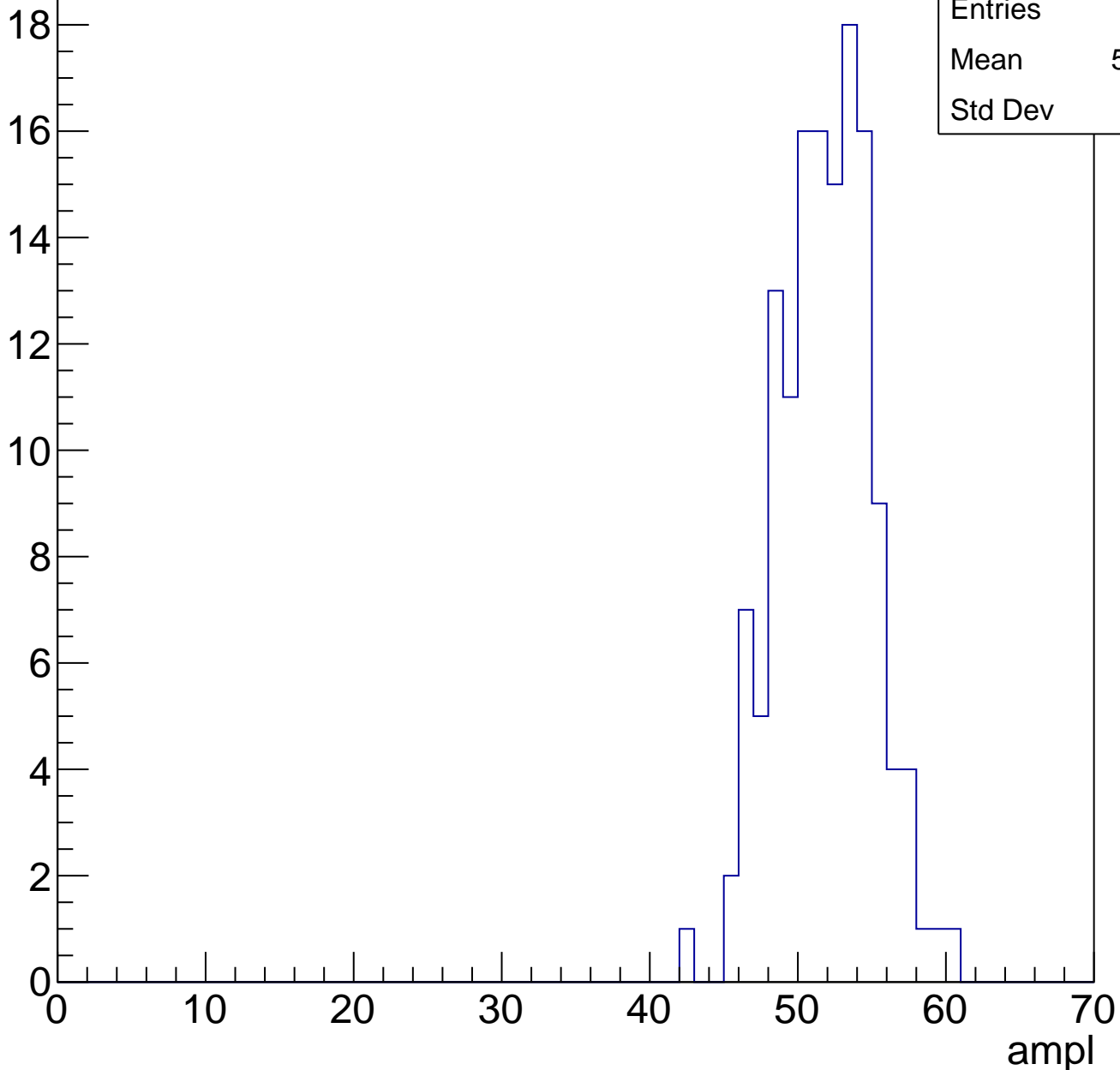
ampl



B1L001S, U25-ch13, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch13, adc4

calib_packv5_042523_0143.root, FC#2, port C2

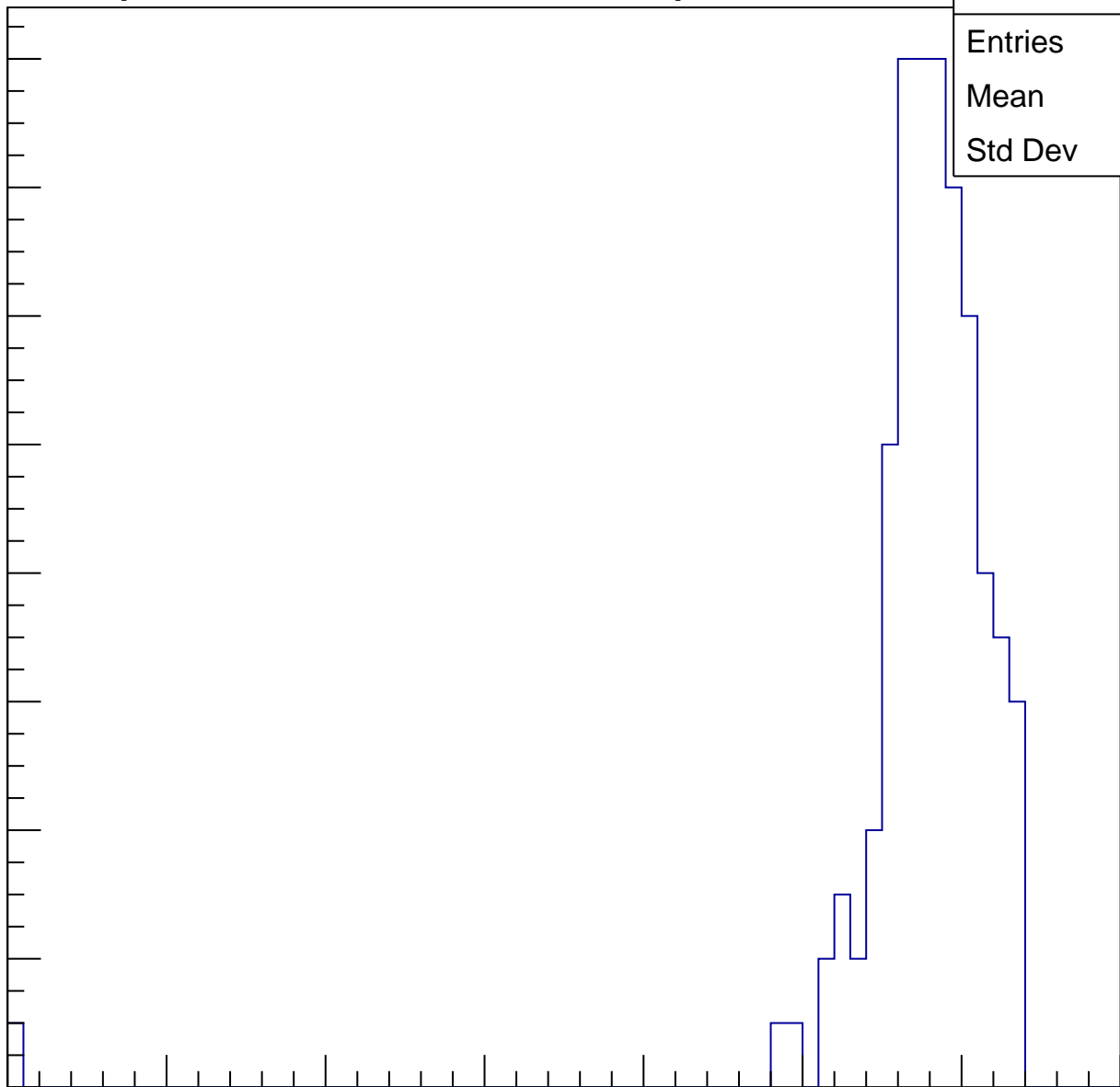
Entries	119
Mean	57.22
Std Dev	6.052

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch13, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	55
Mean	60.16
Std Dev	8.318

ampl

0 10 20 30 40 50 60 70

0

B1L001S, U25-ch13, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch13, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch14, adc0

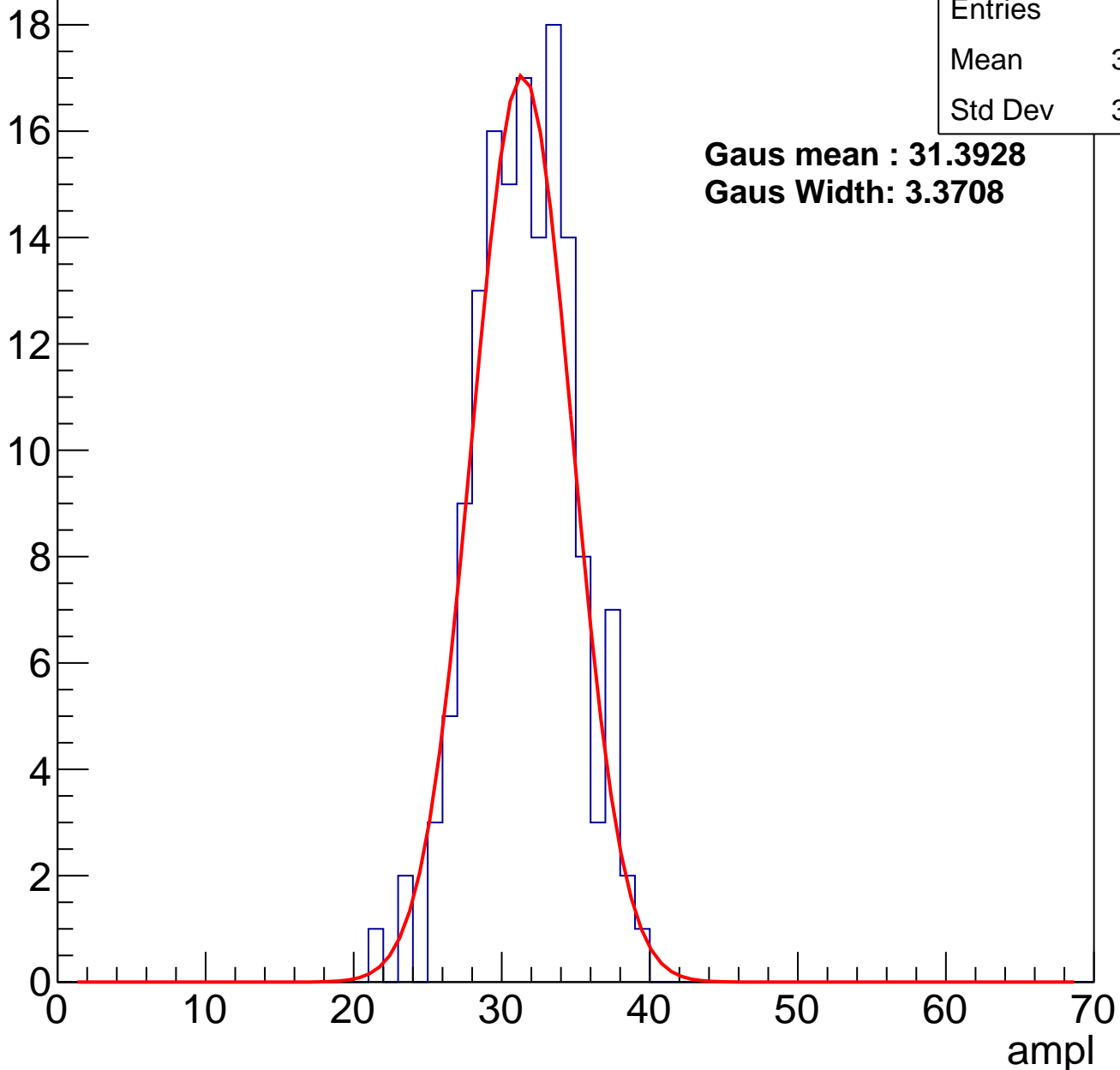
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	31.08
Std Dev	3.318

Gaus mean : 31.3928

Gaus Width: 3.3708

Entry



B1L001S, U25-ch14, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	37.63
Std Dev	3.17

Gaus mean : 38.5587

Gaus Width: 3.2313

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

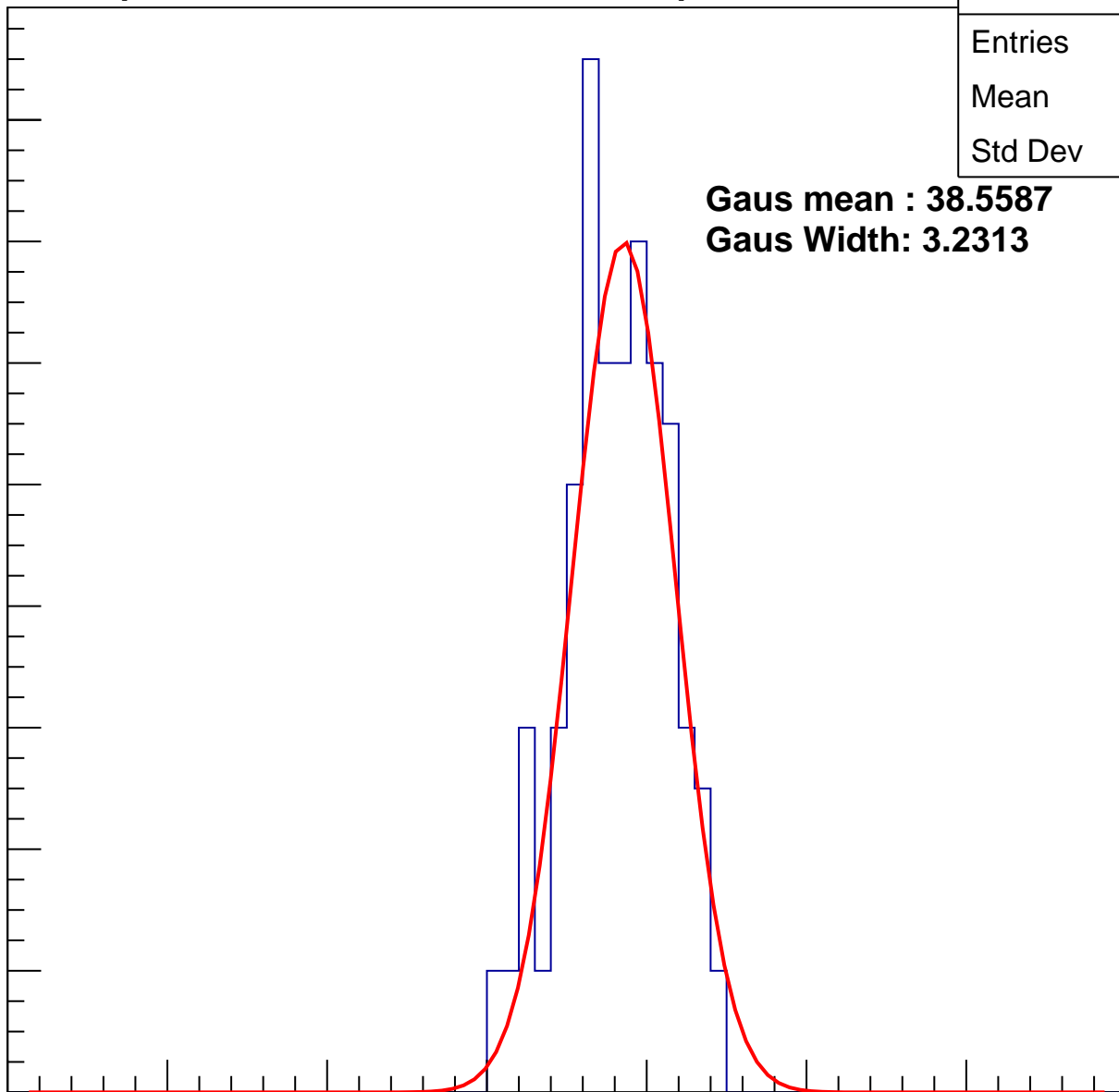
40

50

60

70

ampl



B1L001S, U25-ch14, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

43.89

Std Dev

3.34

Gaus mean : 44.6518

Gaus Width: 3.4750

0

10

20

30

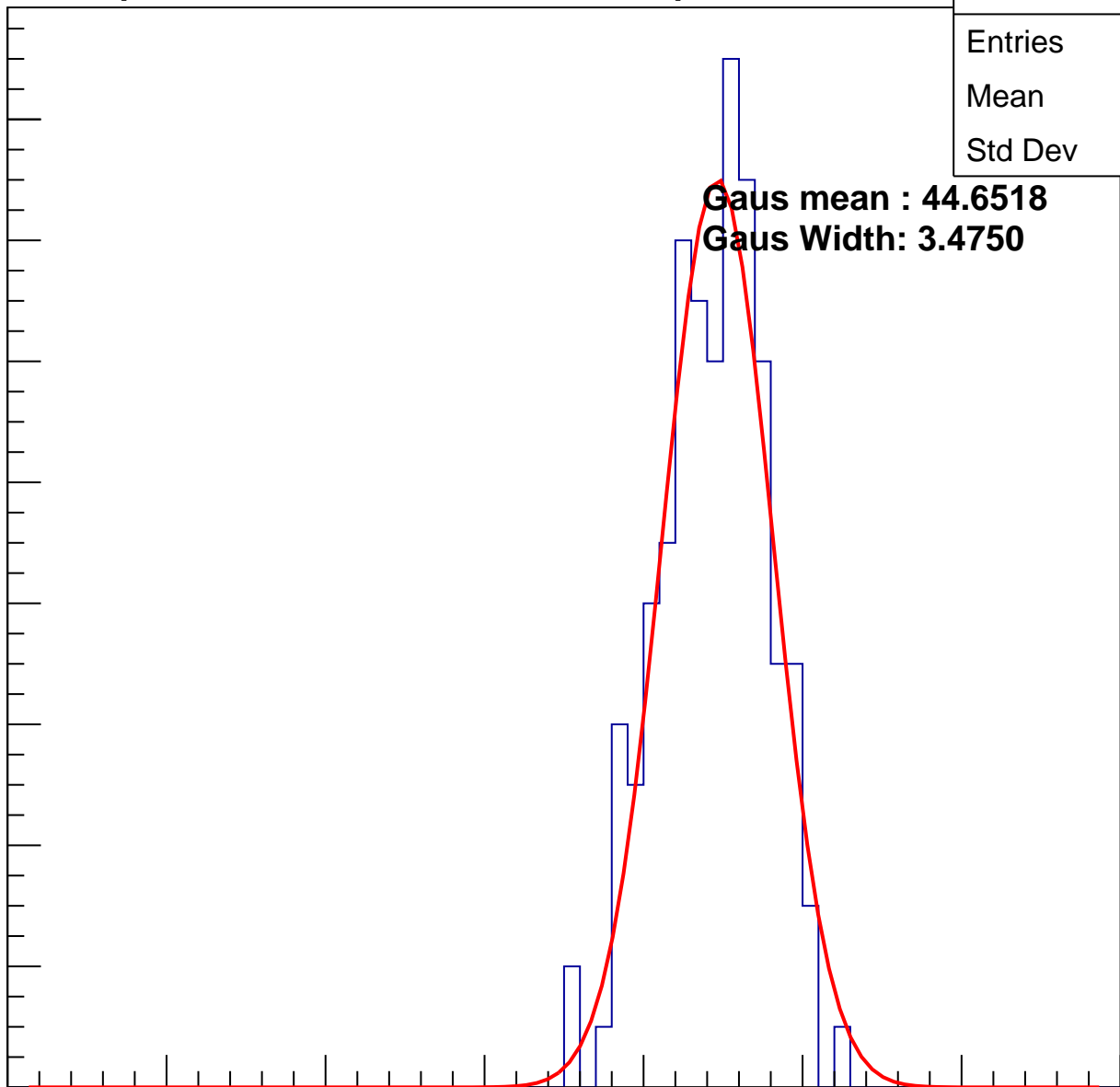
40

50

60

70

ampl

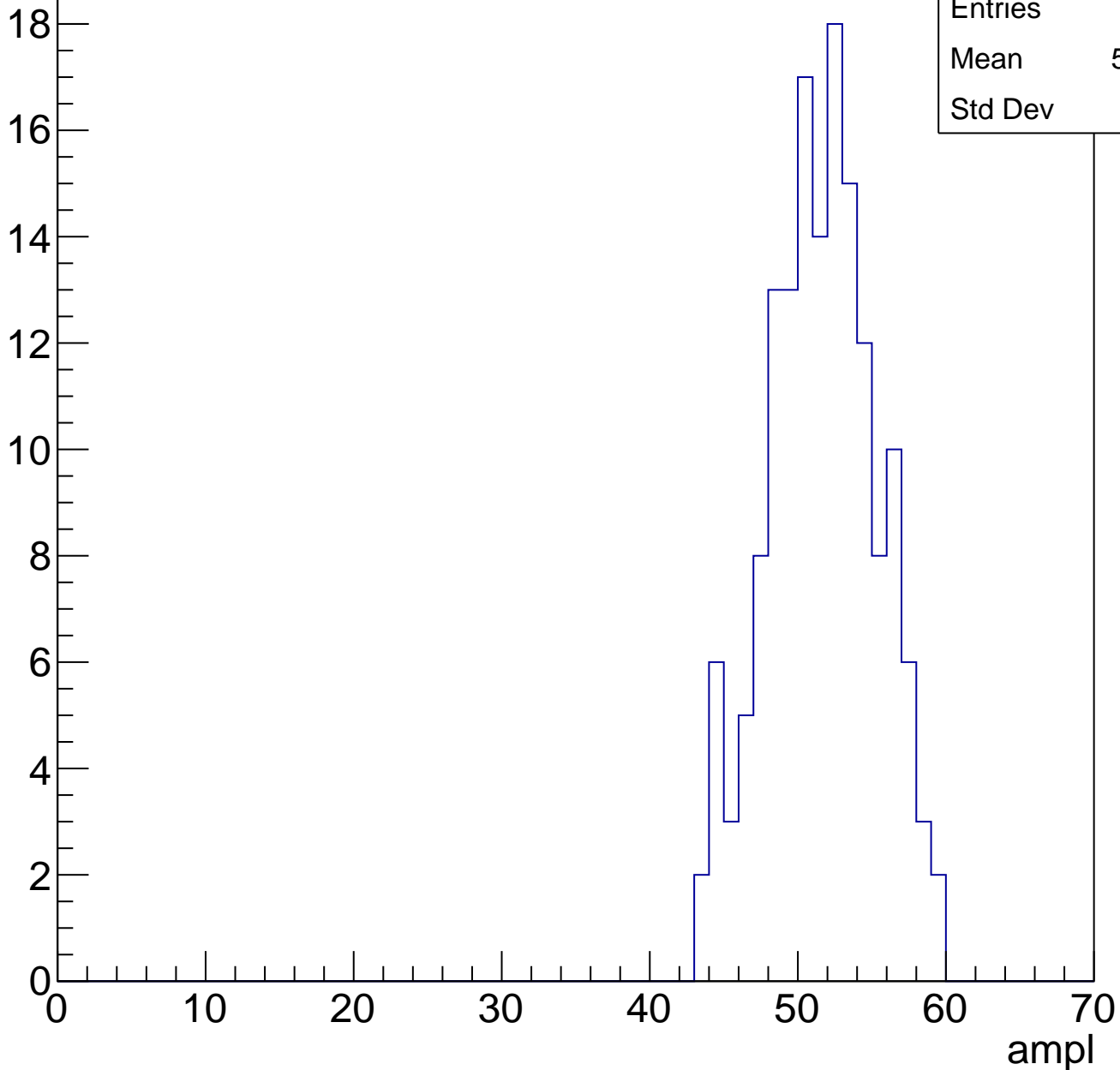


B1L001S, U25-ch14, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	51.15
Std Dev	3.61

Entry



B1L001S, U25-ch14, adc4

calib_packv5_042523_0143.root, FC#2, port C2

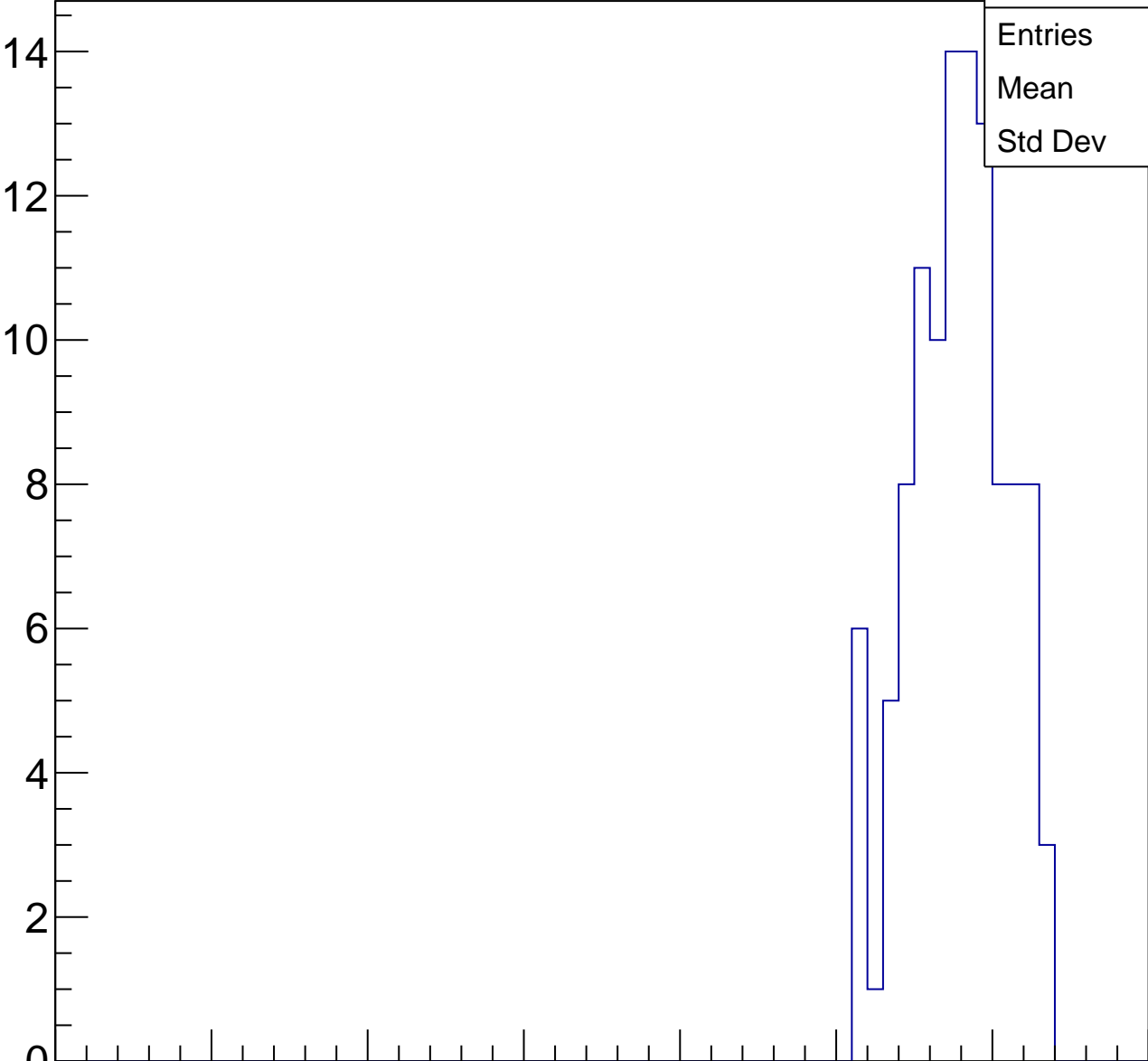
Entries	109
Mean	57.34
Std Dev	3.042

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch14, adc5

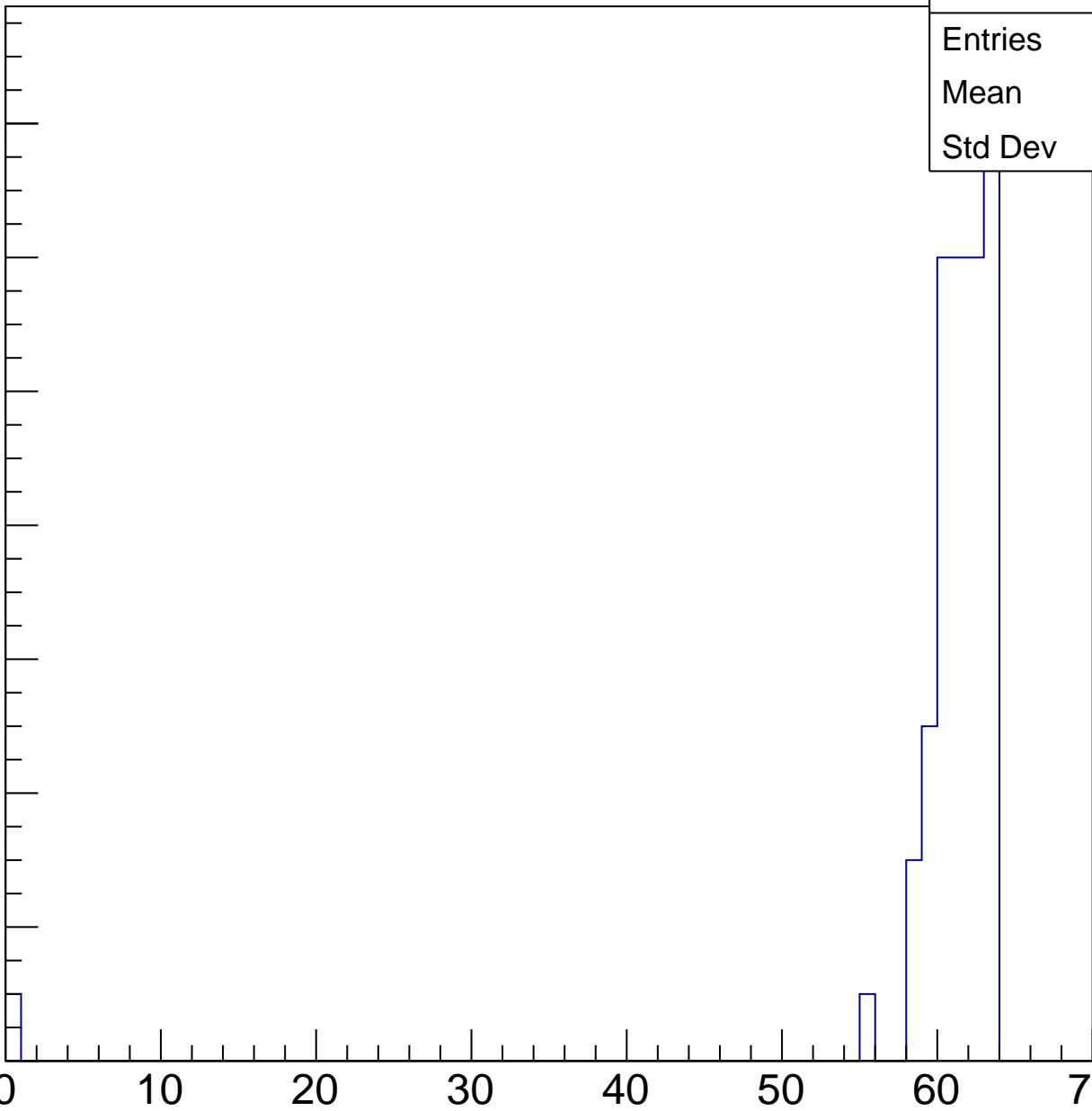
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	61
Mean	60.08
Std Dev	7.931

ampl



B1L001S, U25-ch14, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch14, adc7

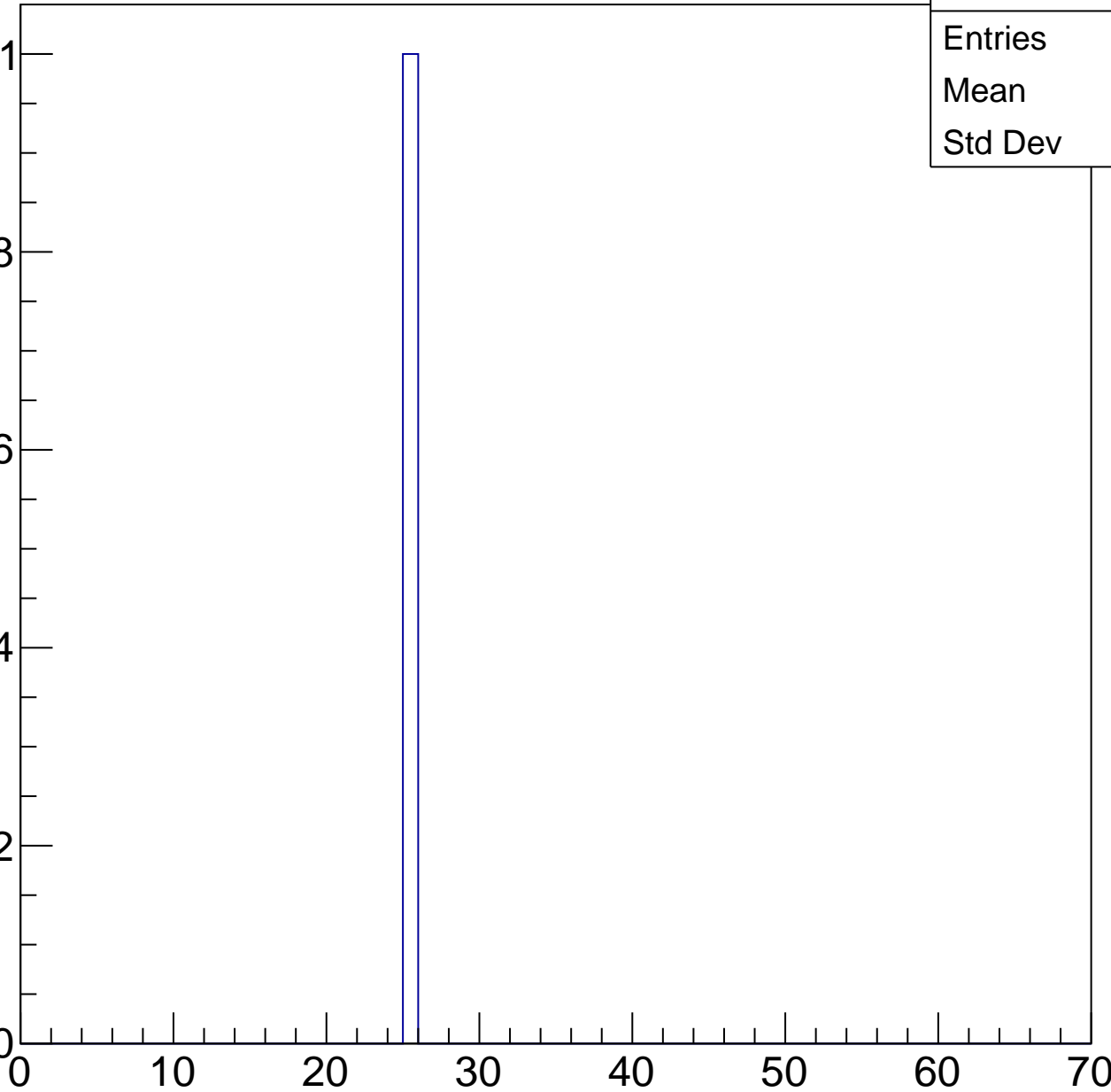
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B1L001S, U25-ch15, adc0

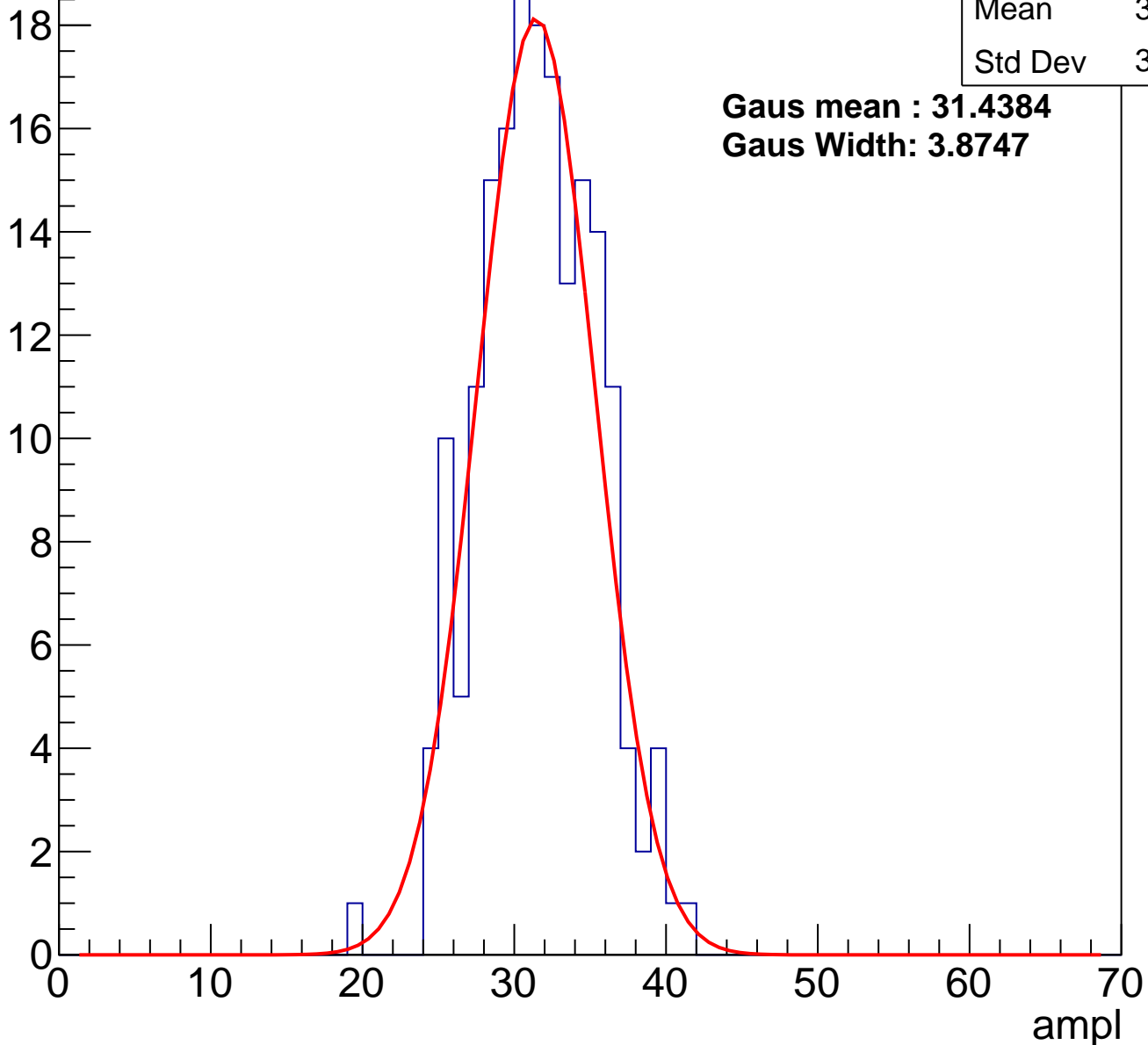
calib_packv5_042523_0143.root, FC#2, port C2

Entries	181
Mean	31.13
Std Dev	3.789

Gaus mean : 31.4384

Gaus Width: 3.8747

Entry



B1L001S, U25-ch15, adc1

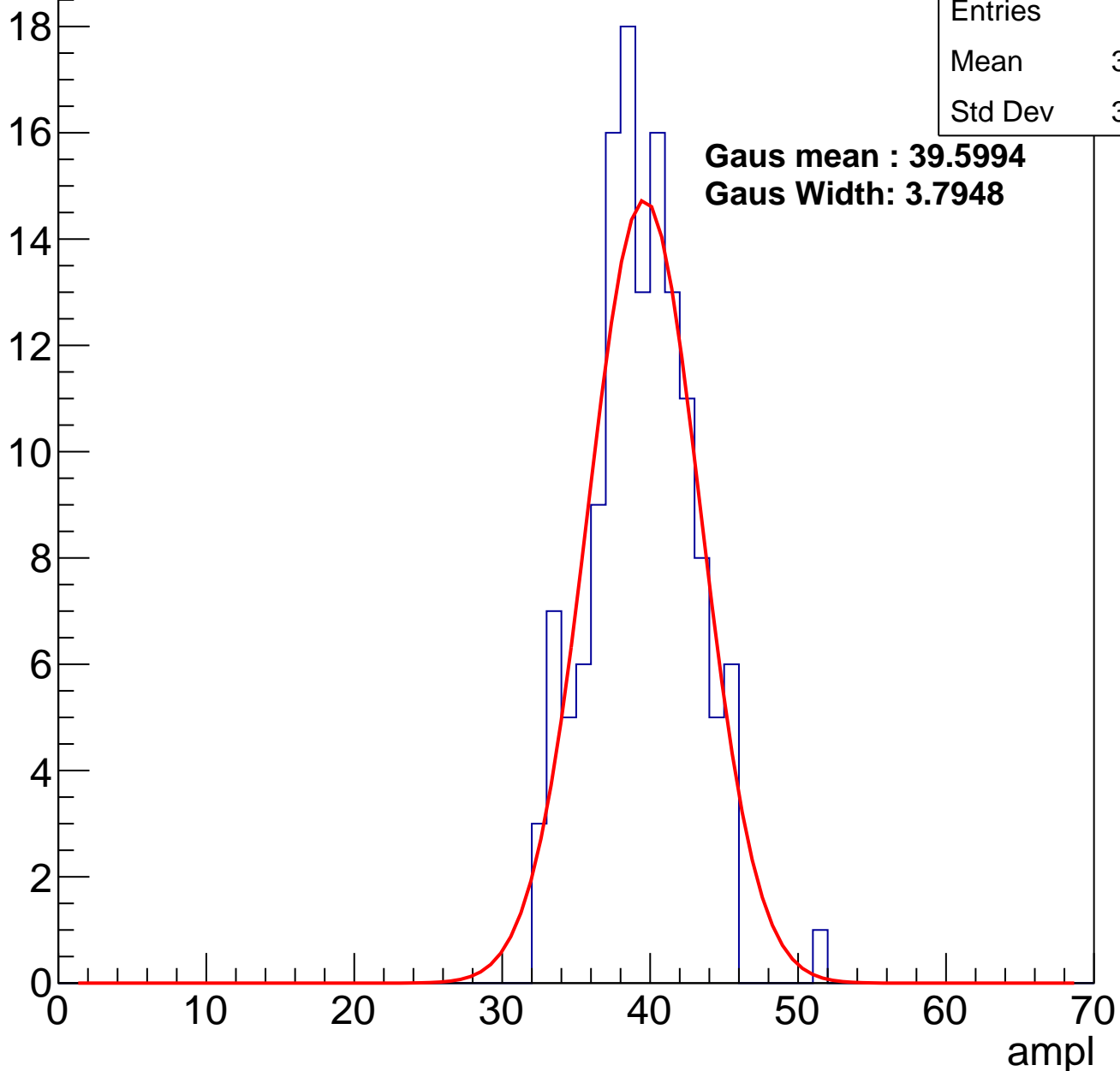
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	38.93
Std Dev	3.377

Gaus mean : 39.5994

Gaus Width: 3.7948

Entry



B1L001S, U25-ch15, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

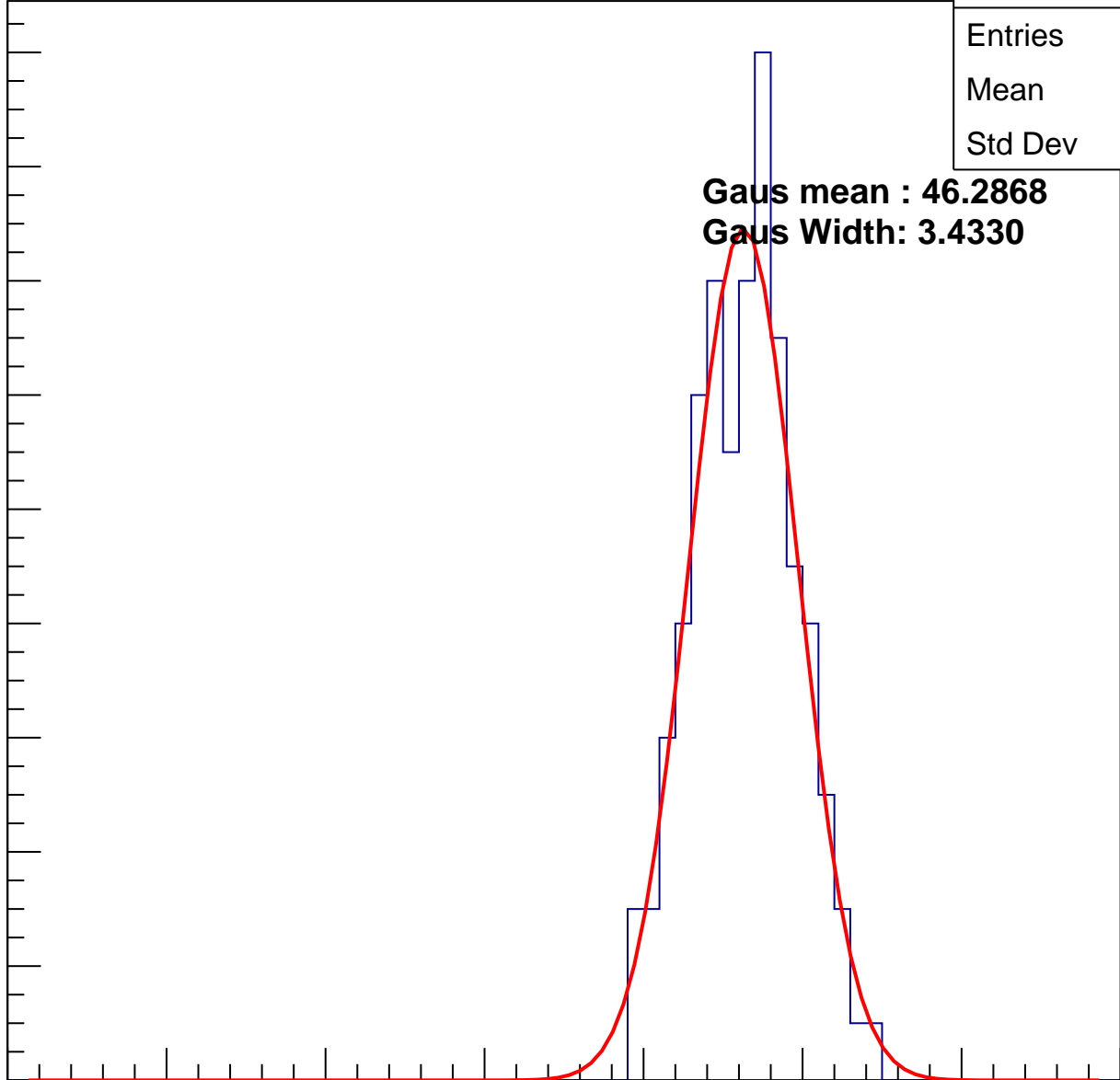
Entries	129
Mean	45.88
Std Dev	3.212

Gaus mean : 46.2868

Gaus Width: 3.4330

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch15, adc3

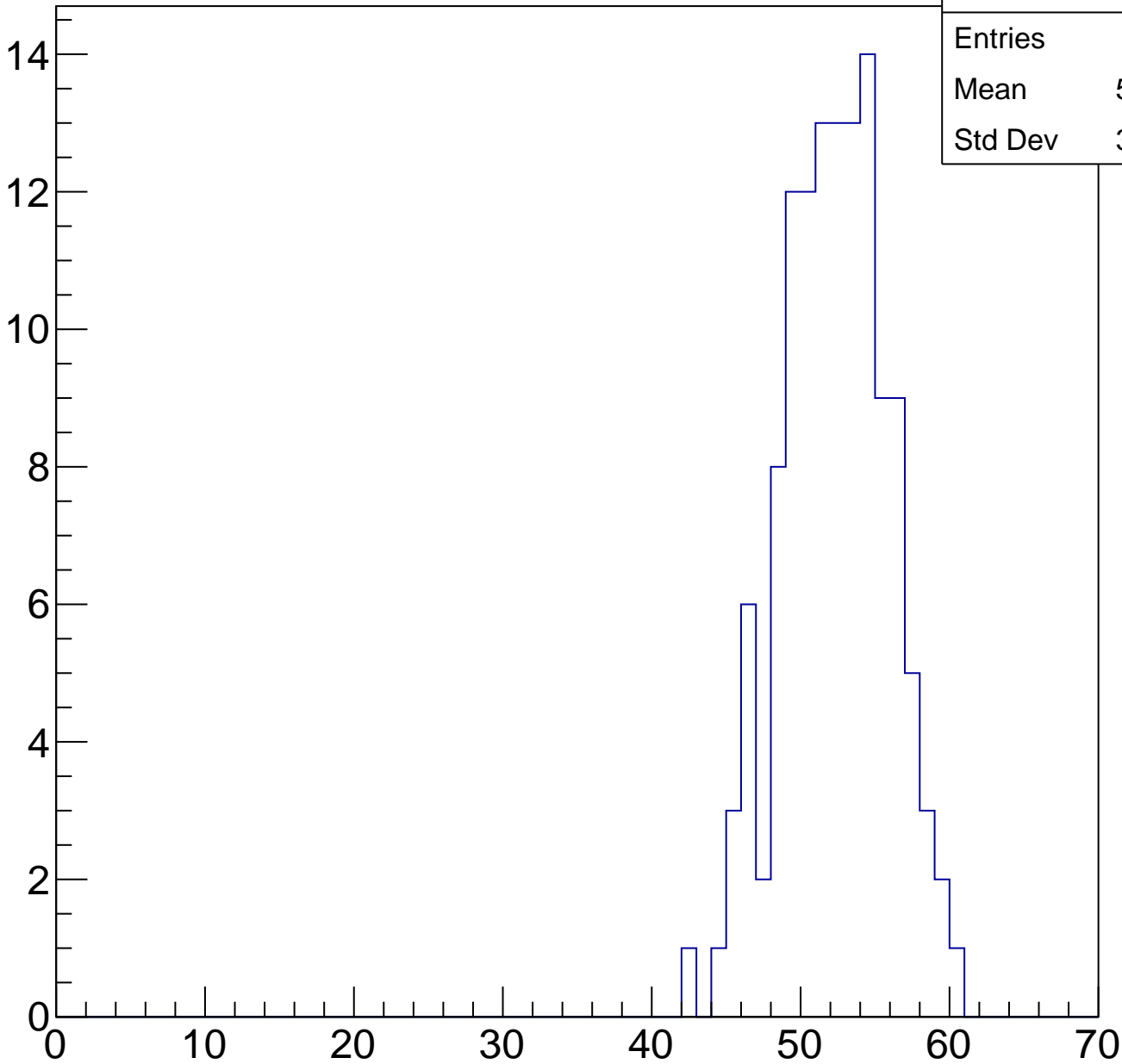
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	51.83
Std Dev	3.504

ampl



B1L001S, U25-ch15, adc4

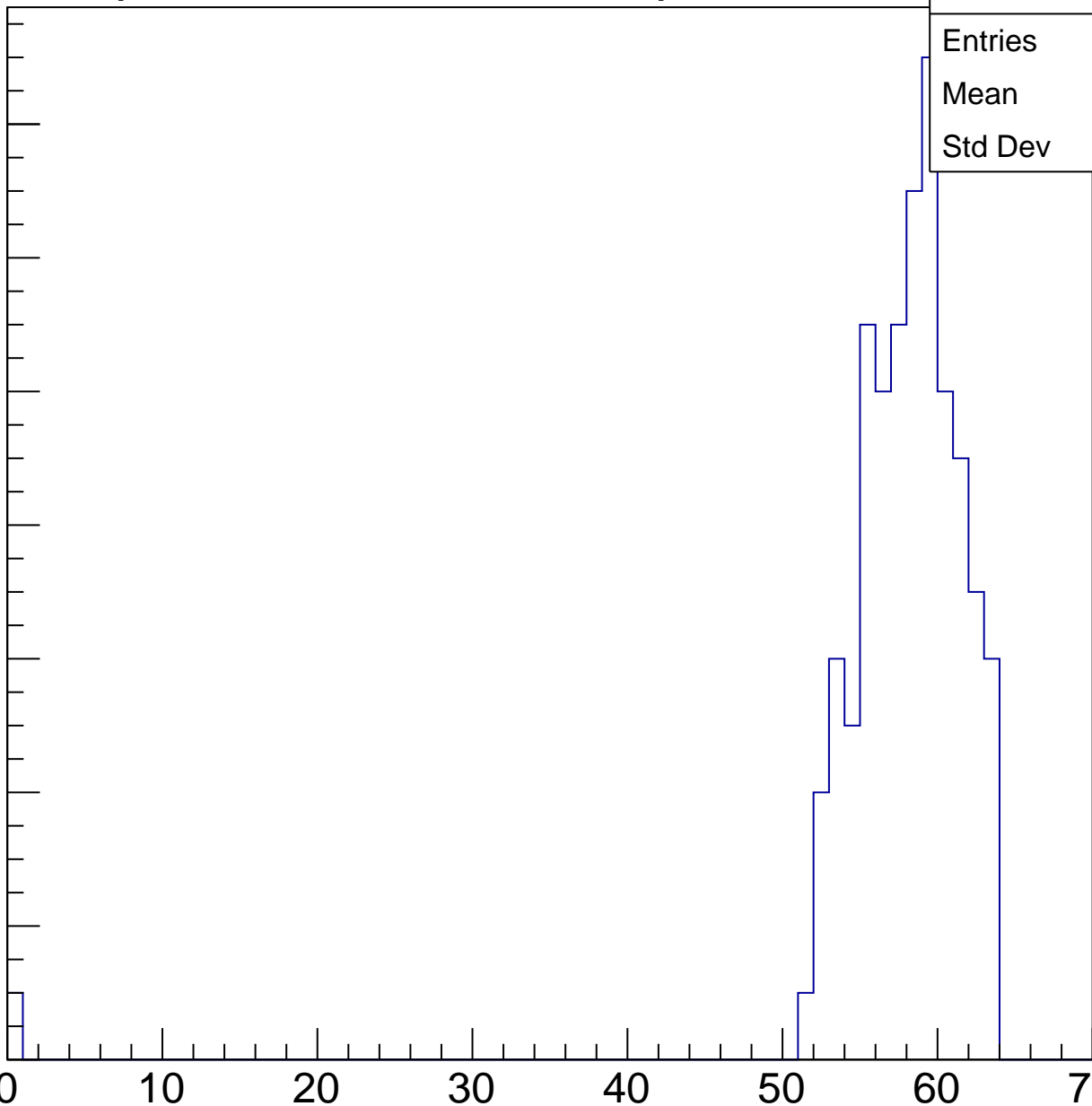
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	57.24
Std Dev	6.26

ampl



B1L001S, U25-ch15, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	59
Mean	59.81
Std Dev	8.077

0

2

4

6

8

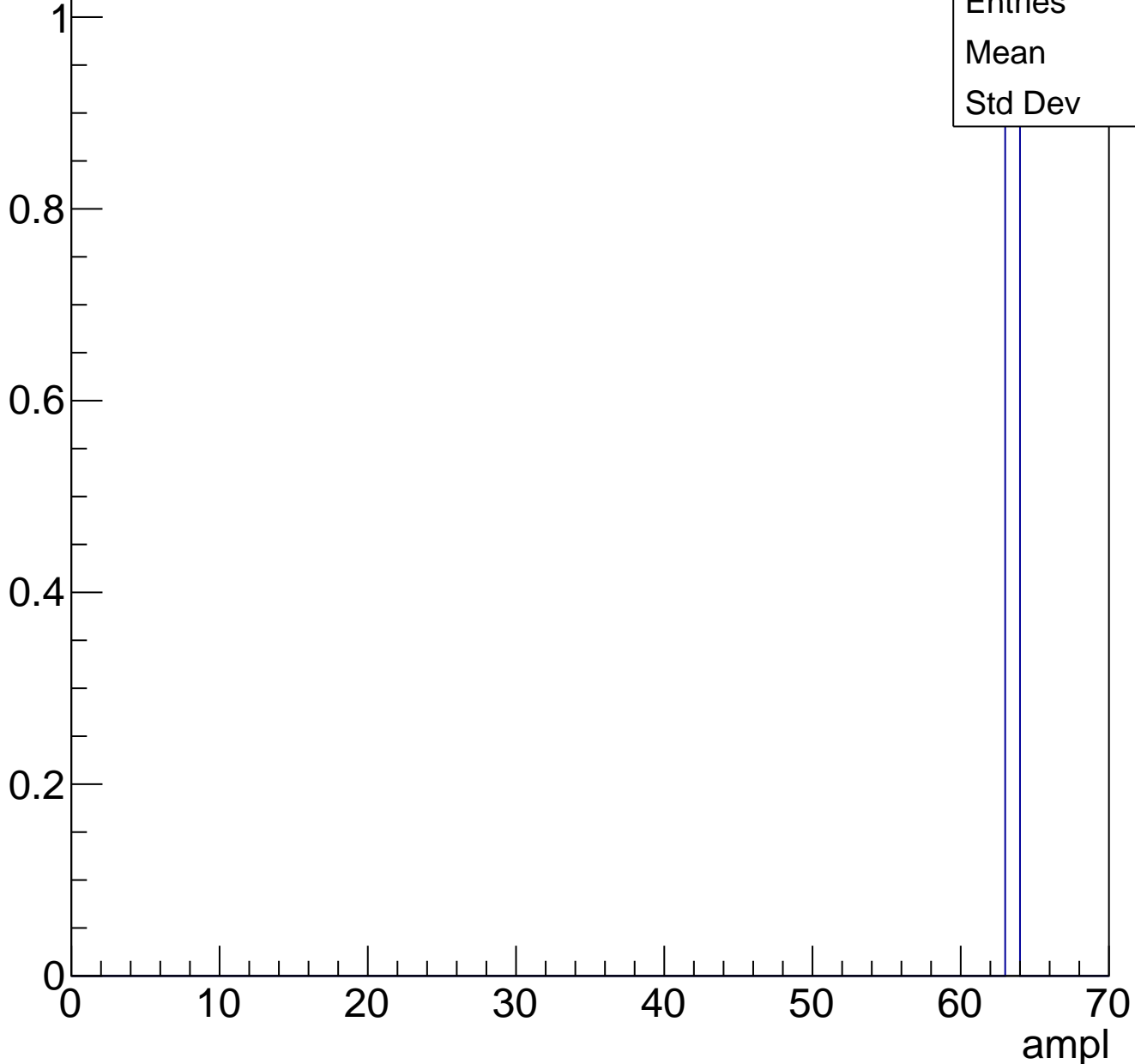
10

12

B1L001S, U25-ch15, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch15, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U25-ch16, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	31.56
Std Dev	4.357

Gaus mean : 32.0934

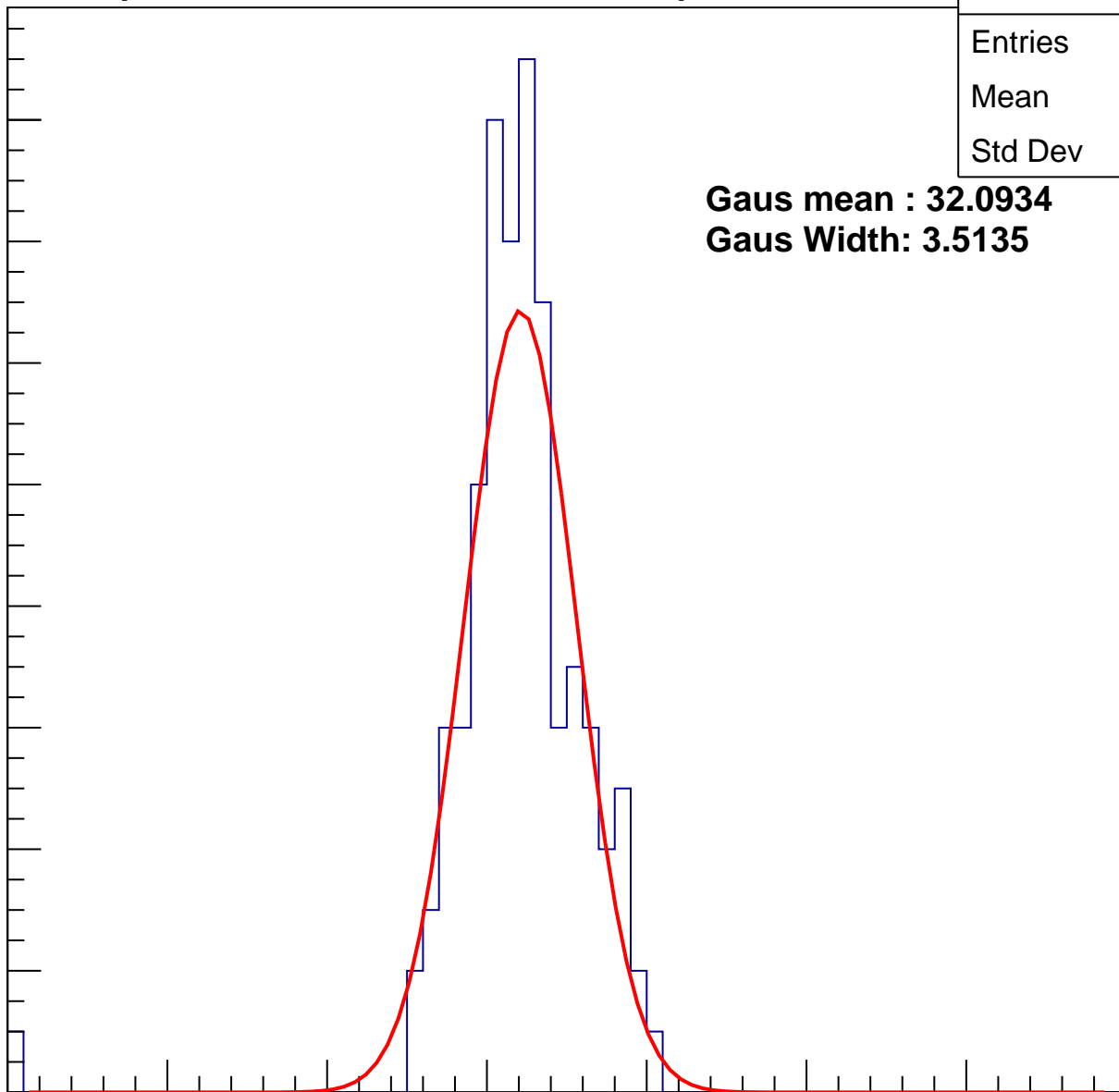
Gaus Width: 3.5135

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch16, adc1

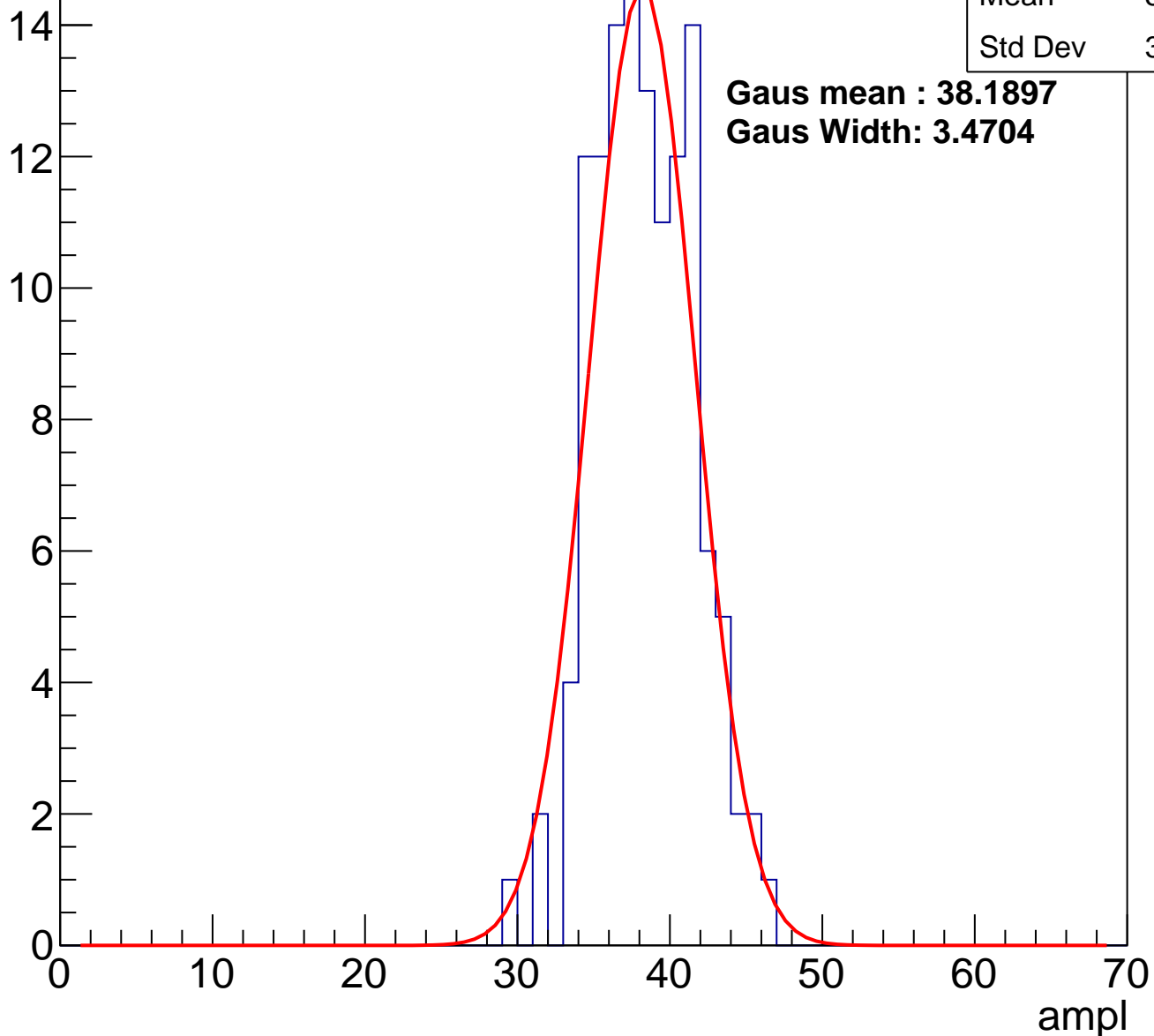
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	37.92
Std Dev	3.199

Gaus mean : 38.1897

Gaus Width: 3.4704

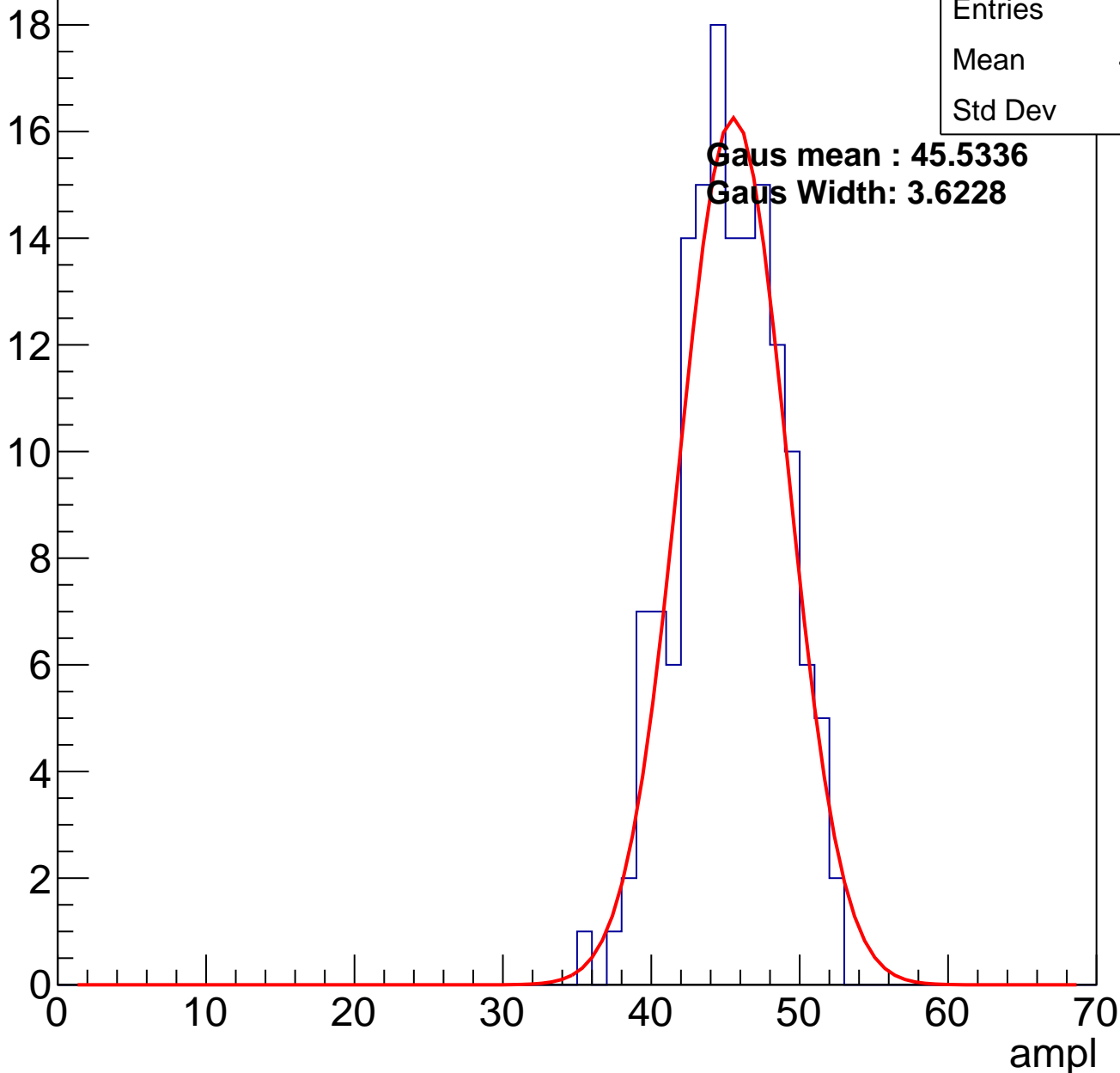
Entry



B1L001S, U25-ch16, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch16, adc3

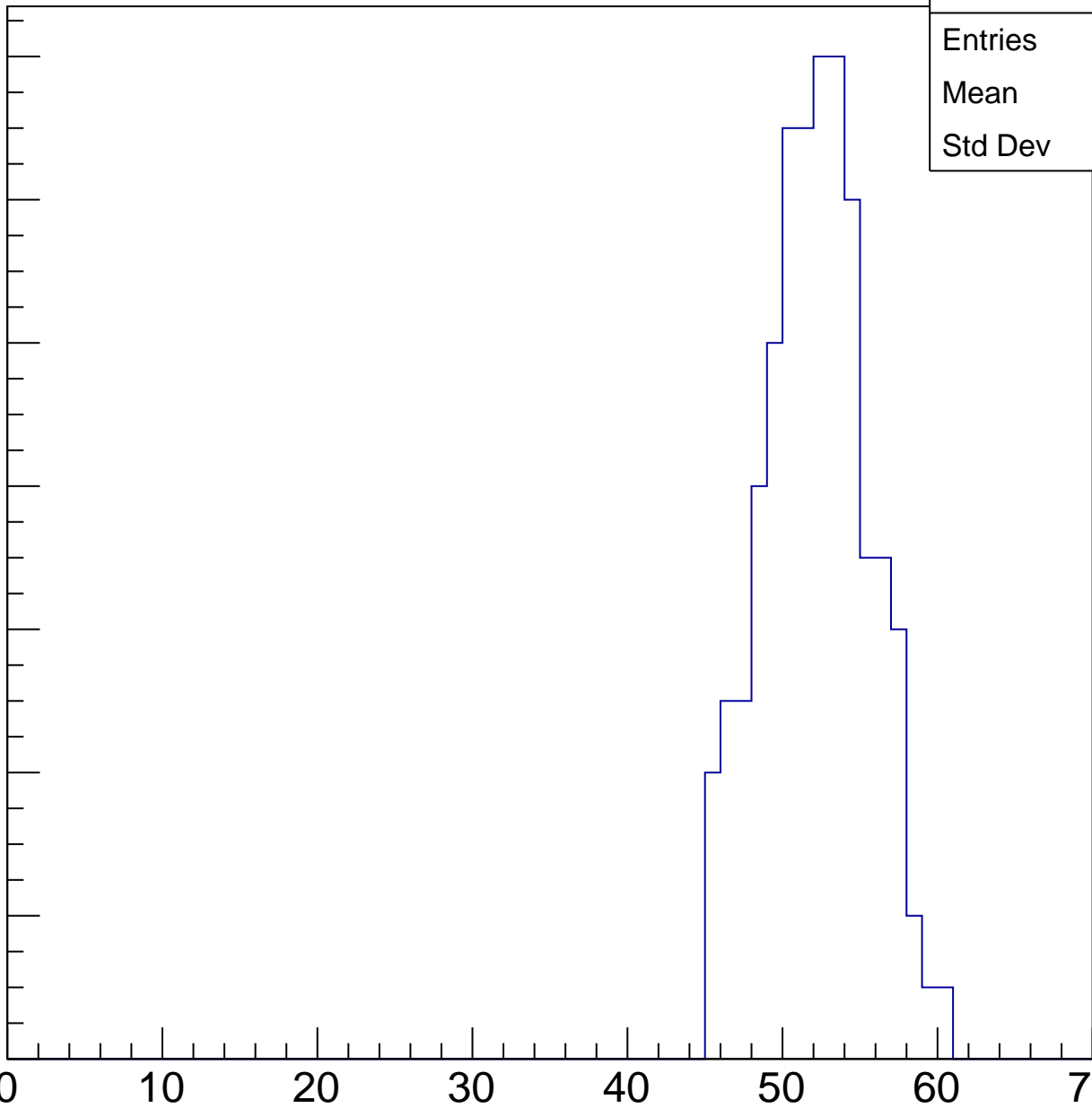
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	51.67
Std Dev	3.318

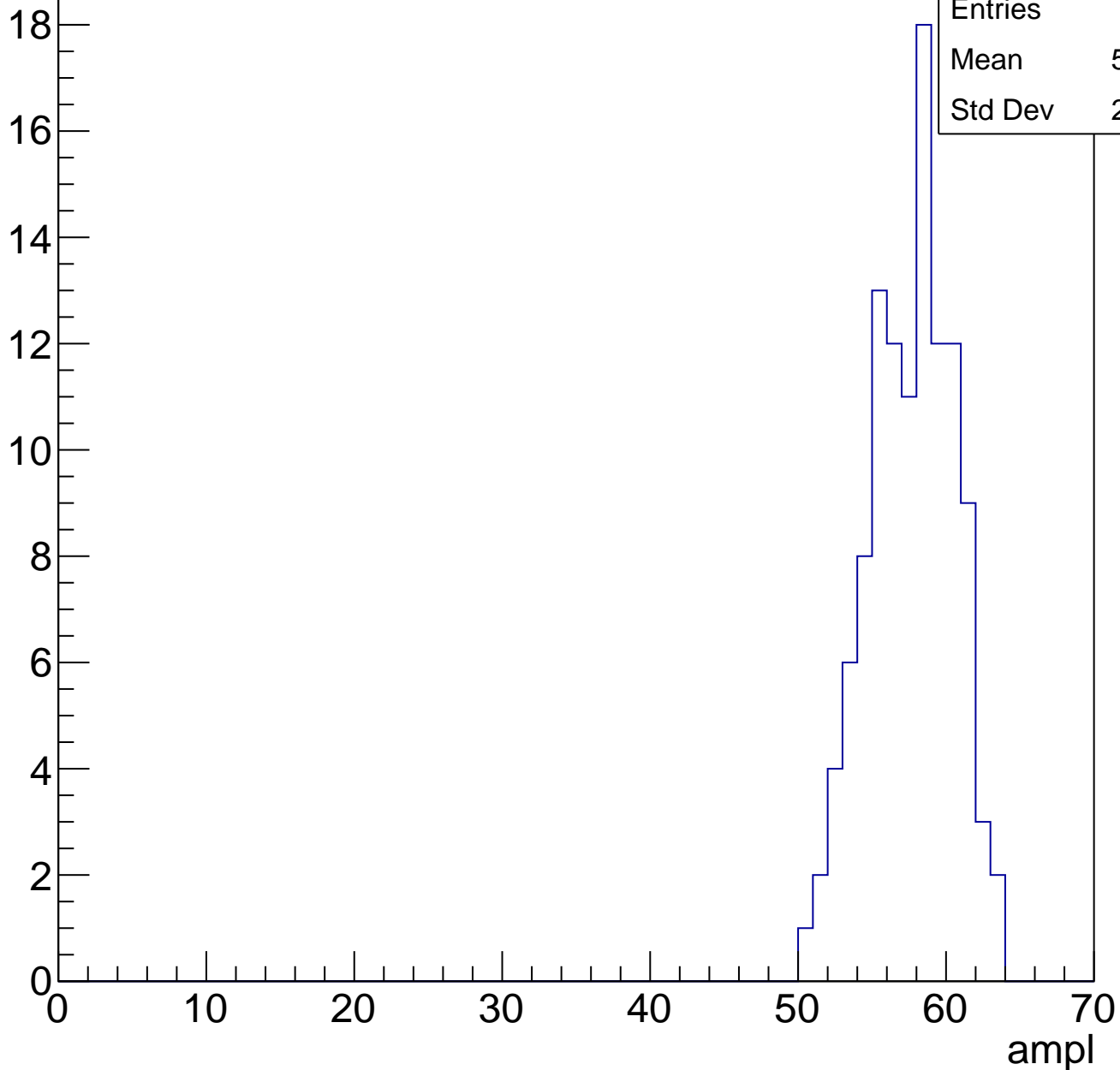
ampl



B1L001S, U25-ch16, adc4

calib_packv5_042523_0143.root, FC#2, port C2

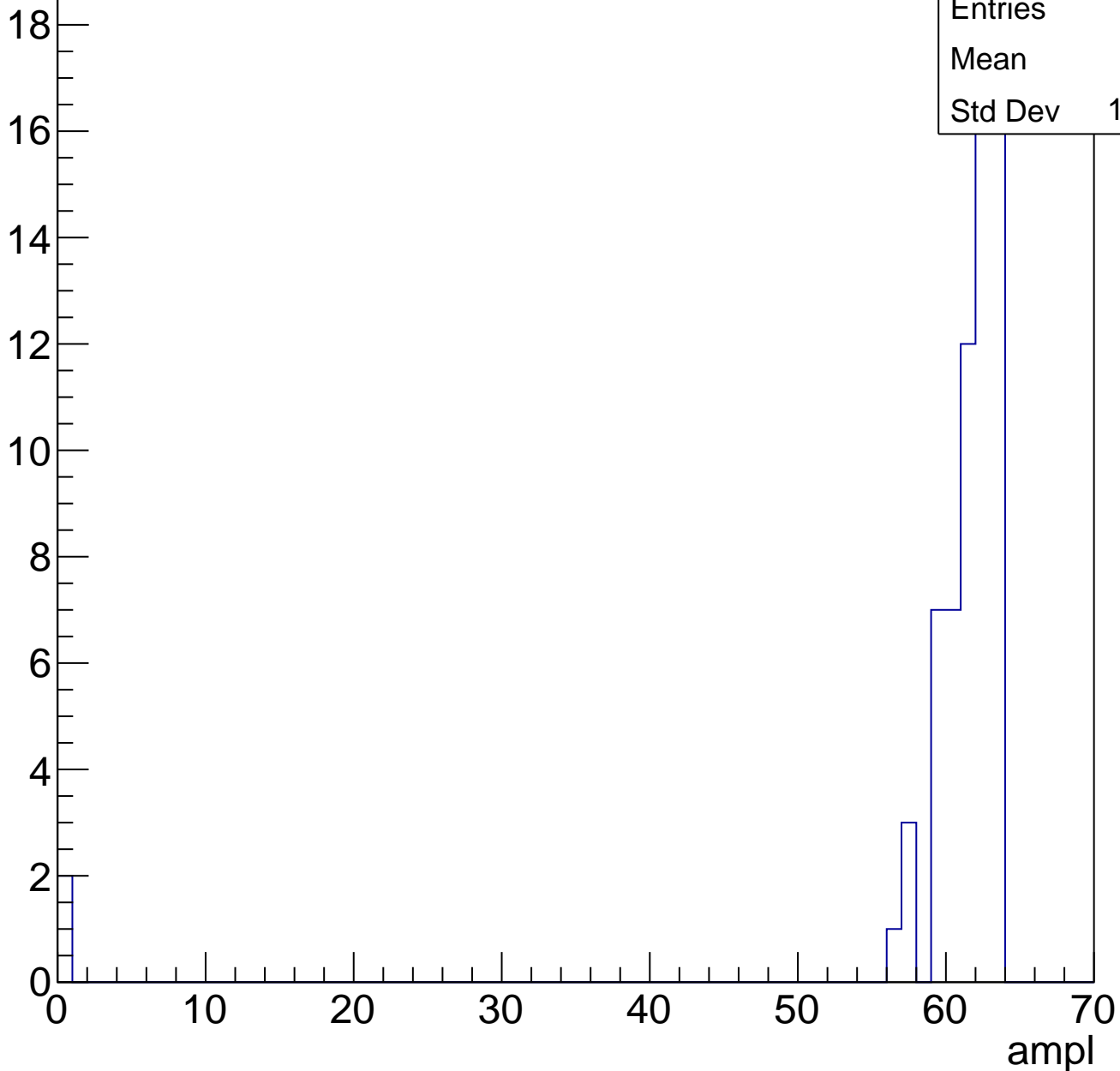
Entry



B1L001S, U25-ch16, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch16, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch16, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch17, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	30.62
Std Dev	4.205

Gaus mean : 31.4953

Gaus Width: 3.6236

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

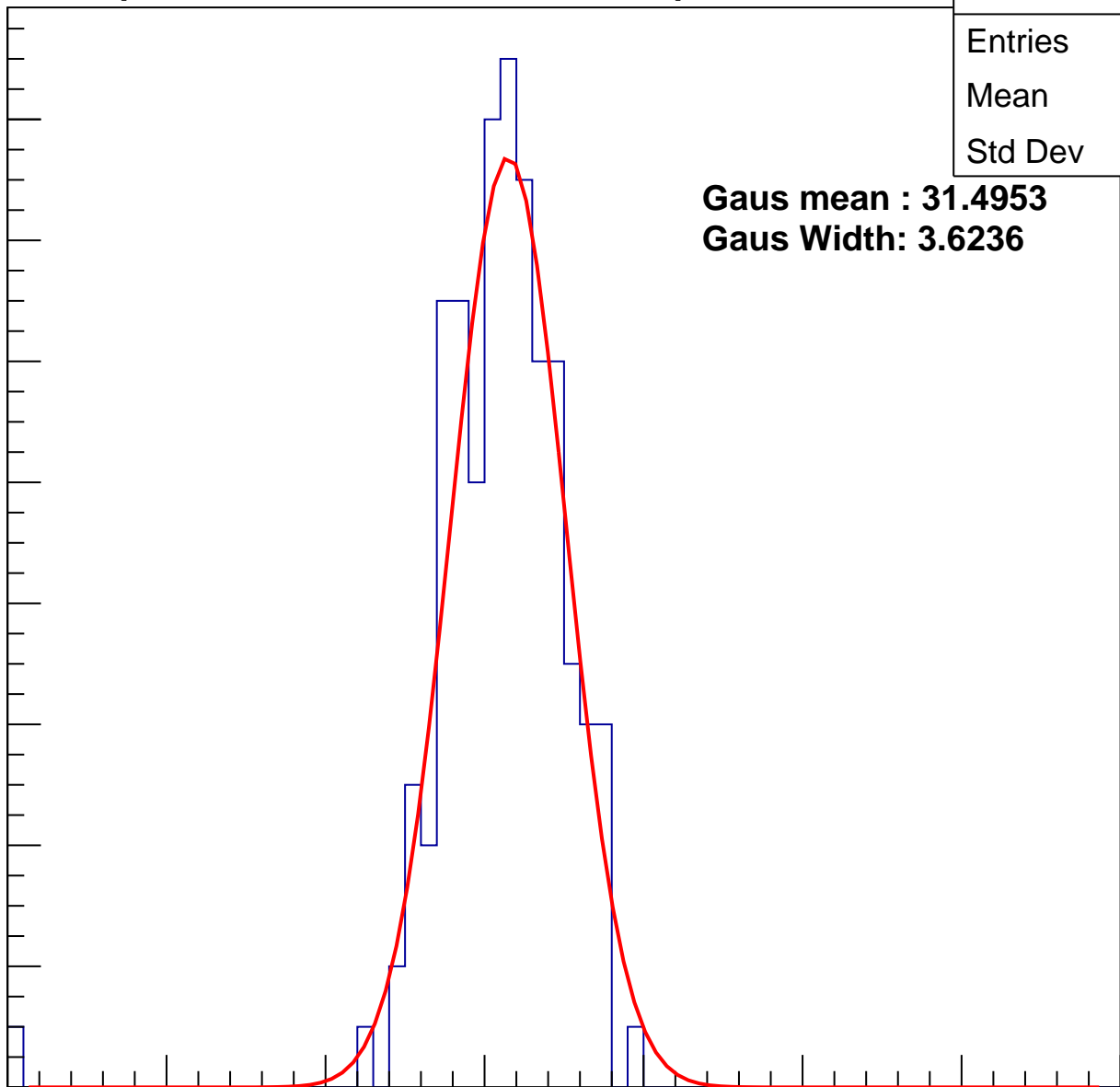
40

50

60

70

ampl



B1L001S, U25-ch17, adc1

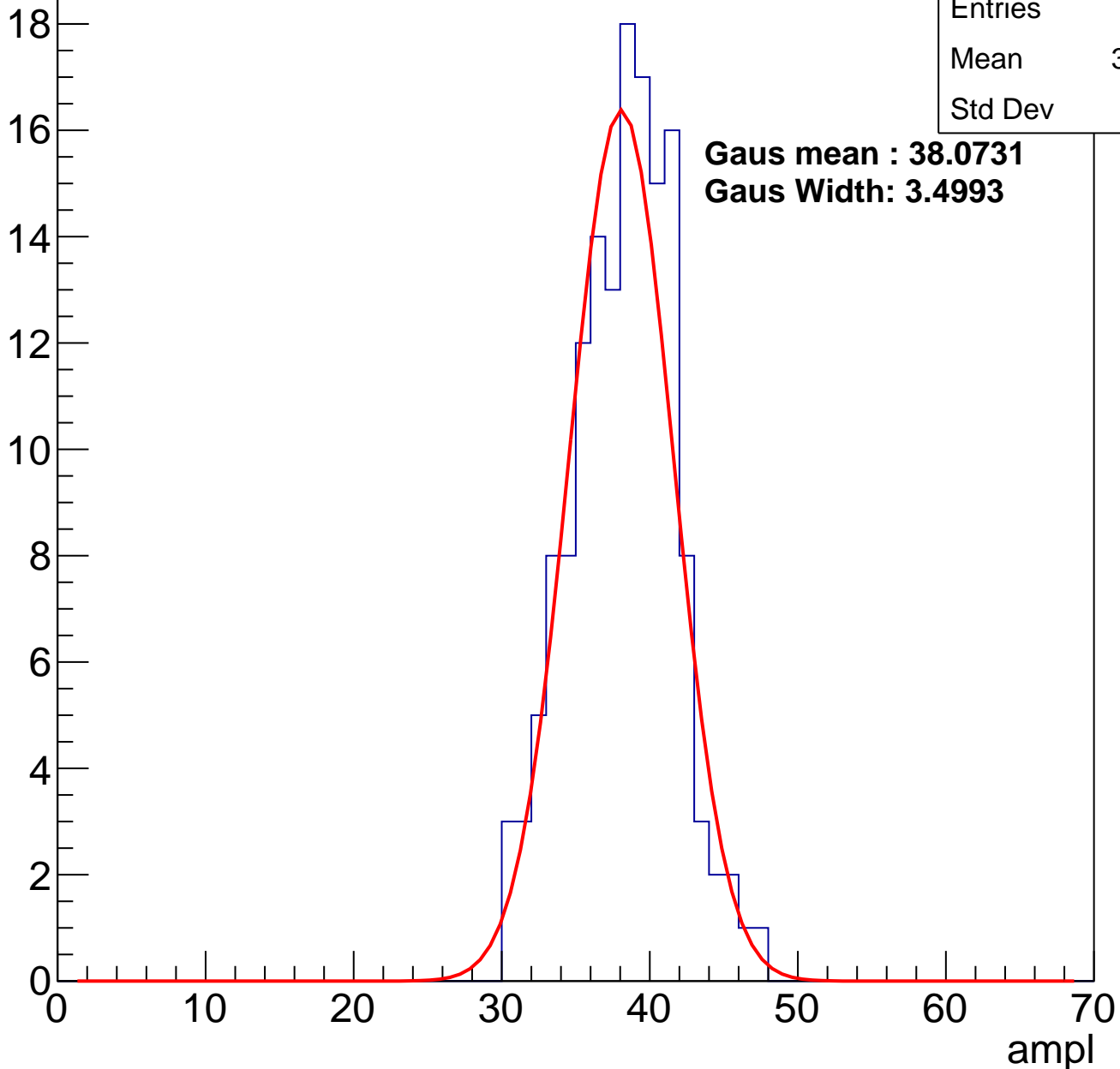
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	37.74
Std Dev	3.43

Gaus mean : 38.0731

Gaus Width: 3.4993

Entry



B1L001S, U25-ch17, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	44.18
Std Dev	2.952

Gaus mean : 44.8288

Gaus Width: 2.7266

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

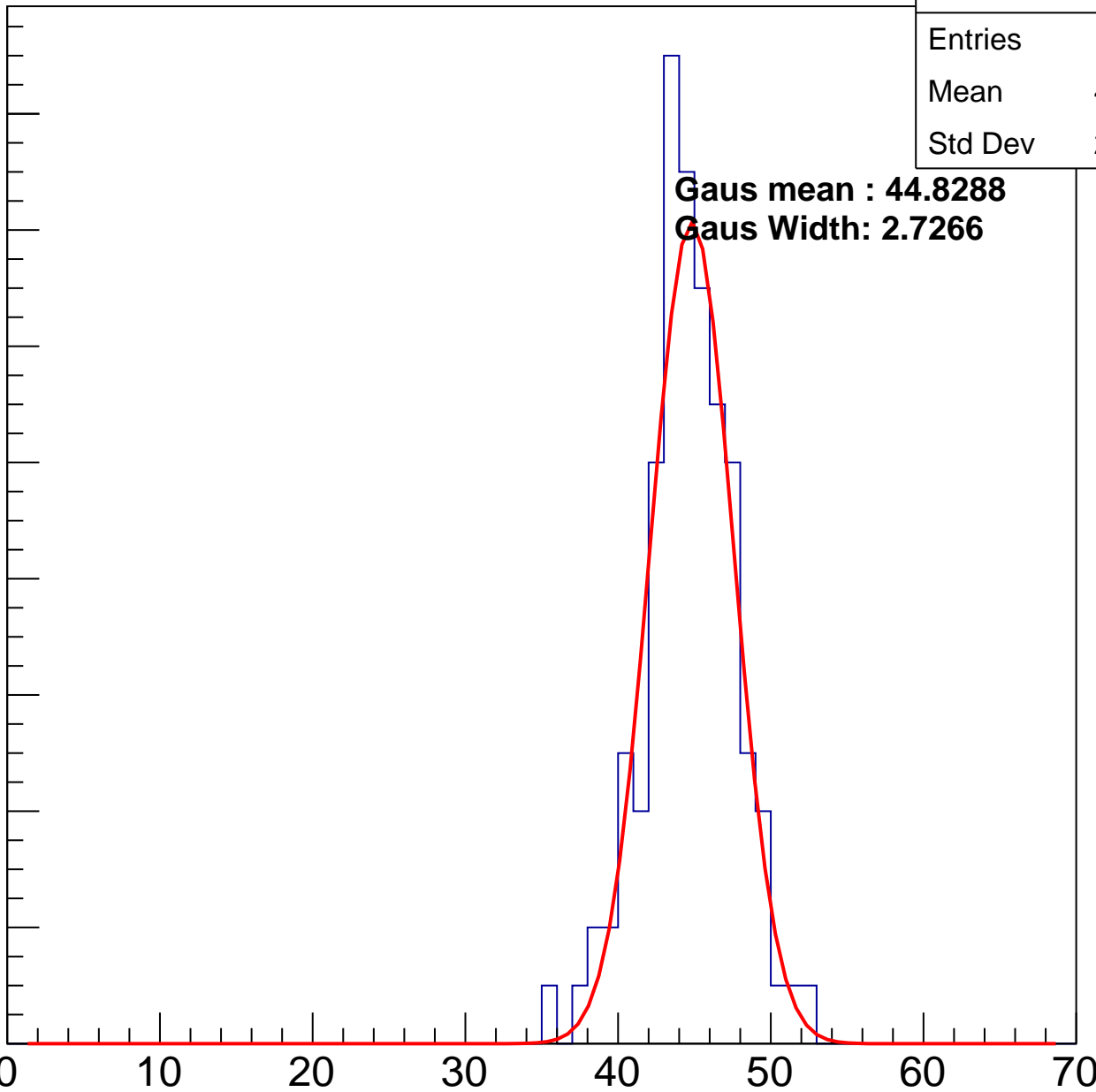
40

50

60

70

ampl

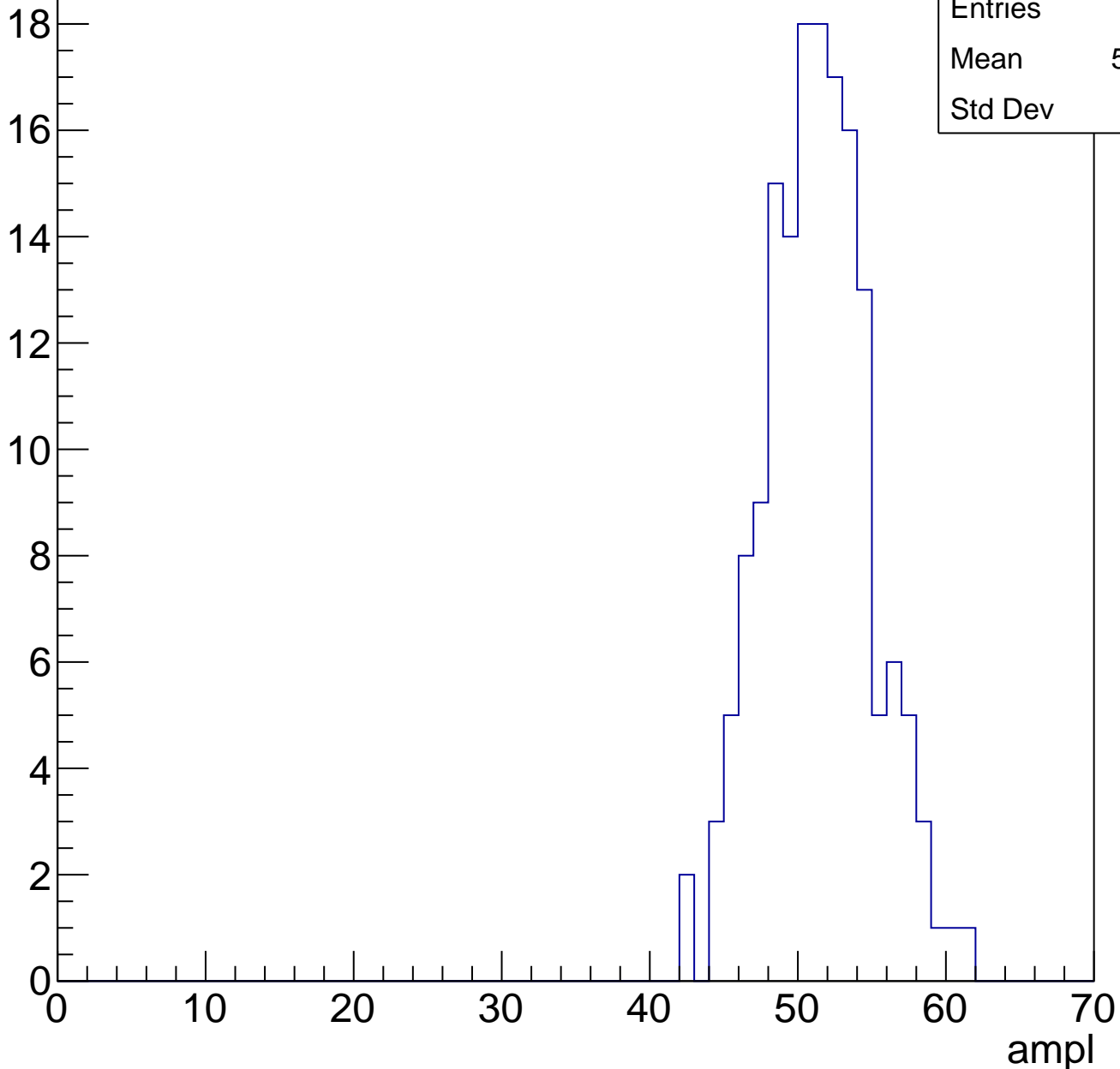


B1L001S, U25-ch17, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	50.88
Std Dev	3.57

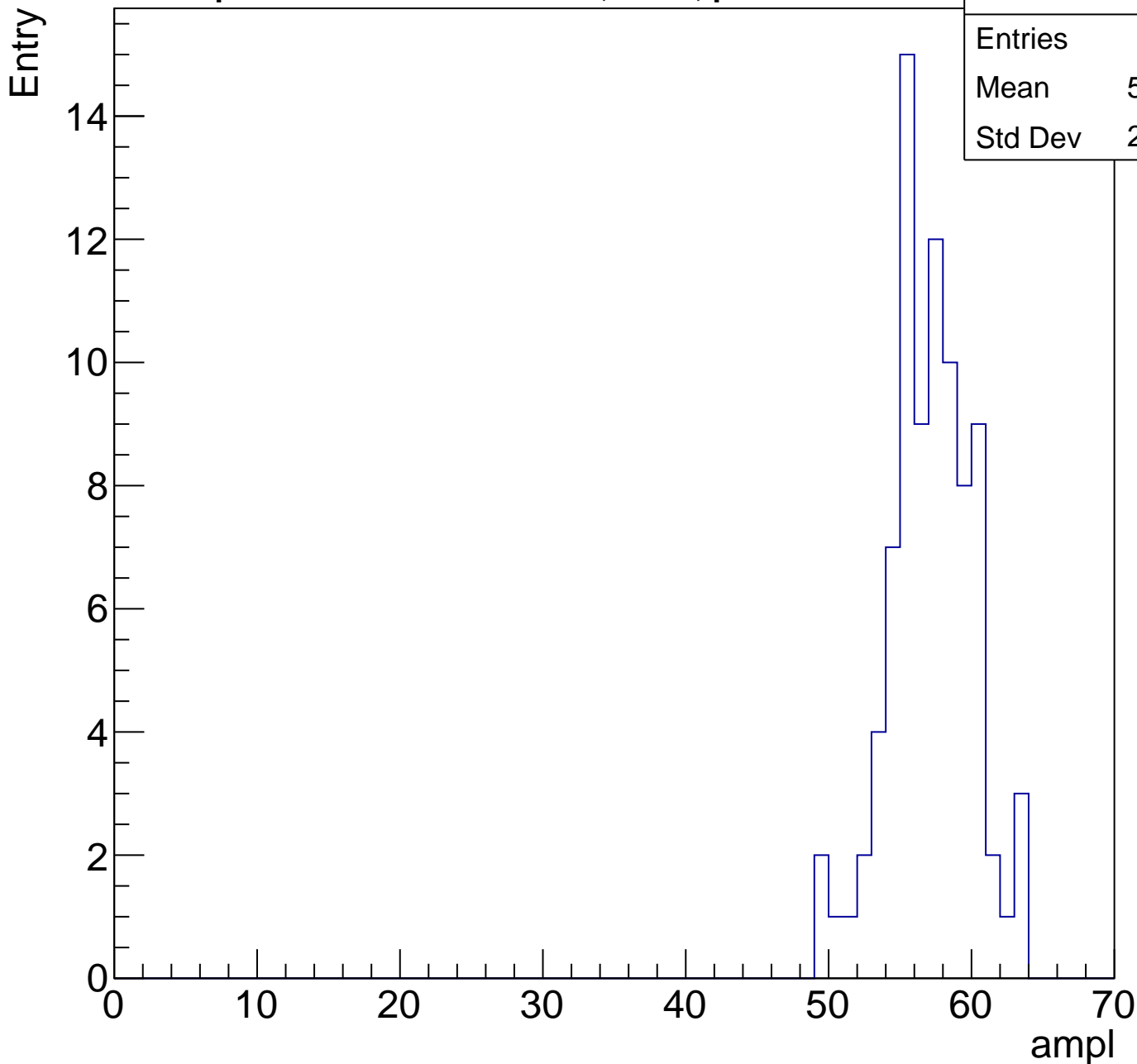
Entry



B1L001S, U25-ch17, adc4

calib_packv5_042523_0143.root, FC#2, port C2

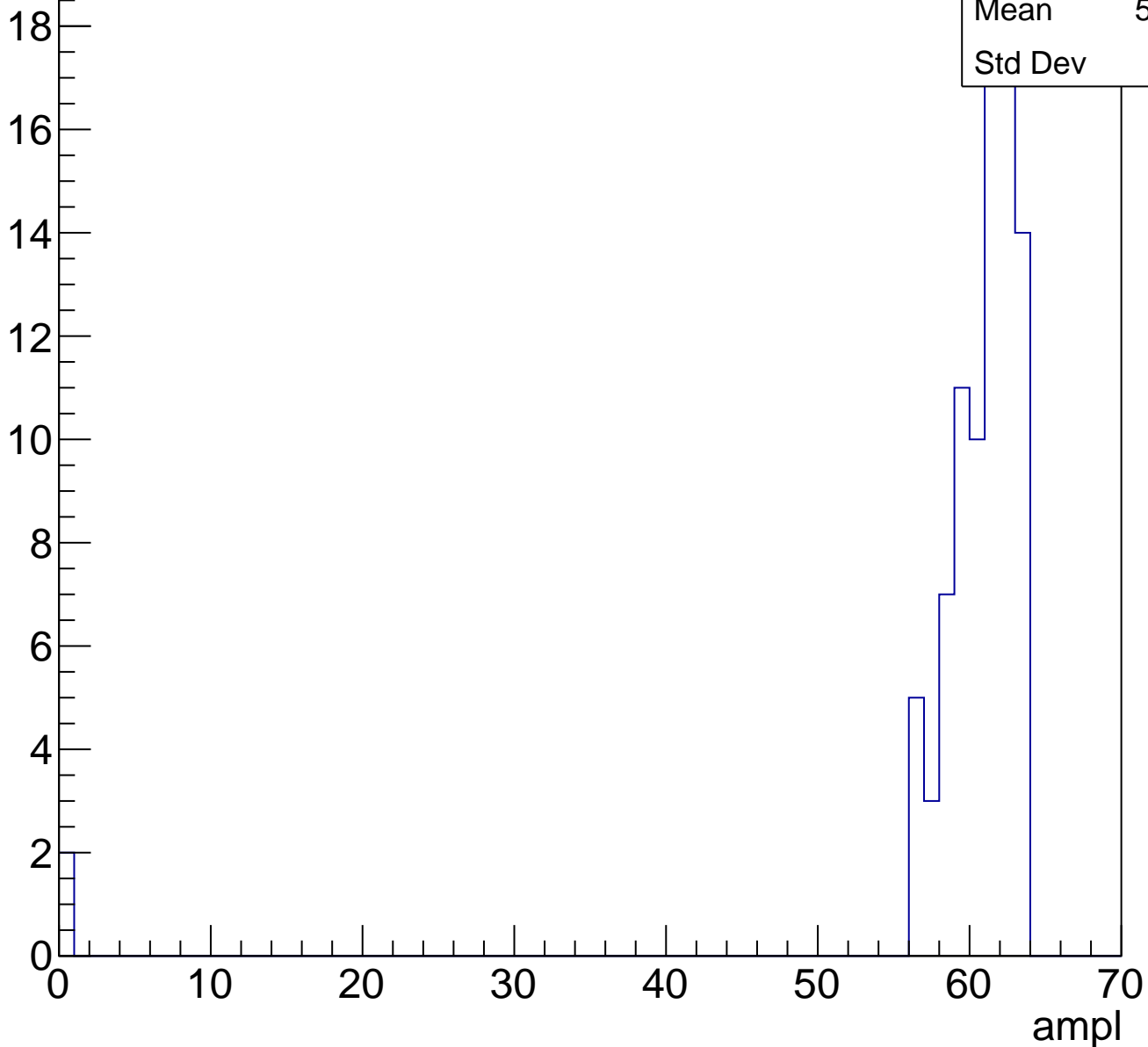
Entries	86
Mean	56.64
Std Dev	2.925



B1L001S, U25-ch17, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch17, adc6

calib_packv5_042523_0143.root, FC#2, port C2

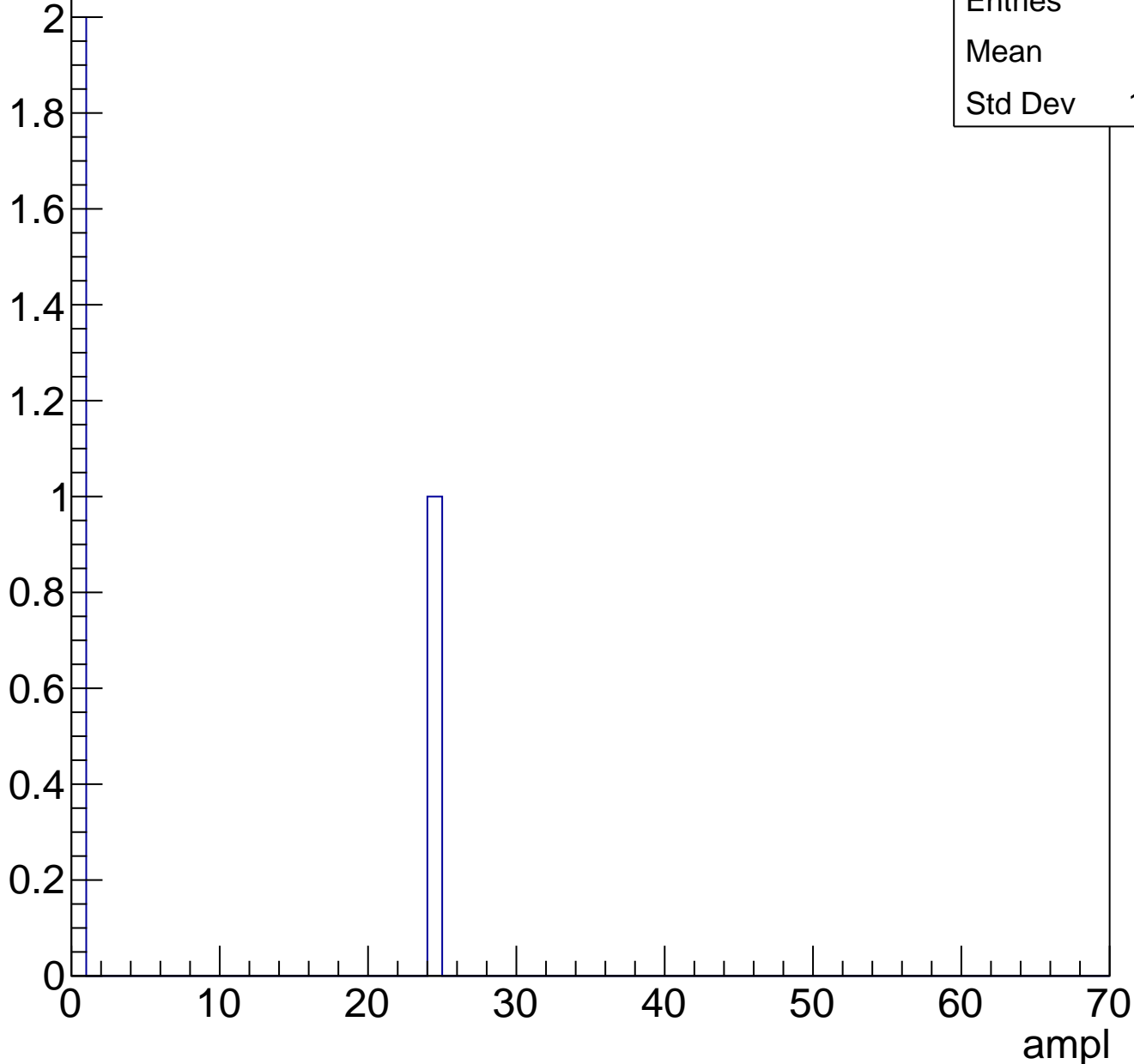
Entry



B1L001S, U25-ch17, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch18, adc0

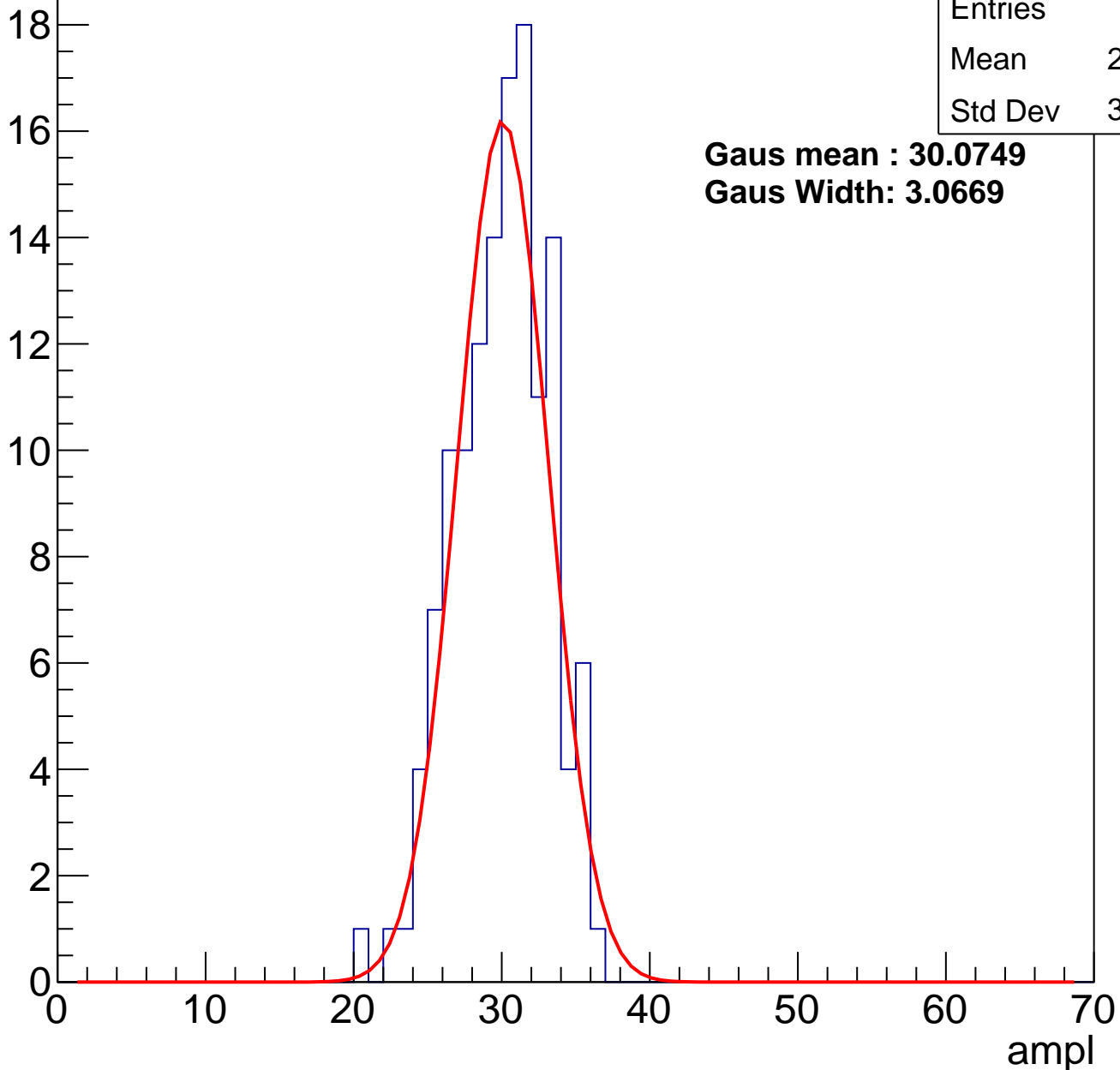
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	29.56
Std Dev	3.097

Gaus mean : 30.0749

Gaus Width: 3.0669

Entry



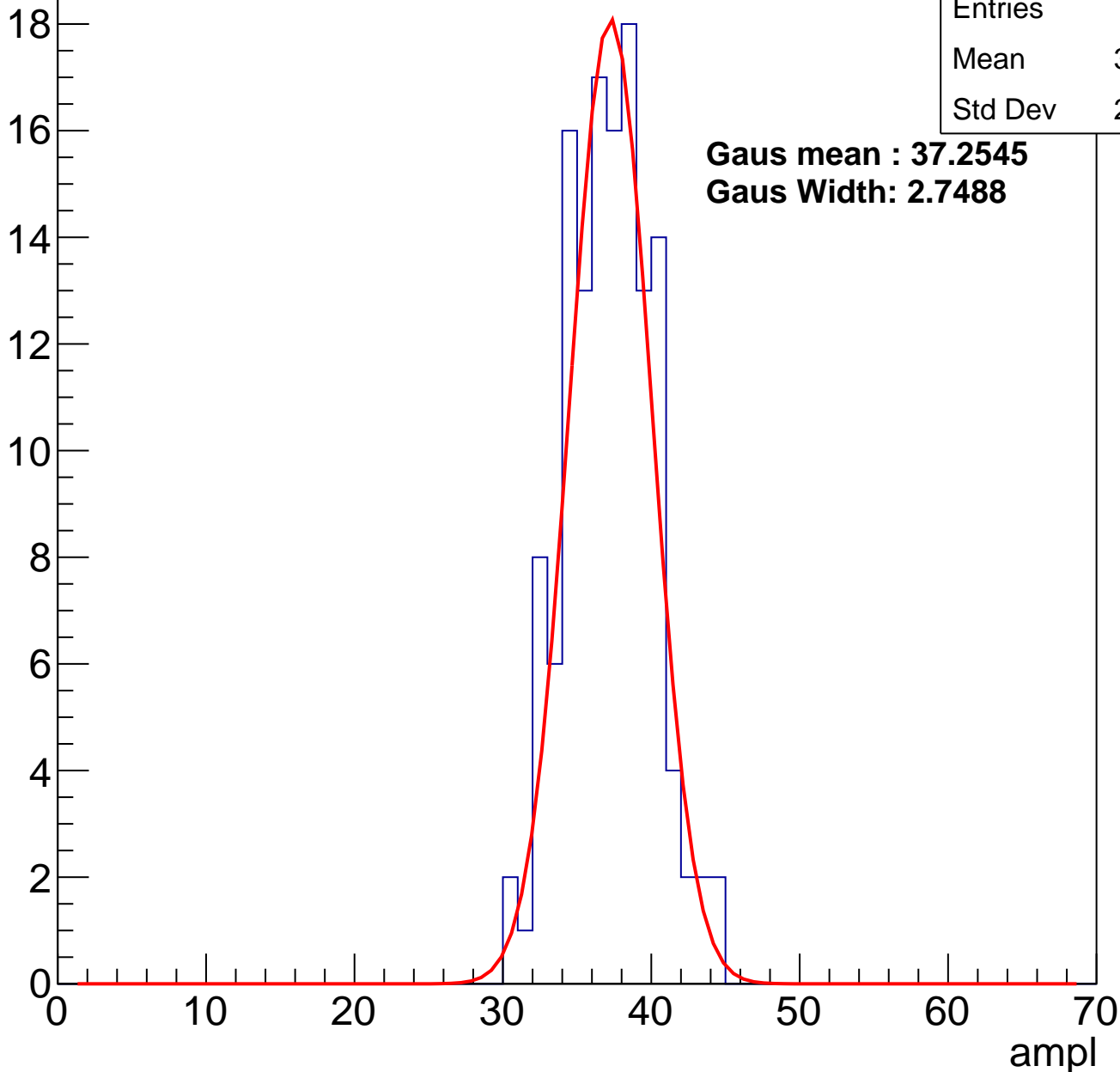
B1L001S, U25-ch18, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	36.72
Std Dev	2.882

Gaus mean : 37.2545
Gaus Width: 2.7488

Entry



B1L001S, U25-ch18, adc2

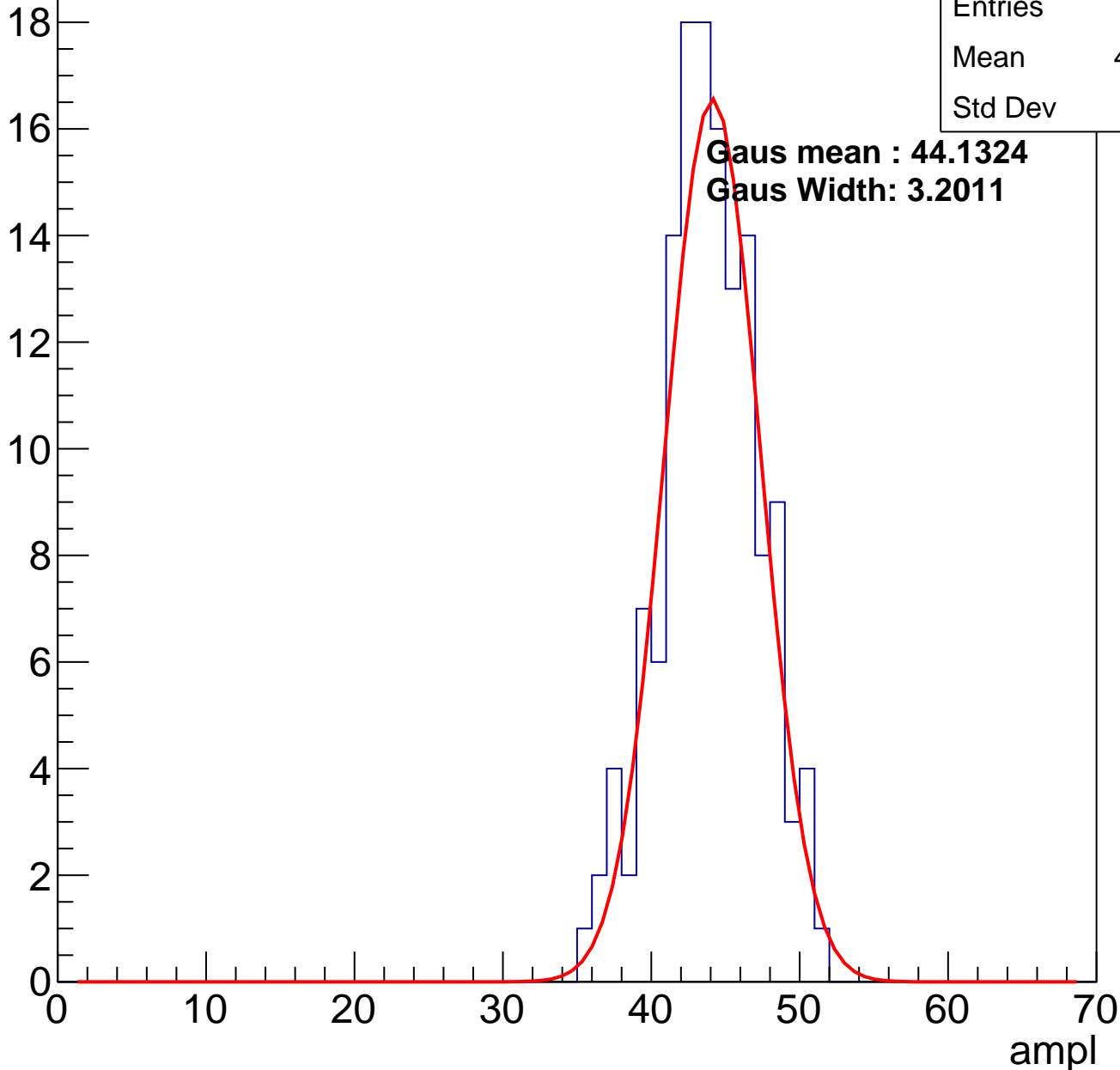
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	43.48
Std Dev	3.25

Gaus mean : 44.1324

Gaus Width: 3.2011

Entry



B1L001S, U25-ch18, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	50.81
Std Dev	3.598

Entry

16

14

12

10

8

6

4

2

0

0

10

20

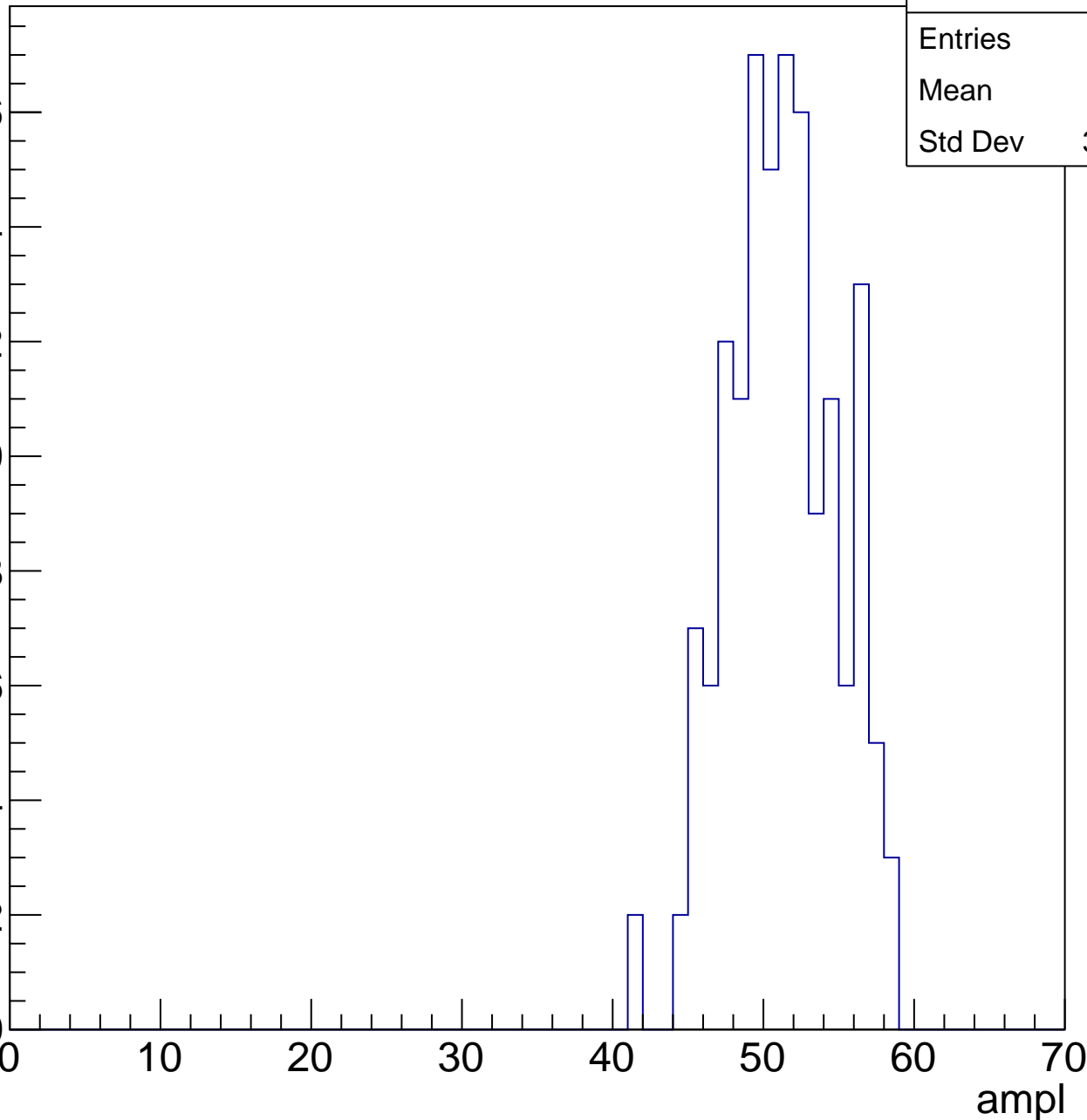
30

40

50

60

ampl

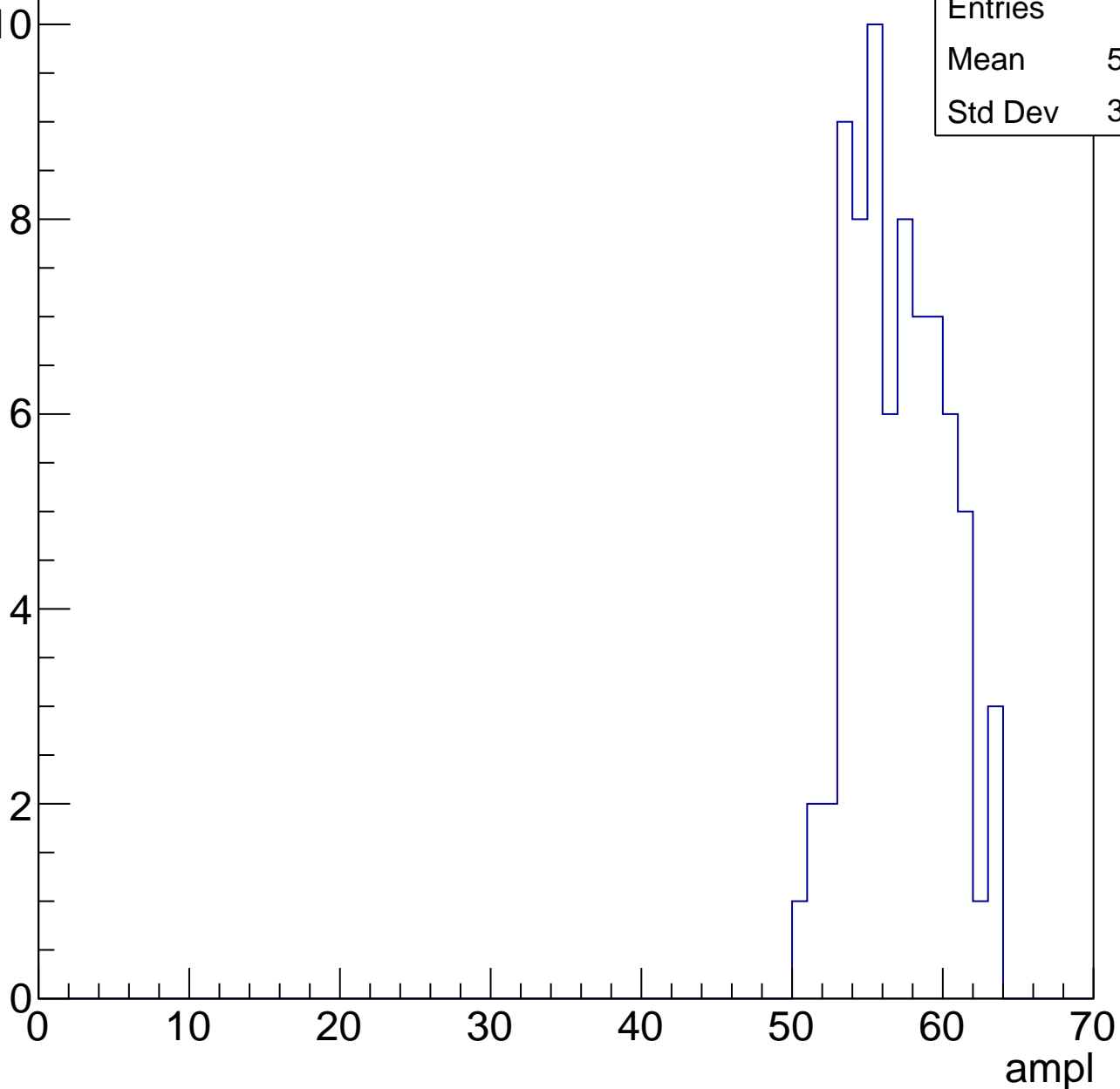


B1L001S, U25-ch18, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	75
Mean	56.56
Std Dev	3.099



B1L001S, U25-ch18, adc5

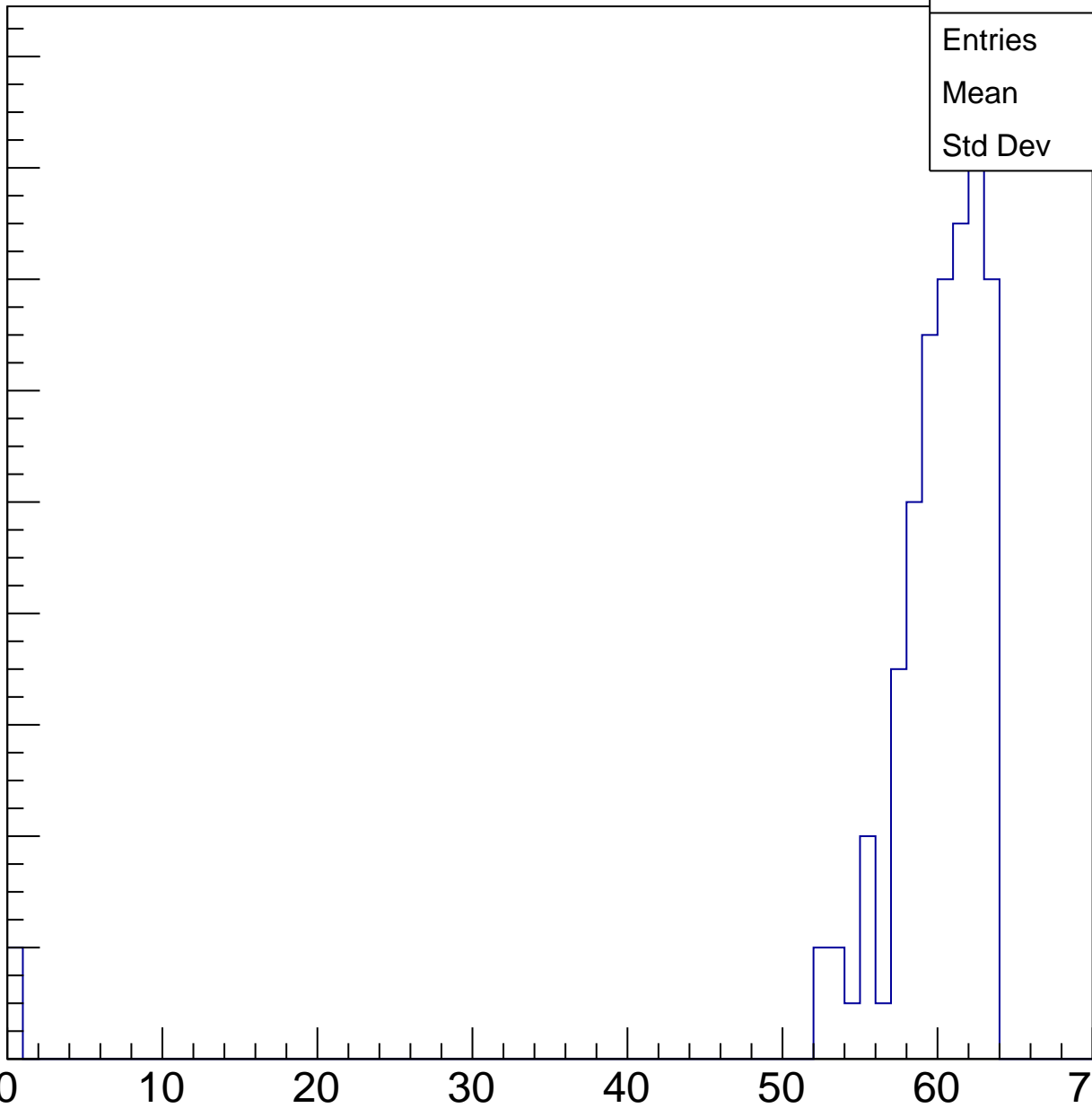
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.63
Std Dev	8.654

ampl



B1L001S, U25-ch18, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch18, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch19, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	32.18
Std Dev	4.529

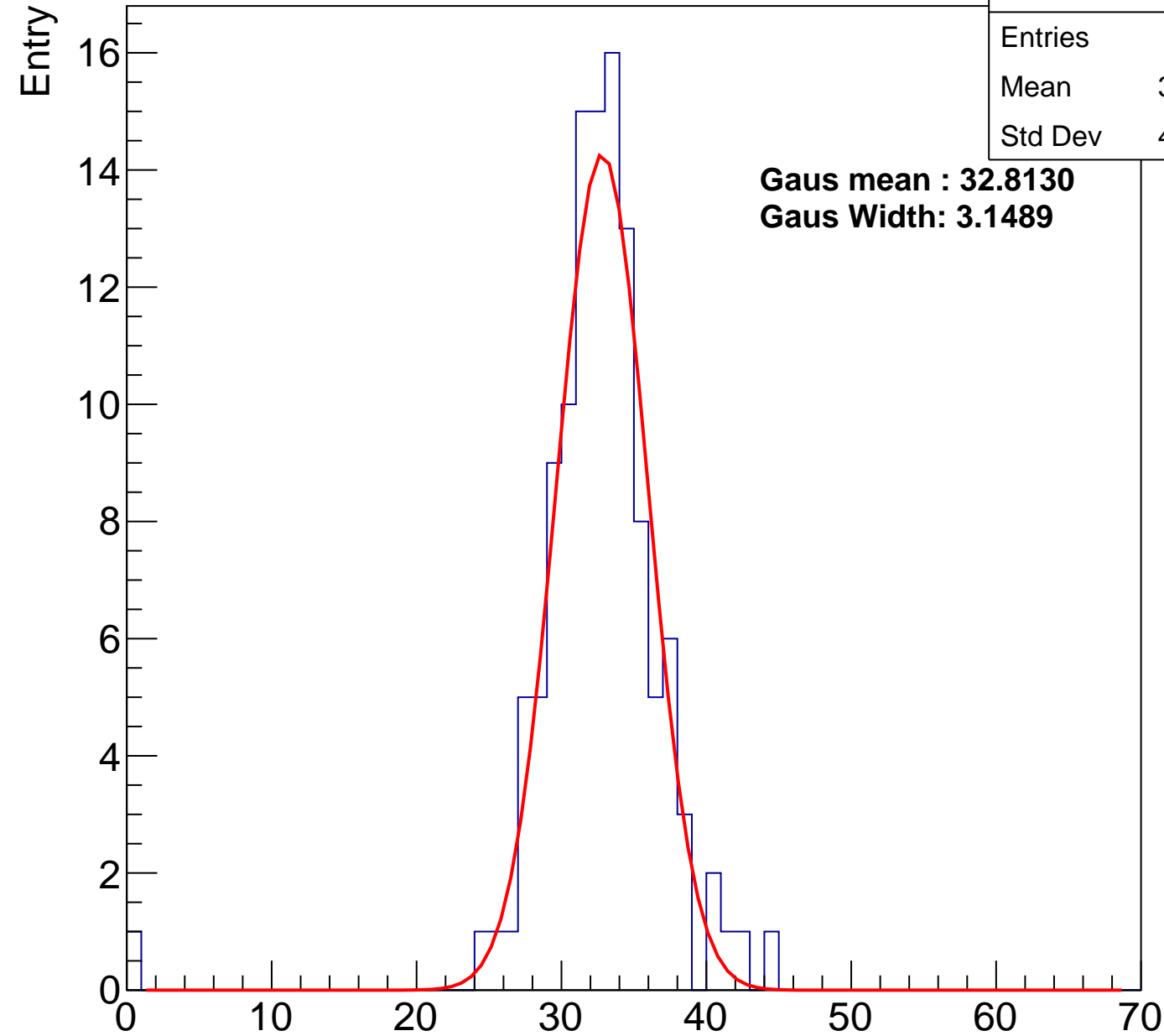
Gaus mean : 32.8130

Gaus Width: 3.1489

Entry

16
14
12
10
8
6
4
2
0

ampl



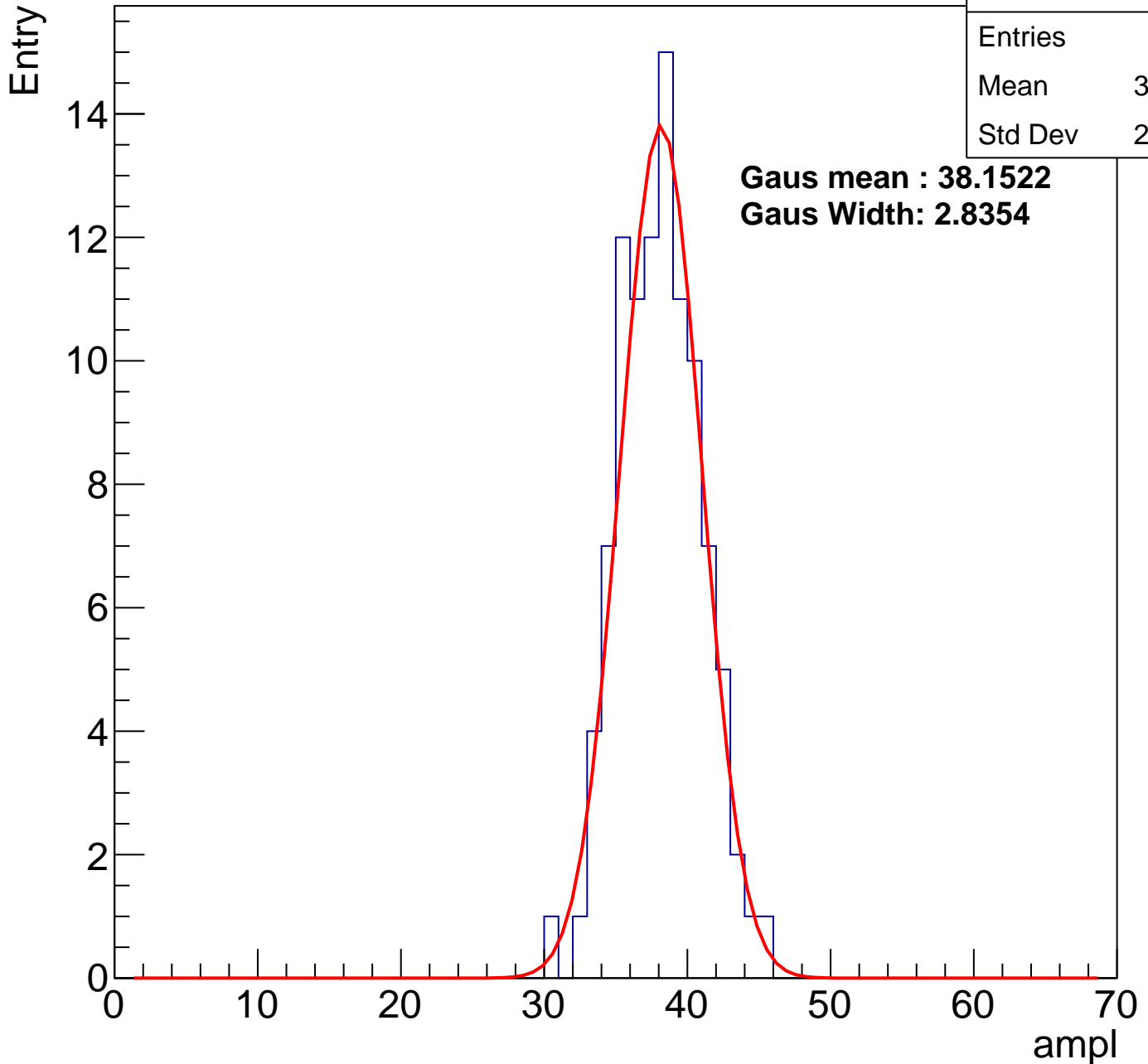
B1L001S, U25-ch19, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	37.63
Std Dev	2.813

Gaus mean : 38.1522

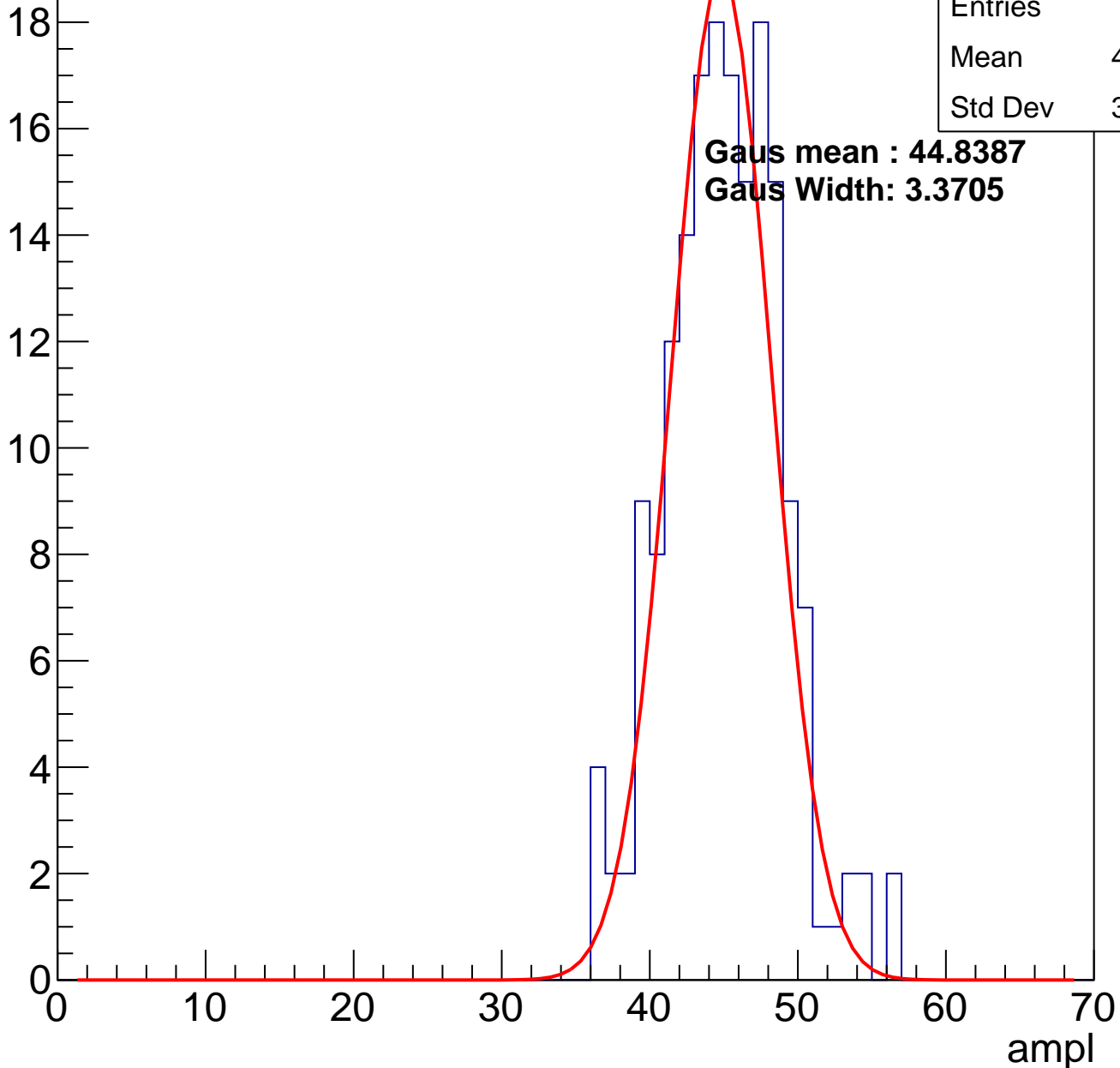
Gaus Width: 2.8354



B1L001S, U25-ch19, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

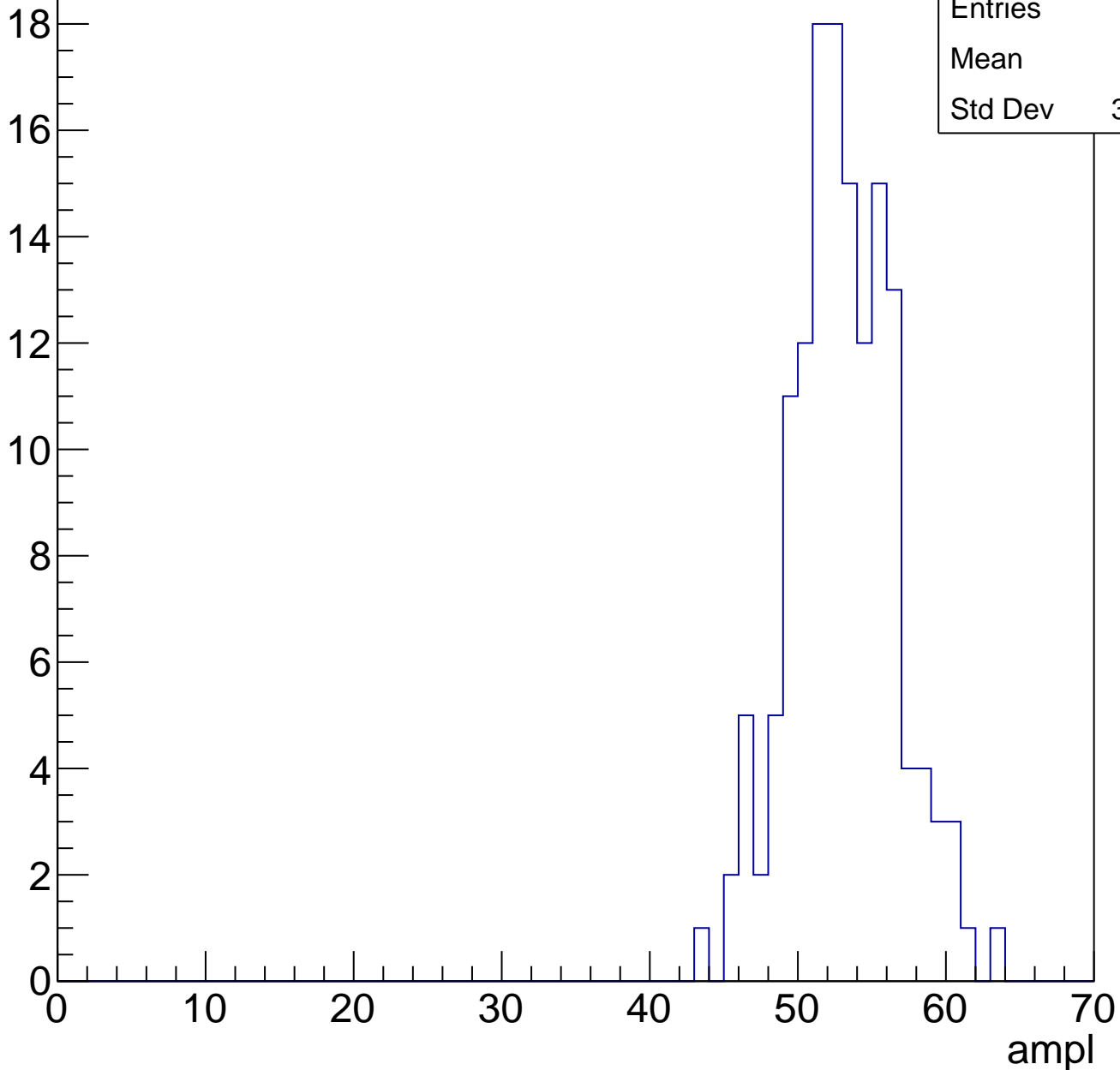


B1L001S, U25-ch19, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

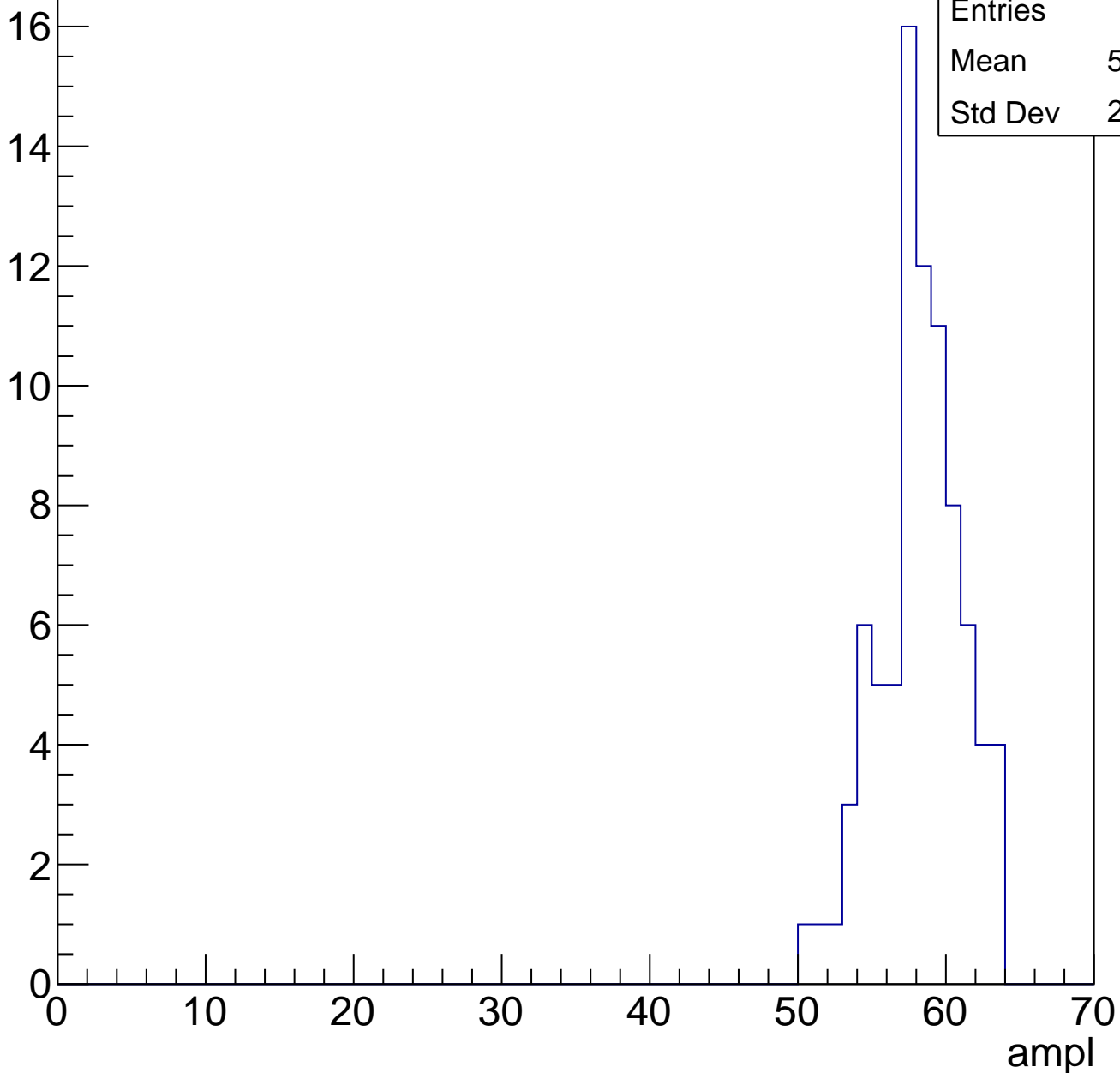
Entries	145
Mean	52.6
Std Dev	3.516



B1L001S, U25-ch19, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch19, adc5

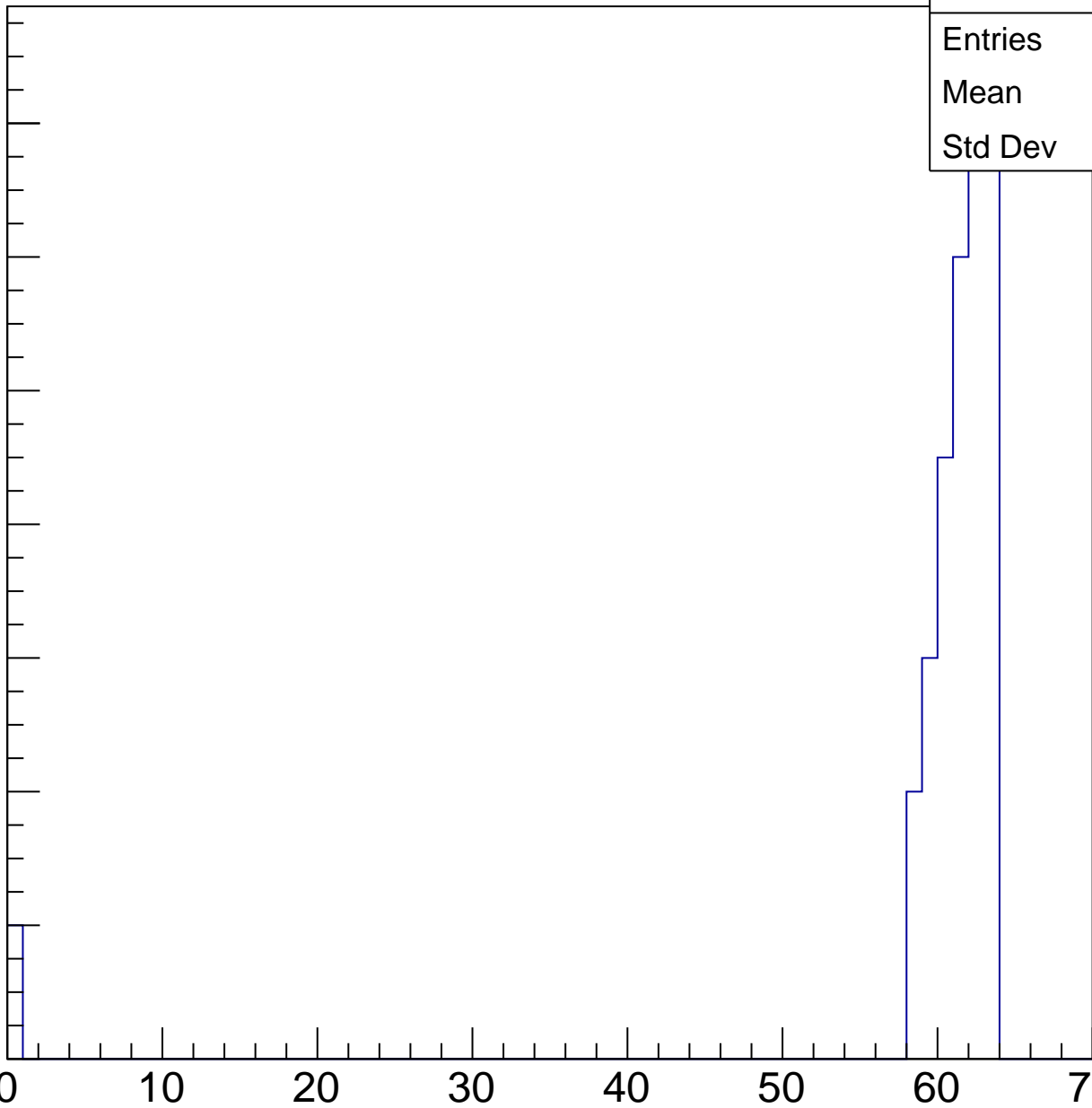
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	62
Mean	59.19
Std Dev	10.91

ampl



B1L001S, U25-ch19, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch19, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch20, adc0

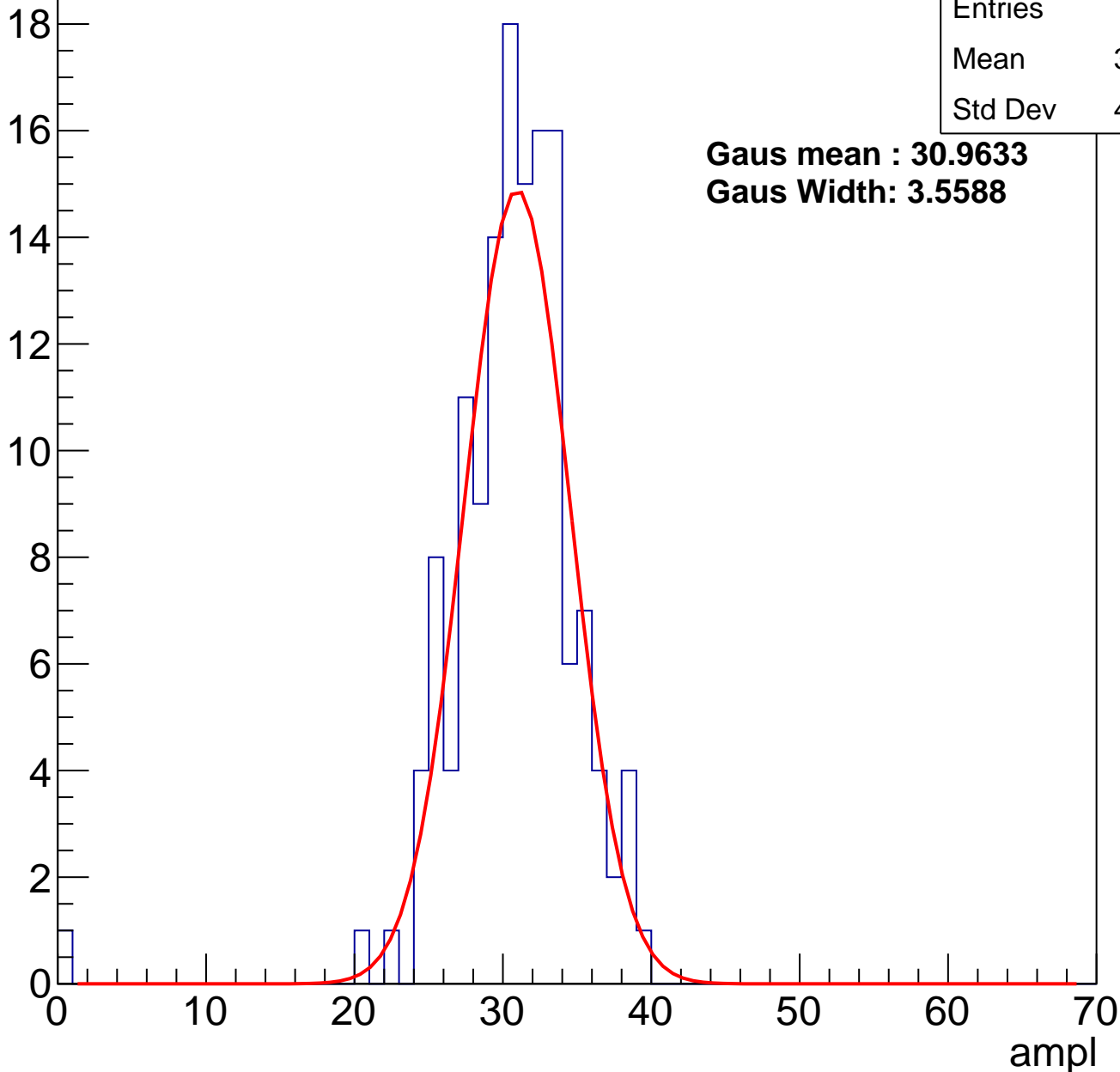
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	30.28
Std Dev	4.347

Gaus mean : 30.9633

Gaus Width: 3.5588

Entry



B1L001S, U25-ch20, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	37.97
Std Dev	3.383

Gaus mean : 38.5912

Gaus Width: 3.5995

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

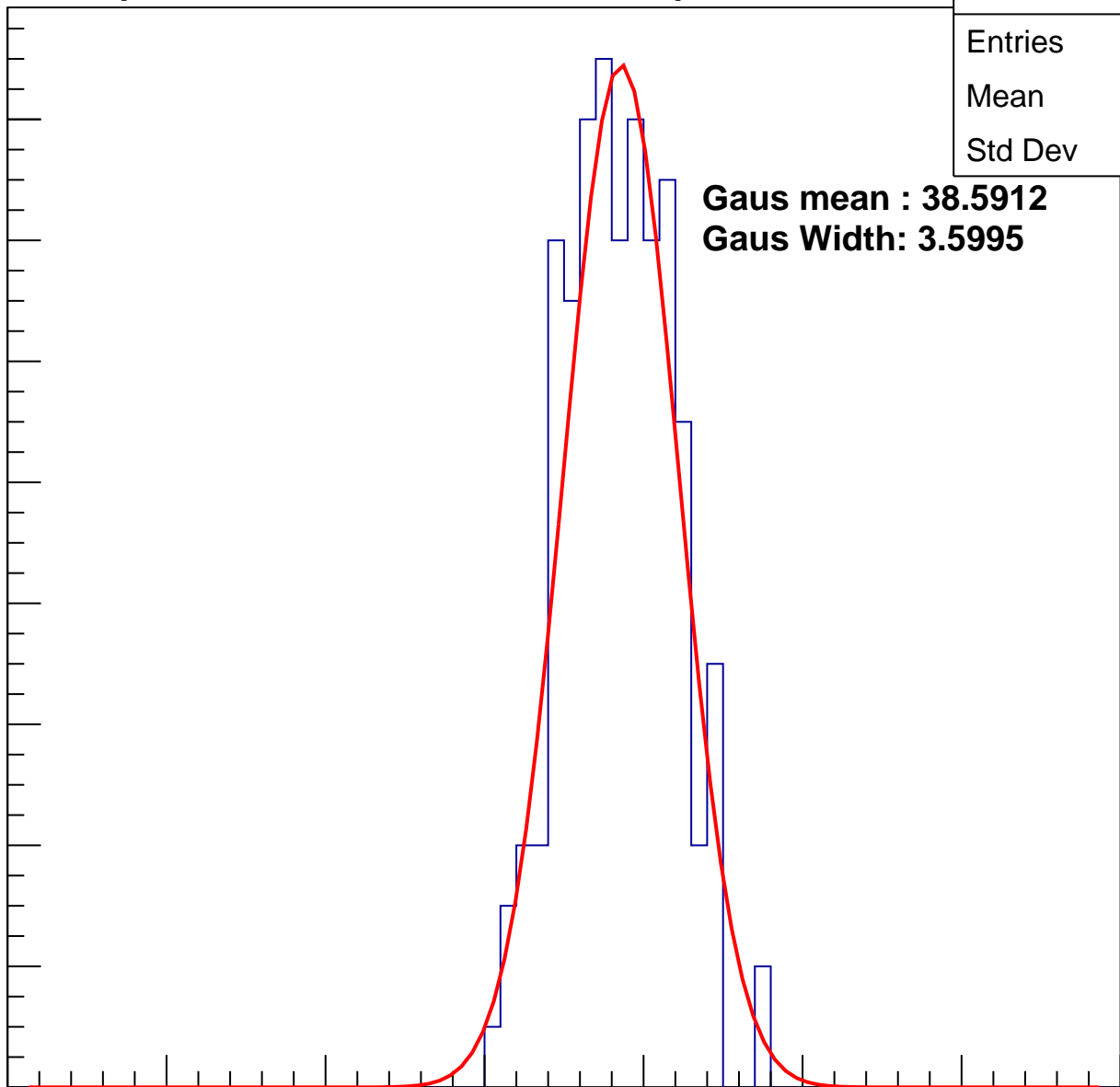
40

50

60

70

ampl



B1L001S, U25-ch20, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	44.82
Std Dev	3.298

Gaus mean : 45.2640

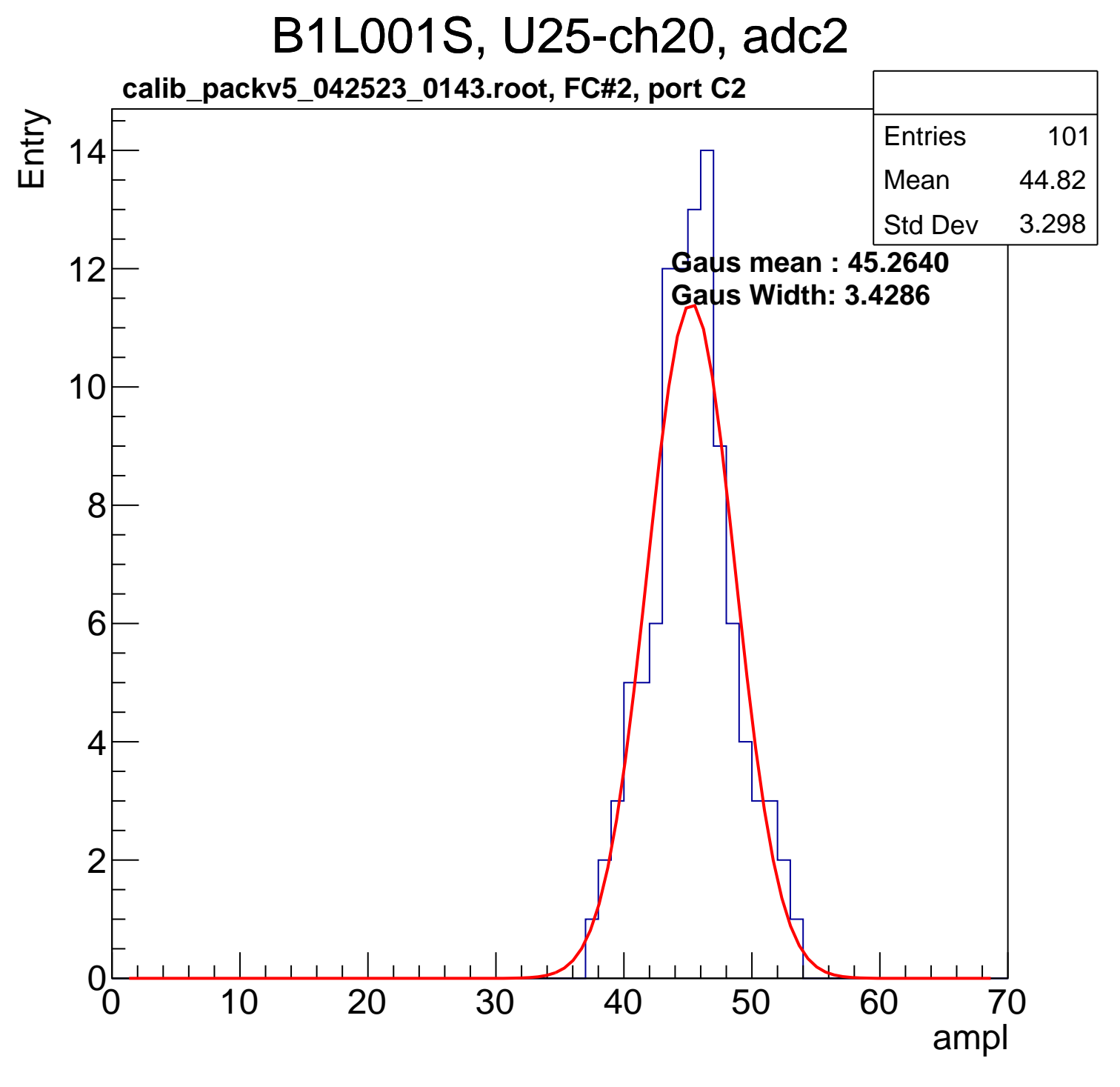
Gaus Width: 3.4286

Entry

14
12
10
8
6
4
2
0

ampl

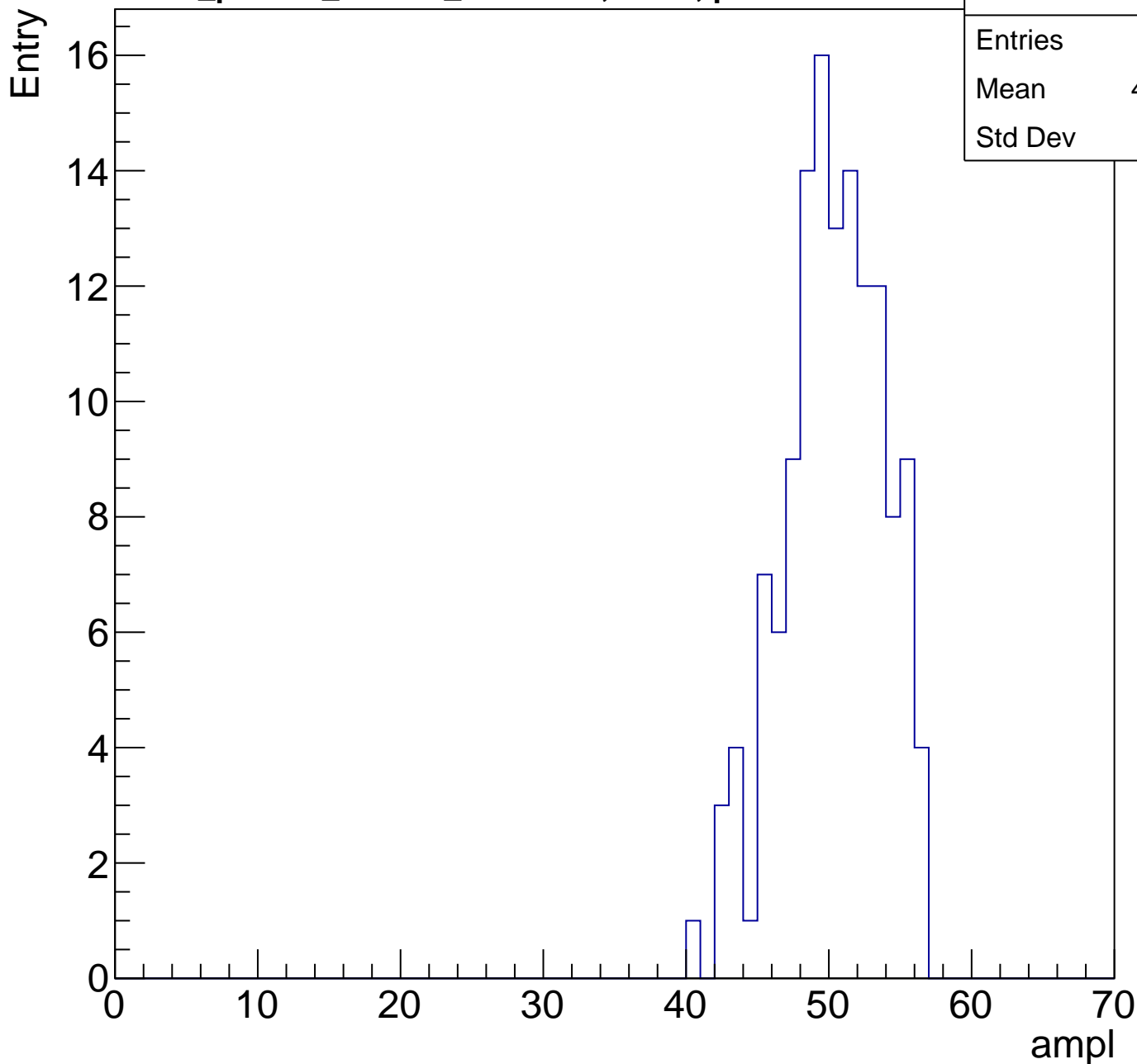
0 10 20 30 40 50 60 70



B1L001S, U25-ch20, adc3

calib_packv5_042523_0143.root, FC#2, port C2

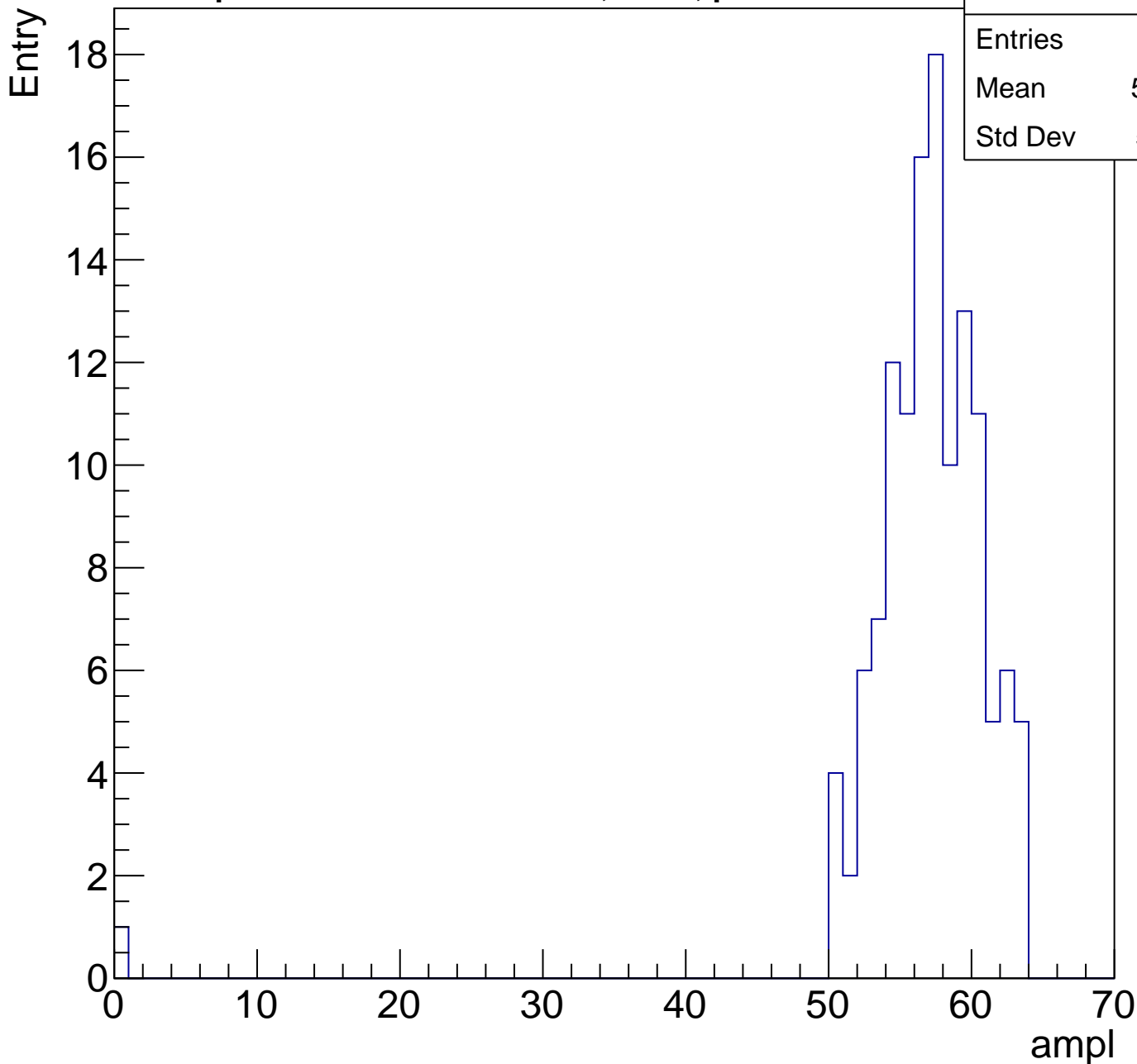
Entries	133
Mean	49.83
Std Dev	3.48



B1L001S, U25-ch20, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	56.37
Std Dev	5.931



B1L001S, U25-ch20, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	66
Mean	59.52
Std Dev	7.678

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch20, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch20, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch21, adc0

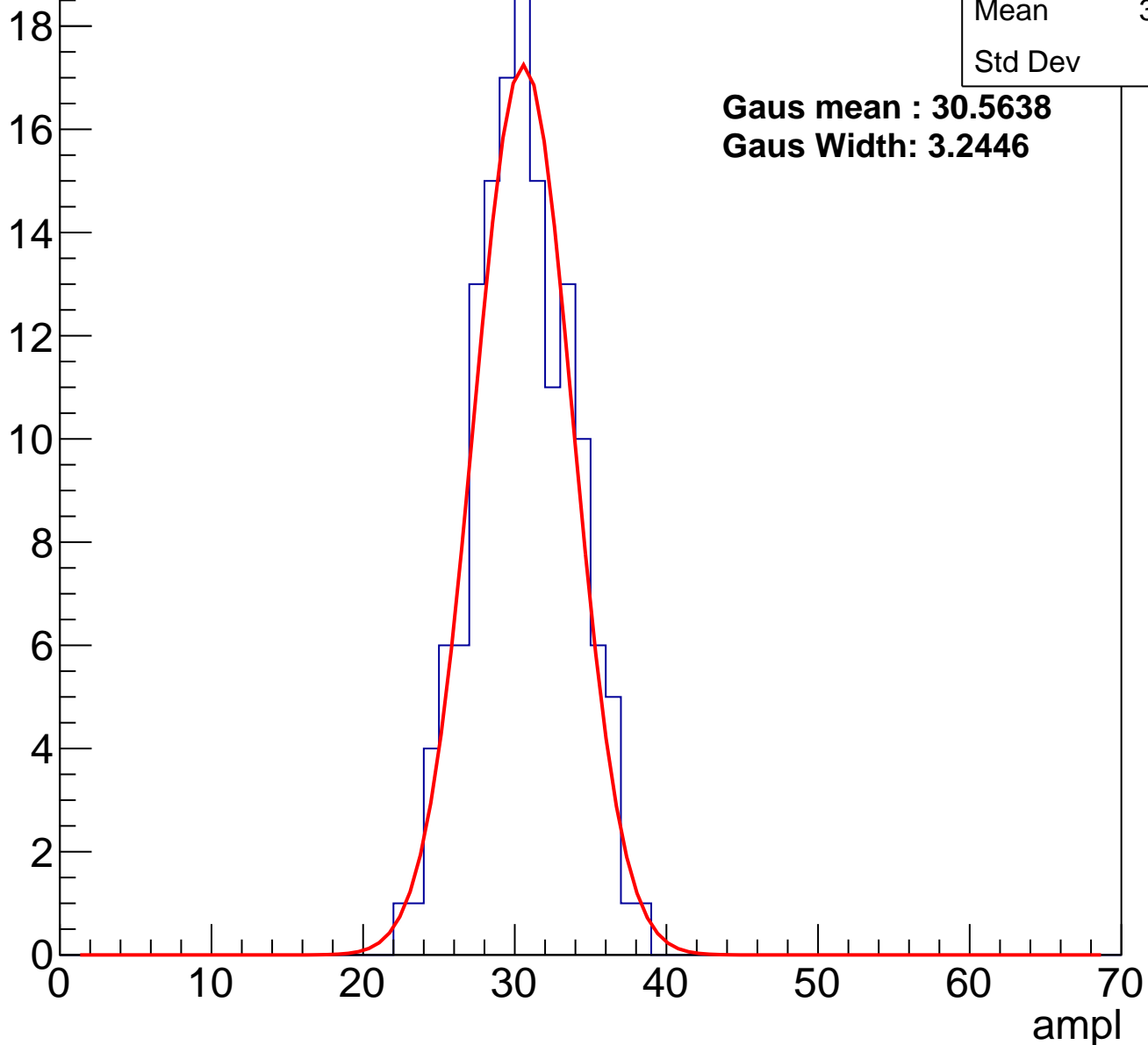
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	30.08
Std Dev	3.2

Gaus mean : 30.5638

Gaus Width: 3.2446

Entry



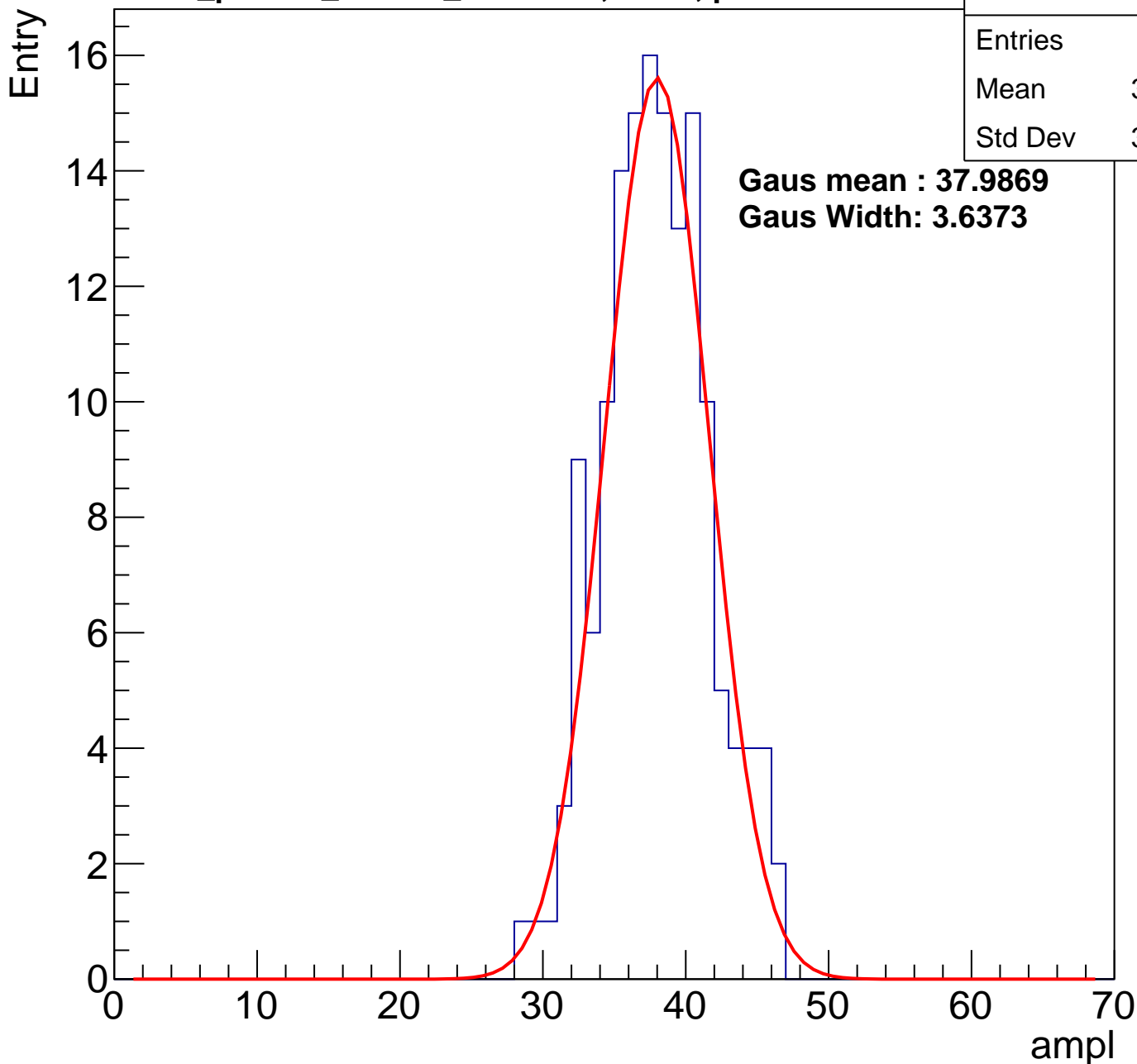
B1L001S, U25-ch21, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	37.47
Std Dev	3.673

Gaus mean : 37.9869

Gaus Width: 3.6373



B1L001S, U25-ch21, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	44.75
Std Dev	3.637

Gaus mean : 45.0681

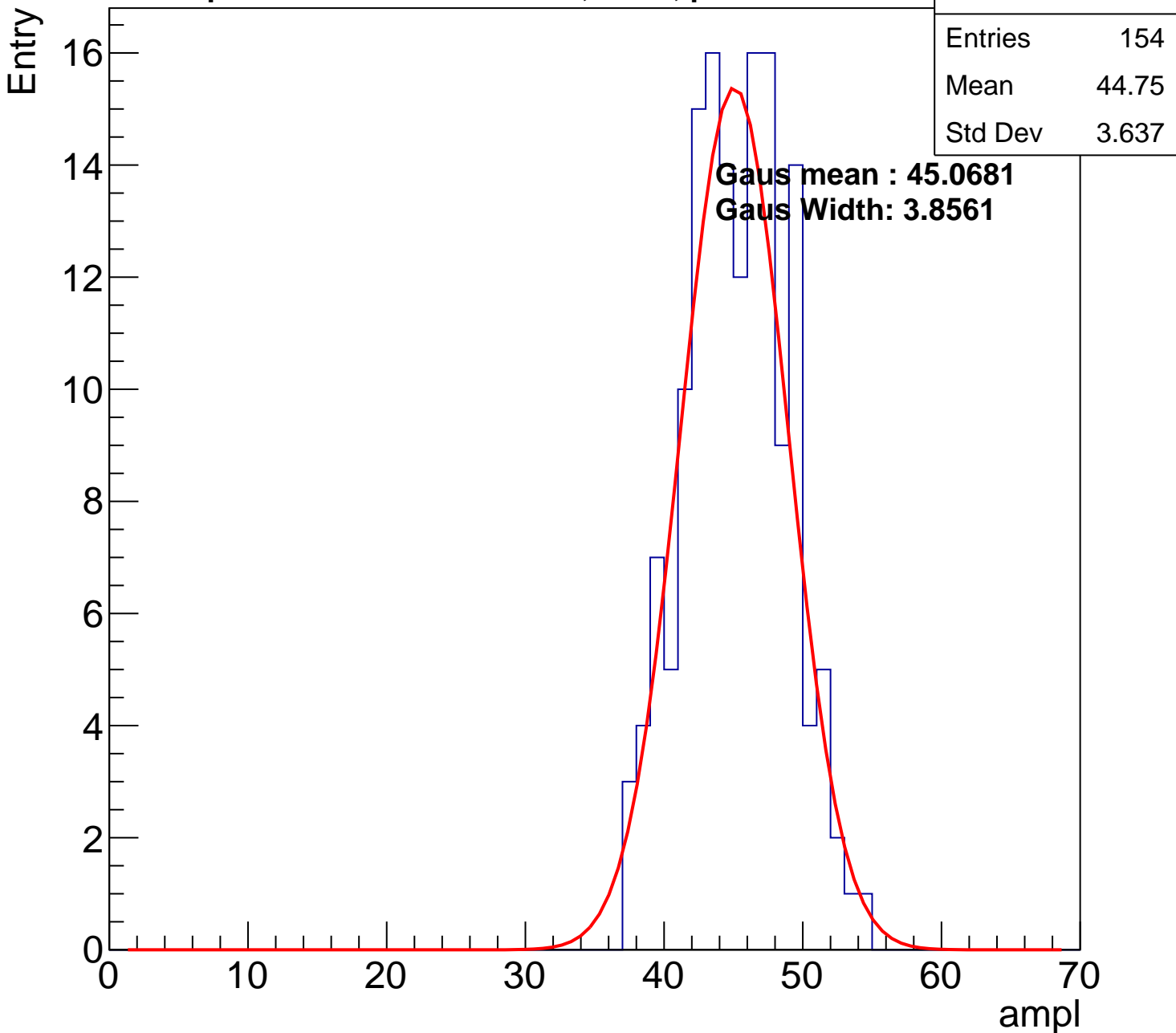
Gaus Width: 3.8561

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch21, adc3

calib_packv5_042523_0143.root, FC#2, port C2

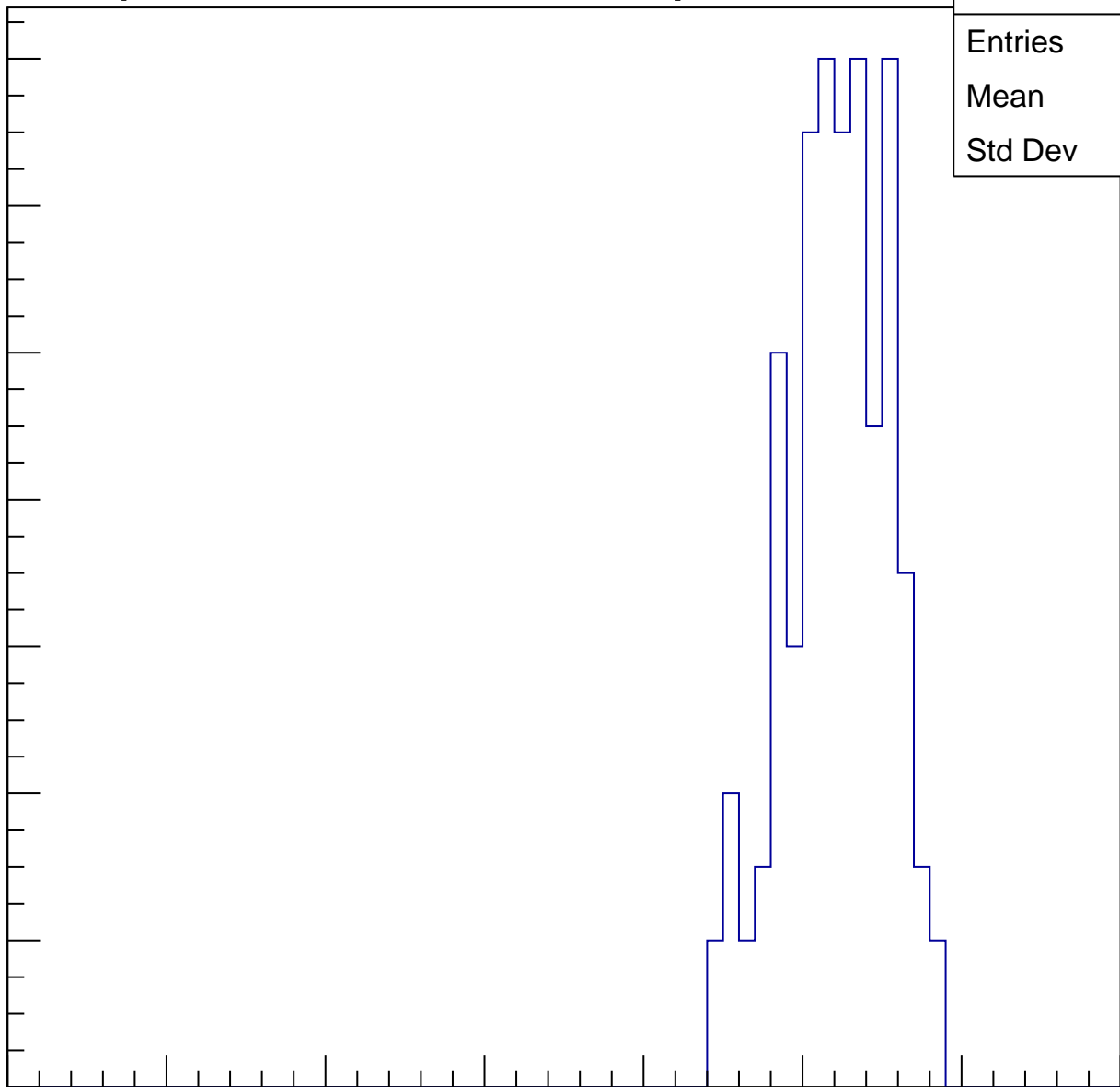
Entries	116
Mean	51.66
Std Dev	3.179

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch21, adc4

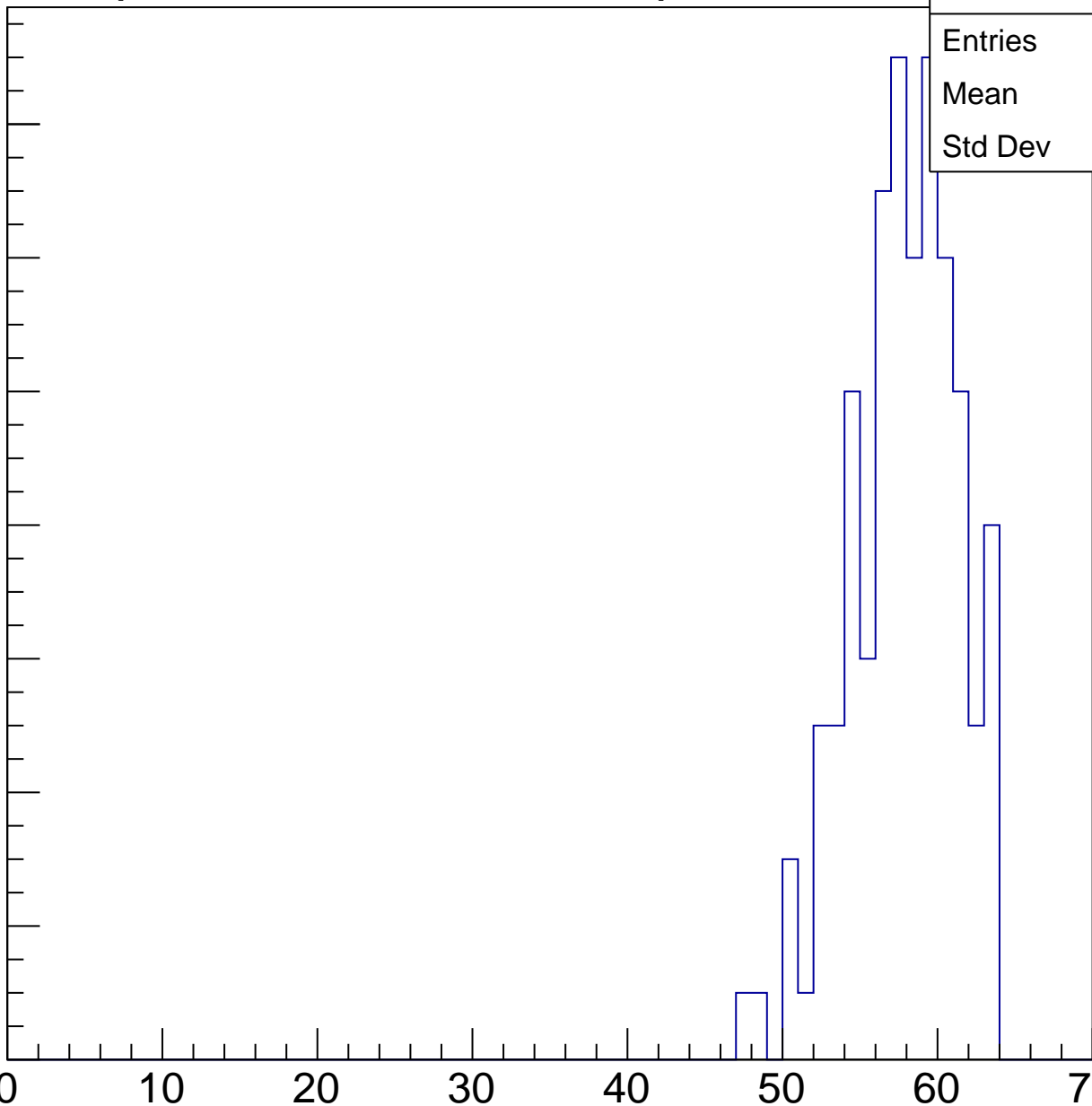
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	57.37
Std Dev	3.427

ampl



B1L001S, U25-ch21, adc5

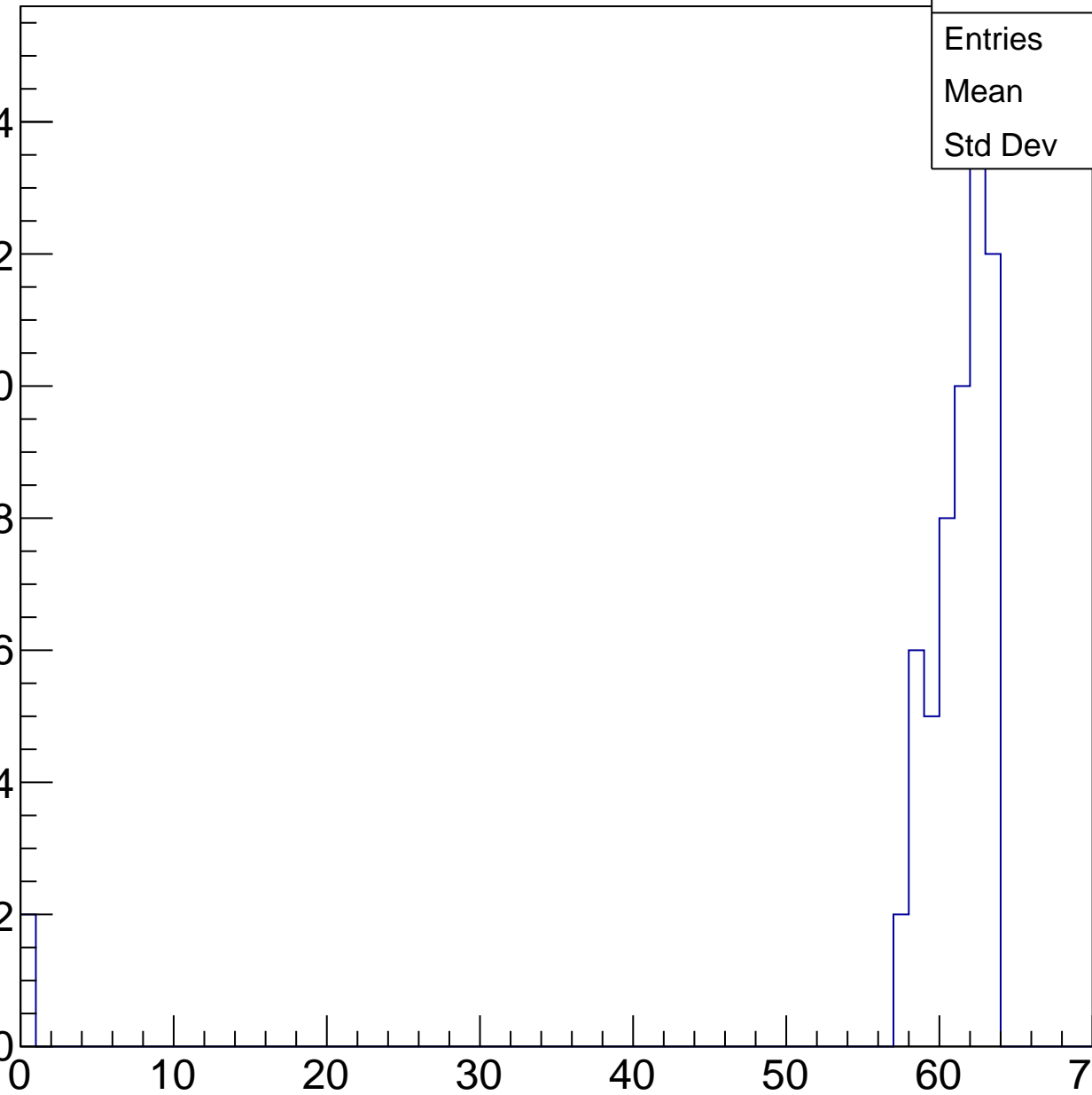
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	60
Mean	58.88
Std Dev	11.07

ampl



B1L001S, U25-ch21, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch21, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch22, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	30.75
Std Dev	3.357

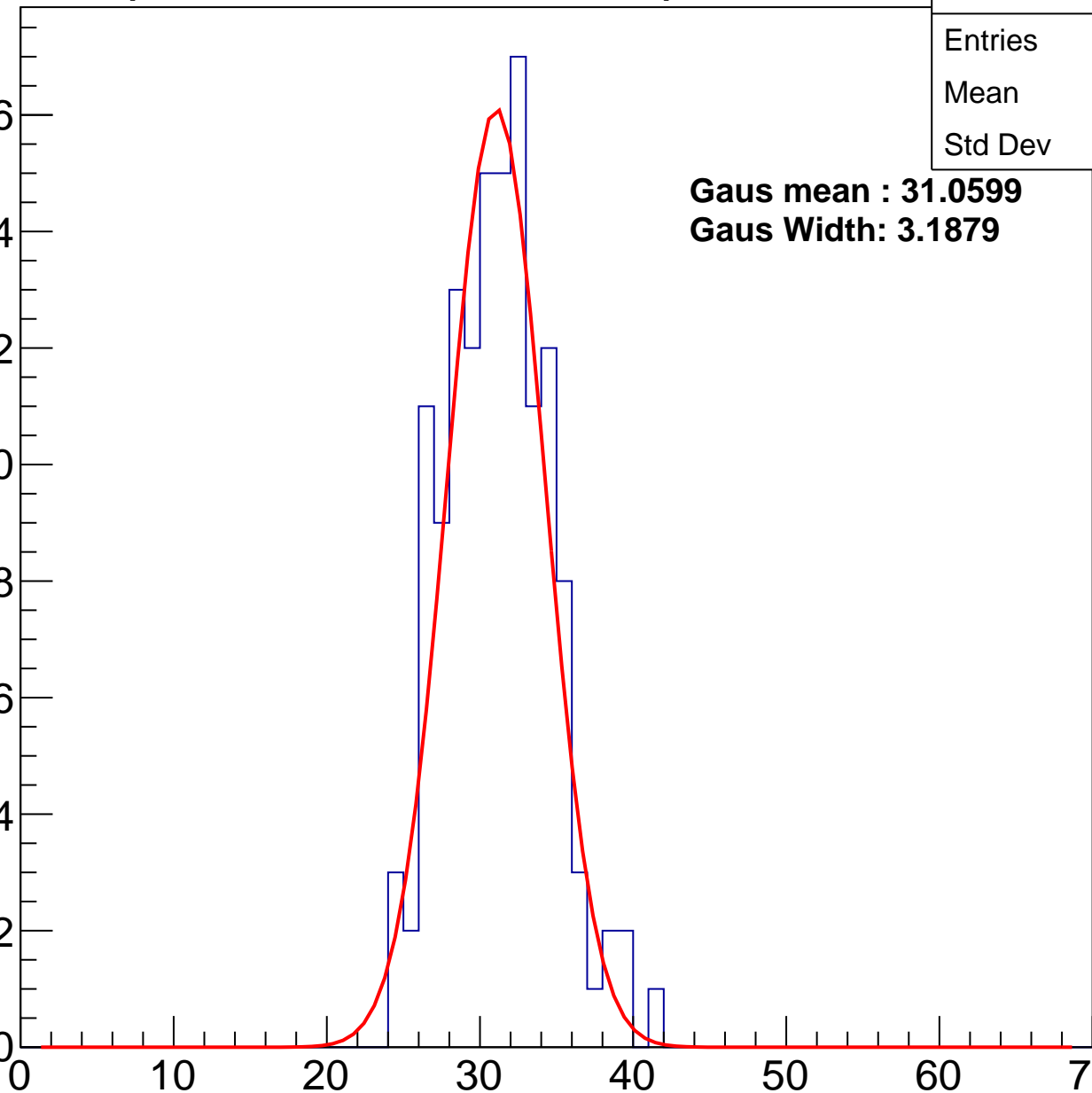
Gaus mean : 31.0599

Gaus Width: 3.1879

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch22, adc1

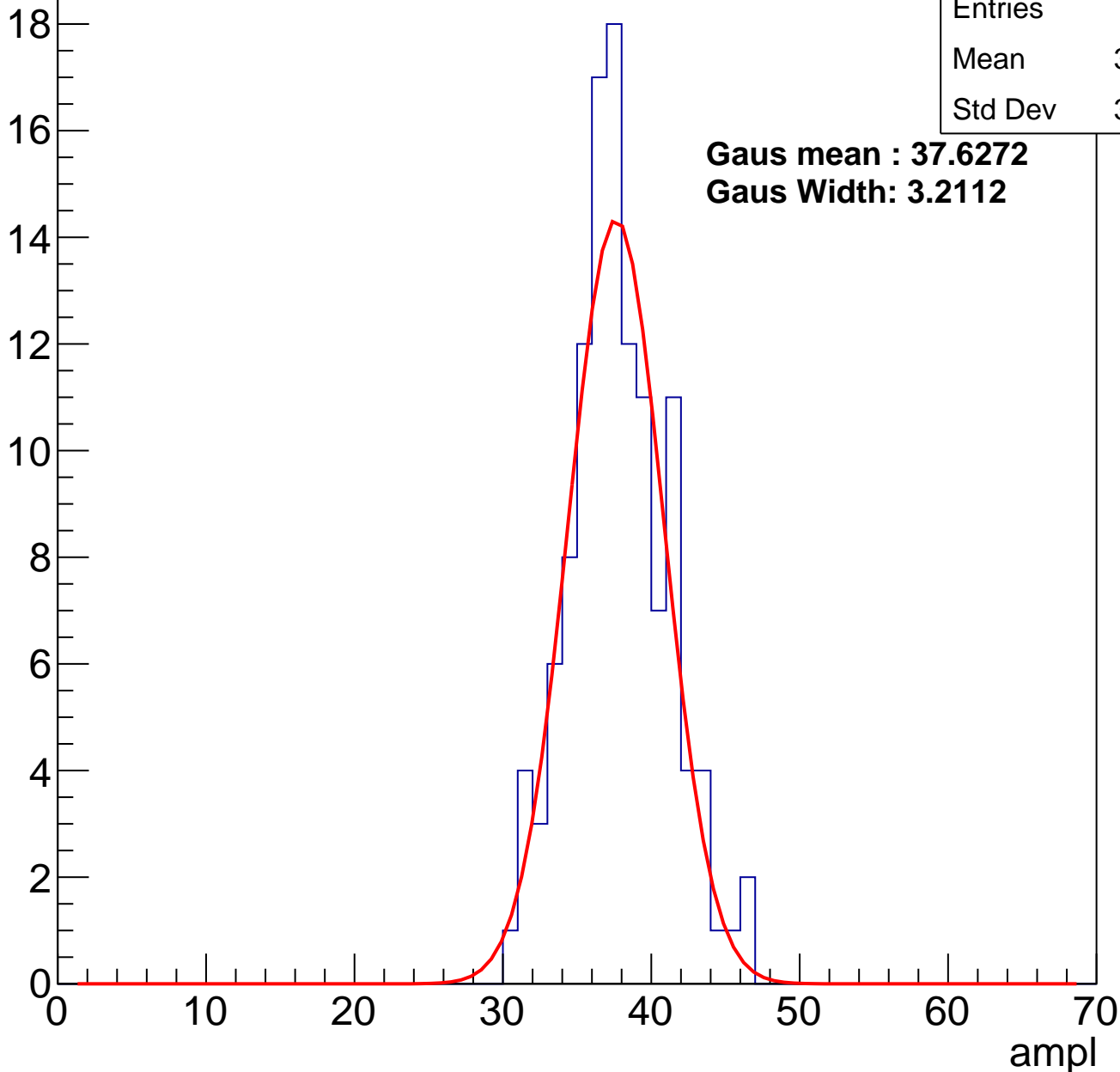
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	37.34
Std Dev	3.266

Gaus mean : 37.6272

Gaus Width: 3.2112

Entry



B1L001S, U25-ch22, adc2

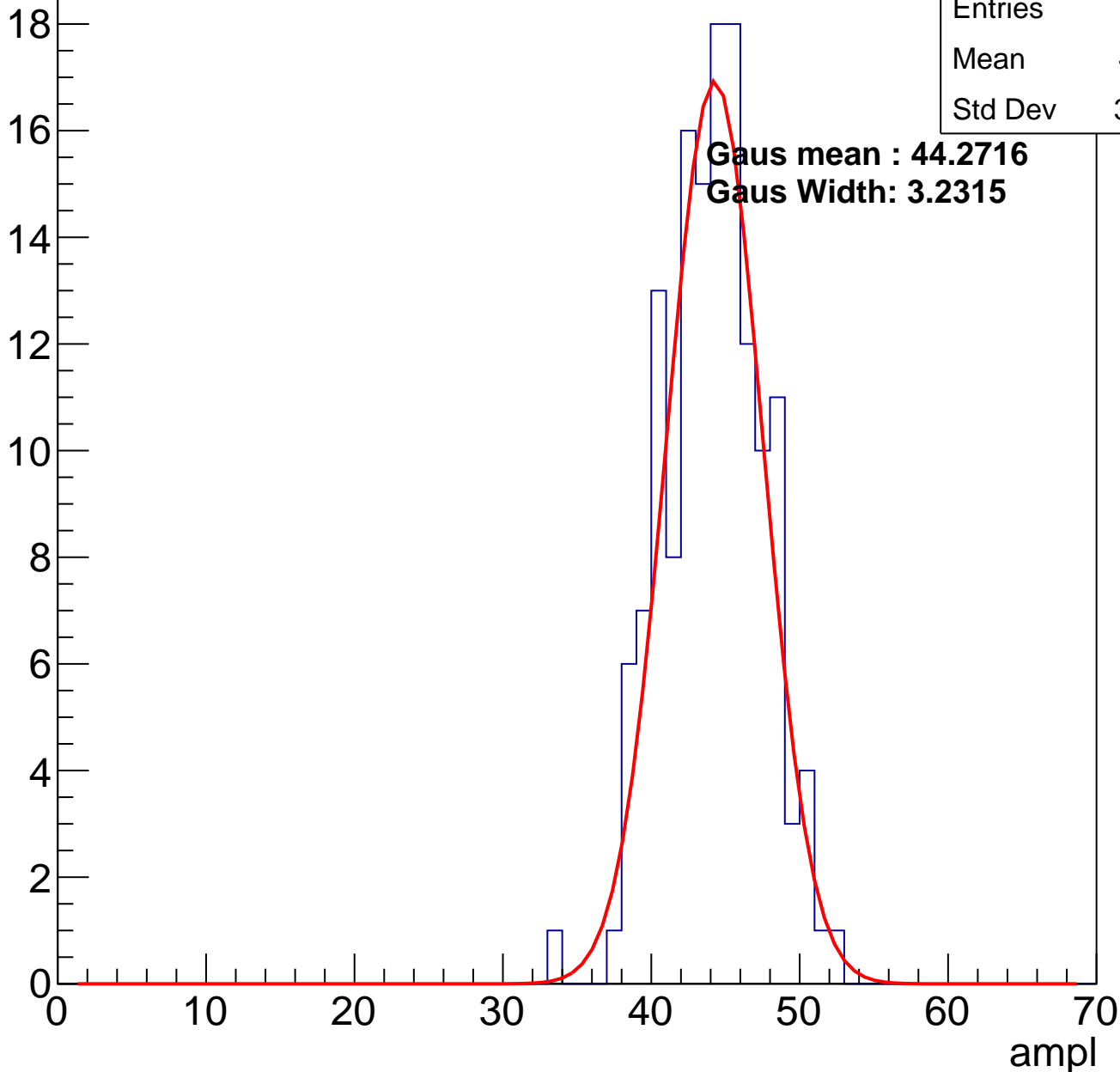
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	43.71
Std Dev	3.292

Gaus mean : 44.2716

Gaus Width: 3.2315

Entry



B1L001S, U25-ch22, adc3

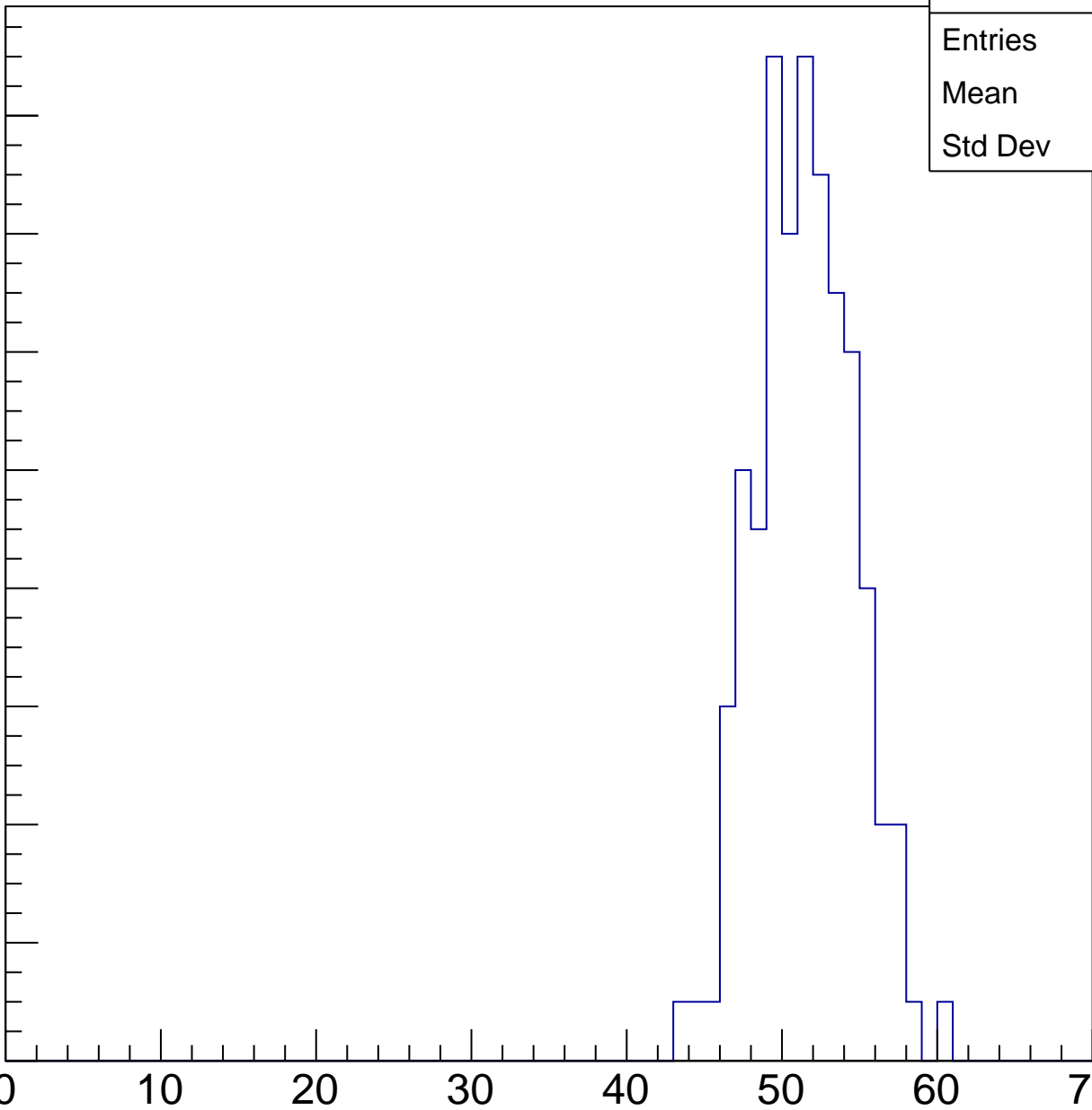
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	134
Mean	51.02
Std Dev	3.113

ampl

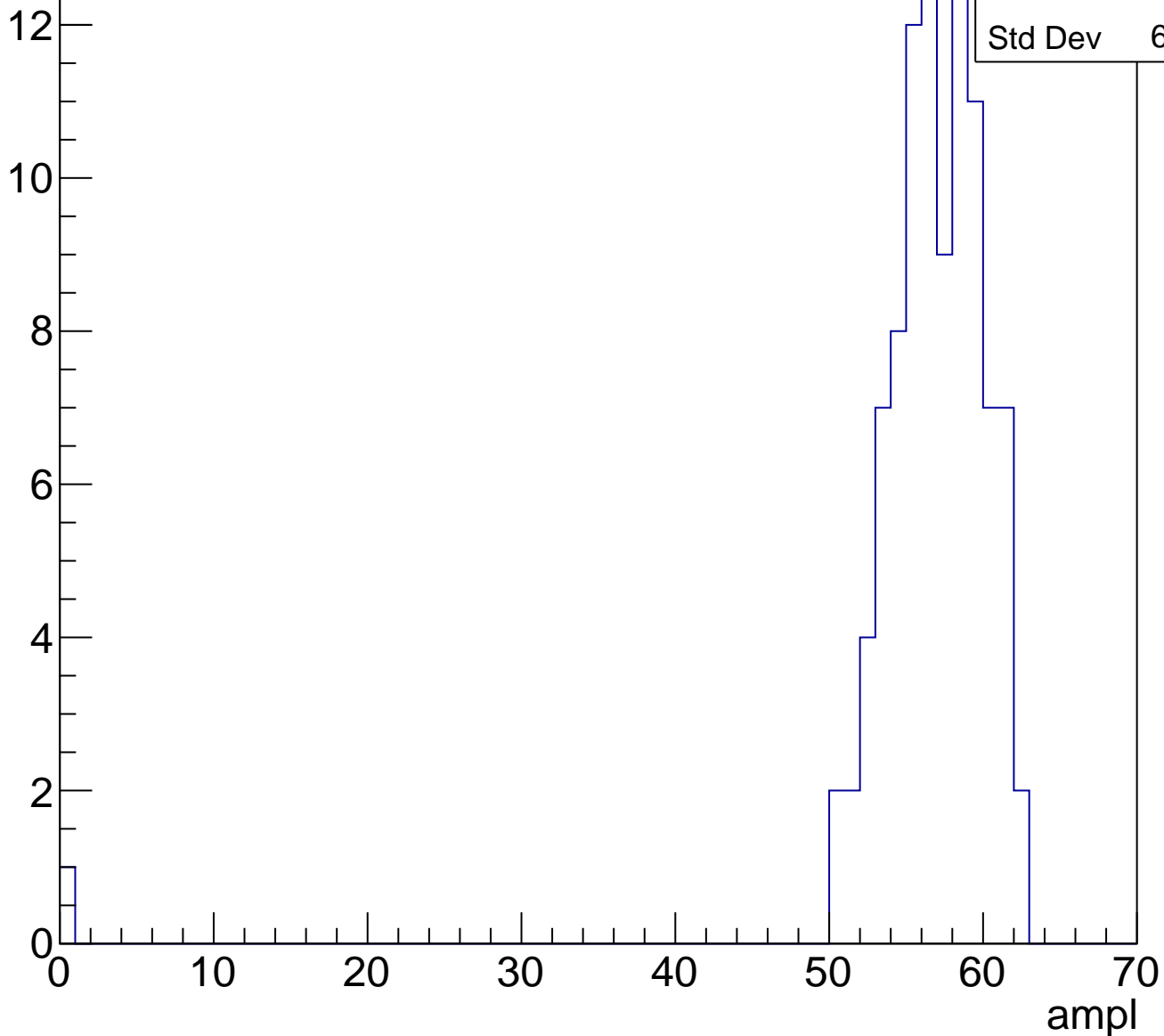


B1L001S, U25-ch22, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	56
Std Dev	6.347

Entry



B1L001S, U25-ch22, adc5

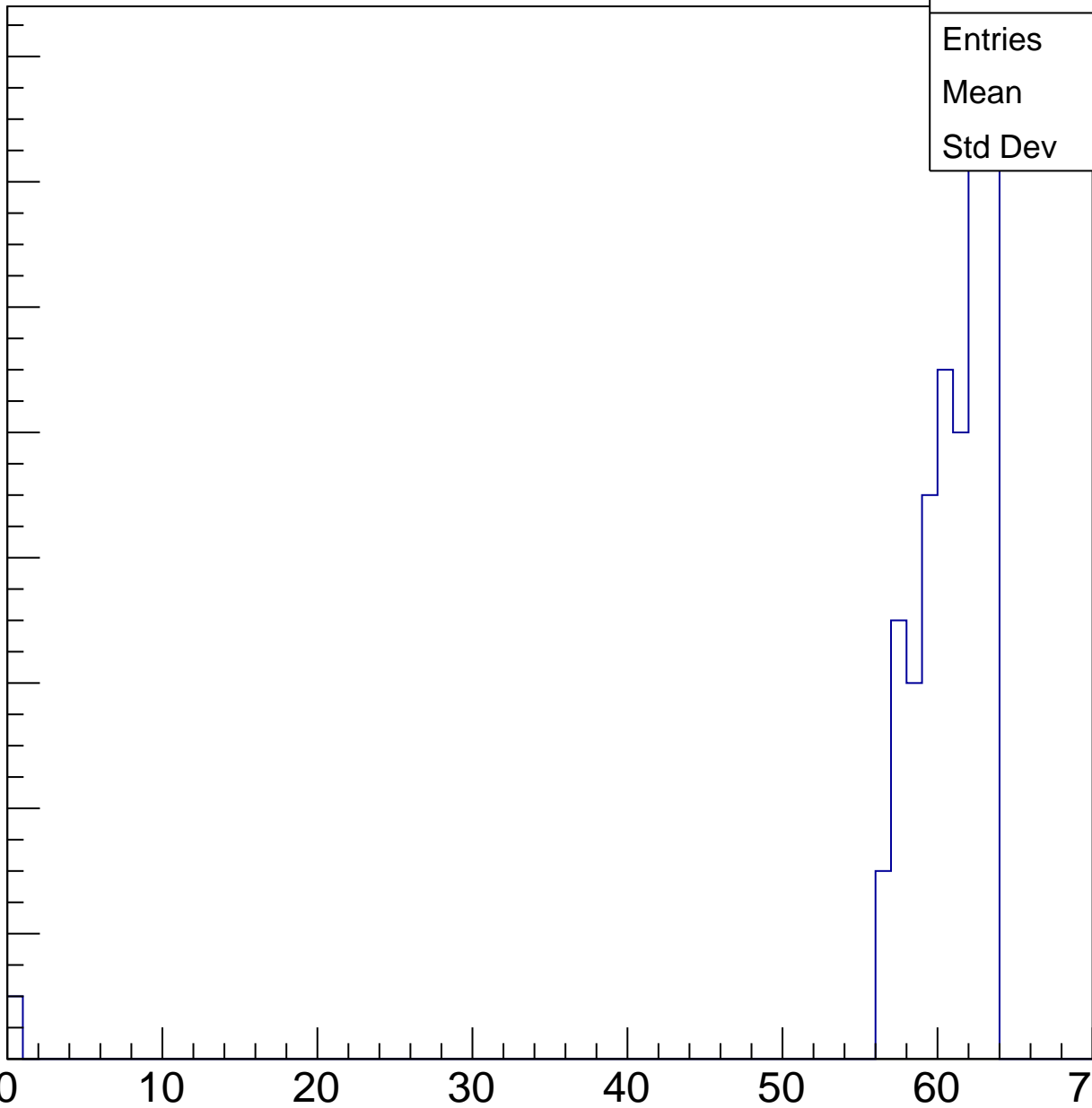
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.67
Std Dev	7.116

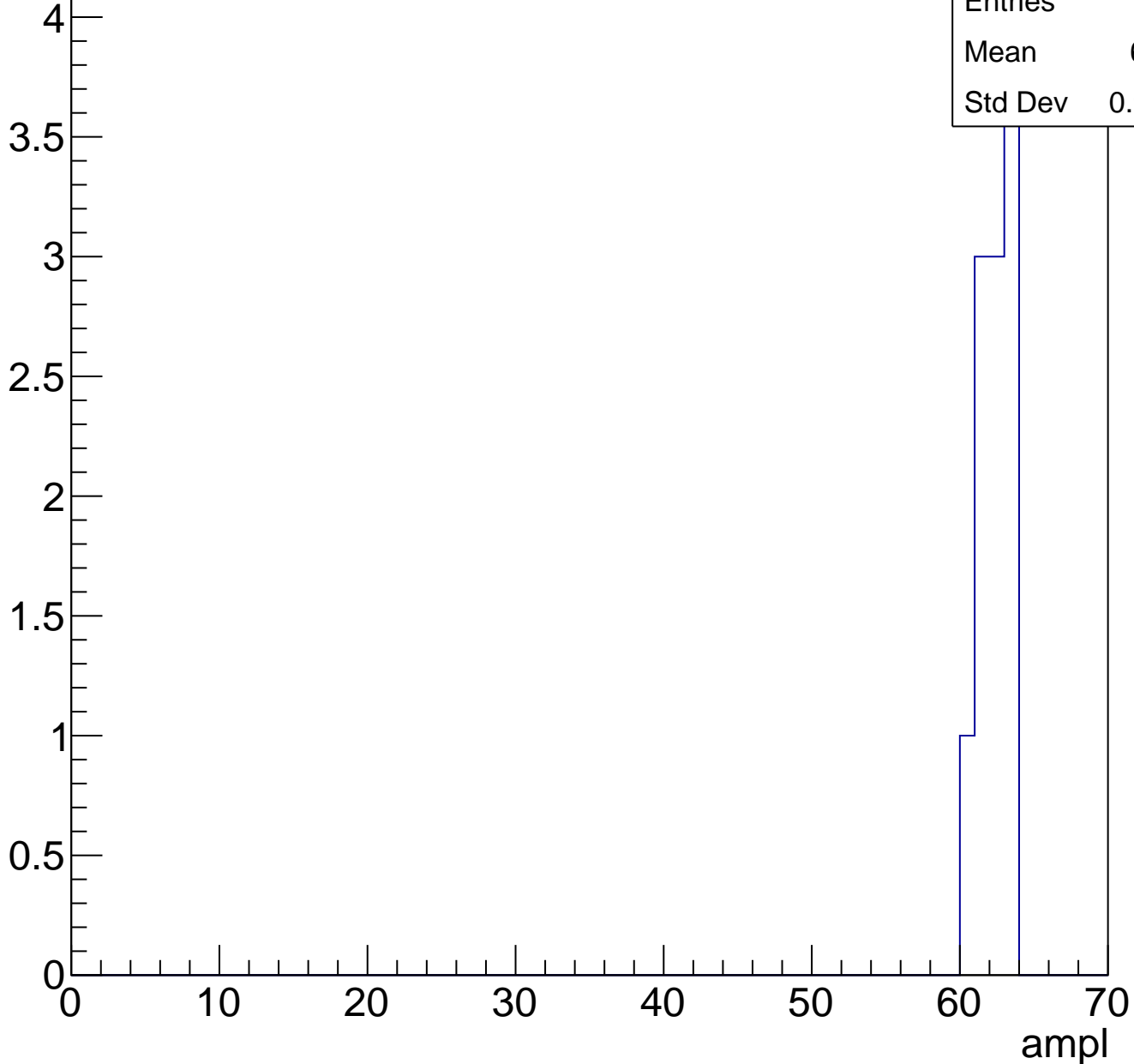
ampl



B1L001S, U25-ch22, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch22, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

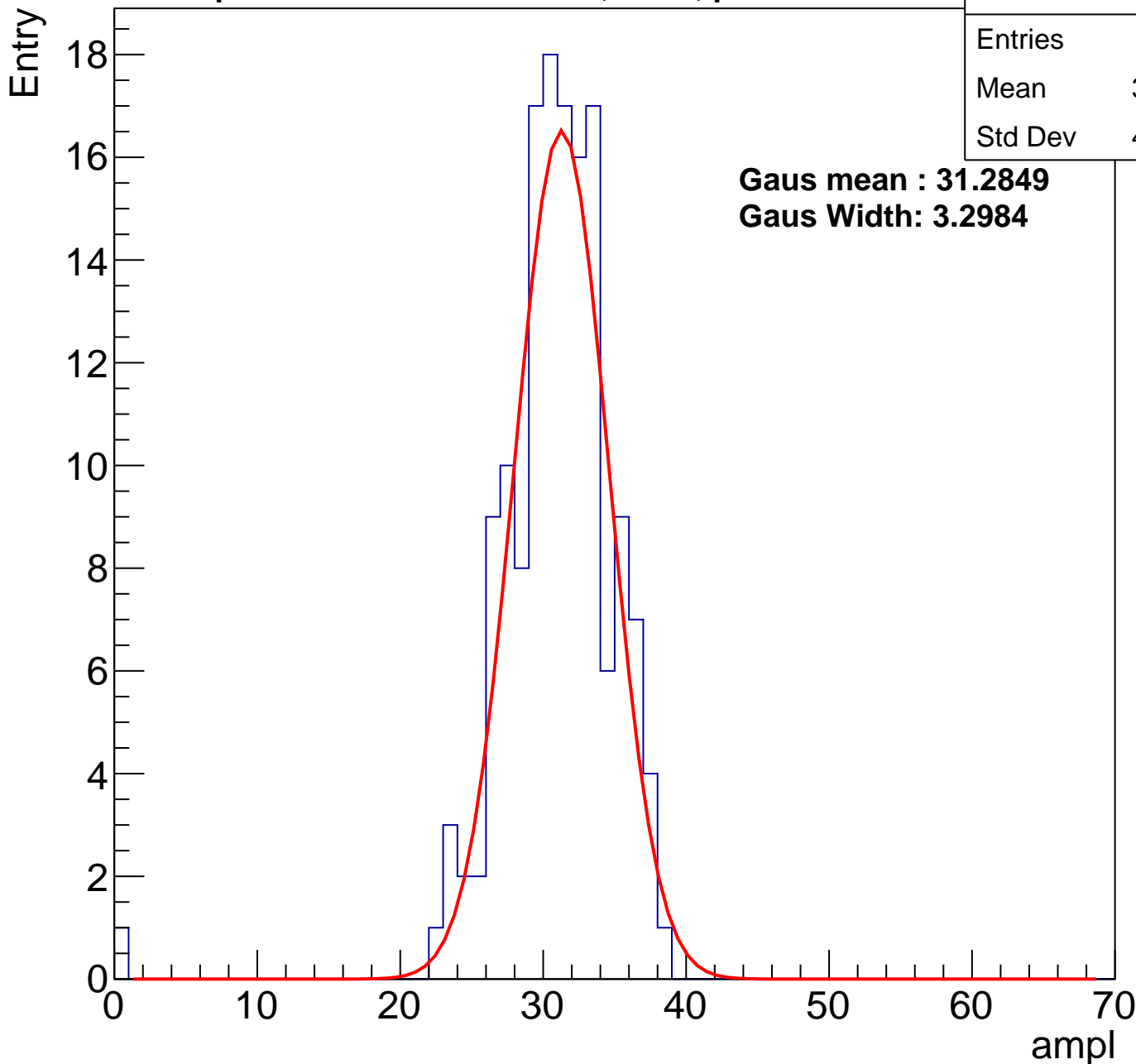
B1L001S, U25-ch23, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	30.45
Std Dev	4.153

Gaus mean : 31.2849

Gaus Width: 3.2984



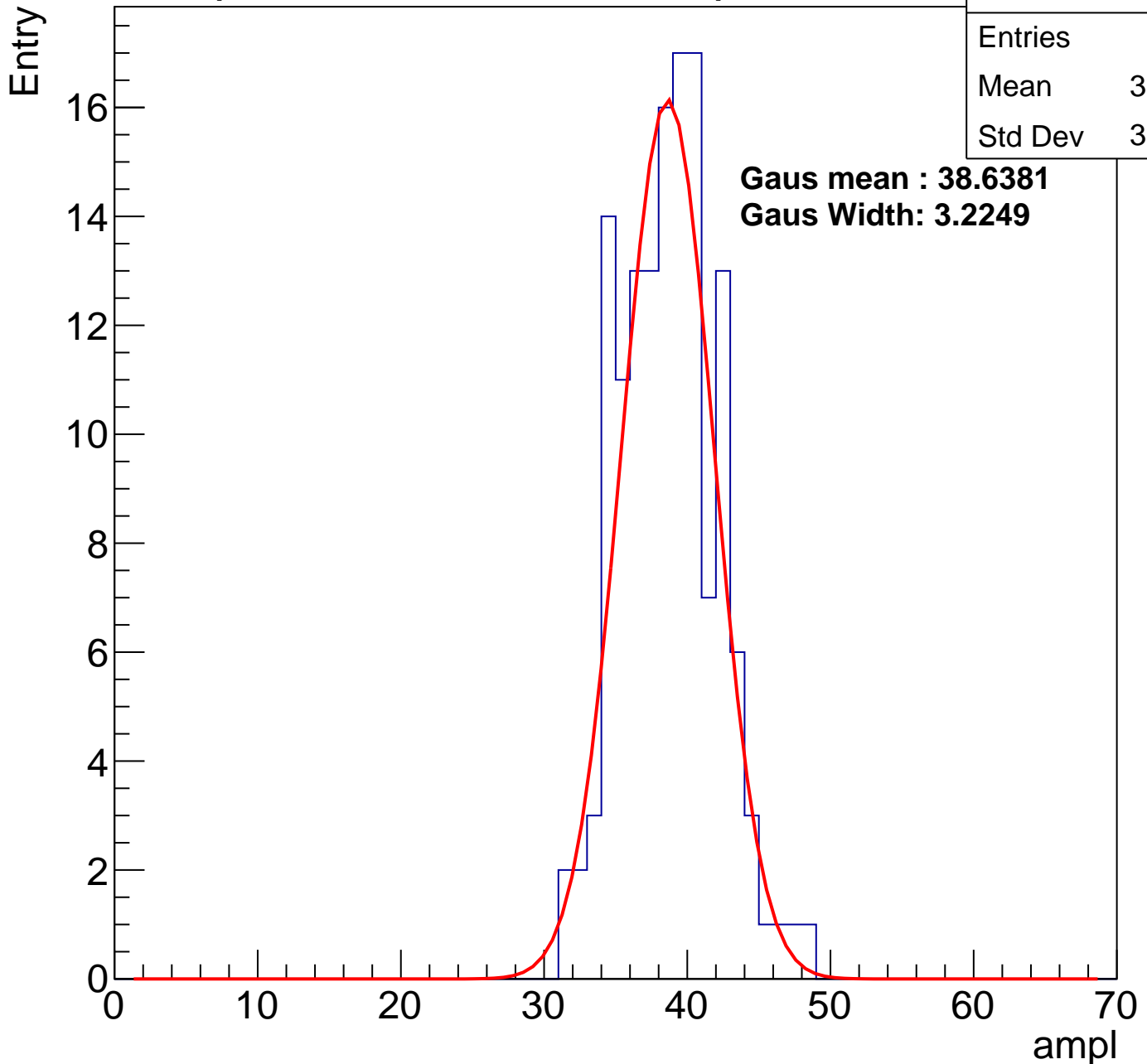
B1L001S, U25-ch23, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	38.26
Std Dev	3.295

Gaus mean : 38.6381

Gaus Width: 3.2249



B1L001S, U25-ch23, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries 101

Mean 43.83

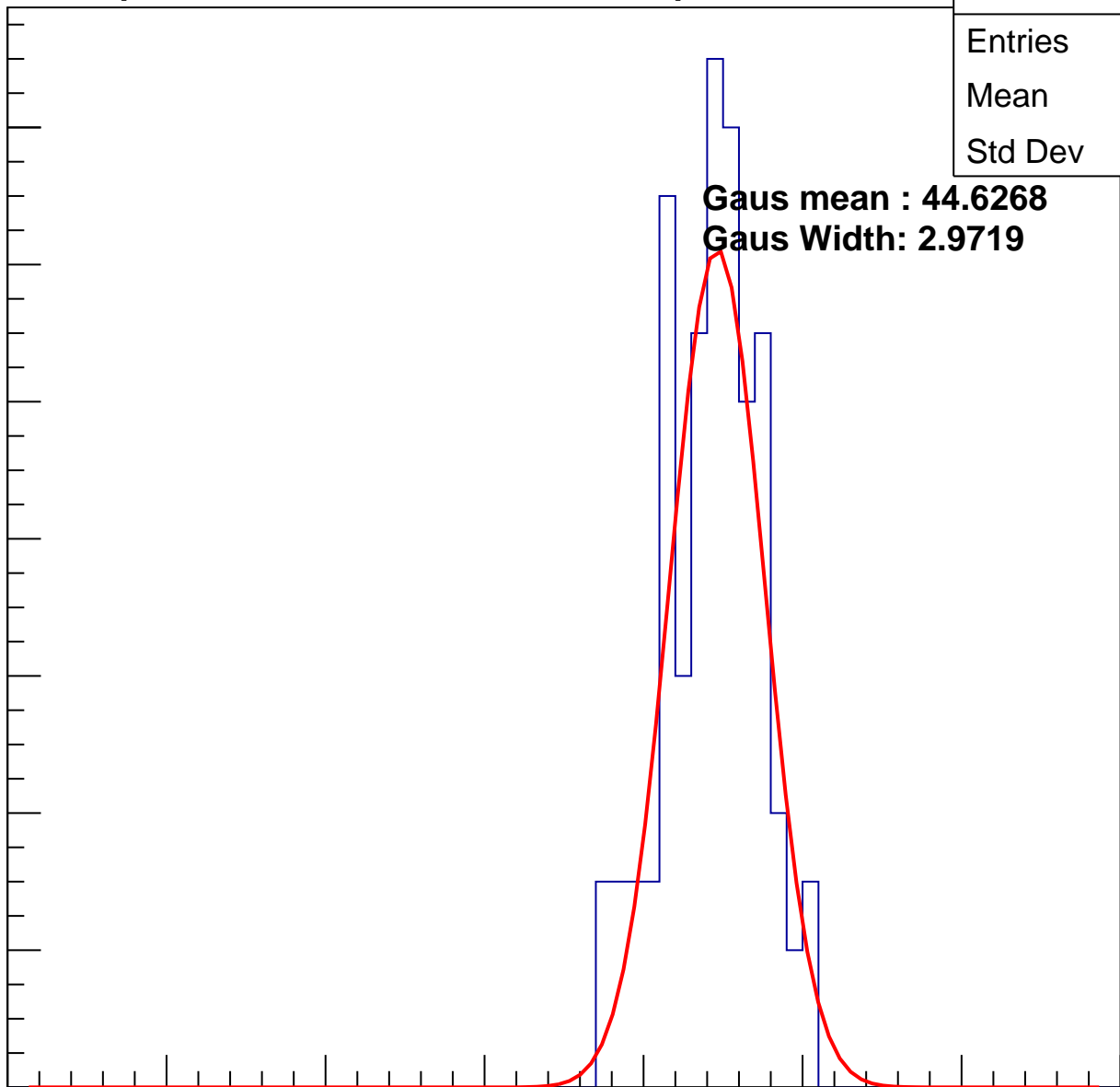
Std Dev 2.989

Gaus mean : 44.6268

Gaus Width: 2.9719

0 10 20 30 40 50 60 70

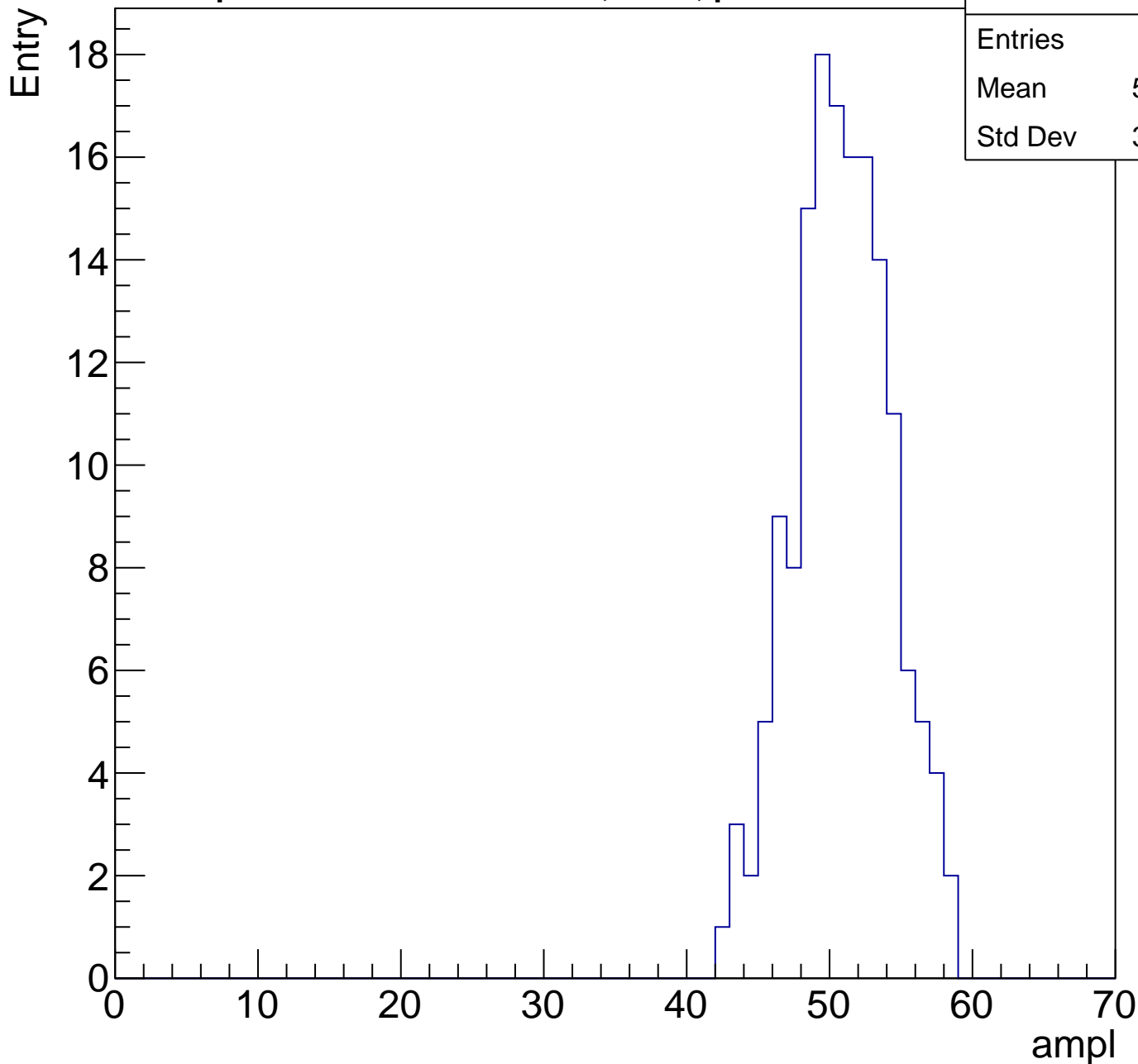
ampl



B1L001S, U25-ch23, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	50.42
Std Dev	3.355



B1L001S, U25-ch23, adc4

calib_packv5_042523_0143.root, FC#2, port C2

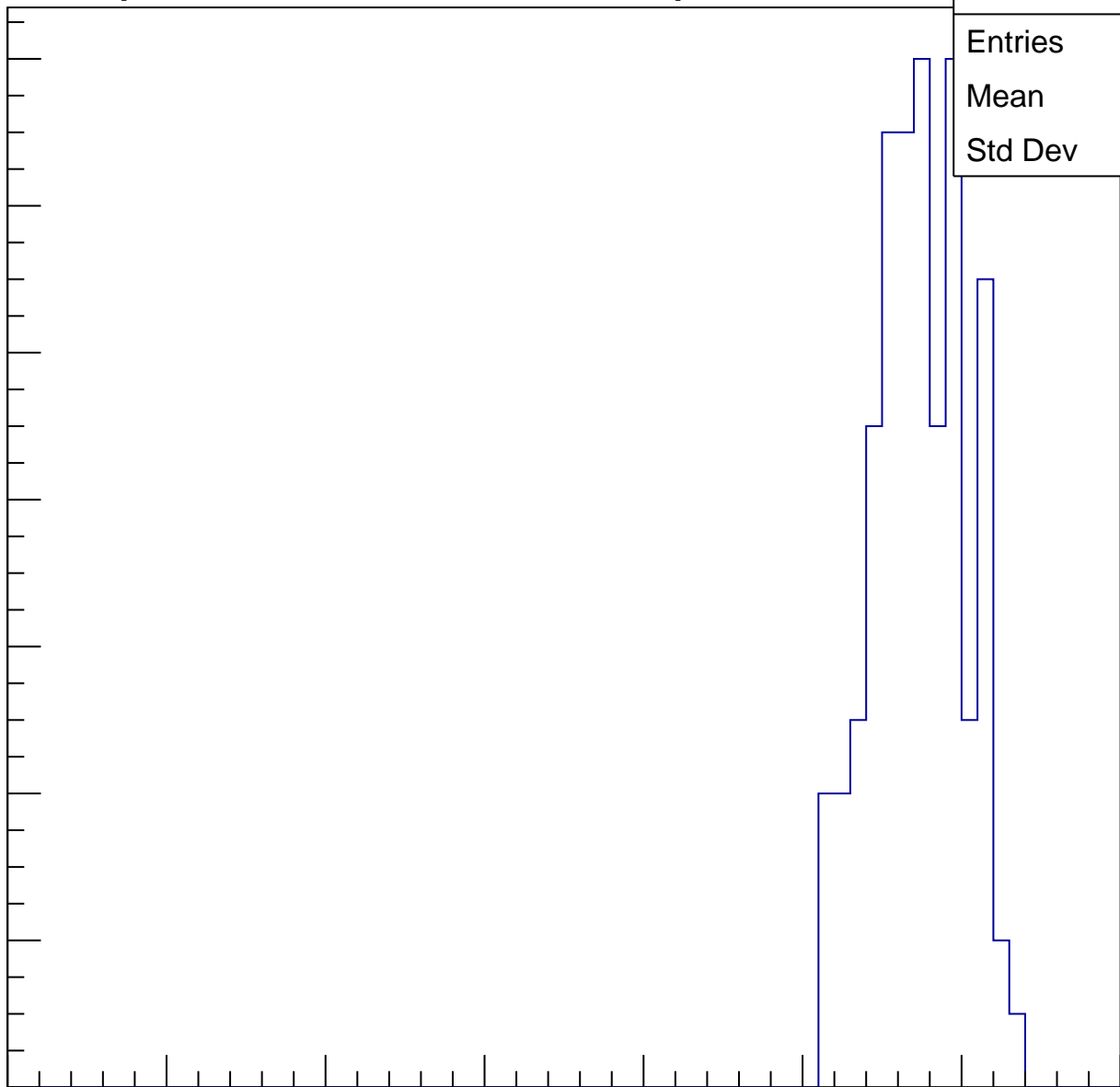
Entries	104
Mean	56.83
Std Dev	2.84

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch23, adc5

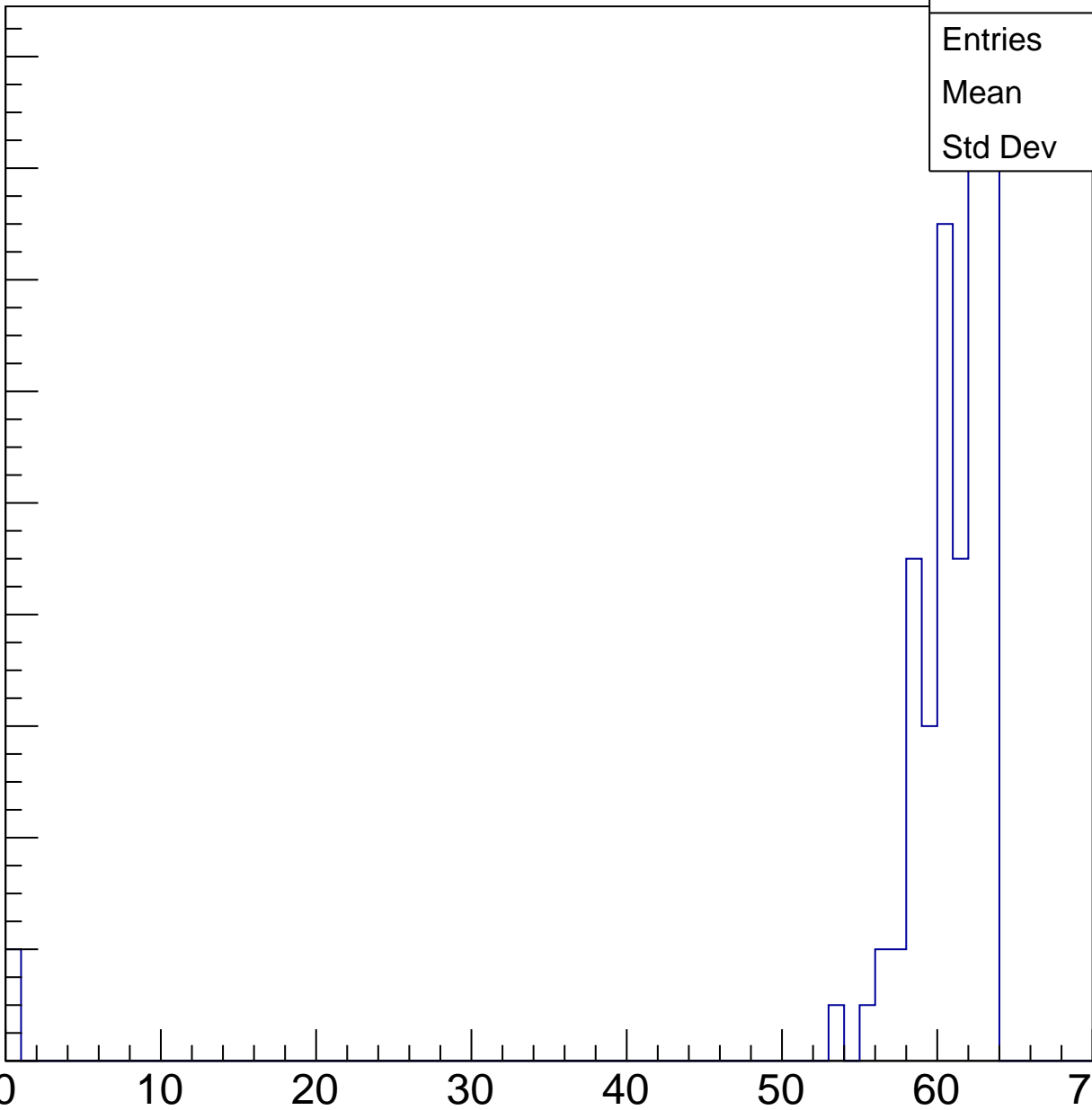
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	82
Mean	59.1
Std Dev	9.585

ampl



B1L001S, U25-ch23, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L001S, U25-ch23, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch24, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	29.88
Std Dev	4.144

Gaus mean : 30.4590

Gaus Width: 3.7139

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

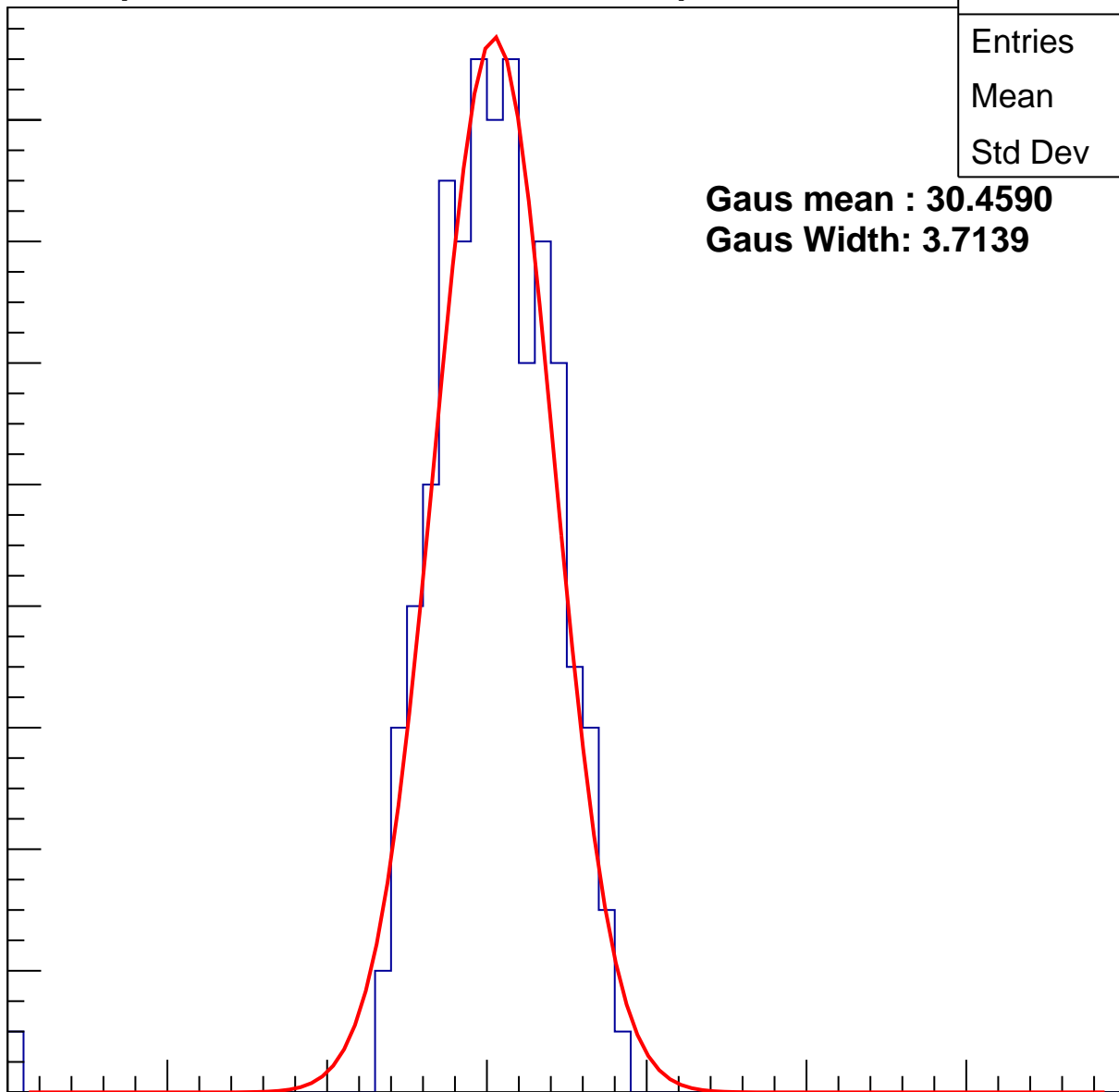
40

50

60

70

ampl

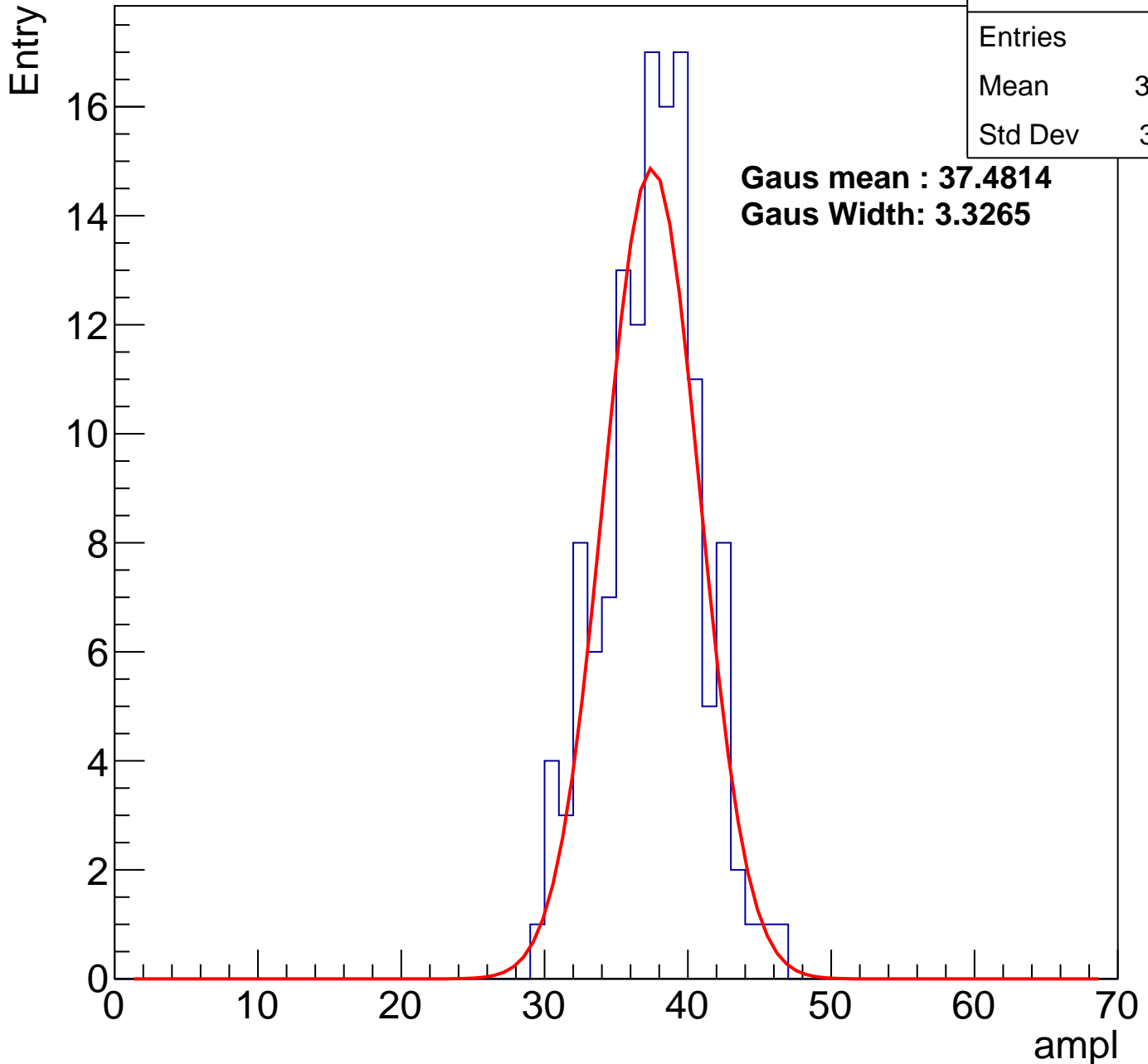


B1L001S, U25-ch24, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	37.02
Std Dev	3.381

Gaus mean : 37.4814
Gaus Width: 3.3265



B1L001S, U25-ch24, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

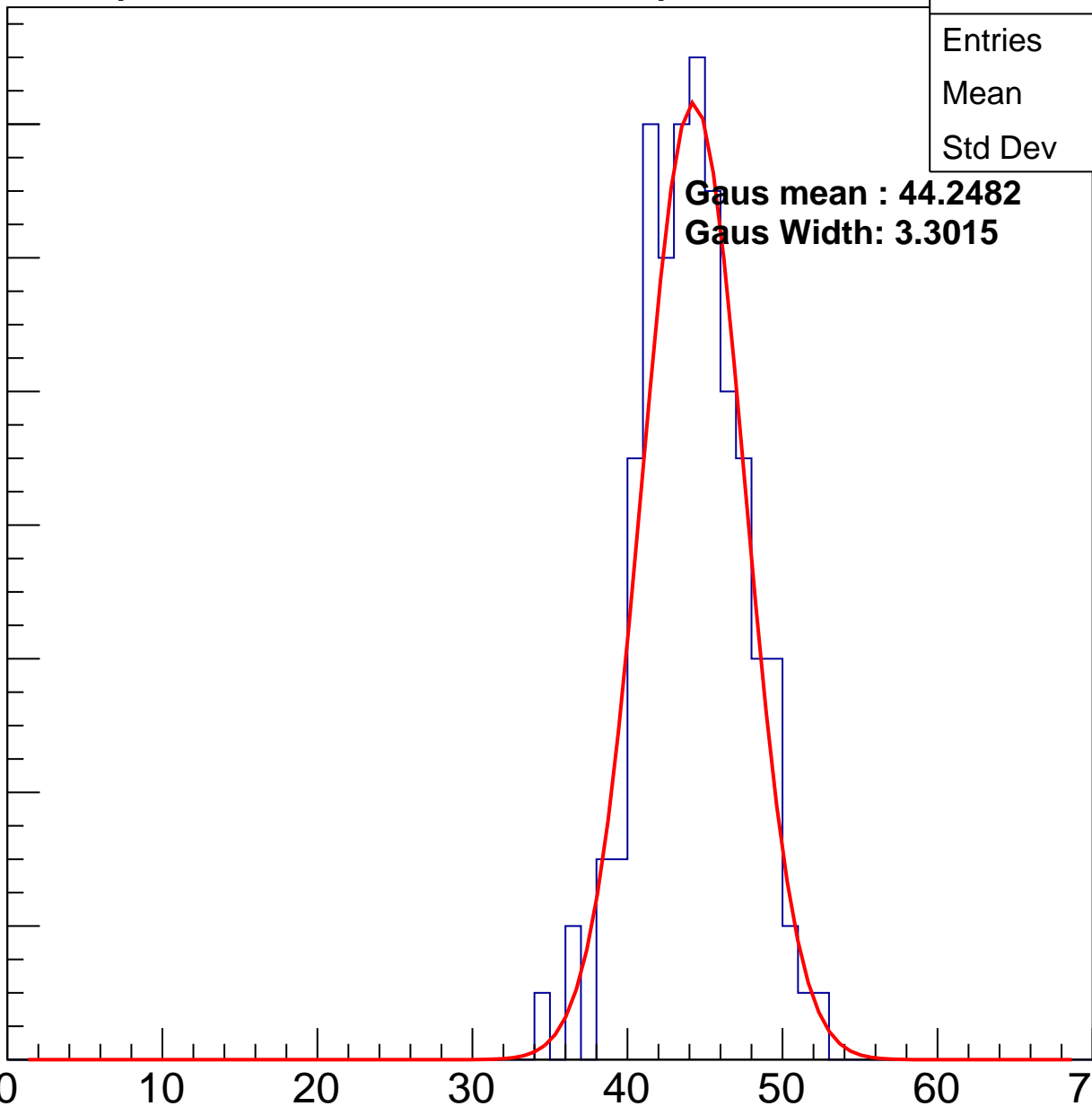
14
12
10
8
6
4
2
0

Entries	121
Mean	43.72
Std Dev	3.276

Gaus mean : 44.2482

Gaus Width: 3.3015

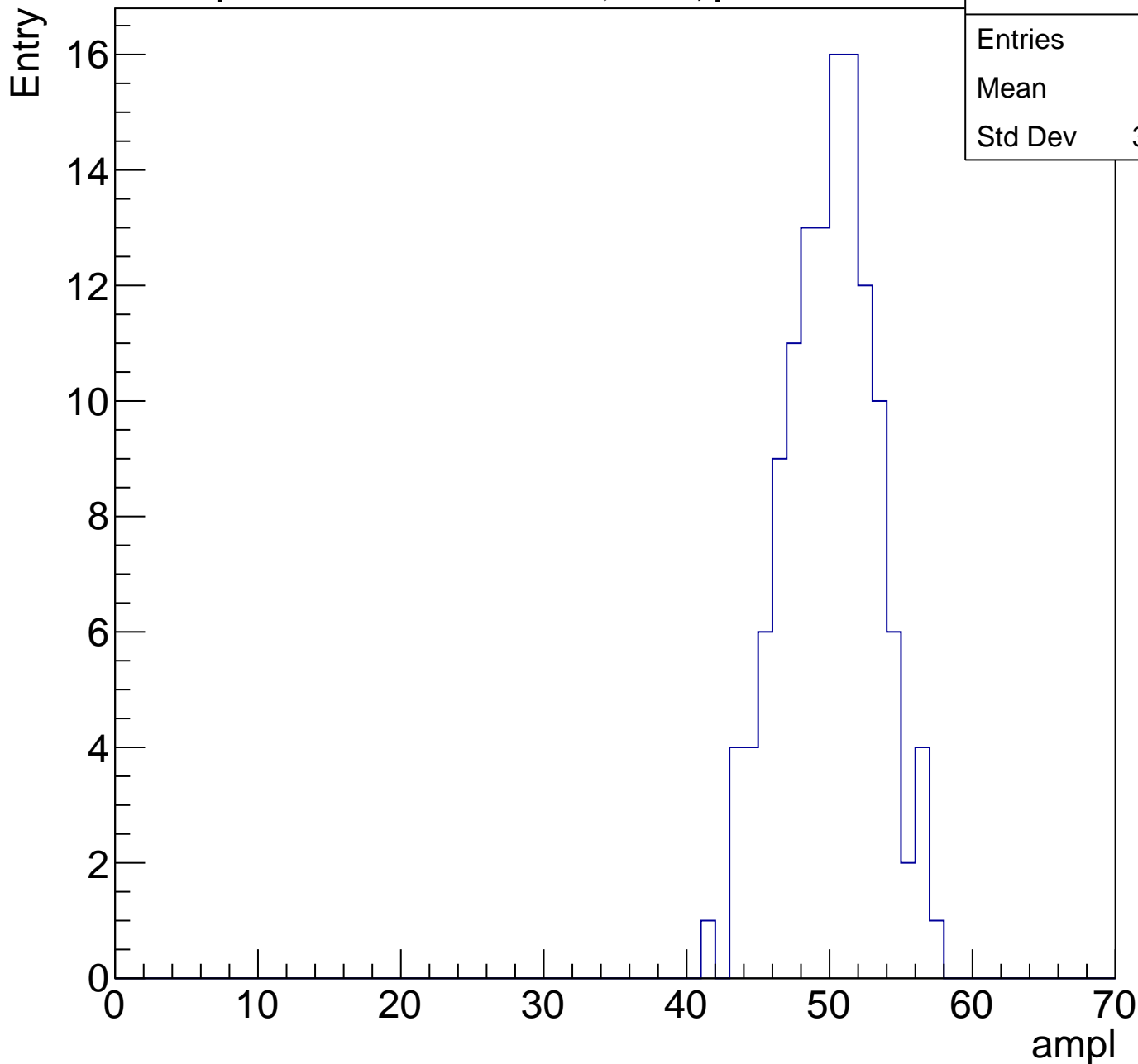
0 10 20 30 40 50 60 70
ampl



B1L001S, U25-ch24, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	49.5
Std Dev	3.236



B1L001S, U25-ch24, adc4

calib_packv5_042523_0143.root, FC#2, port C2

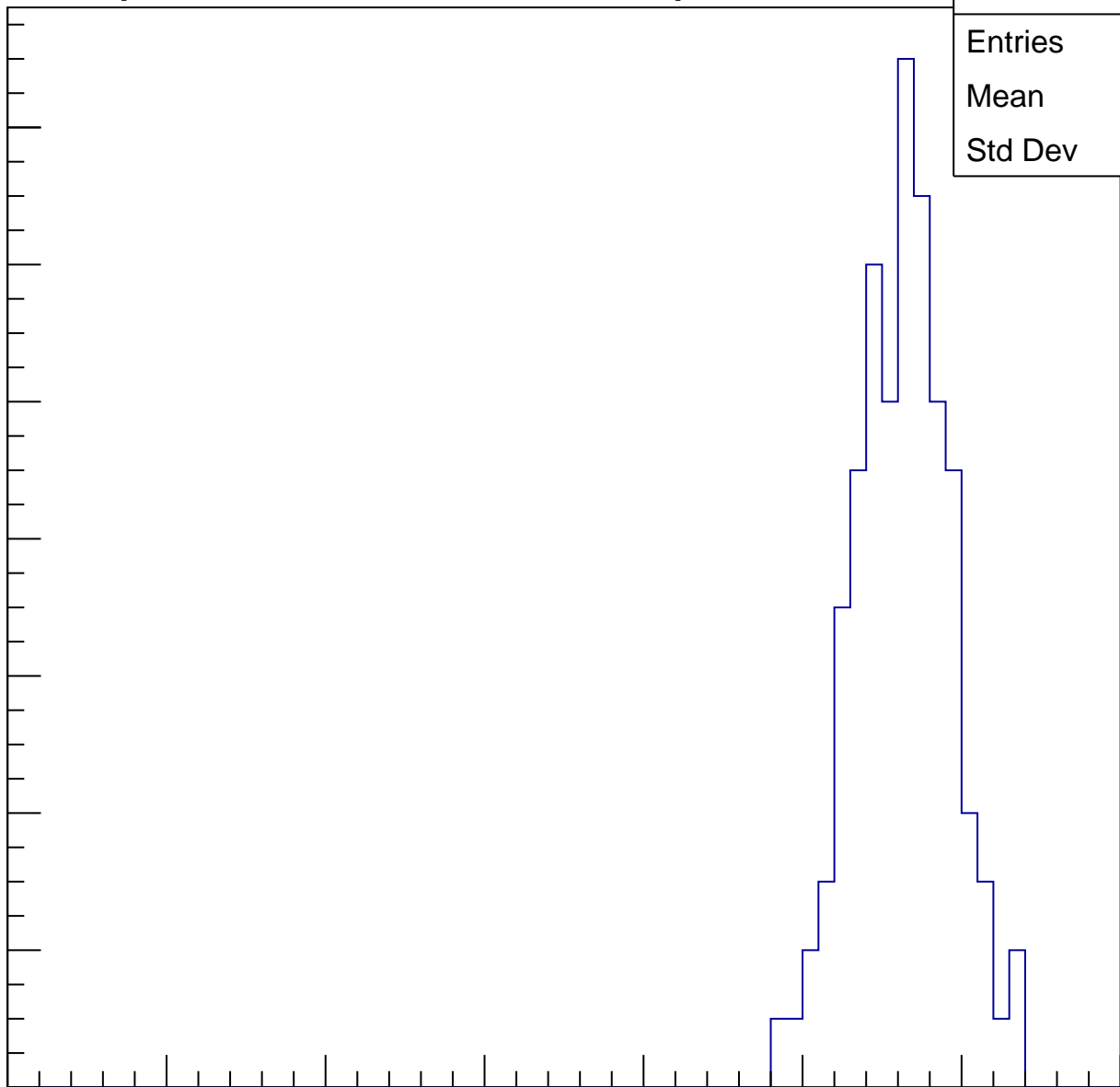
Entries	102
Mean	55.8
Std Dev	2.994

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

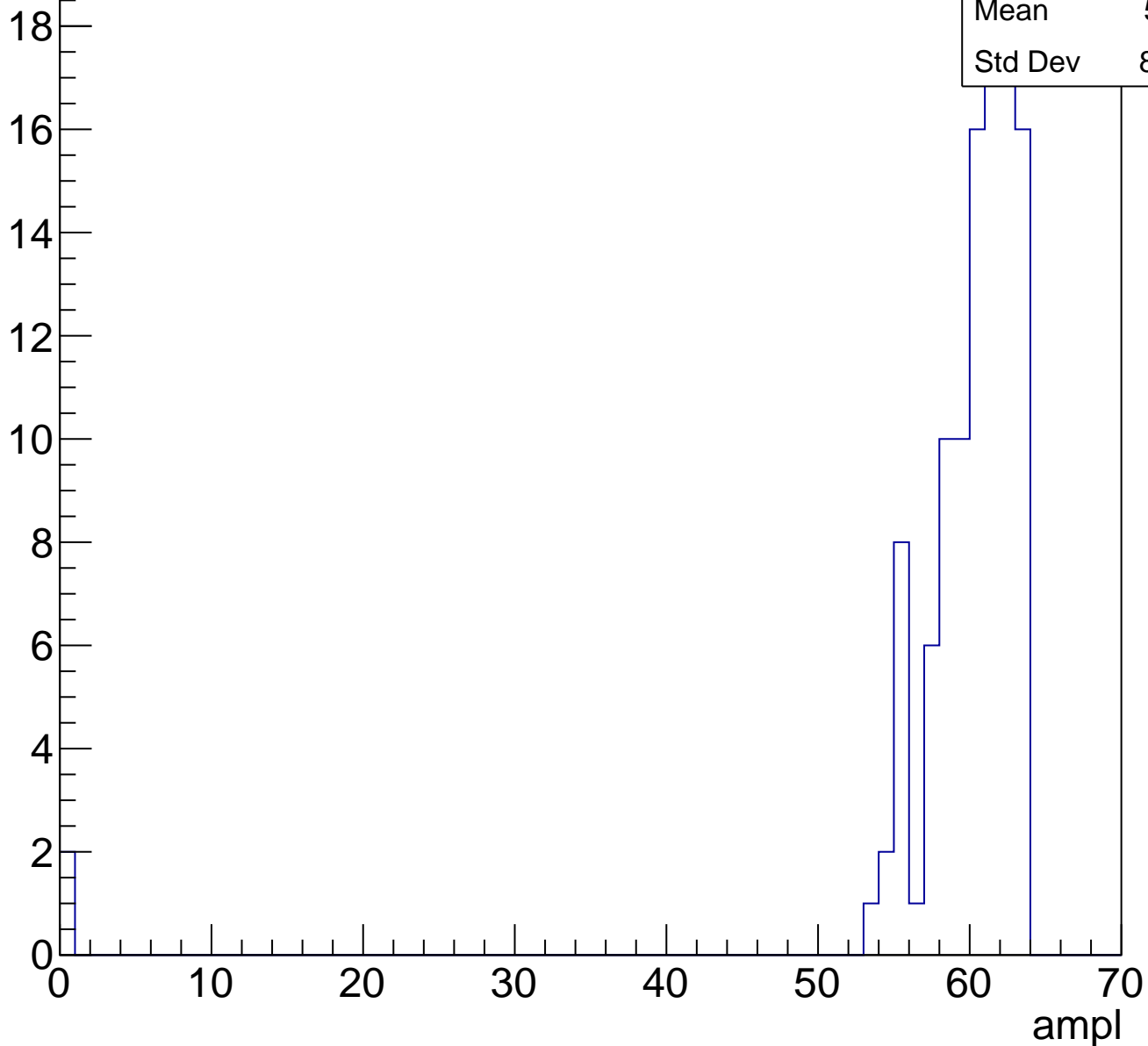


B1L001S, U25-ch24, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	58.81
Std Dev	8.458

Entry



B1L001S, U25-ch24, adc6

calib_packv5_042523_0143.root, FC#2, port C2

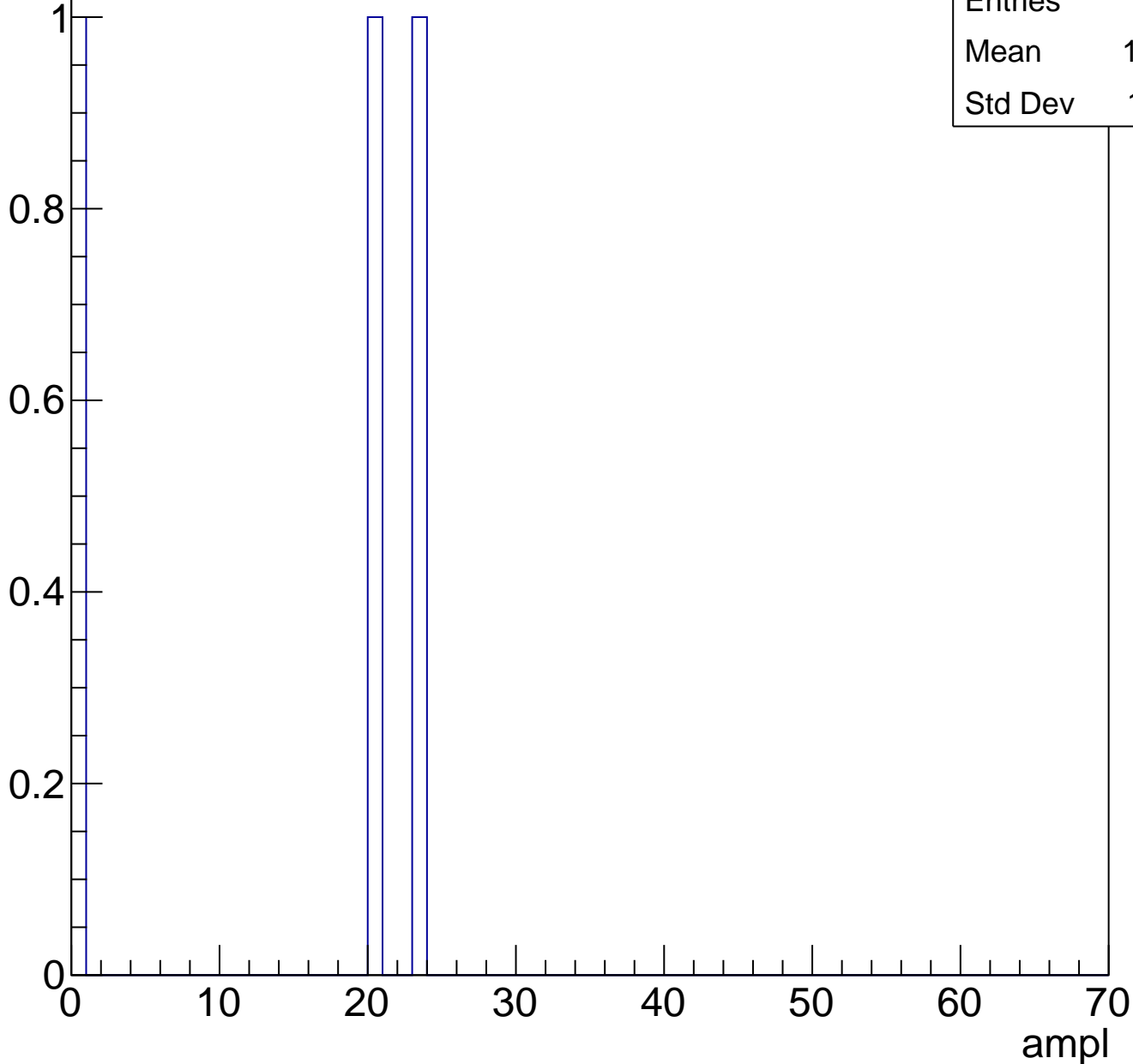
Entry



B1L001S, U25-ch24, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch25, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	32.4
Std Dev	3.515

Gaus mean : 33.0555

Gaus Width: 3.5170

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

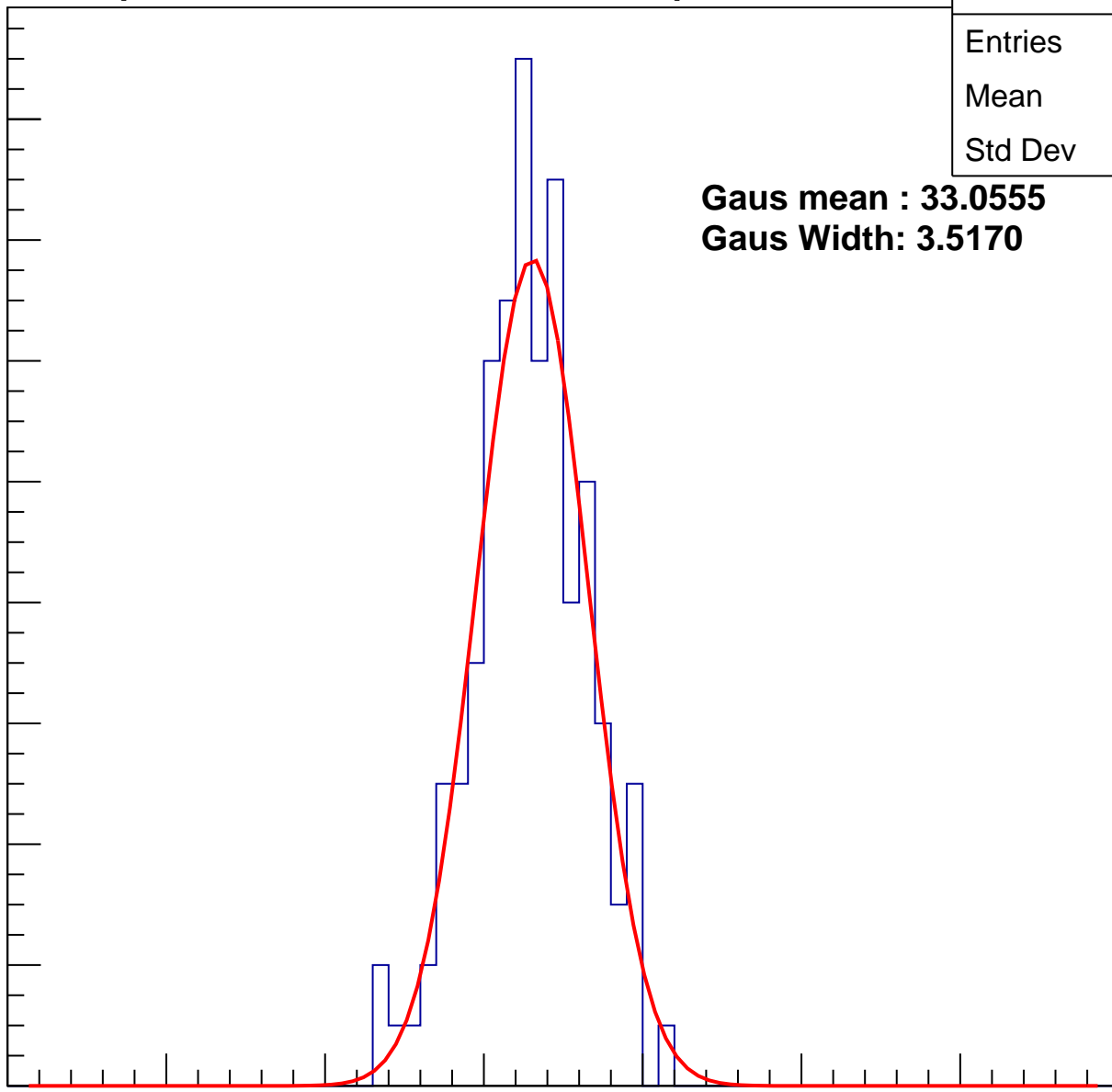
40

50

60

70

ampl



B1L001S, U25-ch25, adc1

calib_packv5_042523_0143.root, FC#2, port C2

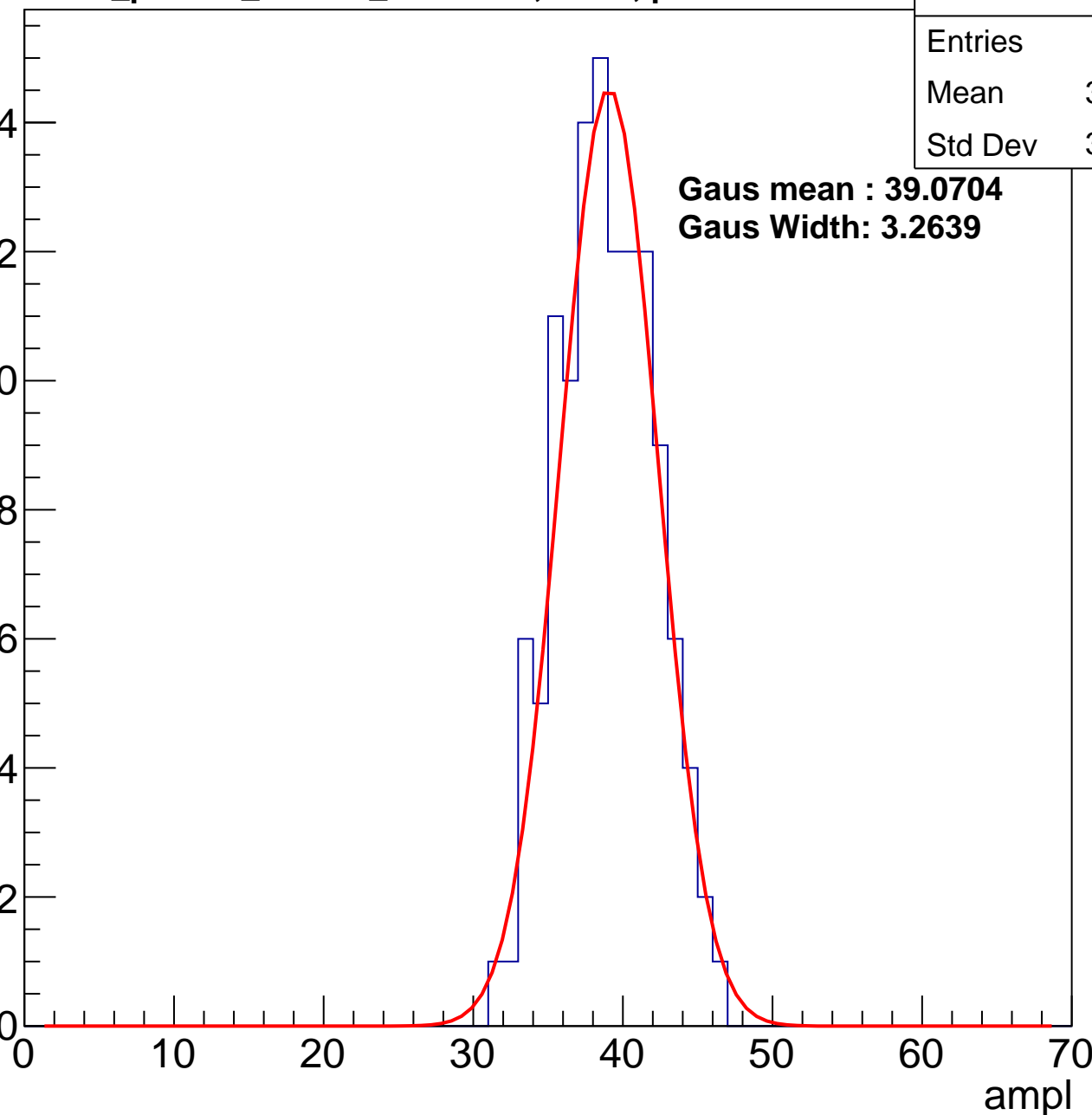
Entry

14
12
10
8
6
4
2
0

Entries	121
Mean	38.45
Std Dev	3.165

Gaus mean : 39.0704

Gaus Width: 3.2639



B1L001S, U25-ch25, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

151

Mean

45.36

Std Dev

3.334

Gaus mean : 45.8271

Gaus Width: 3.5542

0

10

20

30

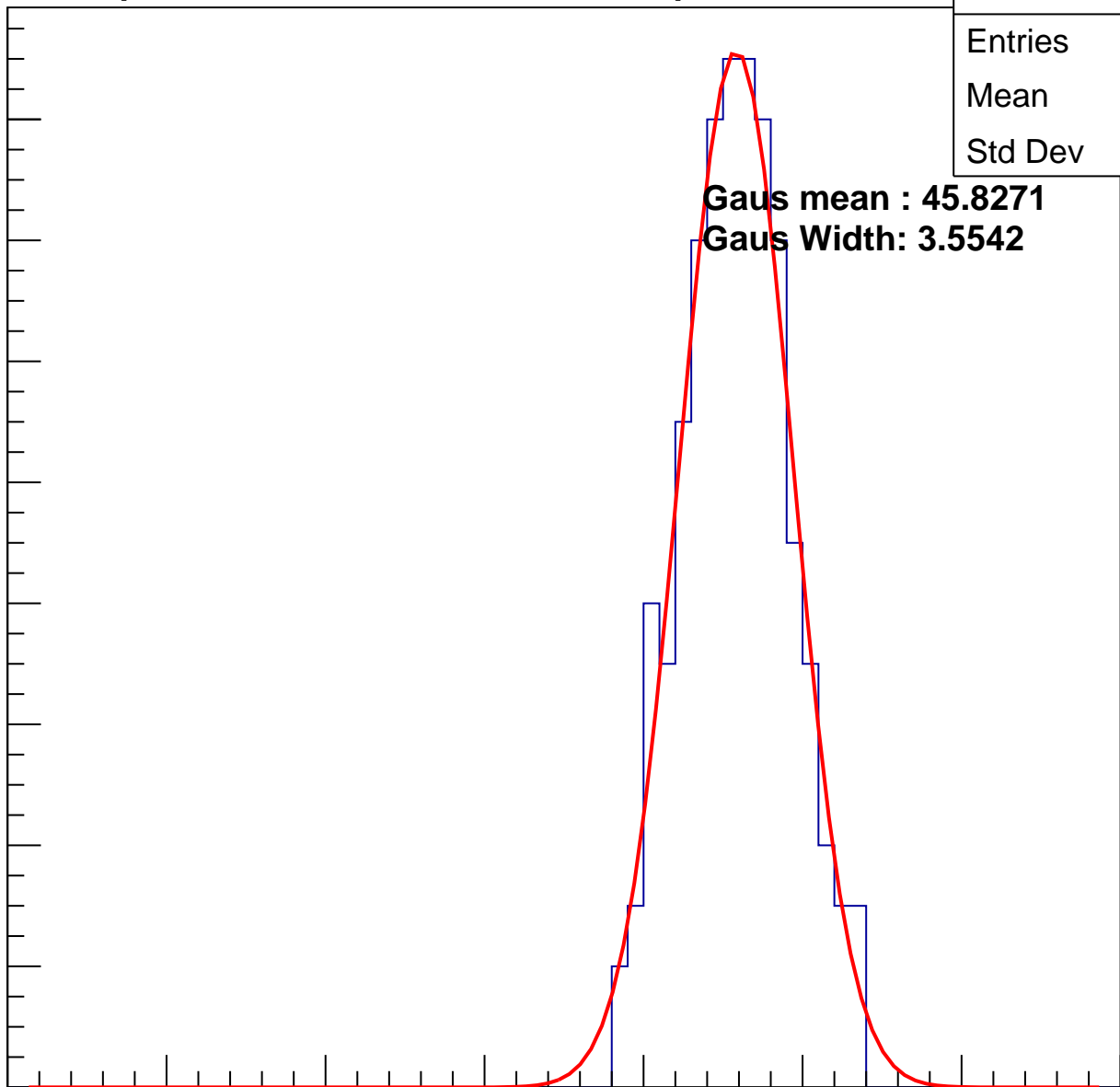
40

50

60

70

ampl



B1L001S, U25-ch25, adc3

calib_packv5_042523_0143.root, FC#2, port C2

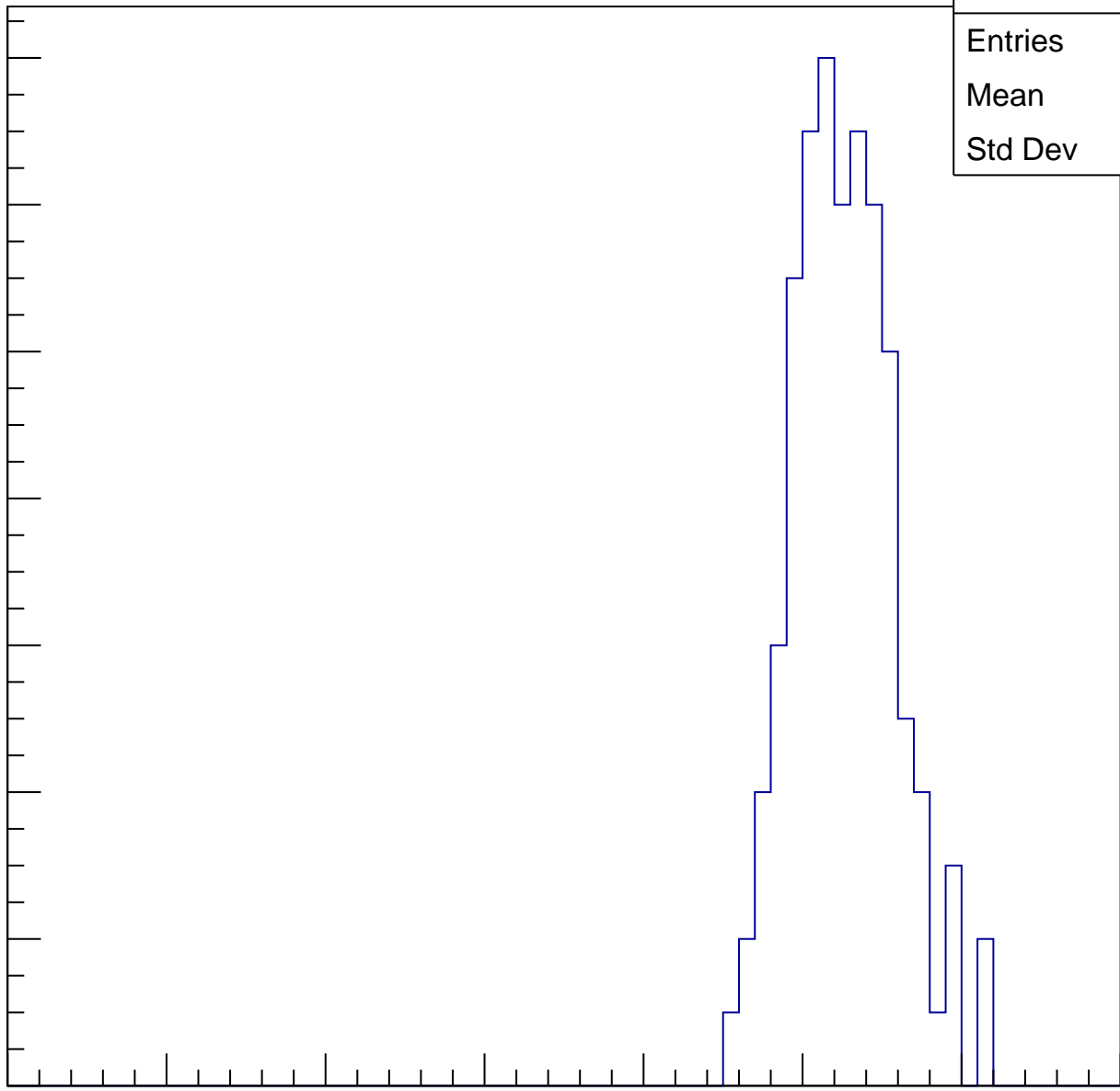
Entries	113
Mean	52.14
Std Dev	3.198

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

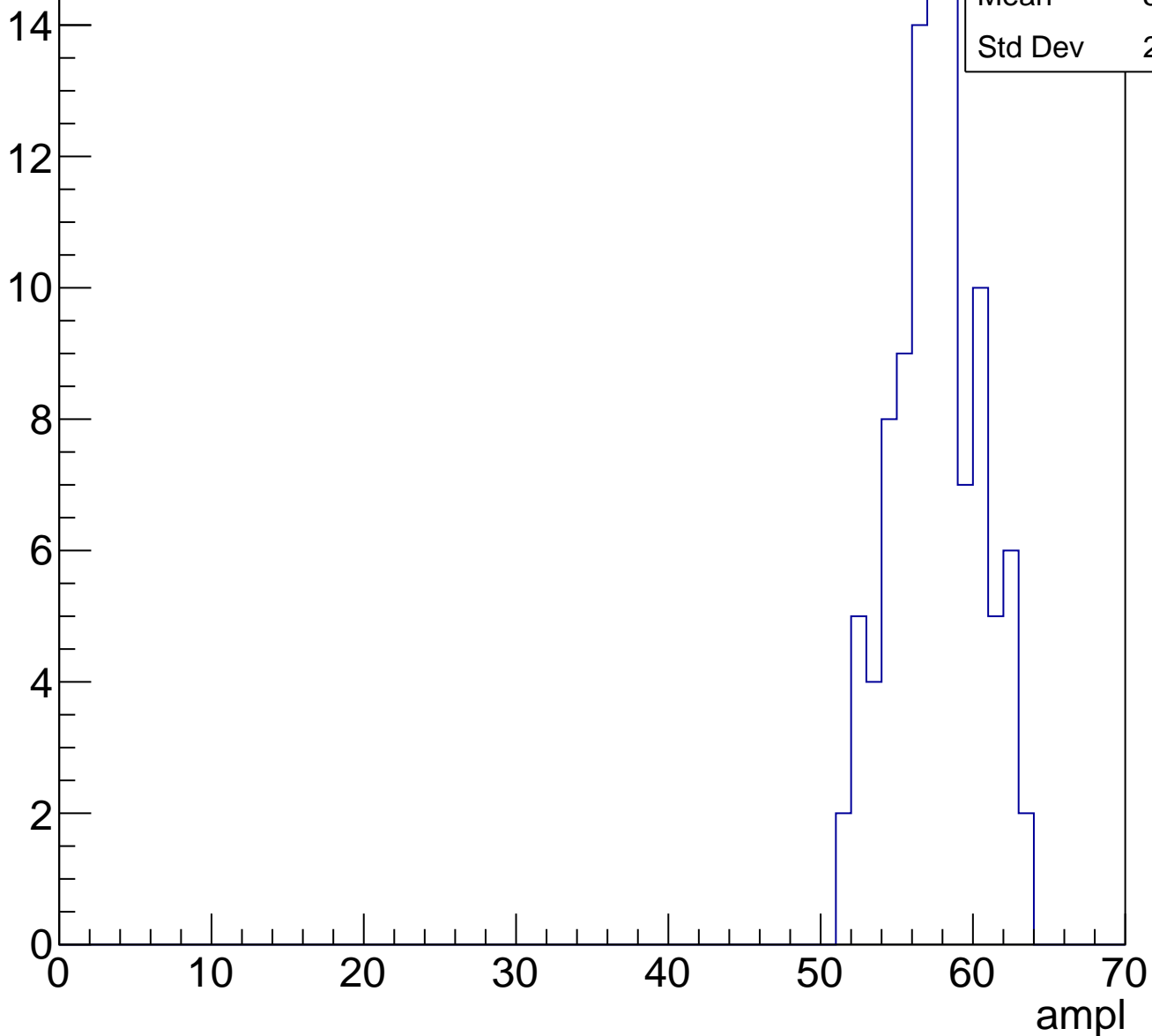


B1L001S, U25-ch25, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	57.12
Std Dev	2.829

Entry



B1L001S, U25-ch25, adc5

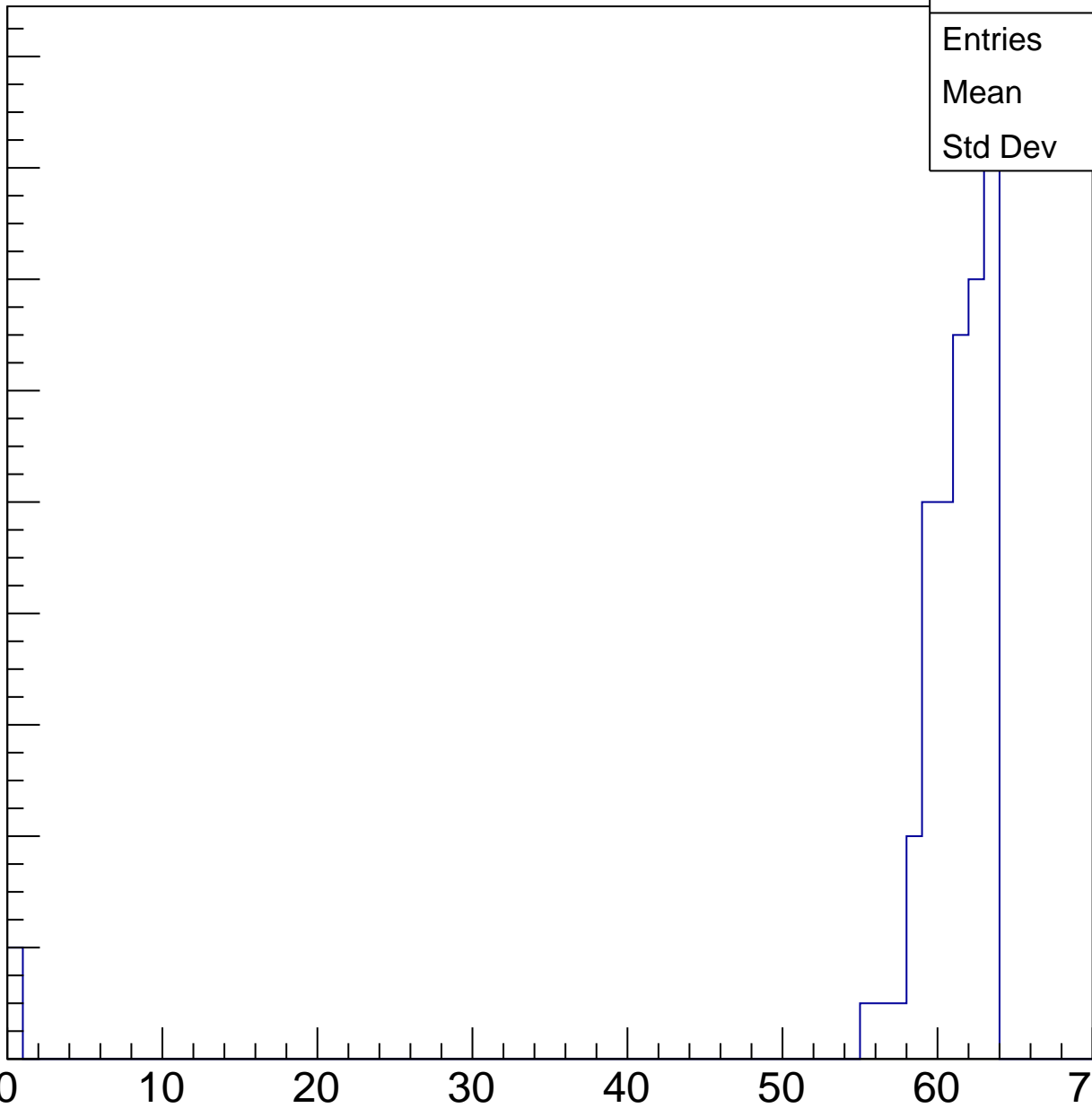
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.26
Std Dev	10.04

ampl



B1L001S, U25-ch25, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

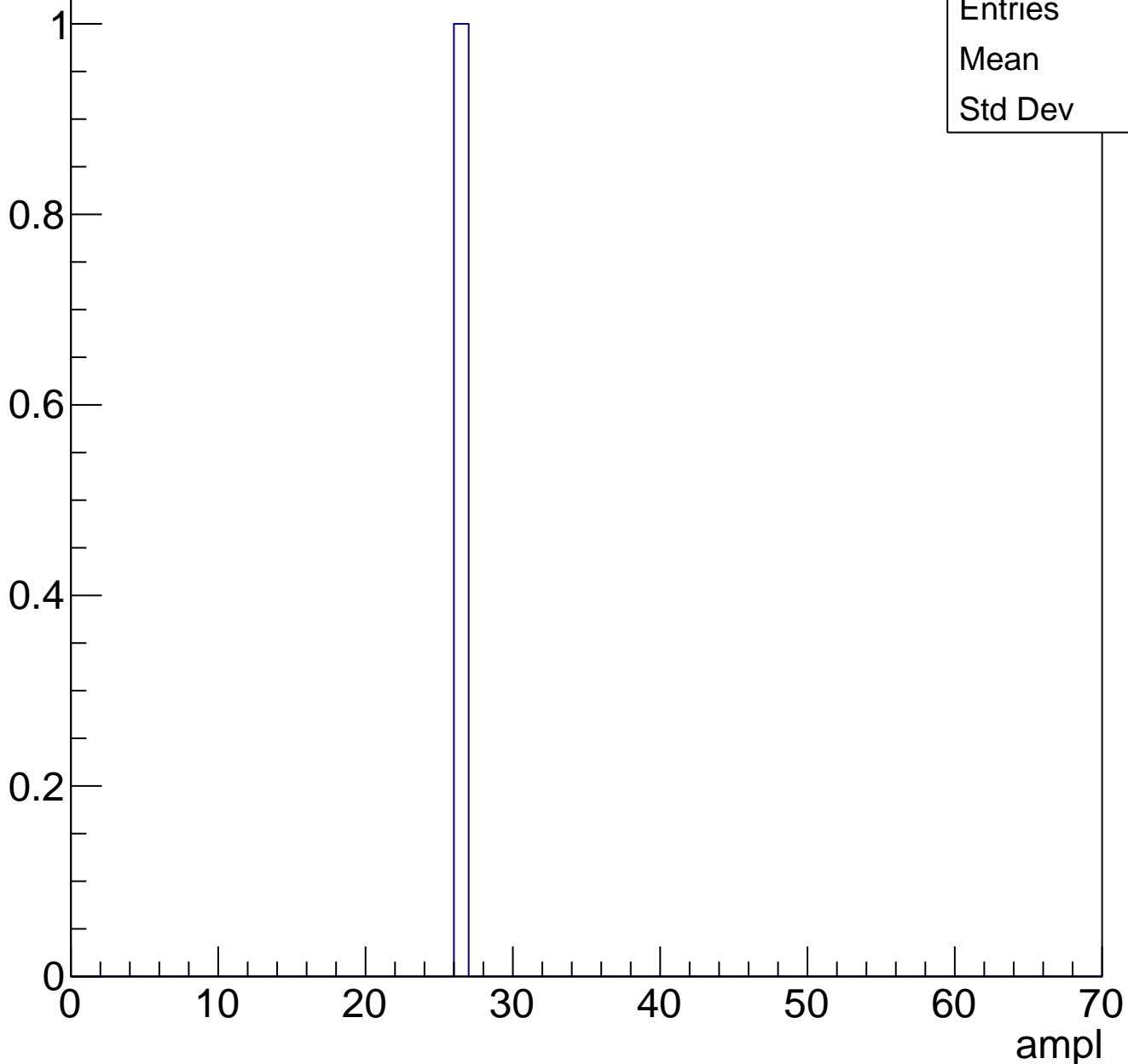


Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch25, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	26
Std Dev	0

B1L001S, U25-ch26, adc0

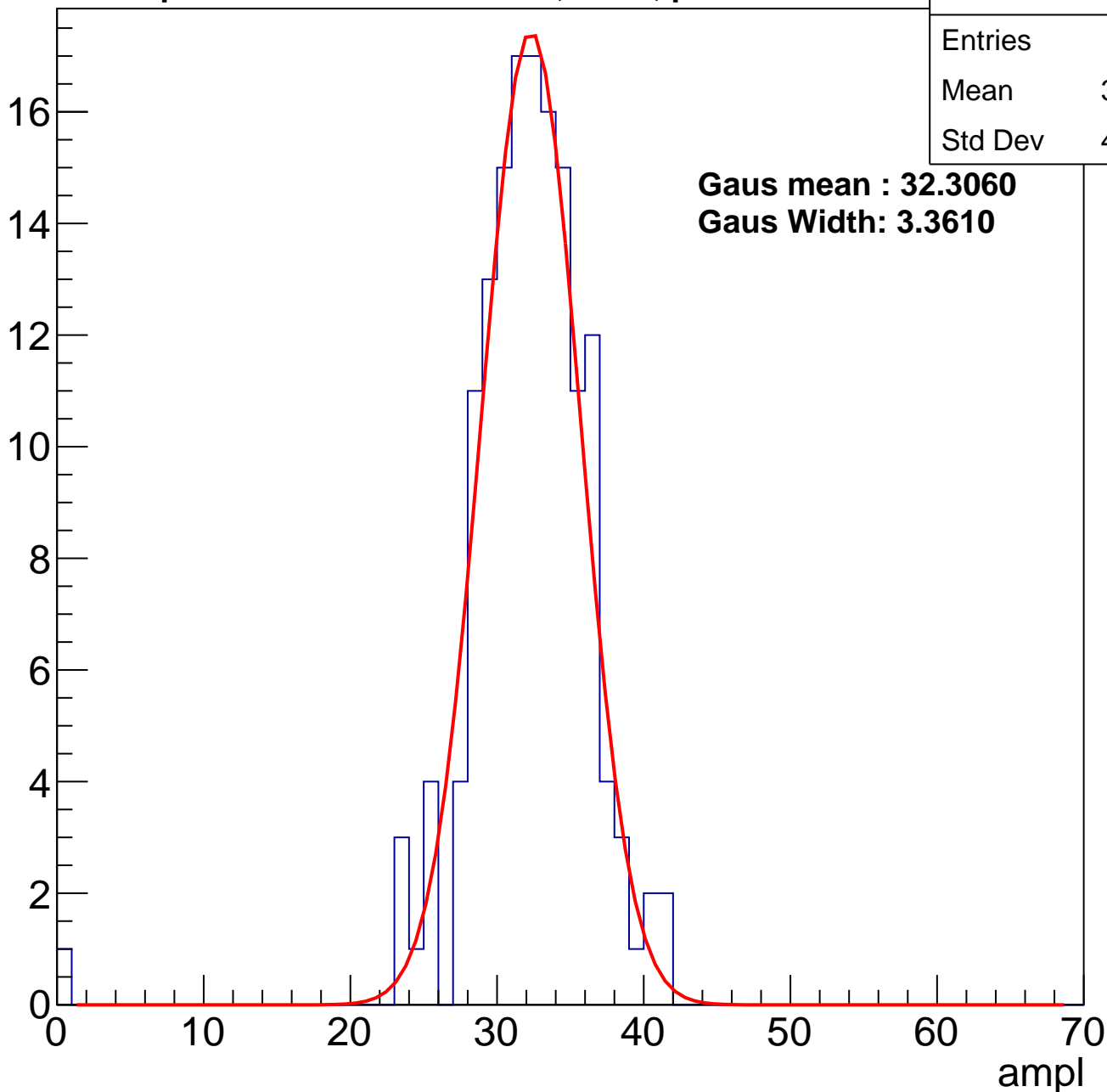
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	31.74
Std Dev	4.369

Gaus mean : 32.3060

Gaus Width: 3.3610

Entry



B1L001S, U25-ch26, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	38.06
Std Dev	2.841

Gaus mean : 38.7074

Gaus Width: 2.8434

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

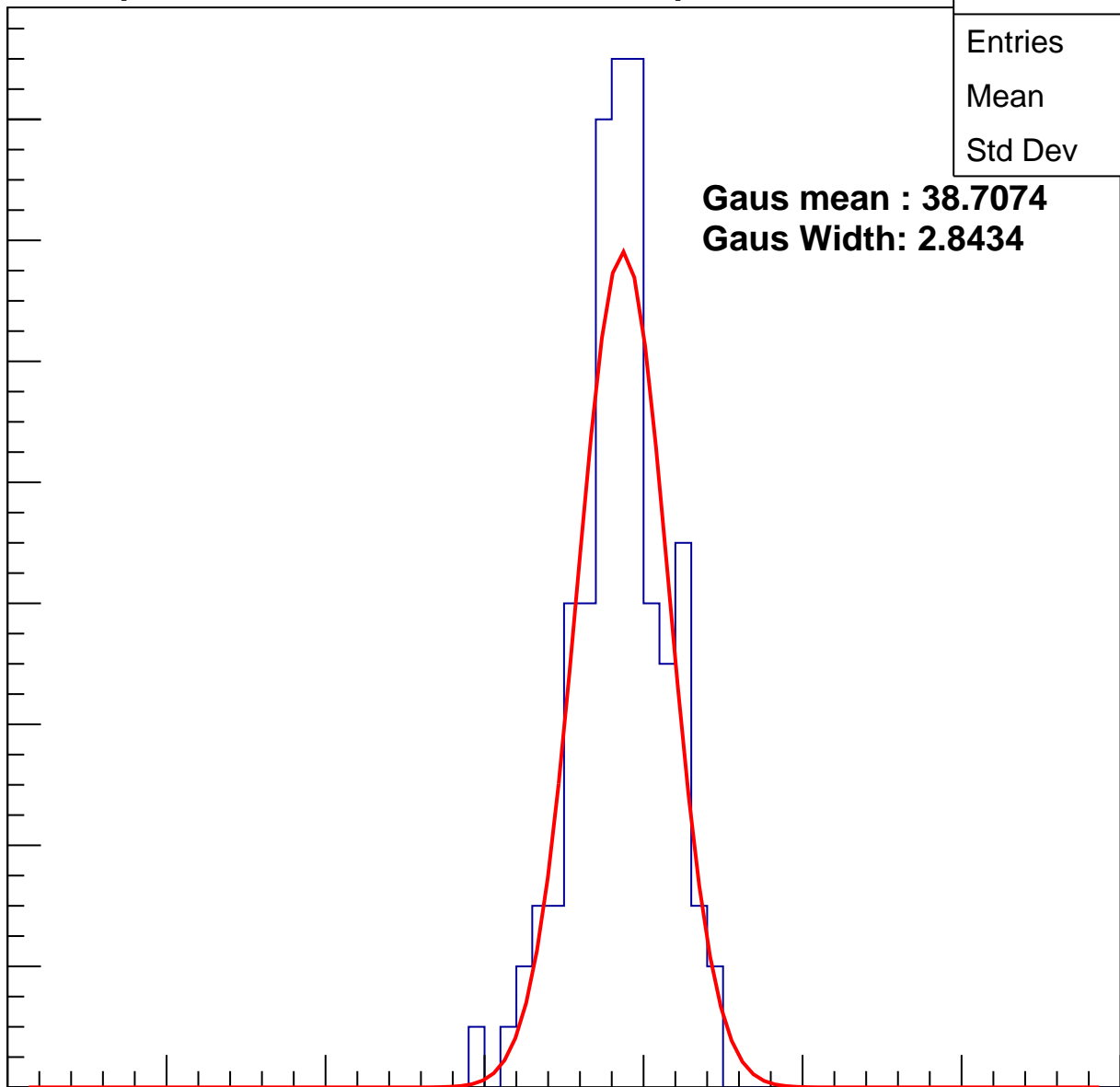
40

50

60

70

ampl



B1L001S, U25-ch26, adc2

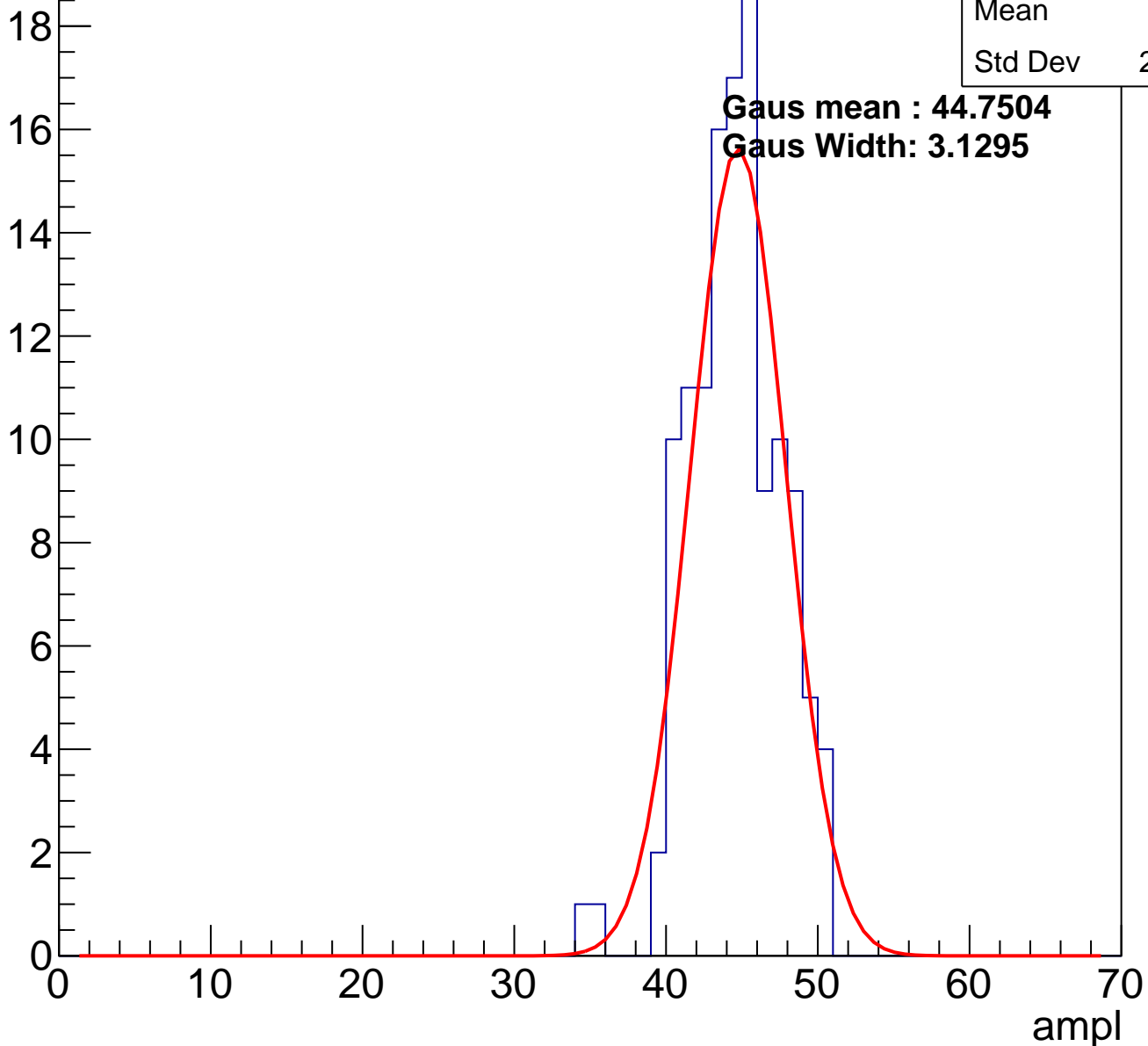
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	44.1
Std Dev	2.976

Gaus mean : 44.7504

Gaus Width: 3.1295

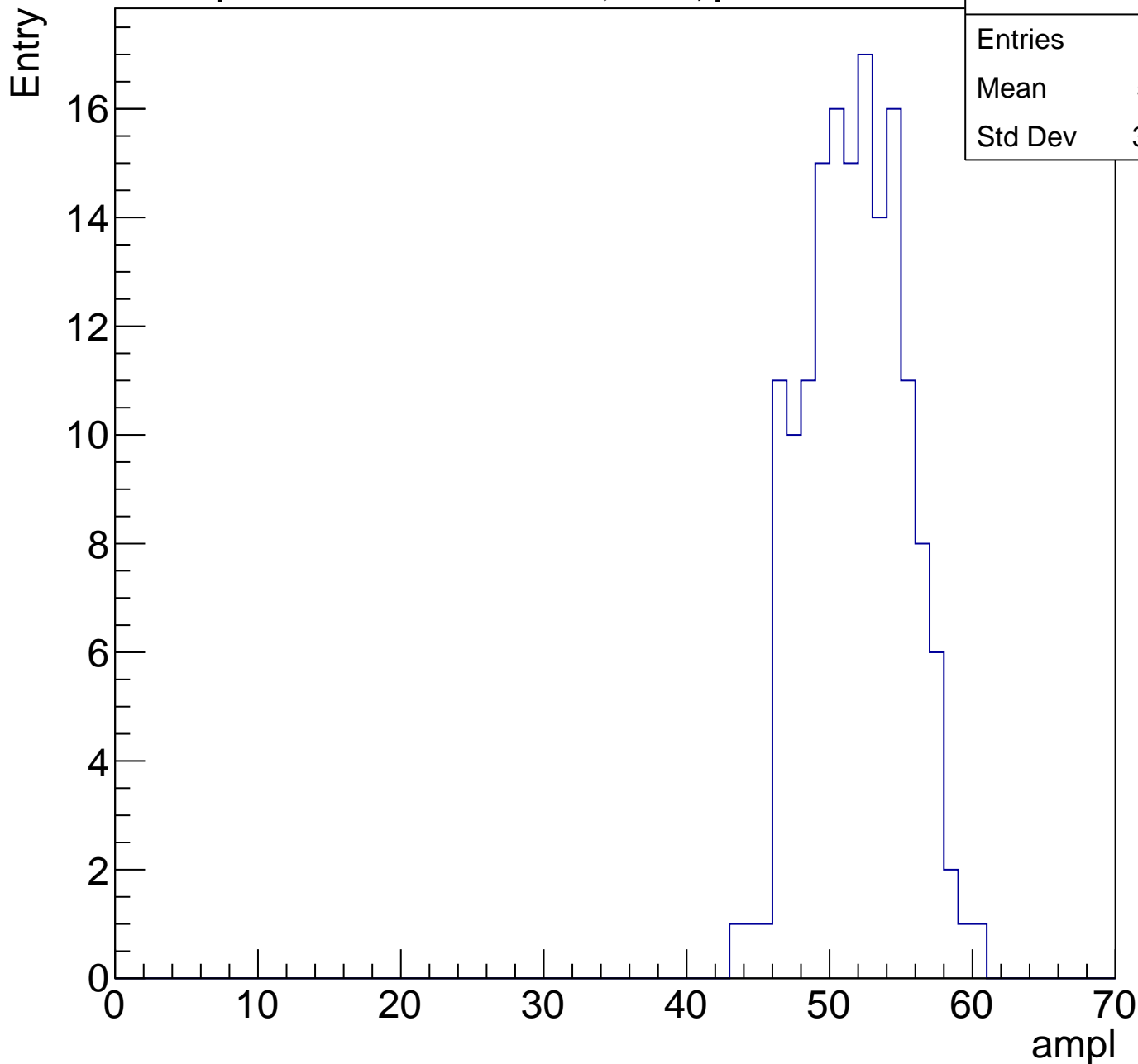
Entry



B1L001S, U25-ch26, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	51.31
Std Dev	3.382



B1L001S, U25-ch26, adc4

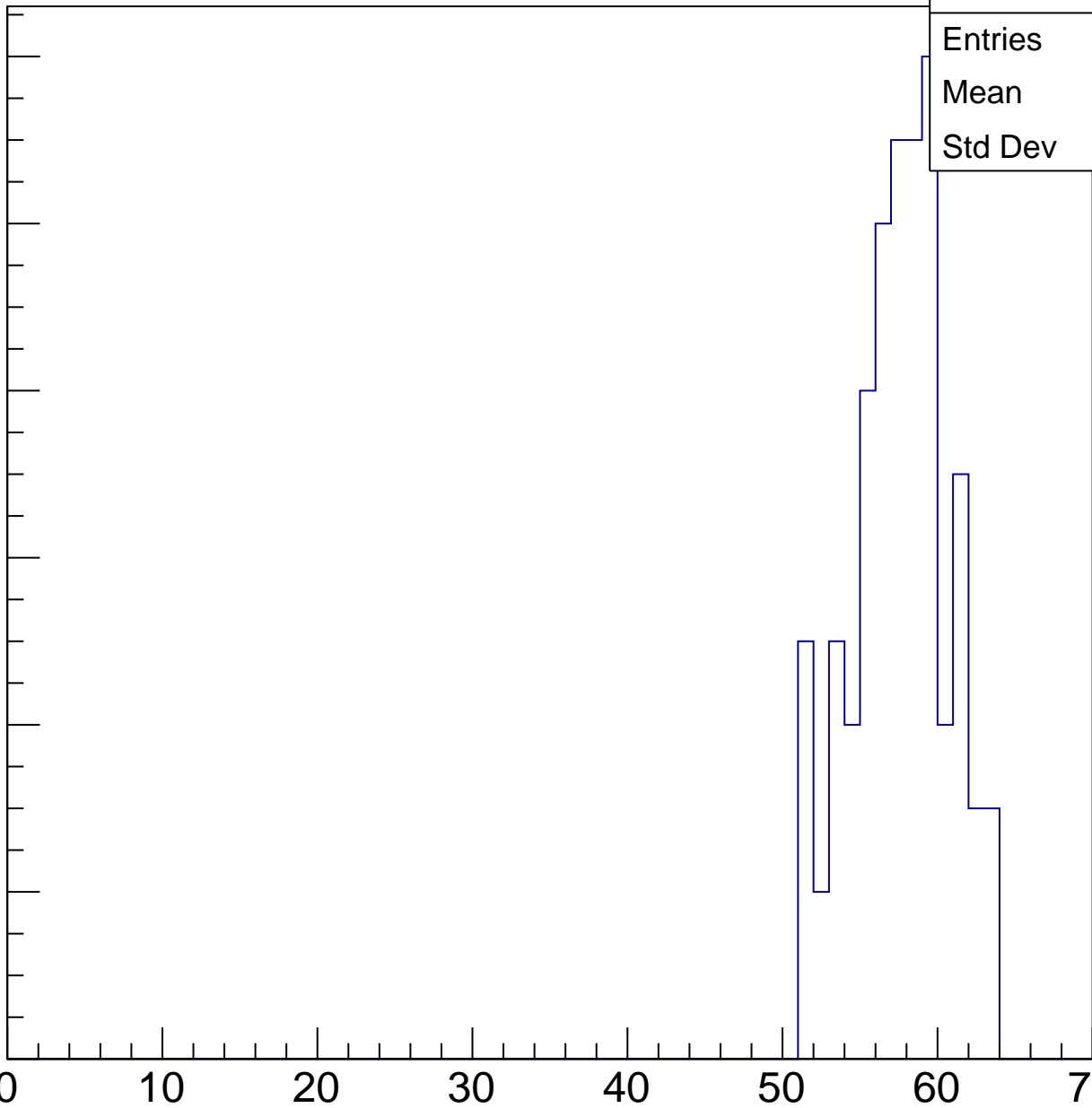
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	85
Mean	57.12
Std Dev	3.023

ampl



B1L001S, U25-ch26, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries	77
Mean	58.65
Std Dev	9.843

ampl

0

10

20

30

40

50

60

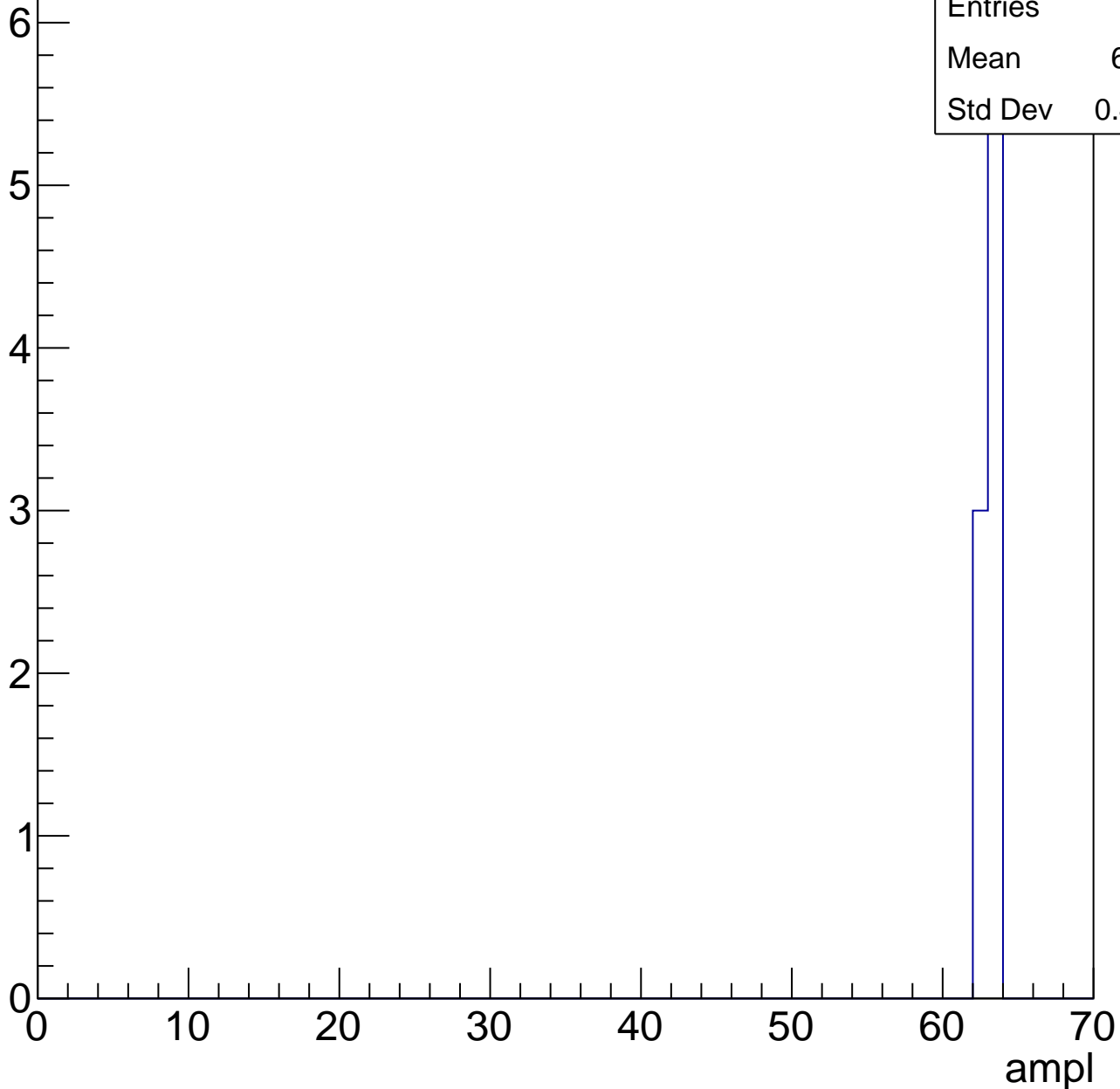
70

B1L001S, U25-ch26, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	9
Mean	62.67
Std Dev	0.4714



B1L001S, U25-ch26, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch27, adc0

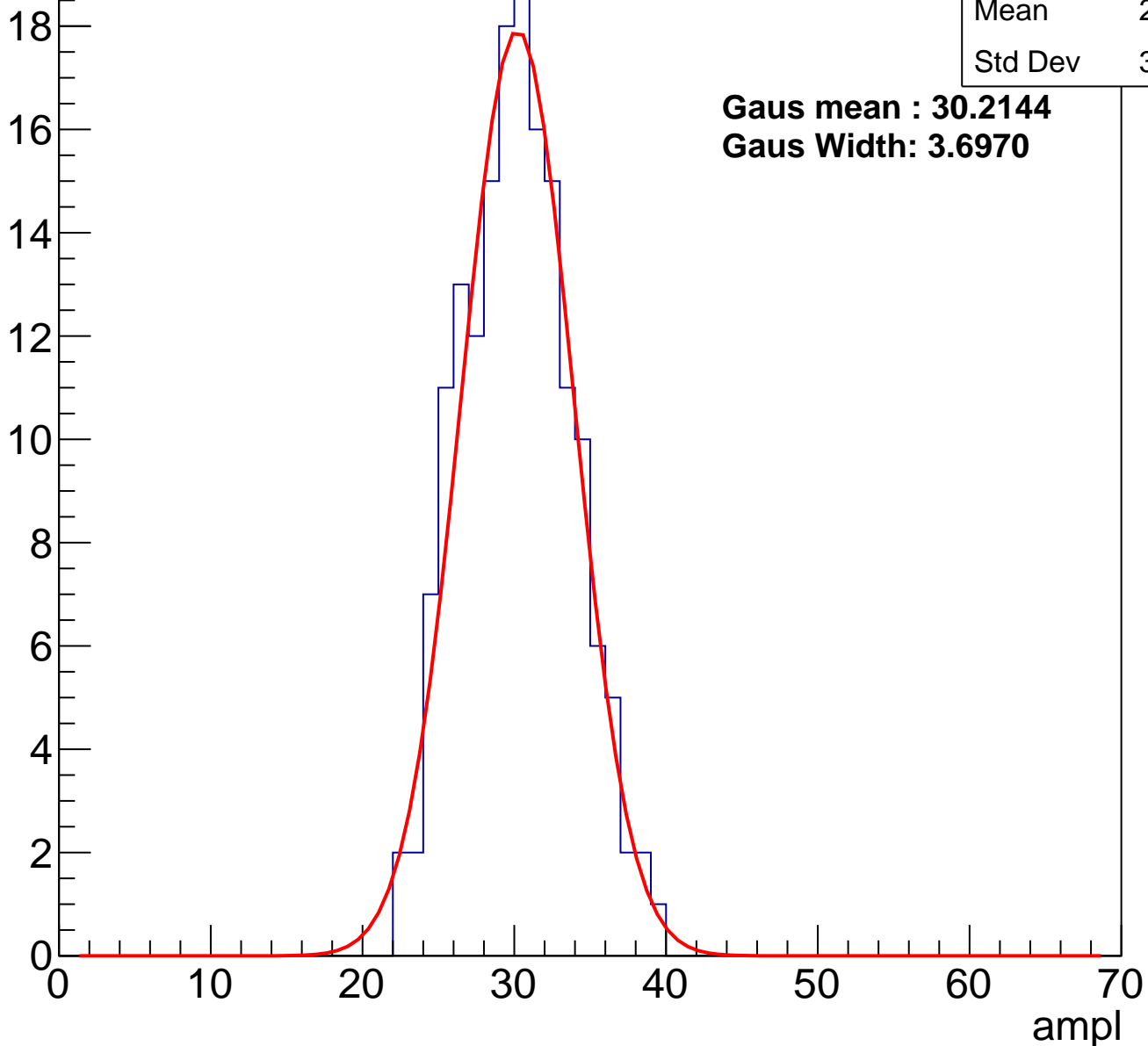
calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	29.73
Std Dev	3.552

Gaus mean : 30.2144

Gaus Width: 3.6970

Entry

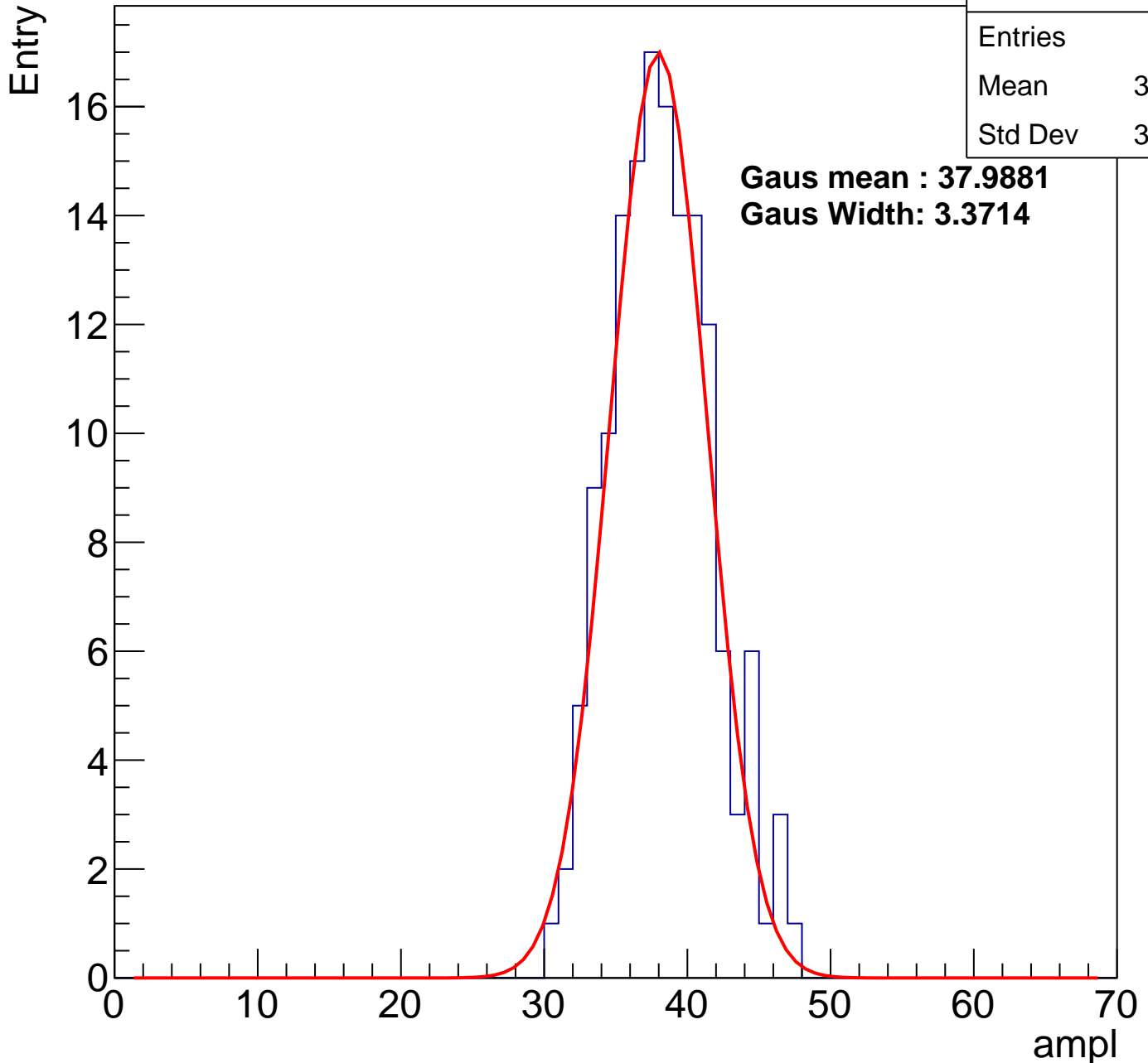


B1L001S, U25-ch27, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	37.78
Std Dev	3.485

Gaus mean : 37.9881
Gaus Width: 3.3714



B1L001S, U25-ch27, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

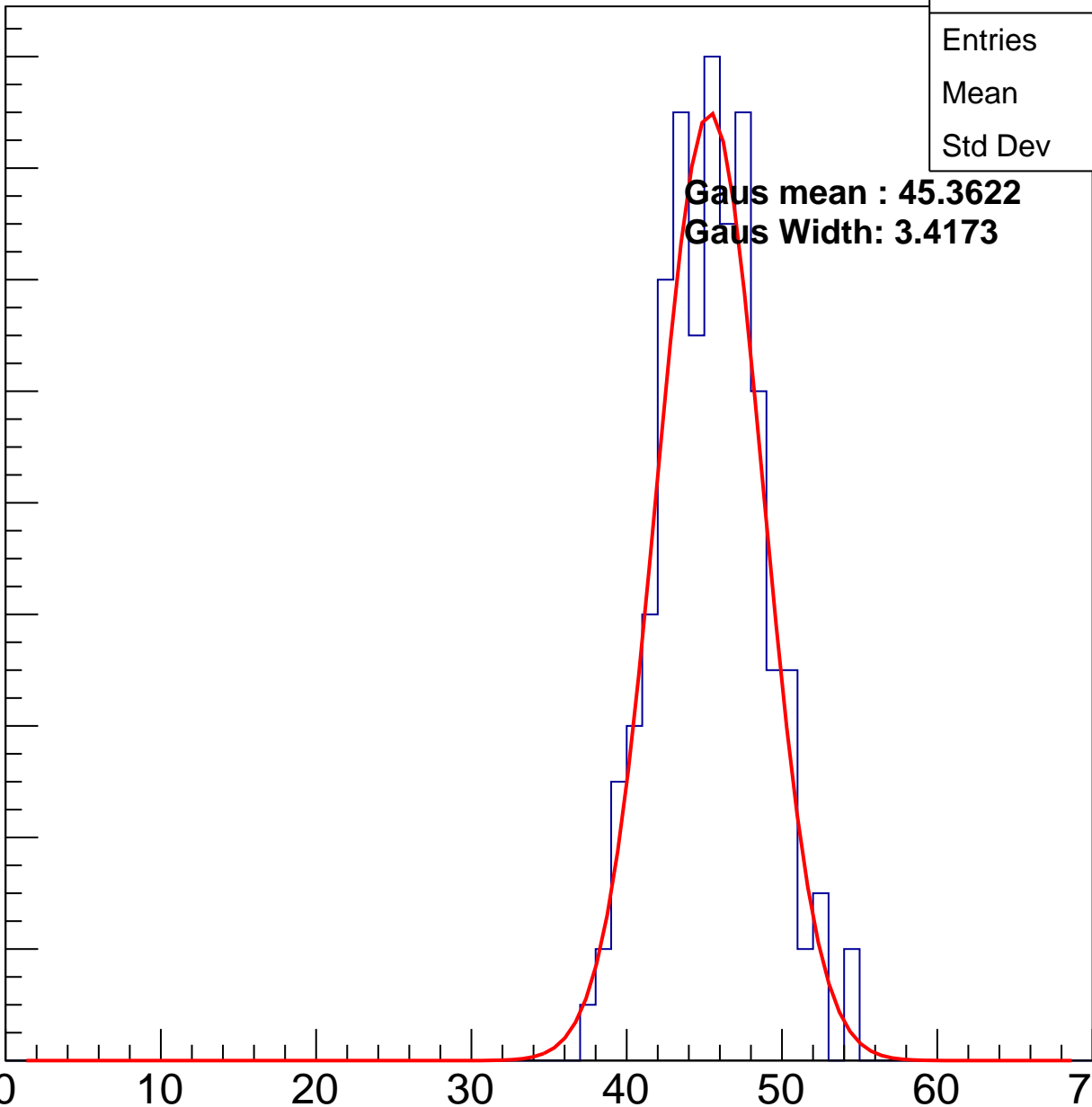
Entries	149
Mean	44.97
Std Dev	3.364

Gaus mean : 45.3622

Gaus Width: 3.4173

0 10 20 30 40 50 60 70

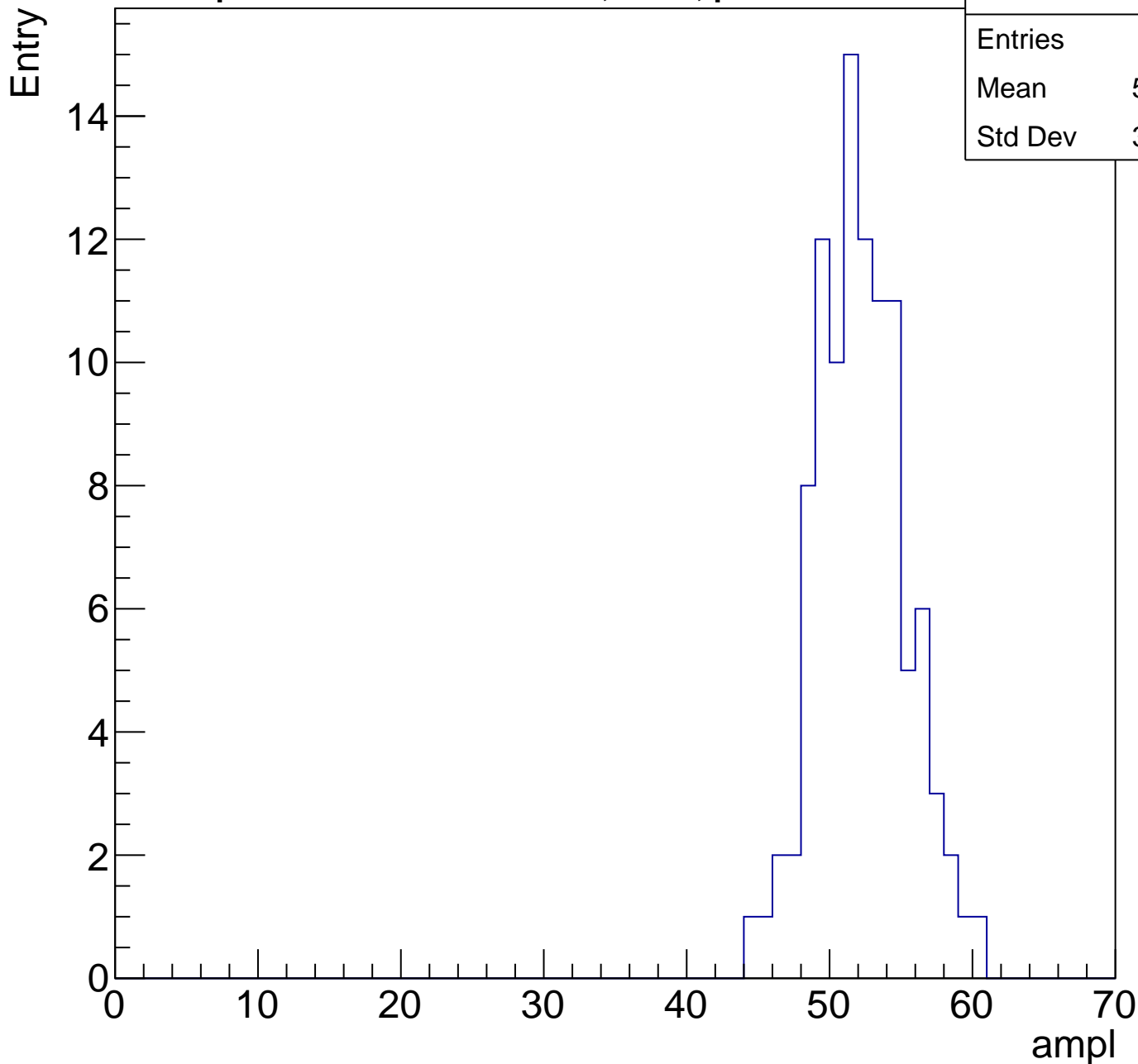
ampl



B1L001S, U25-ch27, adc3

calib_packv5_042523_0143.root, FC#2, port C2

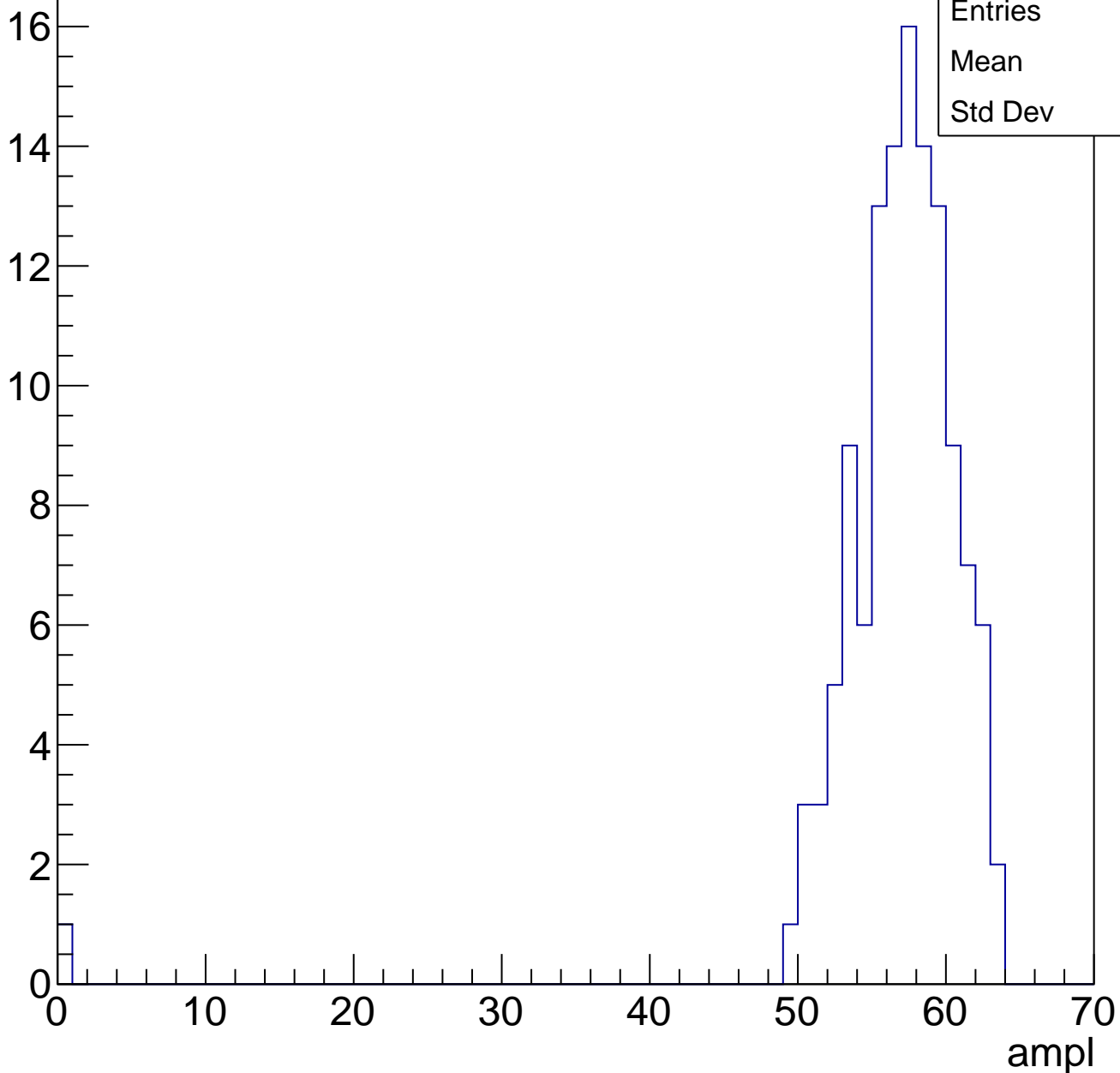
Entries	103
Mean	51.75
Std Dev	3.077



B1L001S, U25-ch27, adc4

calib_packv5_042523_0143.root, FC#2, port C2

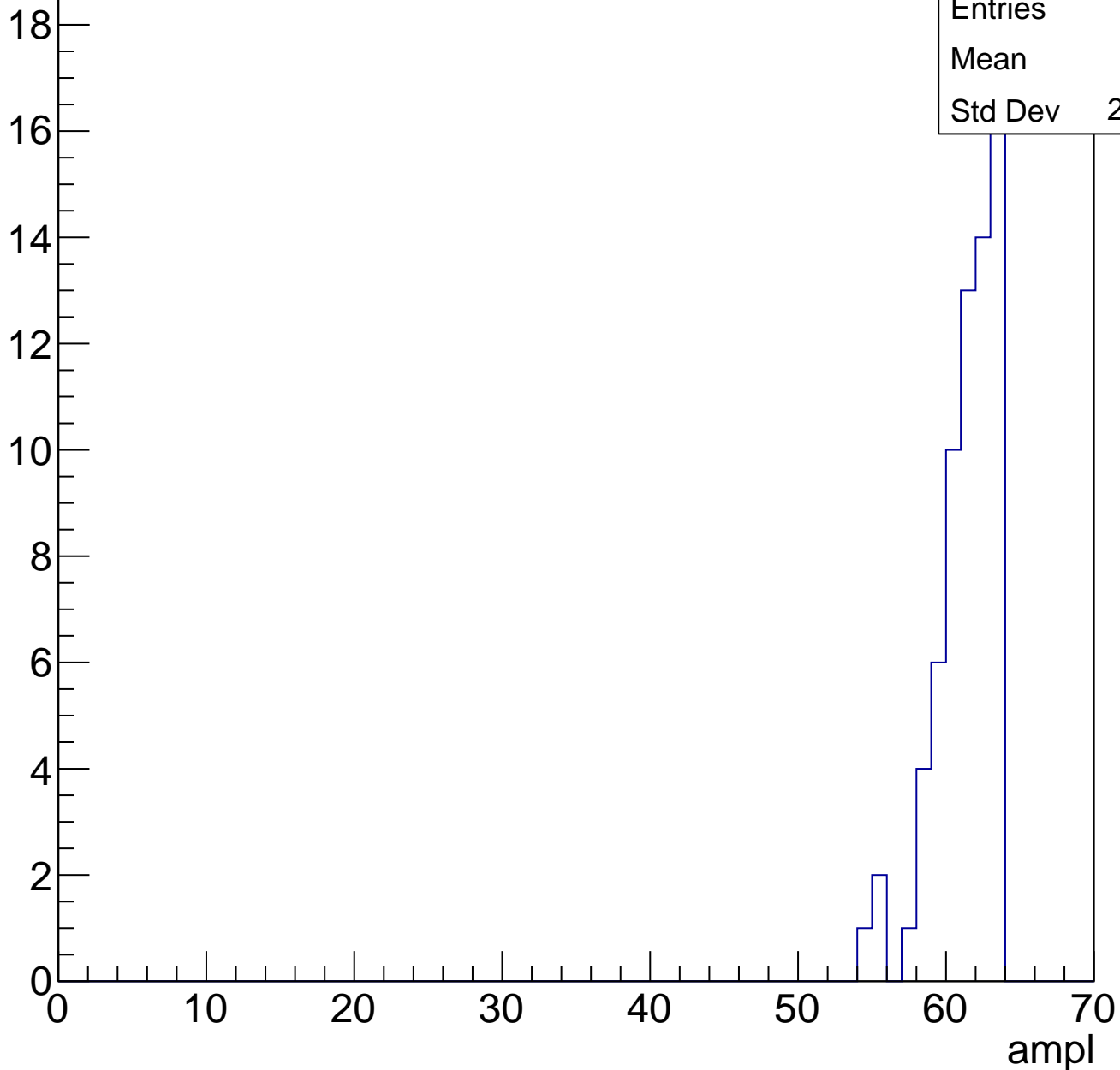
Entry



B1L001S, U25-ch27, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch27, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch27, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L001S, U25-ch28, adc0

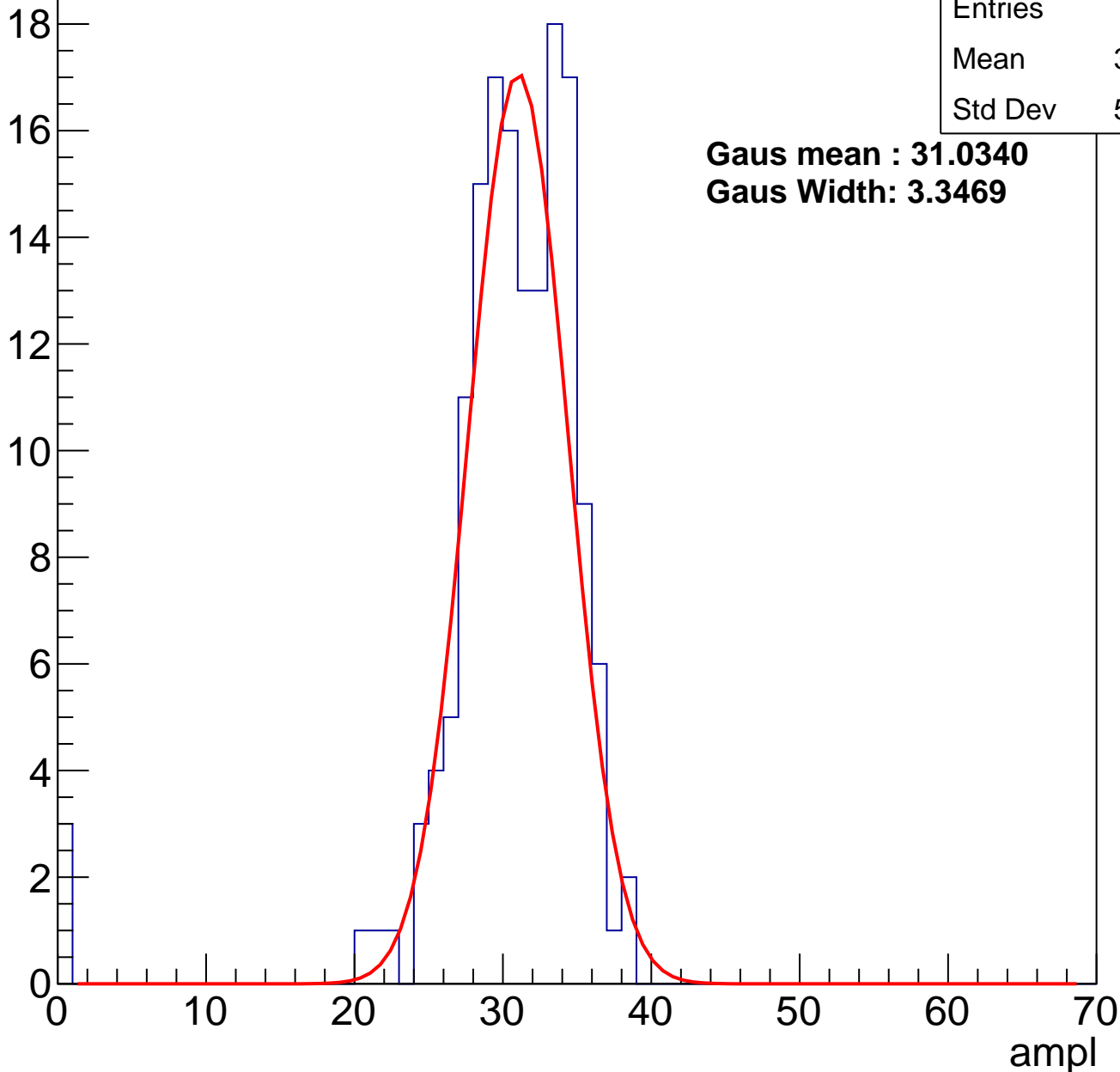
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	30.06
Std Dev	5.386

Gaus mean : 31.0340

Gaus Width: 3.3469

Entry

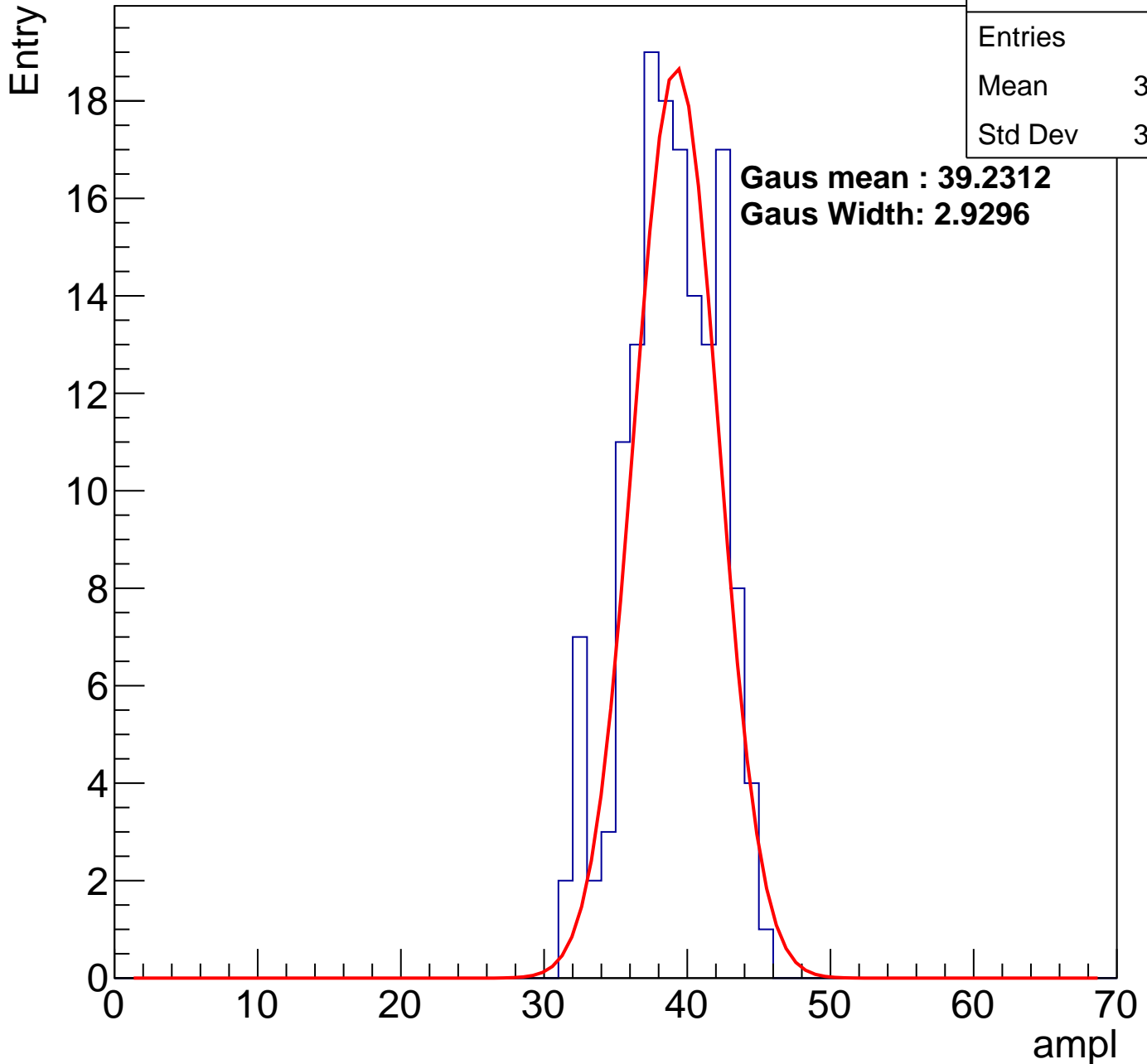


B1L001S, U25-ch28, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	38.45
Std Dev	3.112

Gaus mean : 39.2312
Gaus Width: 2.9296



B1L001S, U25-ch28, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	44.61
Std Dev	2.888

Gaus mean : 45.5551

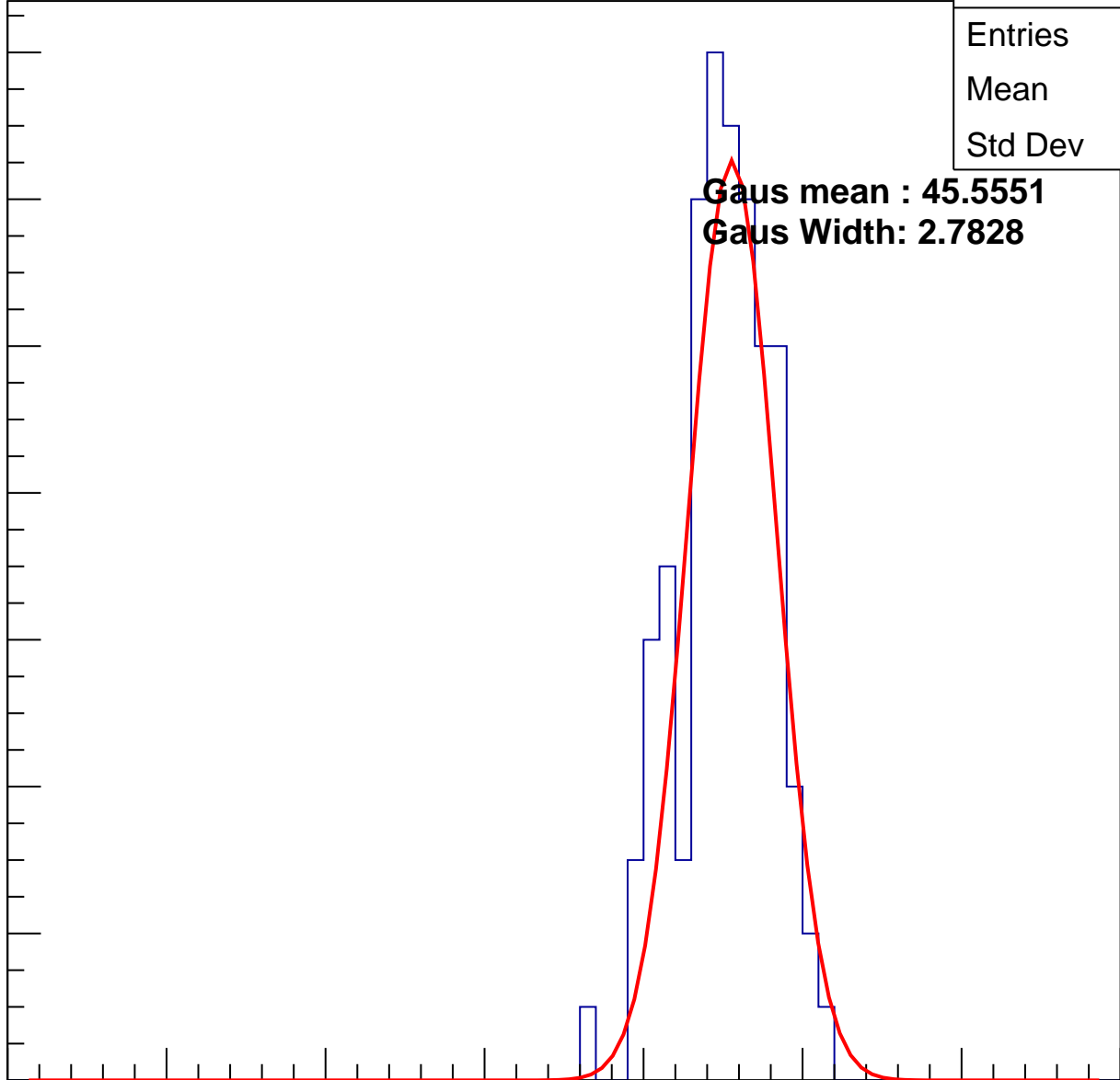
Gaus Width: 2.7828

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

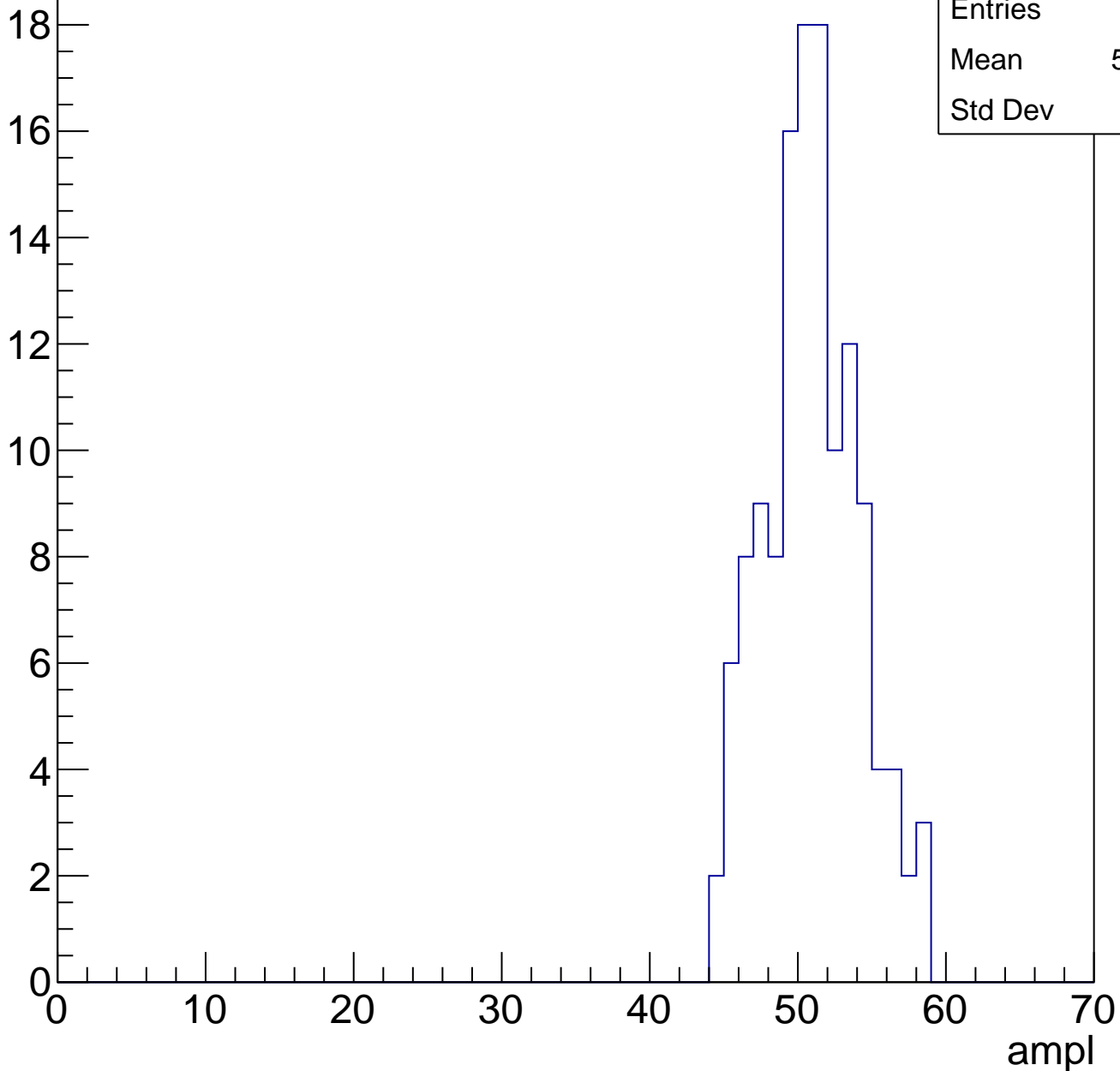


B1L001S, U25-ch28, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	50.46
Std Dev	3.17

Entry



B1L001S, U25-ch28, adc4

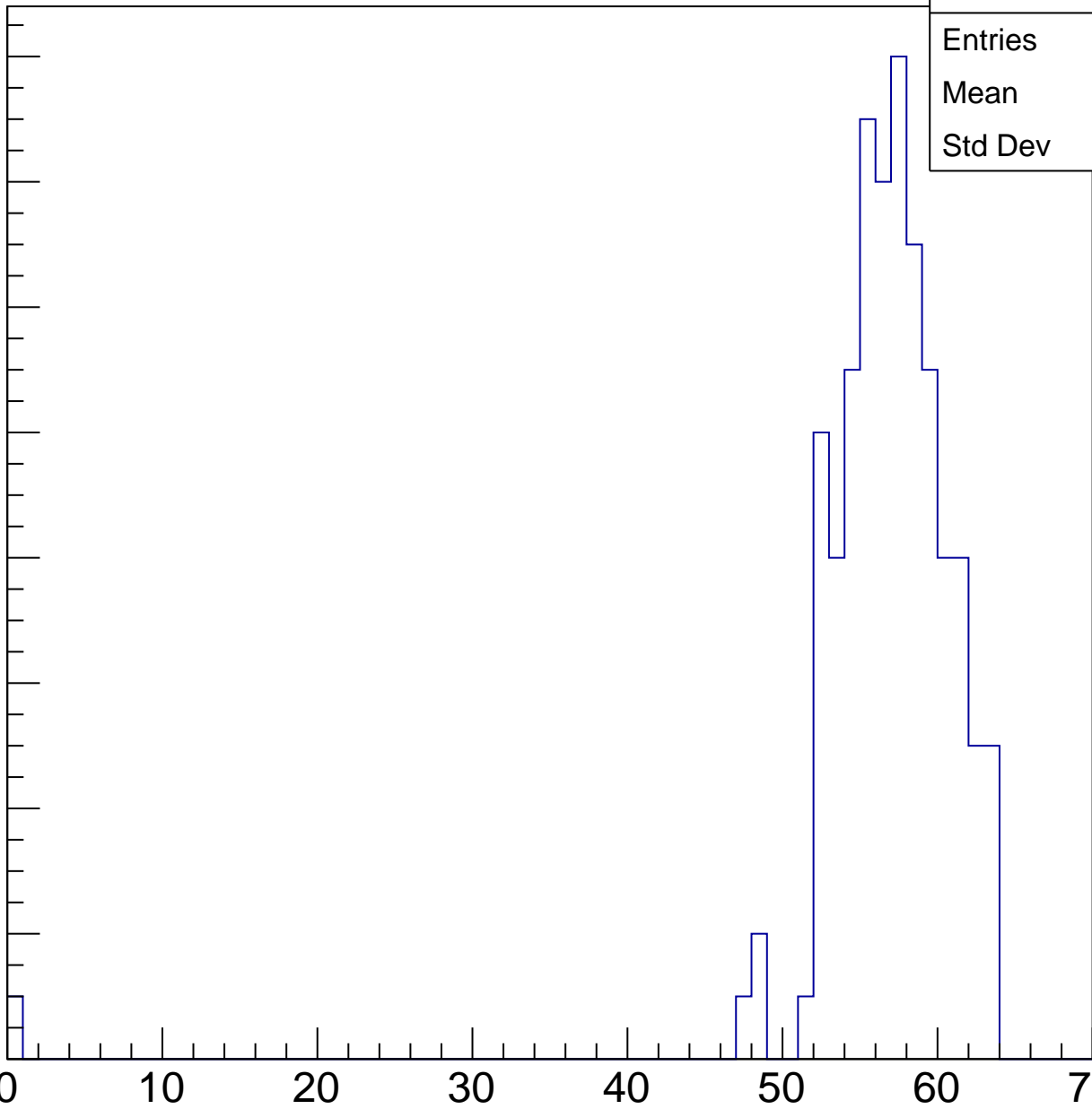
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	56.19
Std Dev	5.956

ampl



B1L001S, U25-ch28, adc5

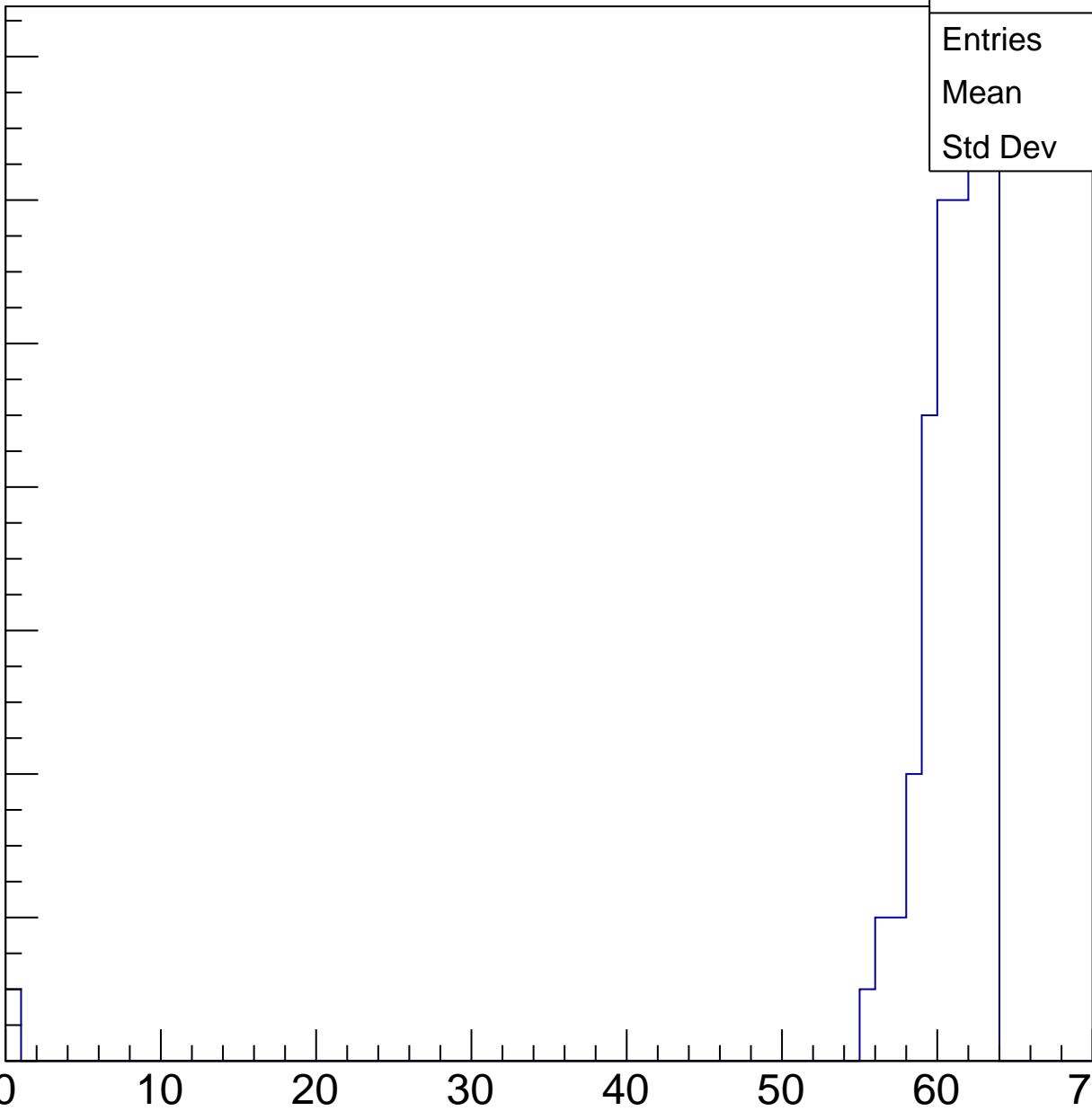
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	59.8
Std Dev	7.403

ampl



B1L001S, U25-ch28, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch28, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

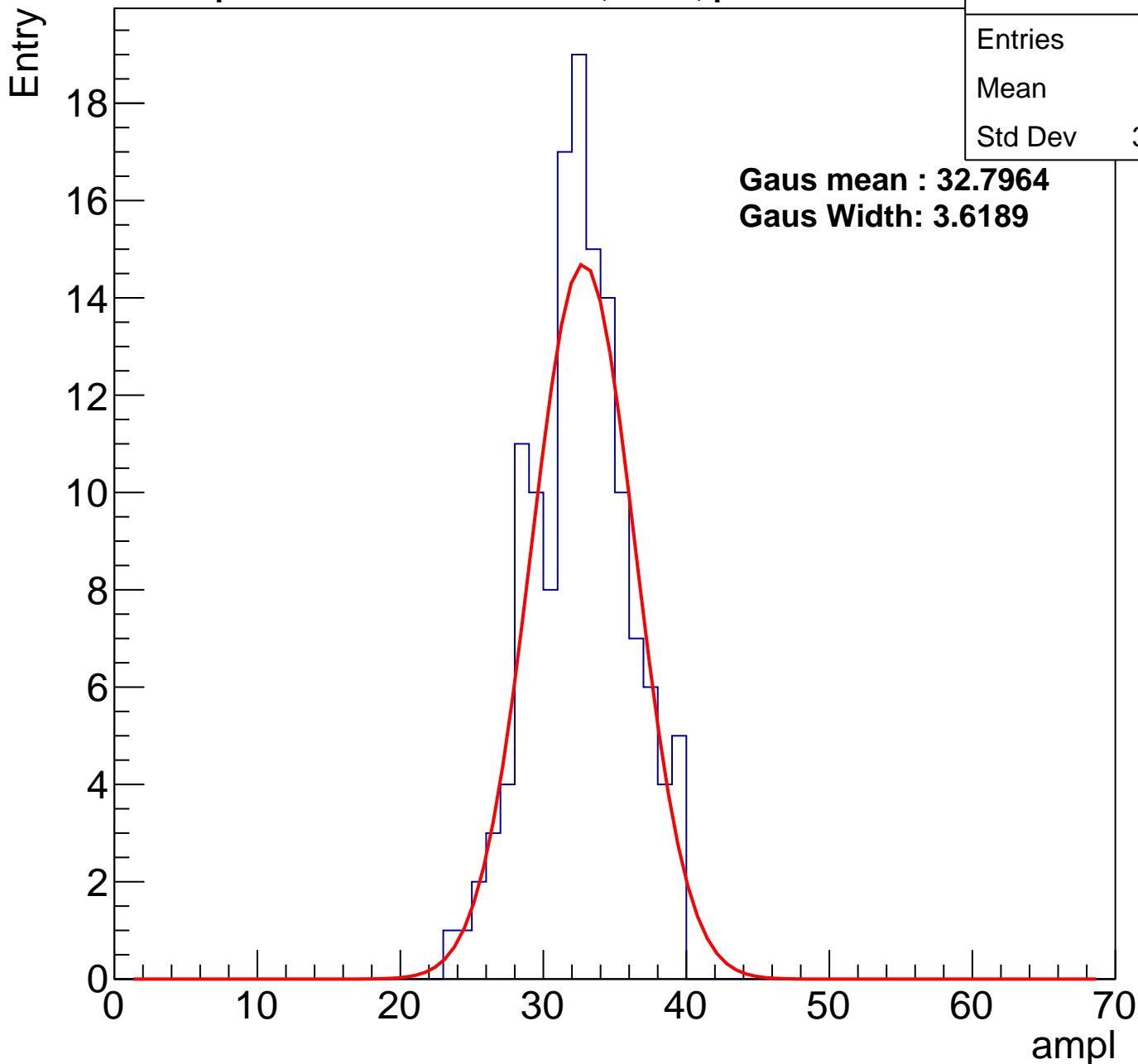
B1L001S, U25-ch29, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	32.1
Std Dev	3.392

Gaus mean : 32.7964

Gaus Width: 3.6189



B1L001S, U25-ch29, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	39.42
Std Dev	3.384

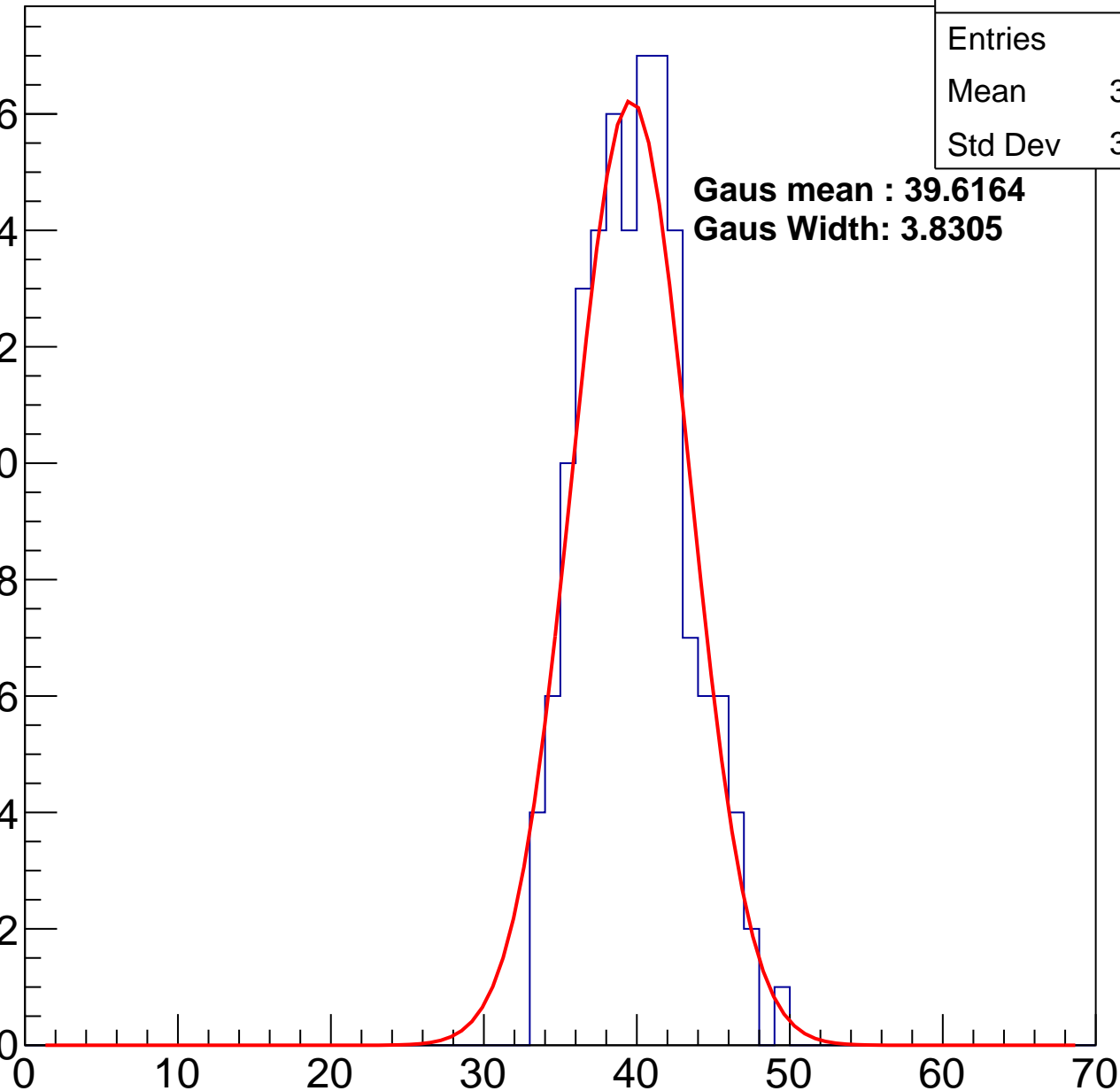
Gaus mean : 39.6164

Gaus Width: 3.8305

Entry

16
14
12
10
8
6
4
2
0

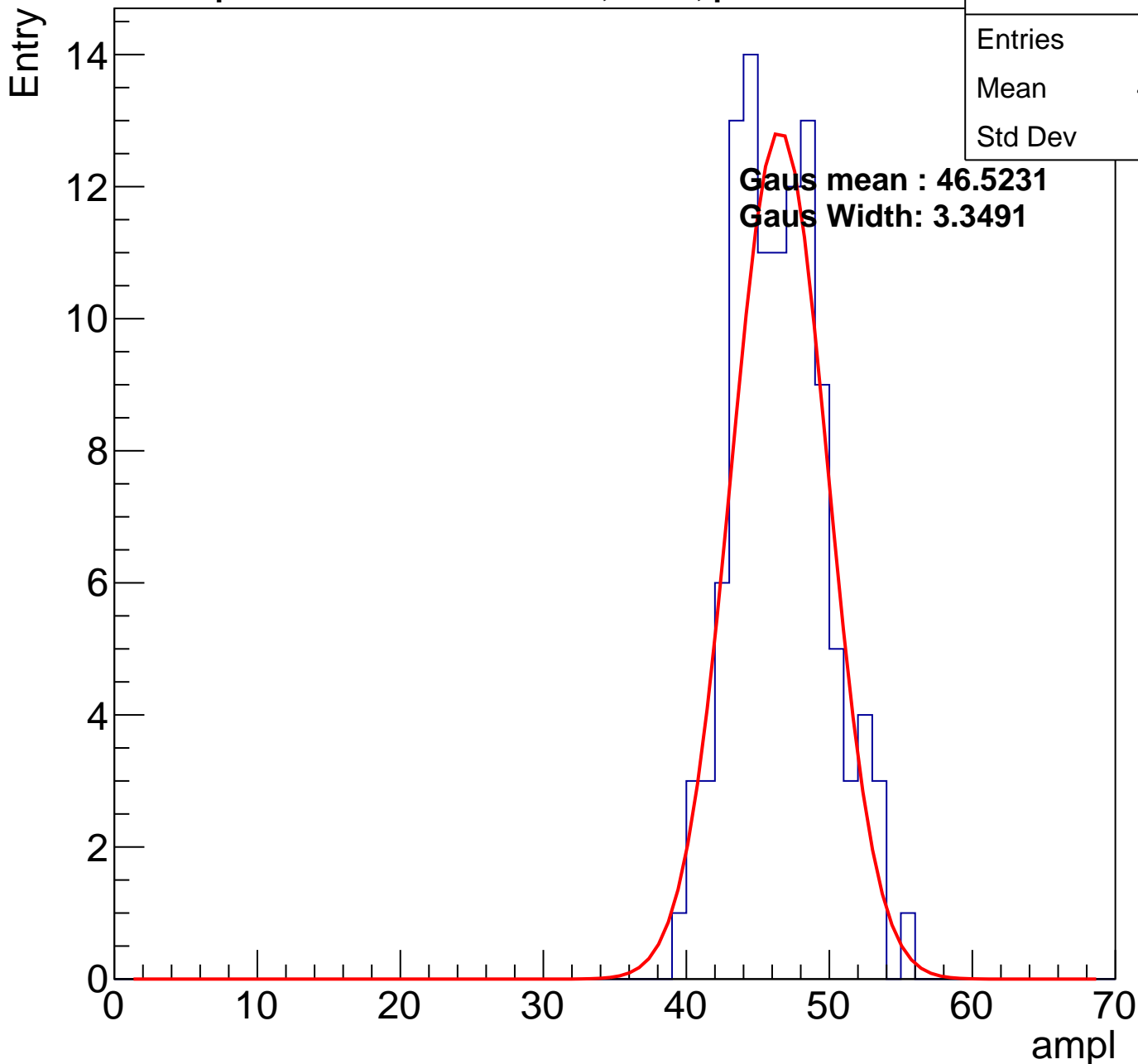
ampl



B1L001S, U25-ch29, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	46.11
Std Dev	3.25



B1L001S, U25-ch29, adc3

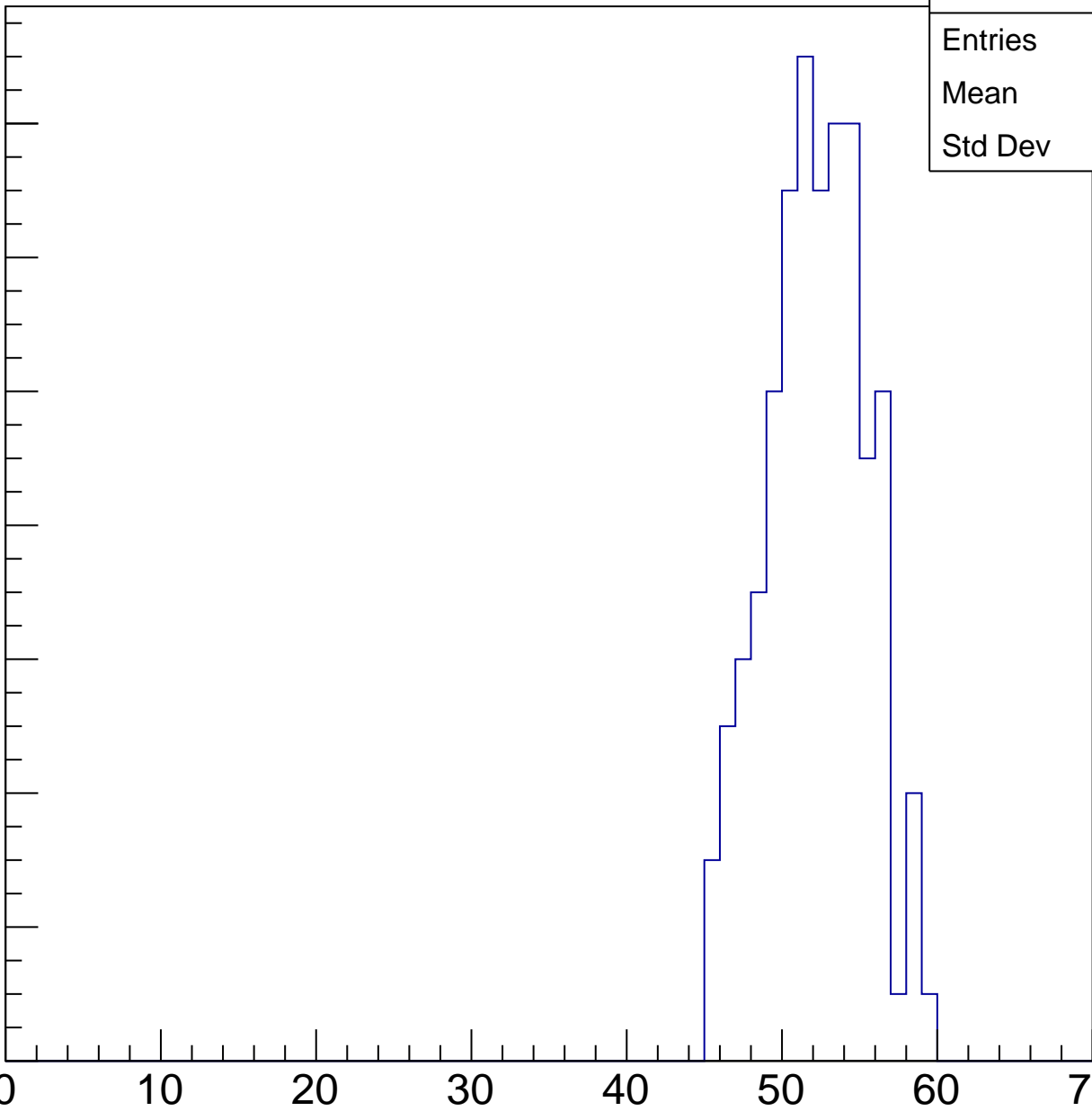
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	51.72
Std Dev	3.181

Entry

14
12
10
8
6
4
2
0

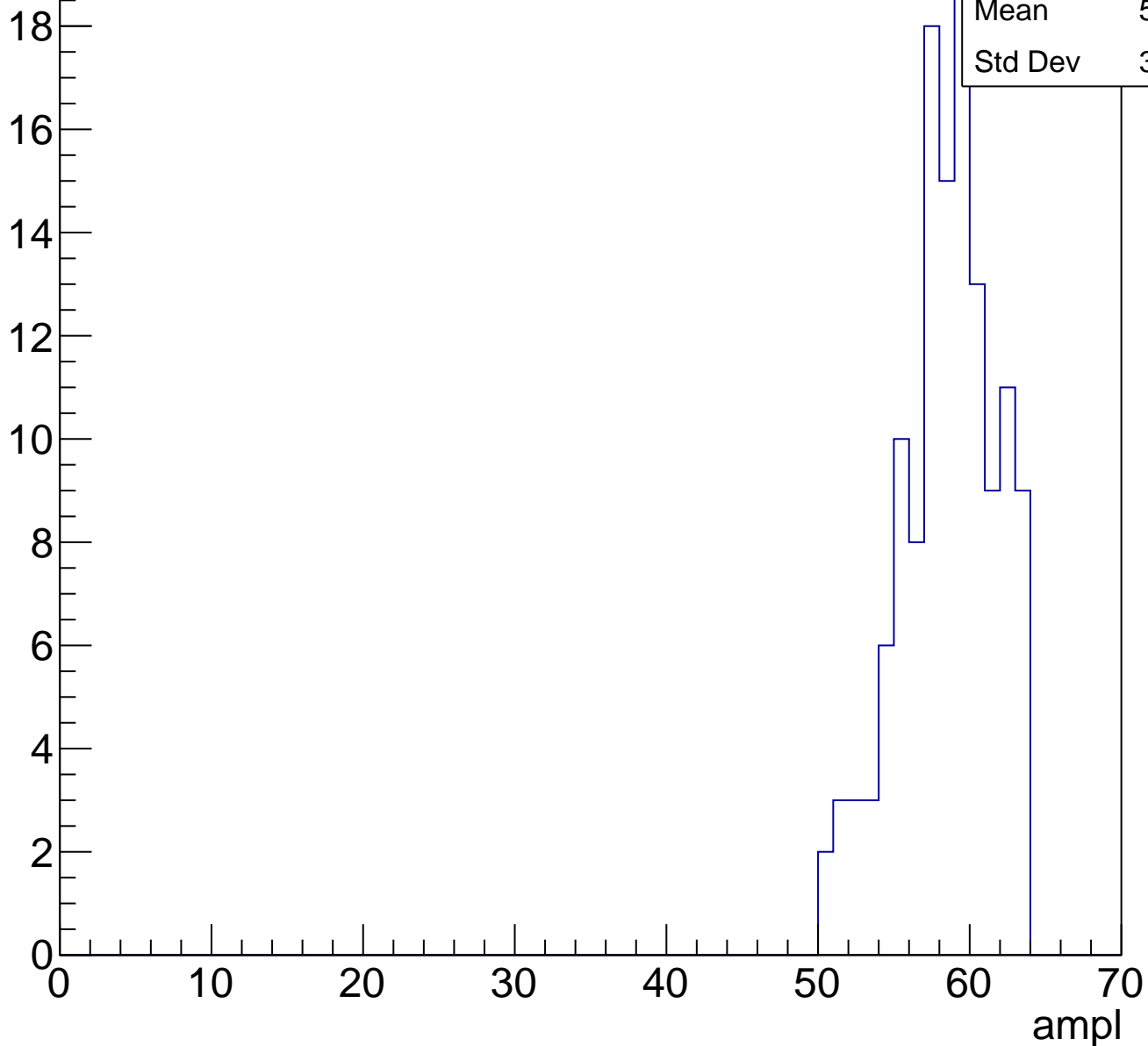
ampl



B1L001S, U25-ch29, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch29, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	43
Mean	58.28
Std Dev	12.99

B1L001S, U25-ch29, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch29, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



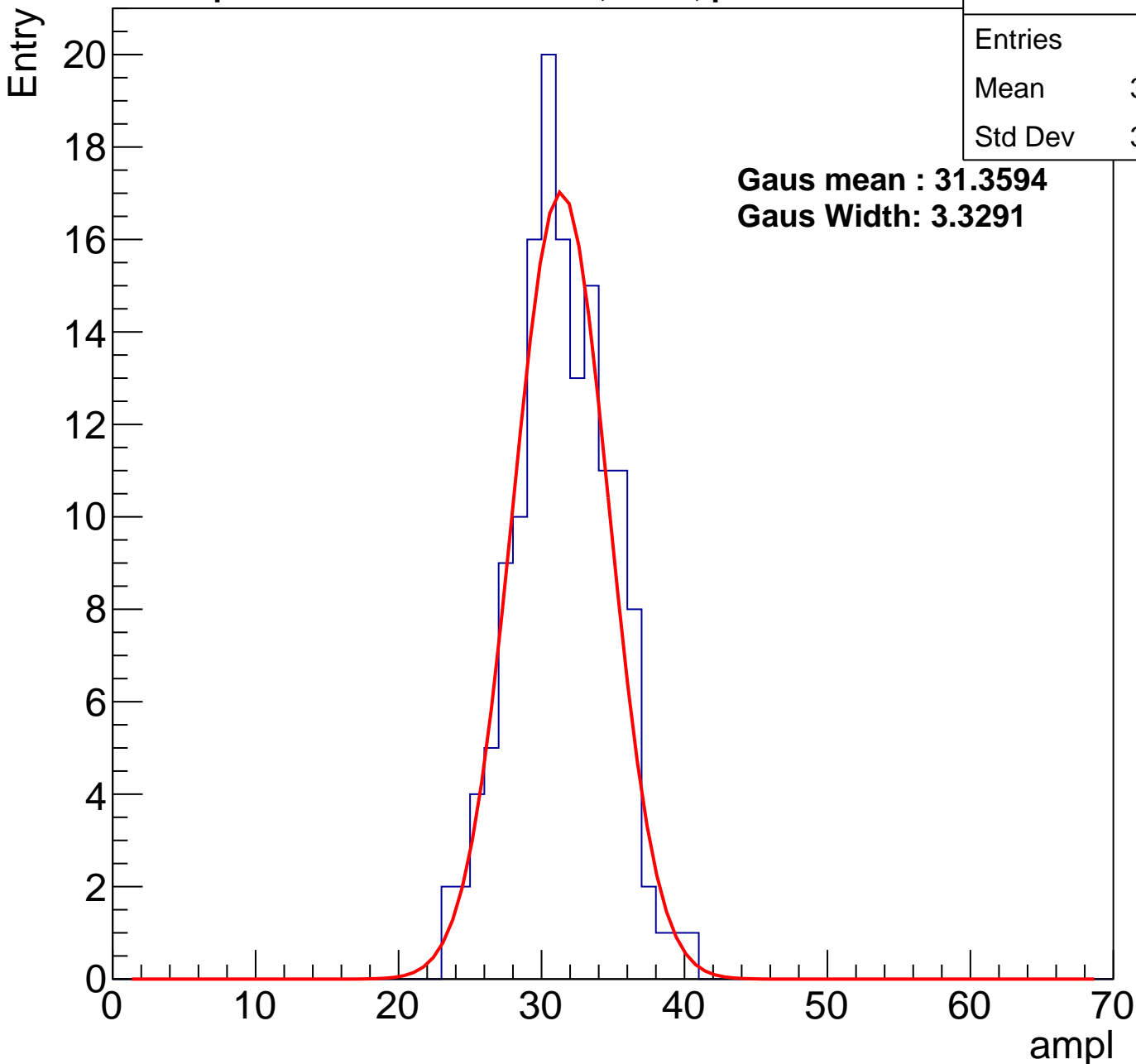
B1L001S, U25-ch30, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	30.99
Std Dev	3.327

Gaus mean : 31.3594

Gaus Width: 3.3291



B1L001S, U25-ch30, adc1

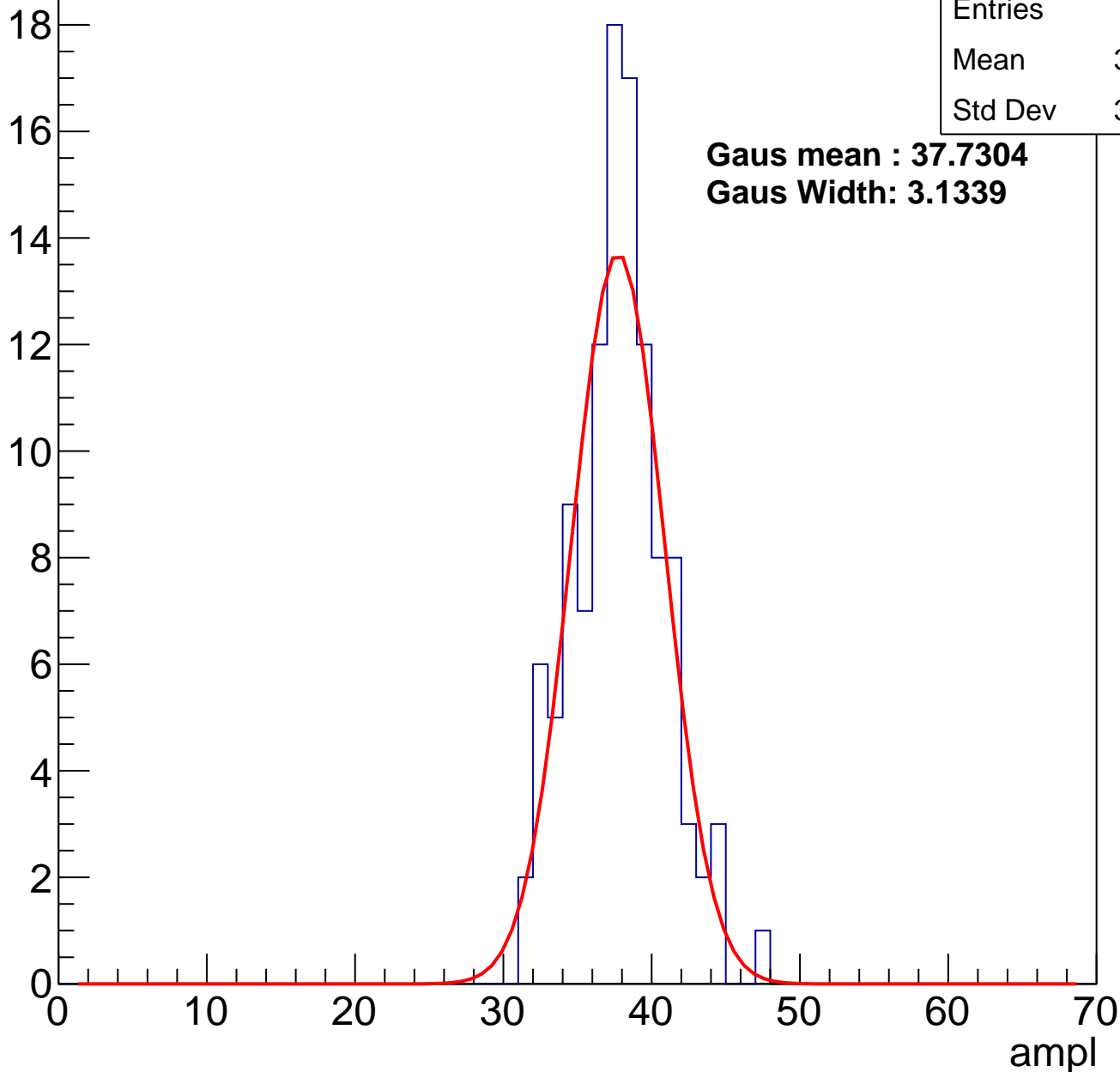
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	37.35
Std Dev	3.074

Gaus mean : 37.7304

Gaus Width: 3.1339

Entry



B1L001S, U25-ch30, adc2

calib_packv5_042523_0143.root, FC#2, port C2

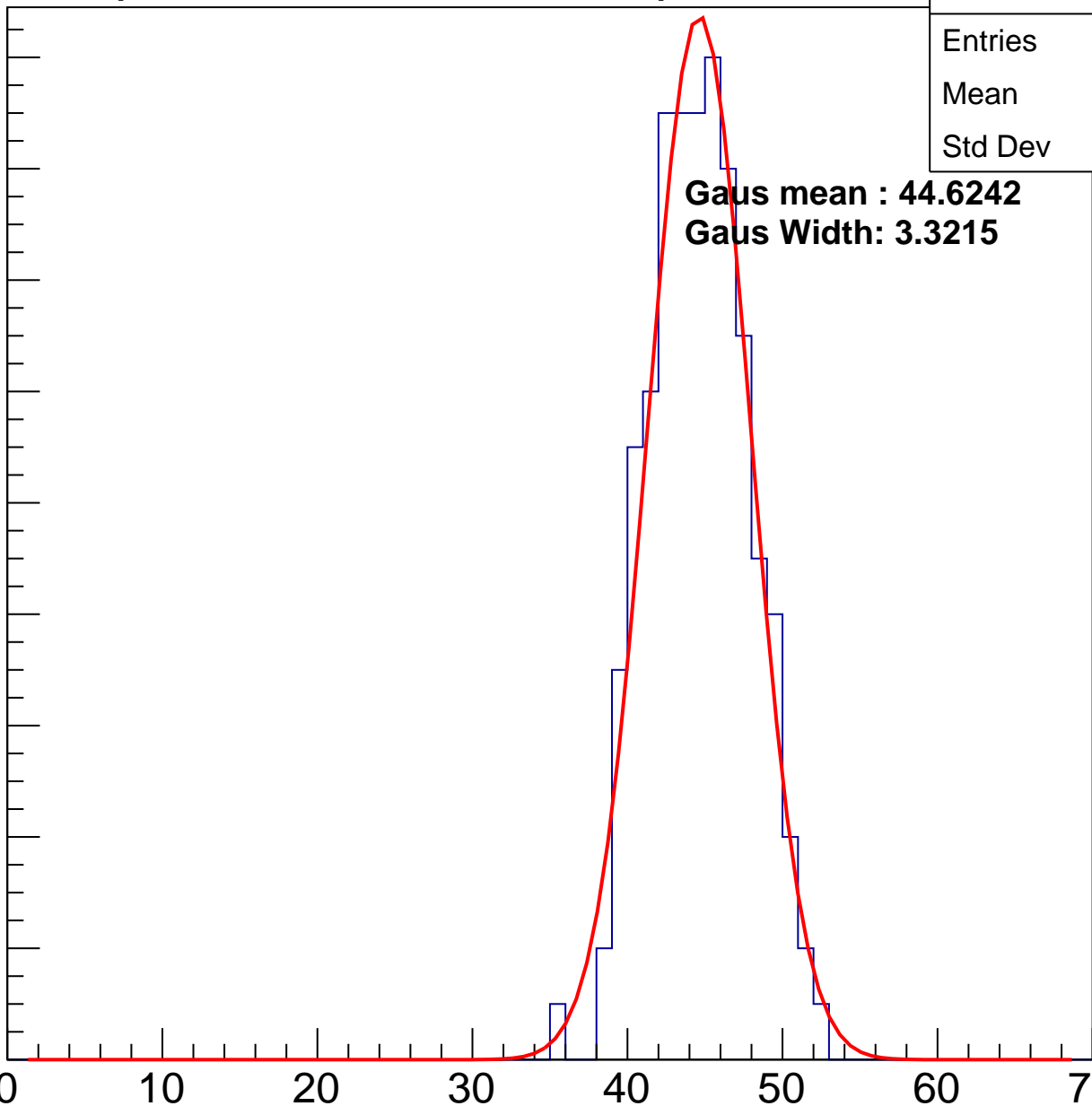
Entry

18
16
14
12
10
8
6
4
2
0

Entries	155
Mean	44.15
Std Dev	3.154

Gaus mean : 44.6242
Gaus Width: 3.3215

0 10 20 30 40 50 60 70
ampl



B1L001S, U25-ch30, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	51.59
Std Dev	3.393

Entry

18
16
14
12
10
8
6
4
2
0

0

10

20

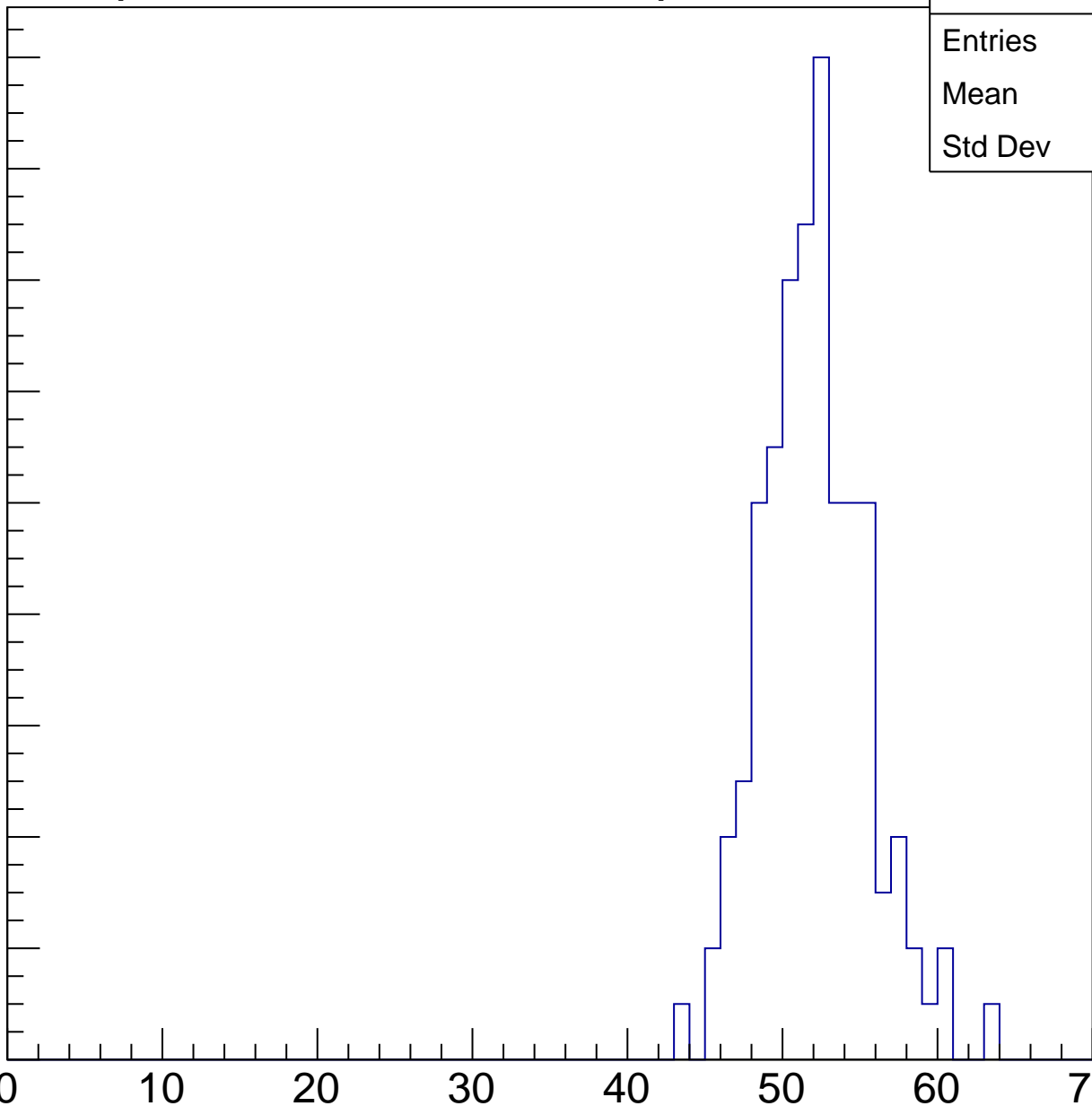
30

40

50

60

ampl



B1L001S, U25-ch30, adc4

calib_packv5_042523_0143.root, FC#2, port C2

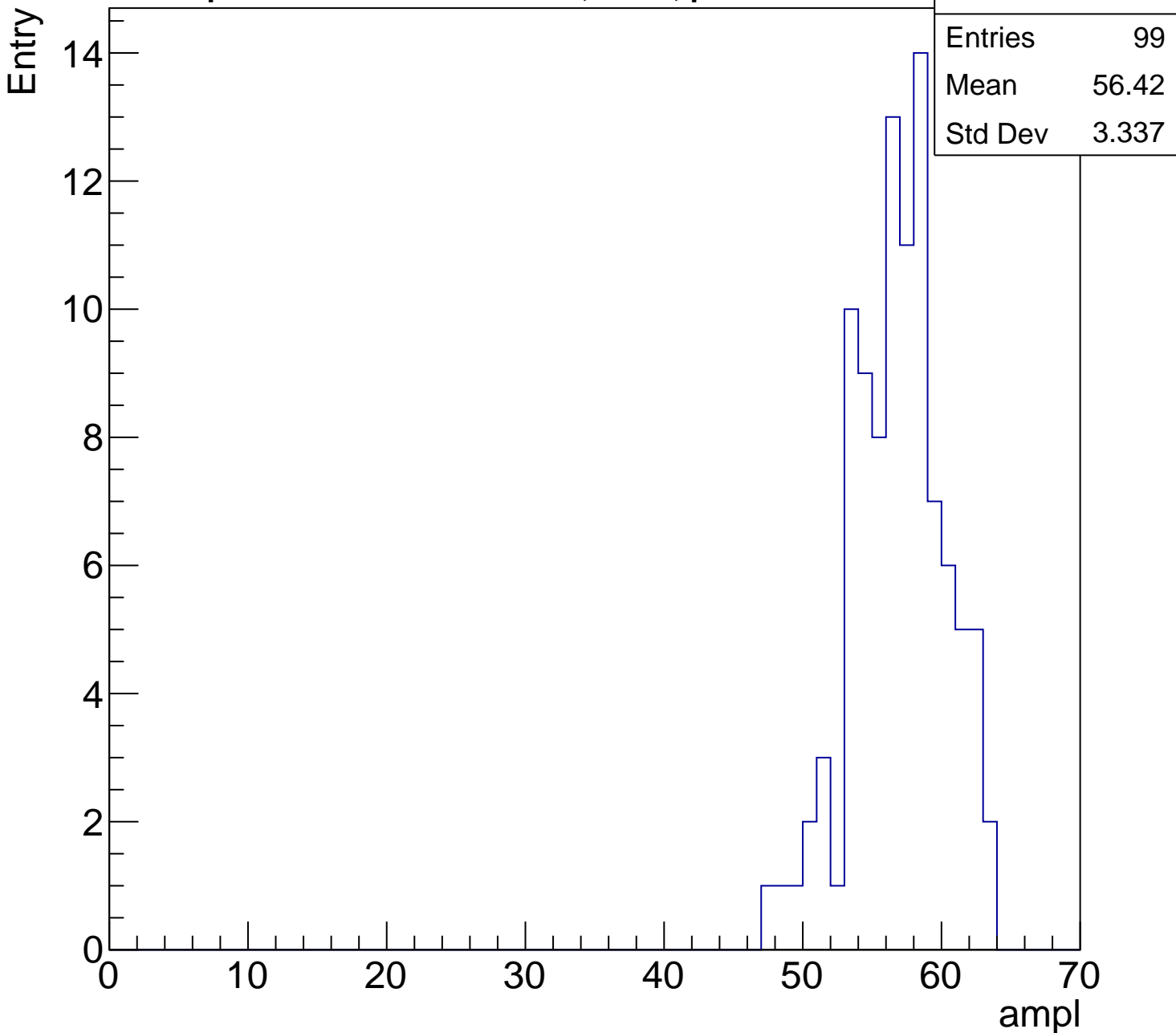
Entries	99
Mean	56.42
Std Dev	3.337

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch30, adc5

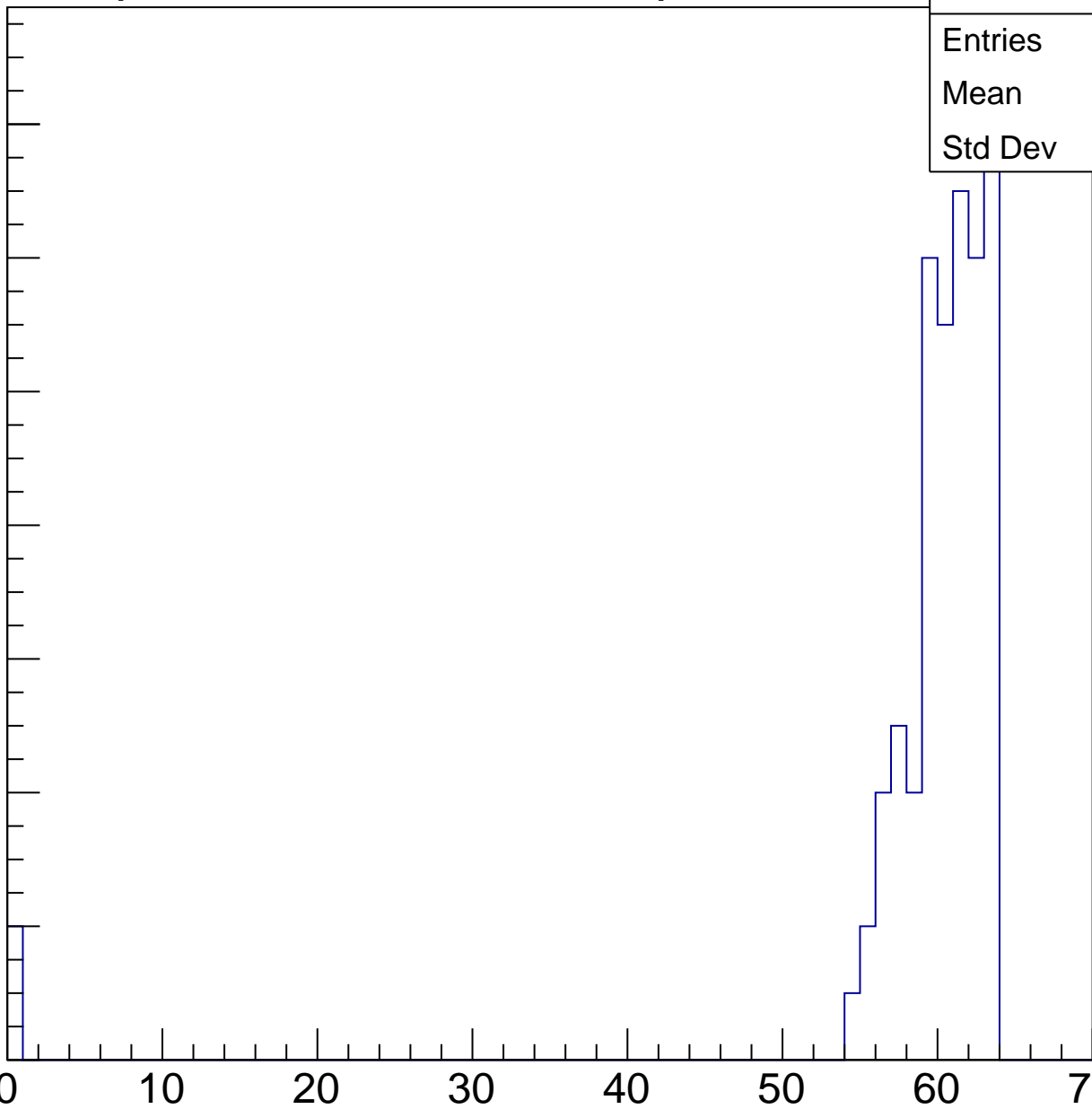
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	58.7
Std Dev	9.612

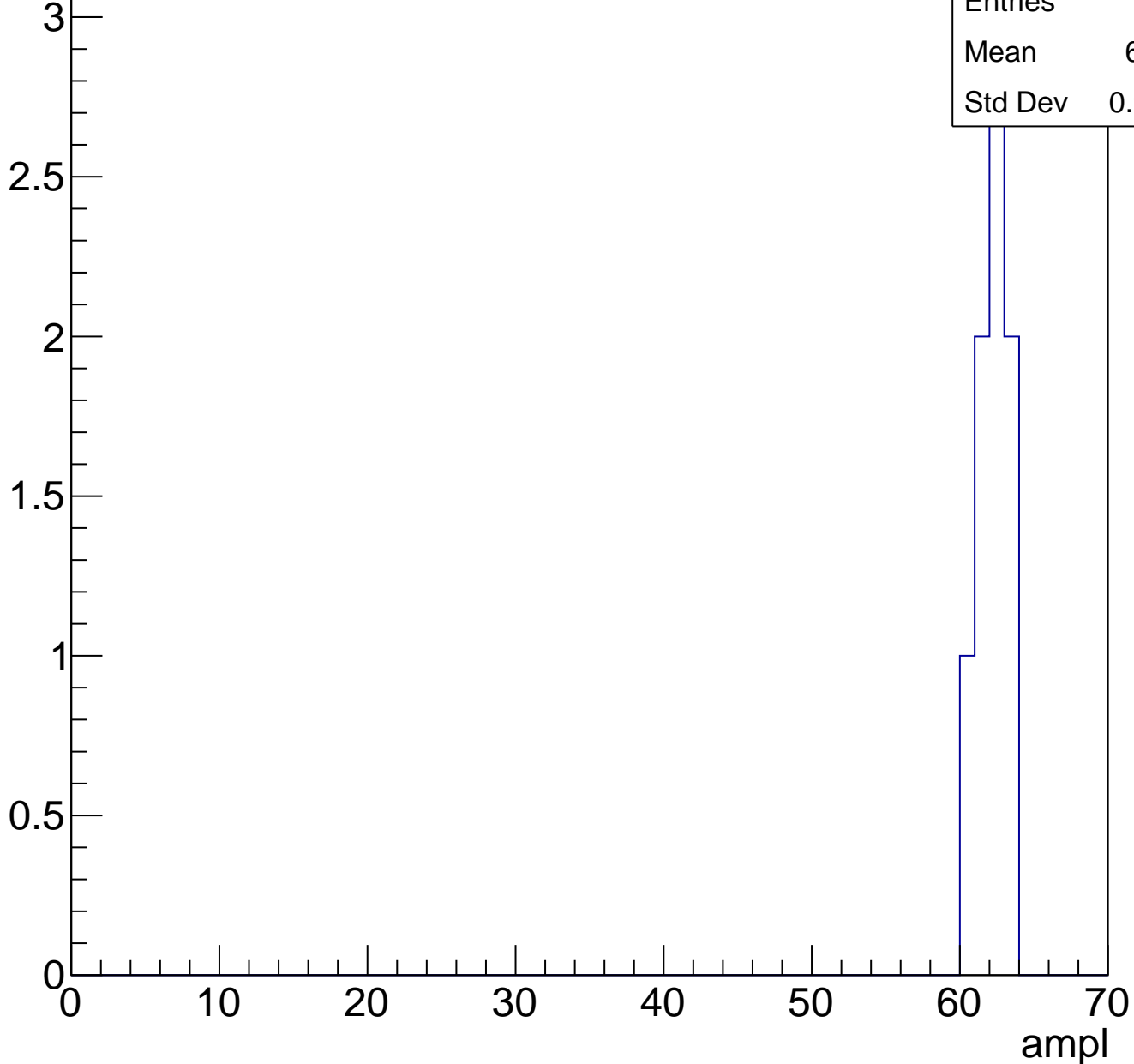
ampl



B1L001S, U25-ch30, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch30, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U25-ch31, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	31.32
Std Dev	4.266

Gaus mean : 32.1822

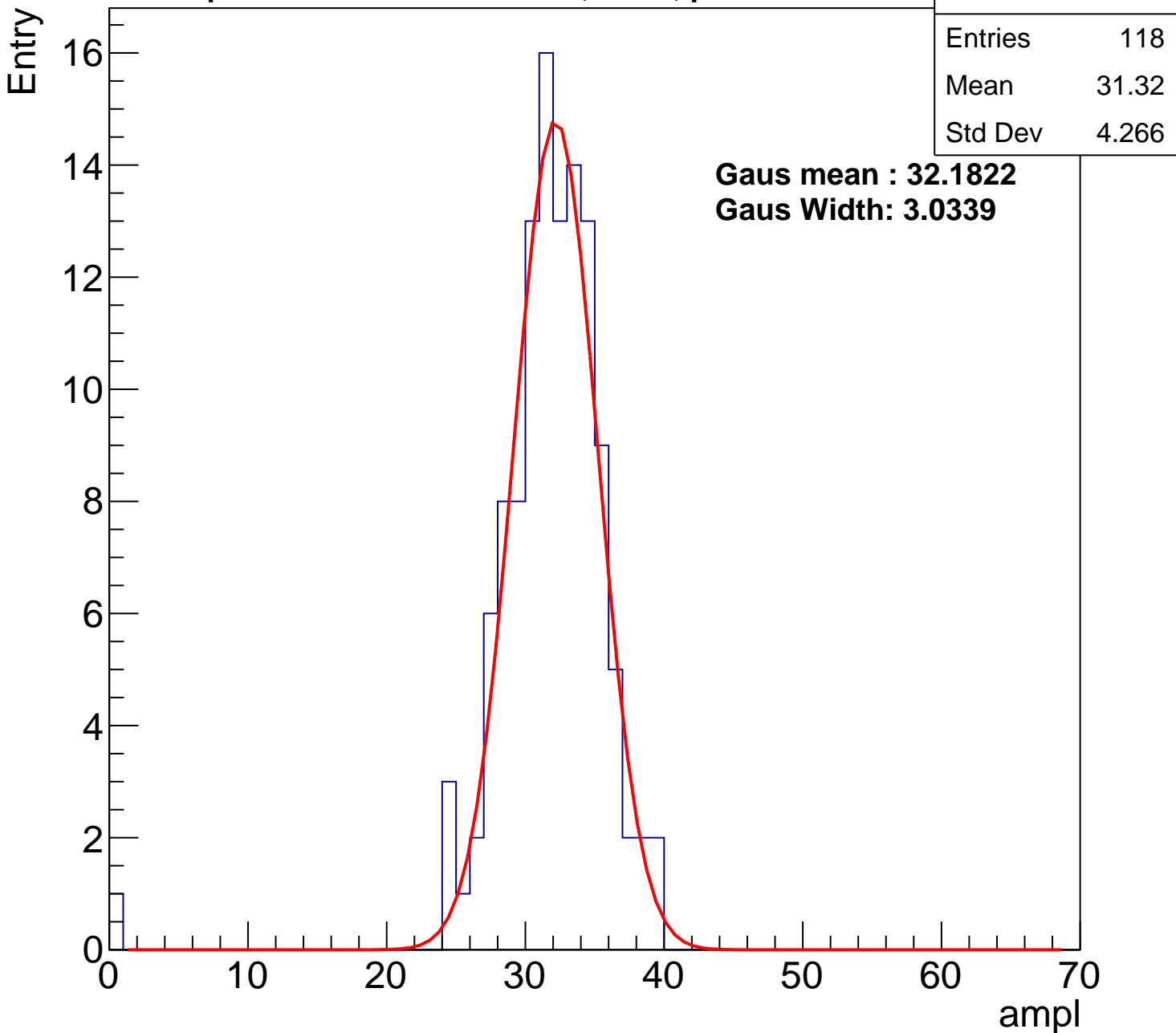
Gaus Width: 3.0339

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

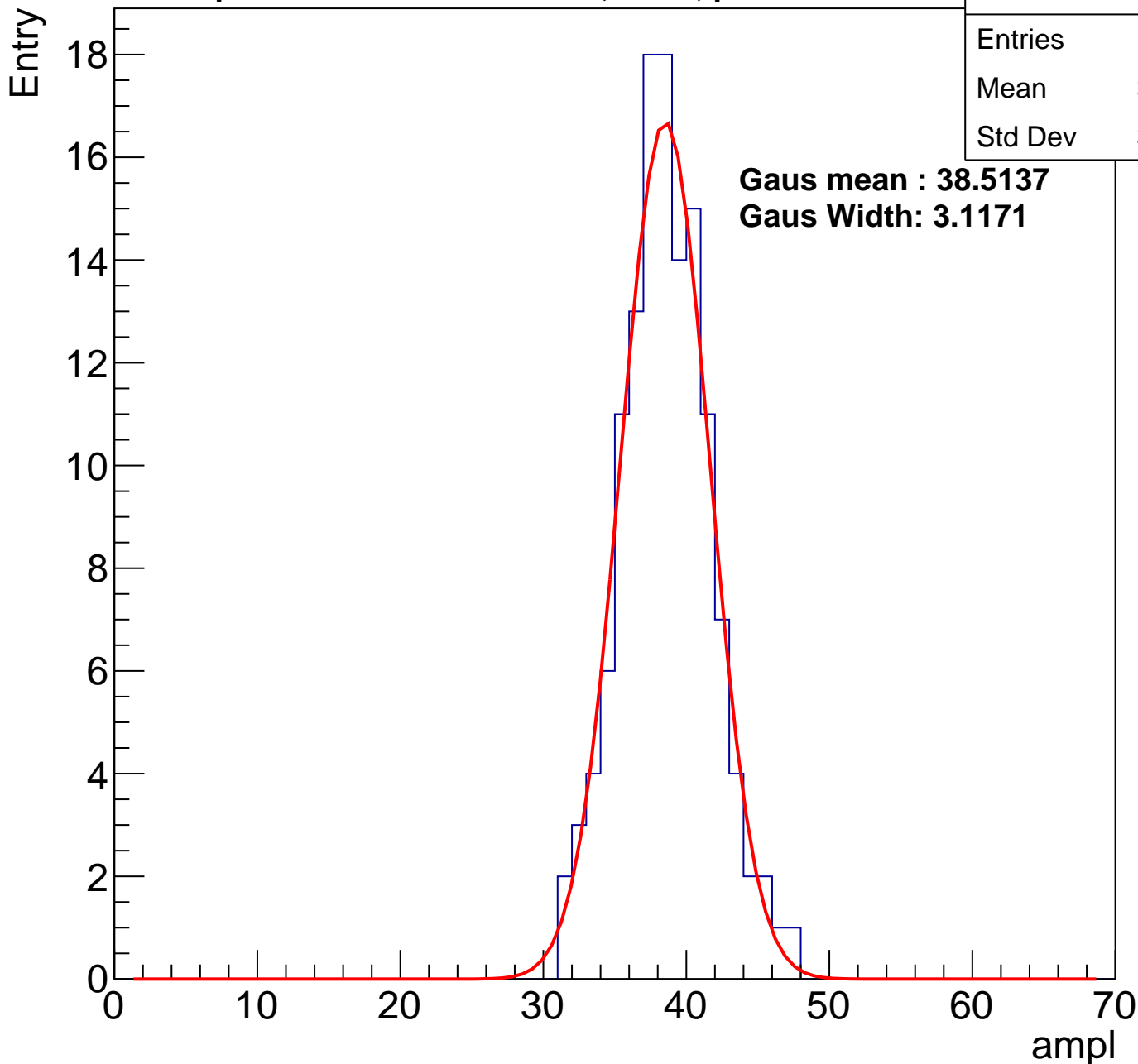


B1L001S, U25-ch31, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	38.11
Std Dev	3.101

Gaus mean : 38.5137
Gaus Width: 3.1171



B1L001S, U25-ch31, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	44.5
Std Dev	3.061

Gaus mean : 44.8362

Gaus Width: 3.2431

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

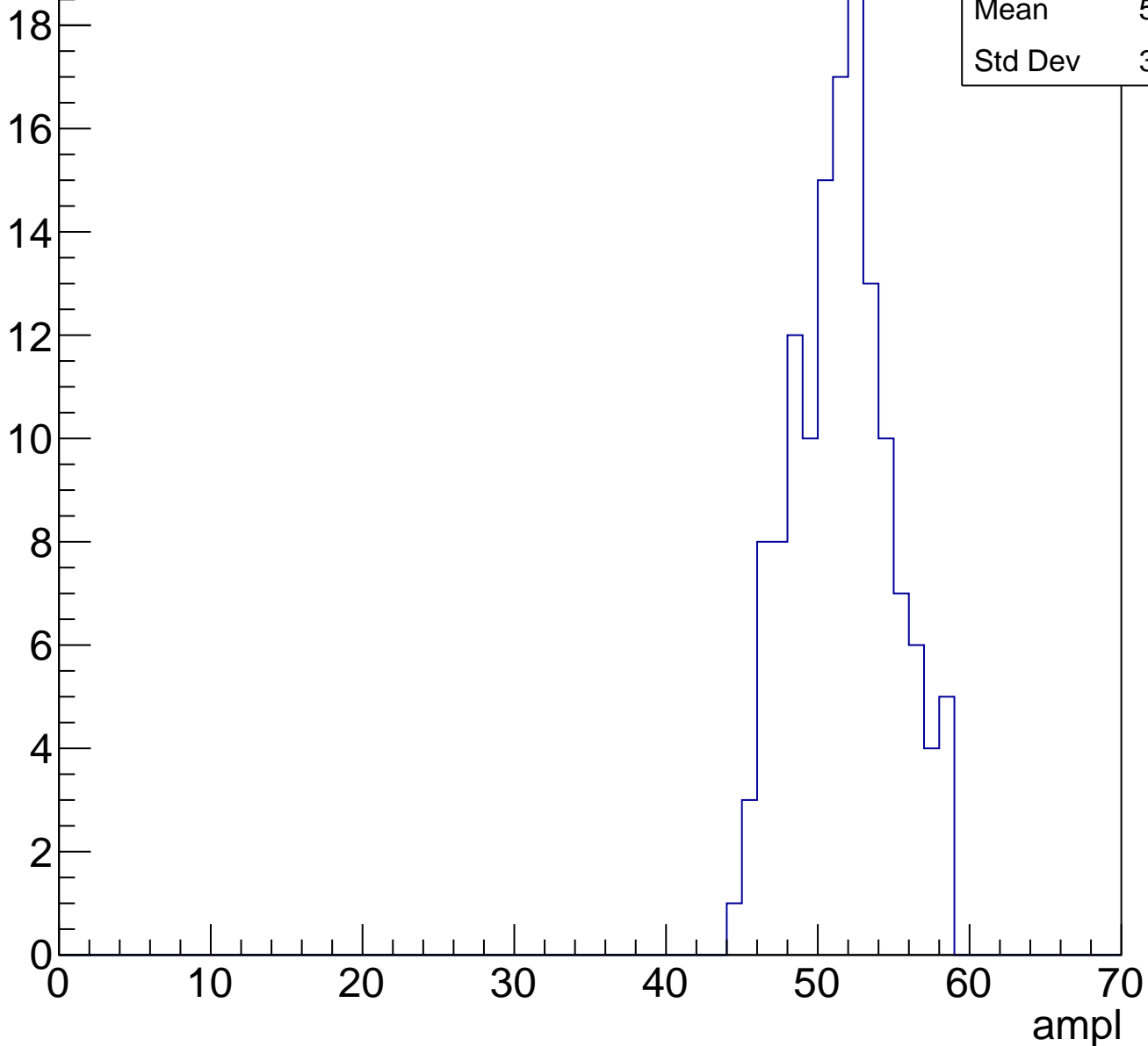
ampl

70

B1L001S, U25-ch31, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch31, adc4

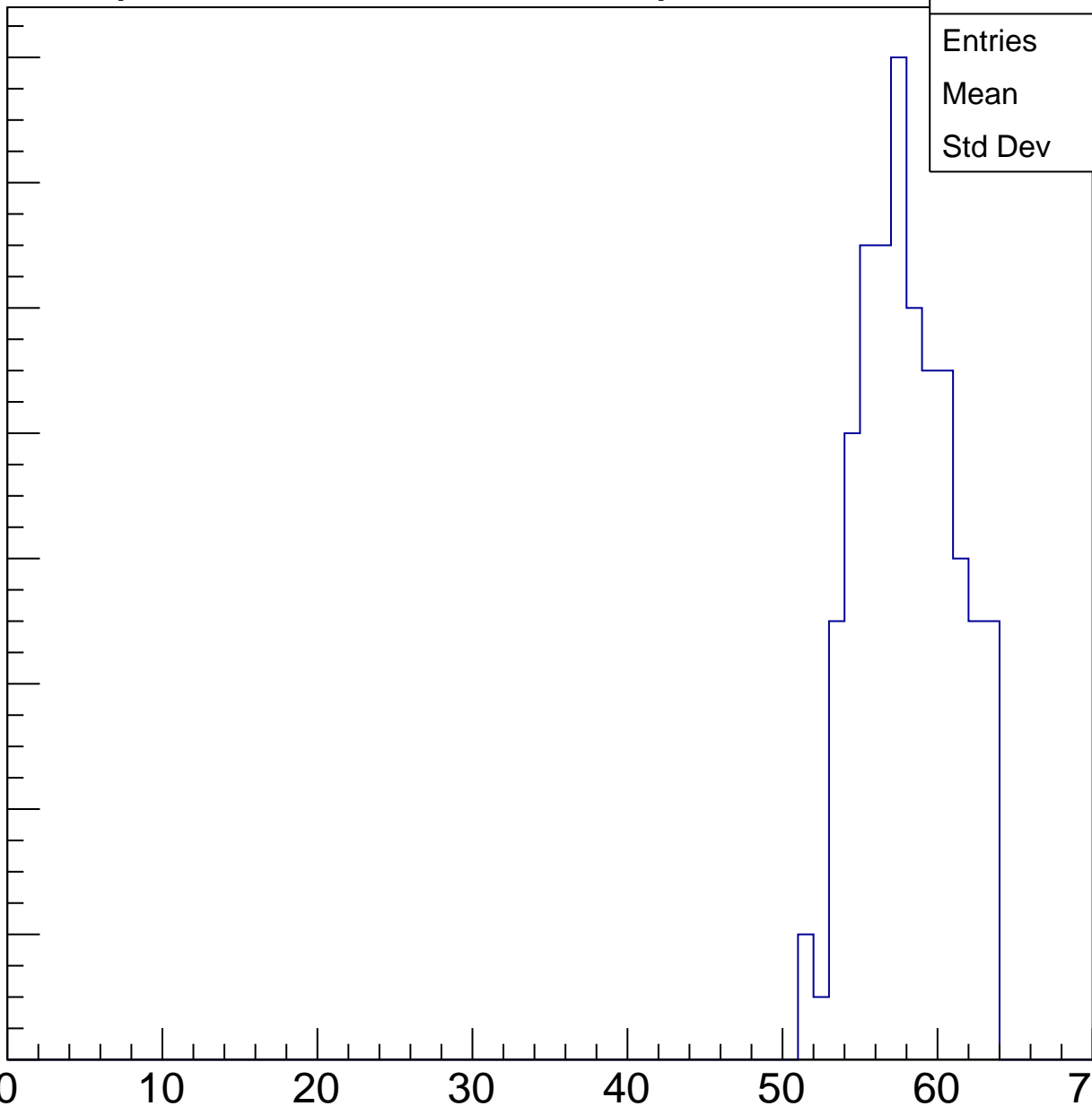
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	57.53
Std Dev	2.971

ampl



B1L001S, U25-ch31, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	59
Mean	59.93
Std Dev	8.042

0

2

4

6

8

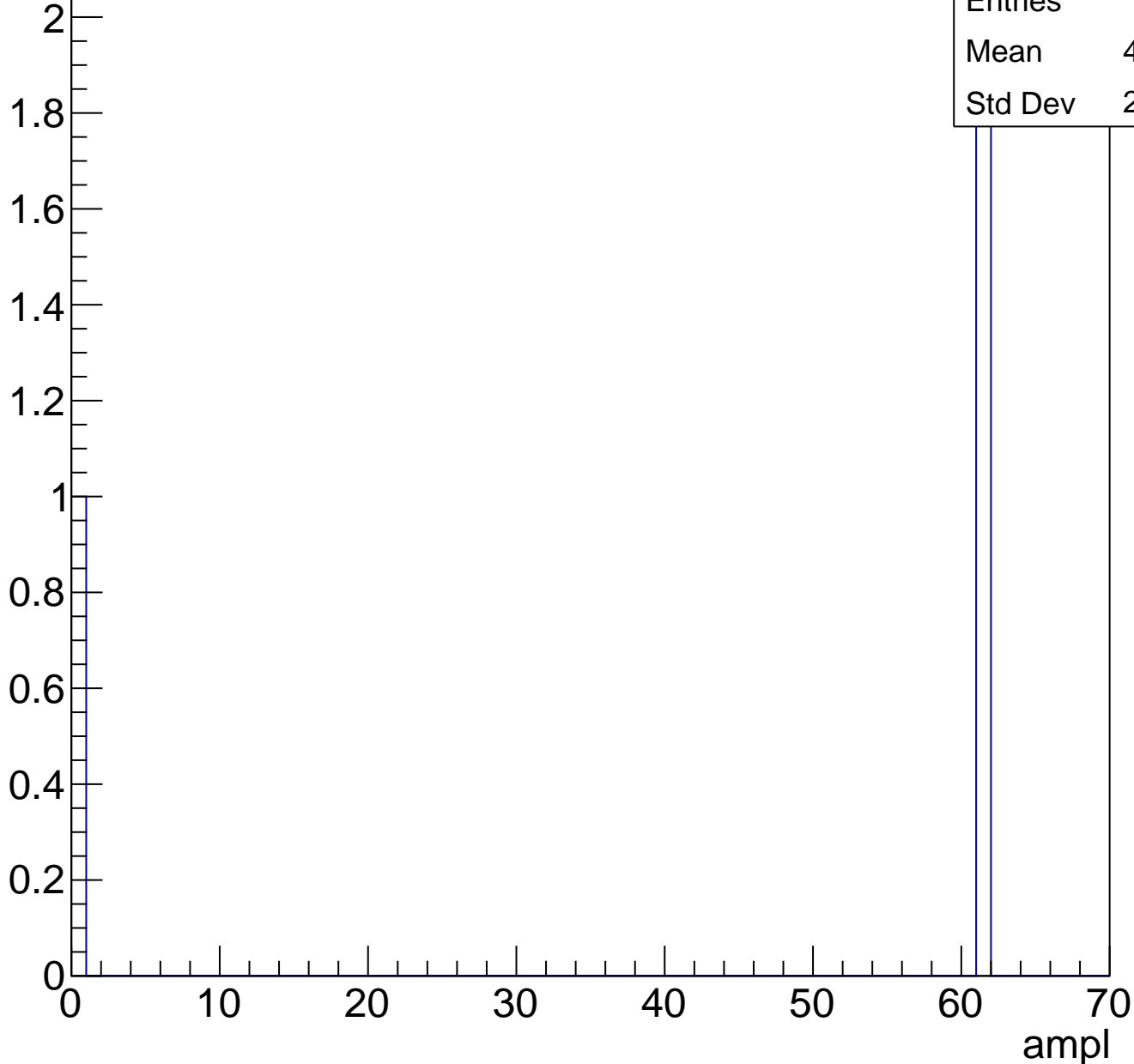
10

12

B1L001S, U25-ch31, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch31, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch32, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	30.2
Std Dev	4.246

Gaus mean : 30.5918

Gaus Width: 2.9983

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

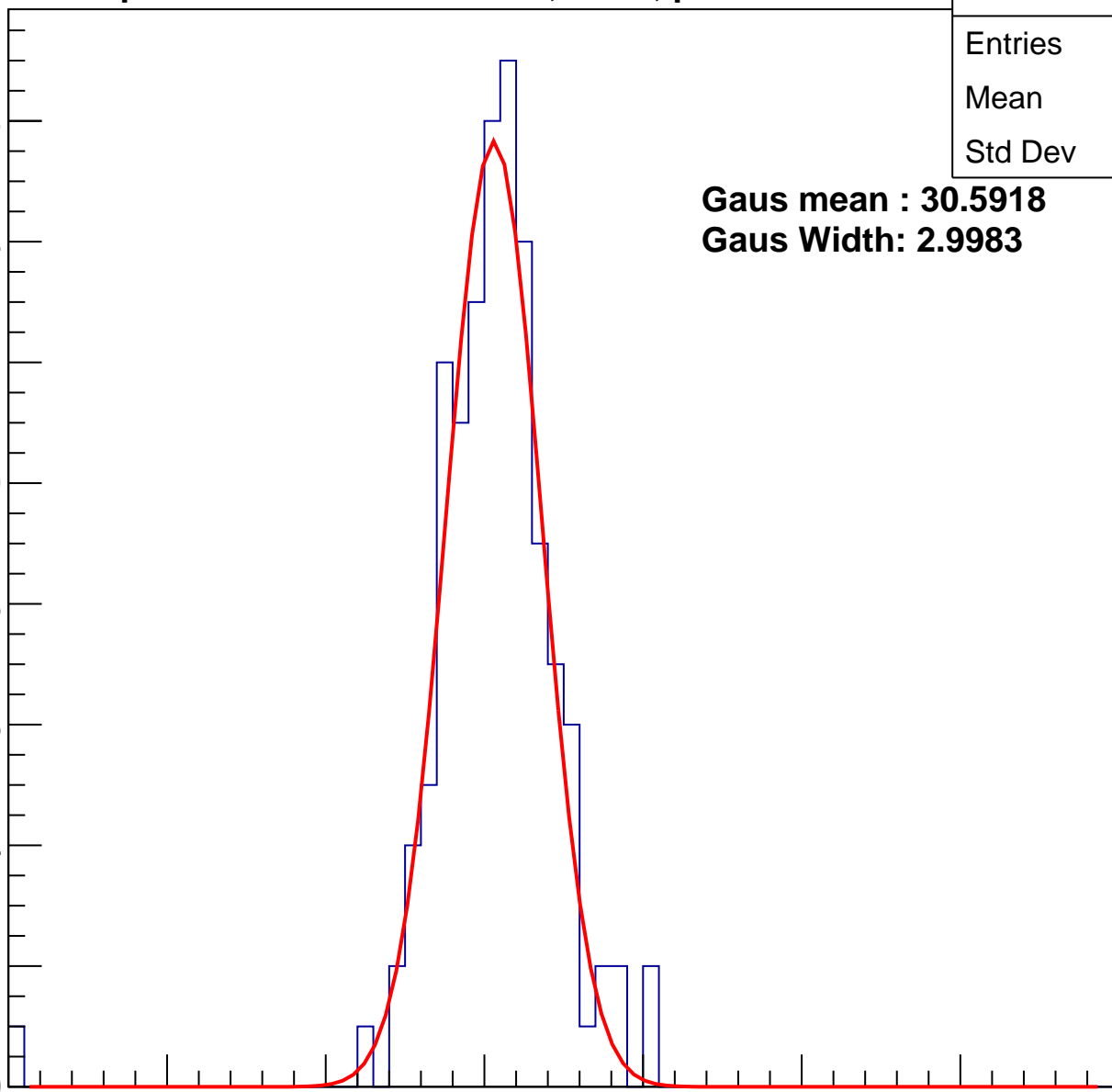
40

50

60

70

ampl



B1L001S, U25-ch32, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.95
Std Dev	2.942

Gaus mean : 37.3531

Gaus Width: 2.9360

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

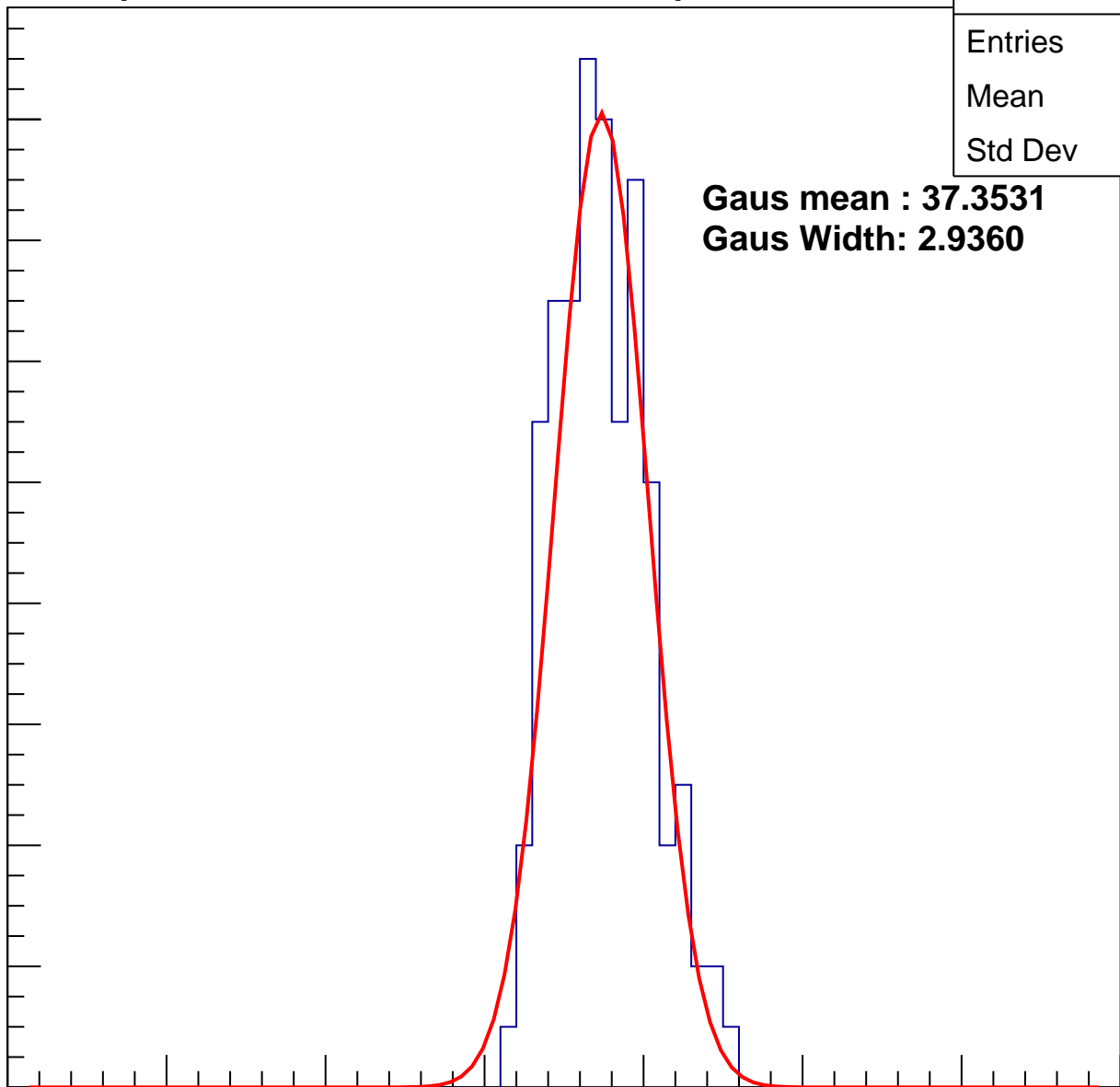
40

50

60

70

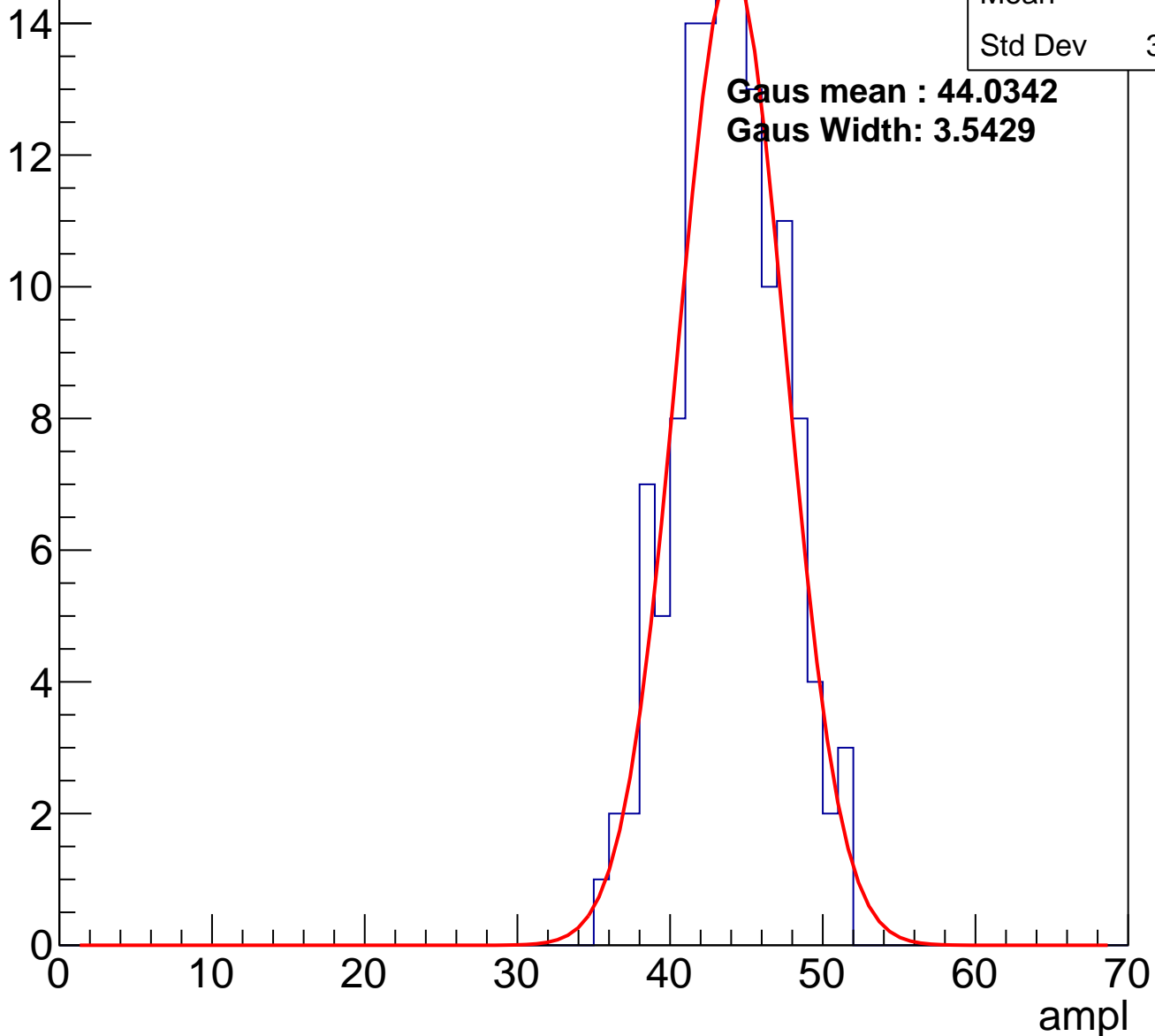
ampl



B1L001S, U25-ch32, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	134
Mean	43.46
Std Dev	3.422

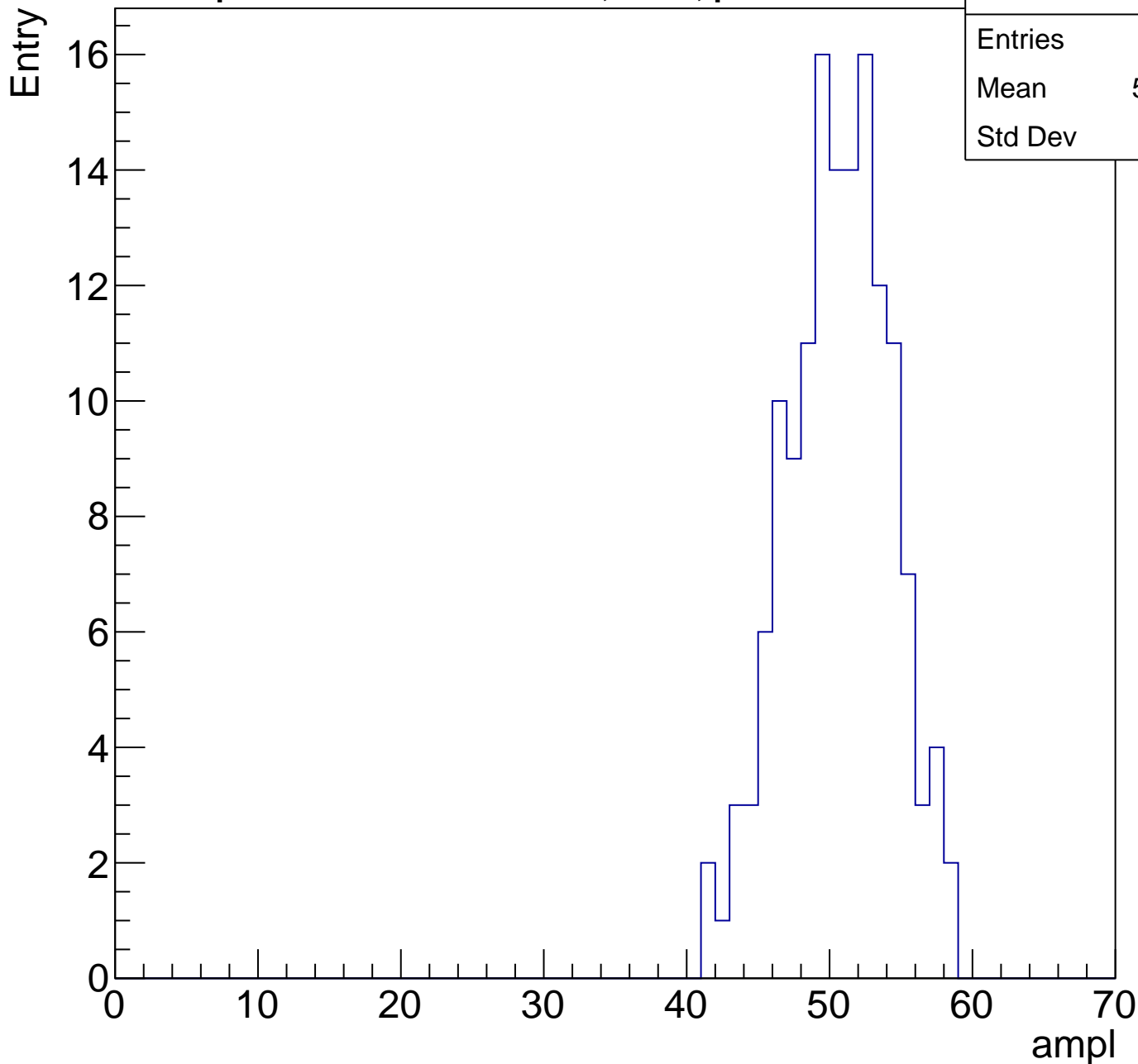
Gaus mean : 44.0342

Gaus Width: 3.5429

B1L001S, U25-ch32, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	50.16
Std Dev	3.63



B1L001S, U25-ch32, adc4

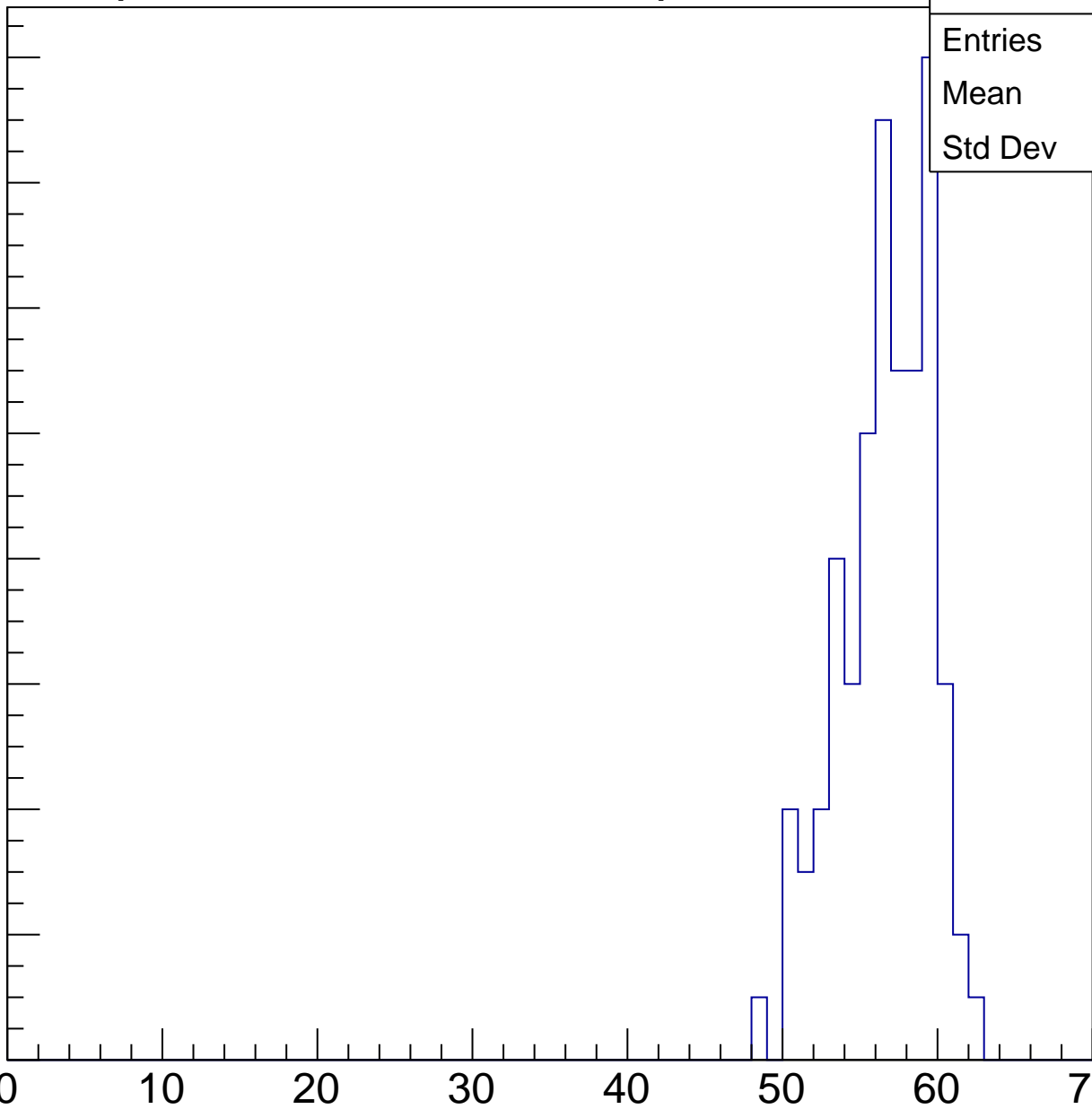
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	56.12
Std Dev	2.915

ampl



B1L001S, U25-ch32, adc5

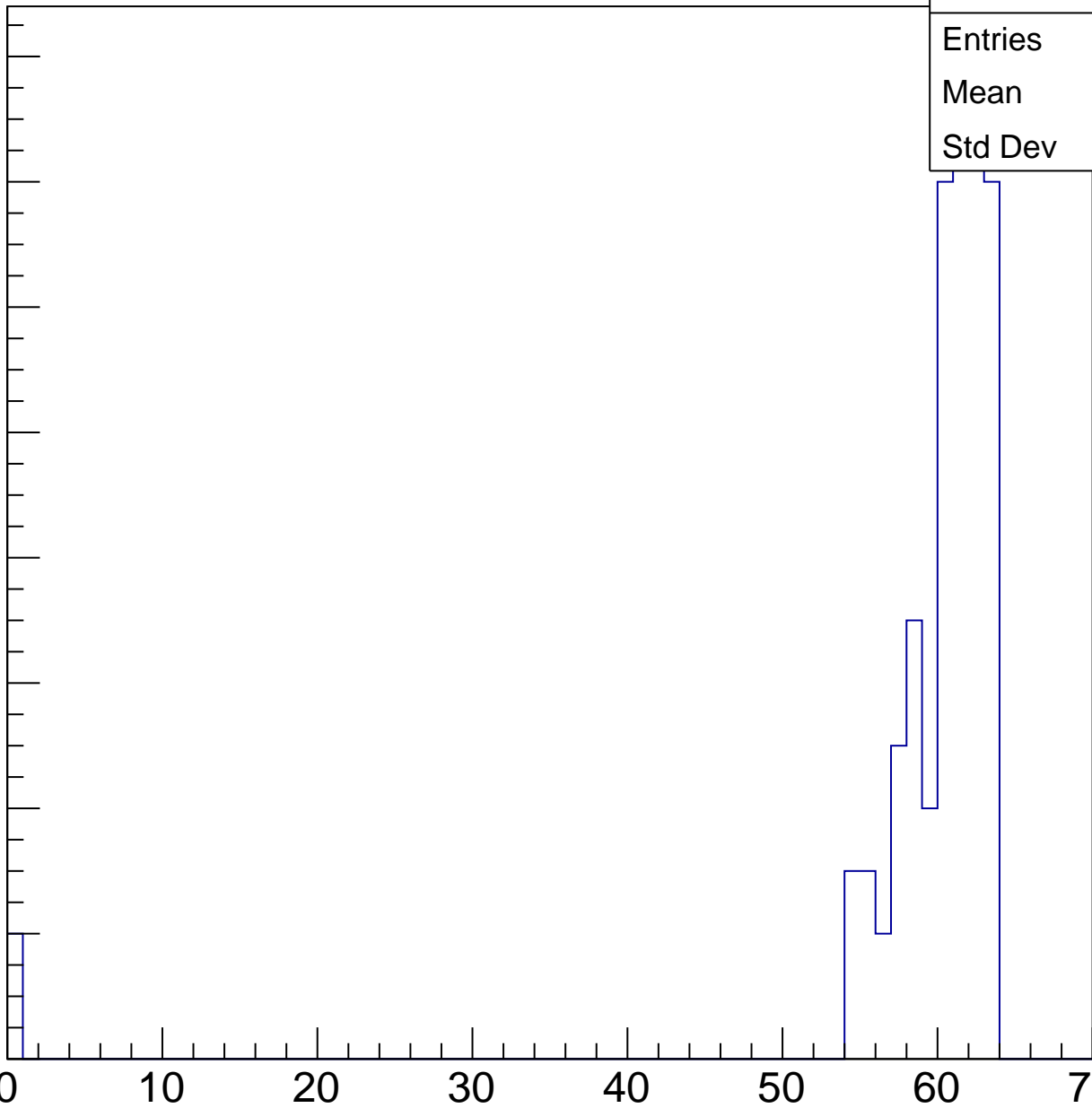
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	58.76
Std Dev	9.435

ampl

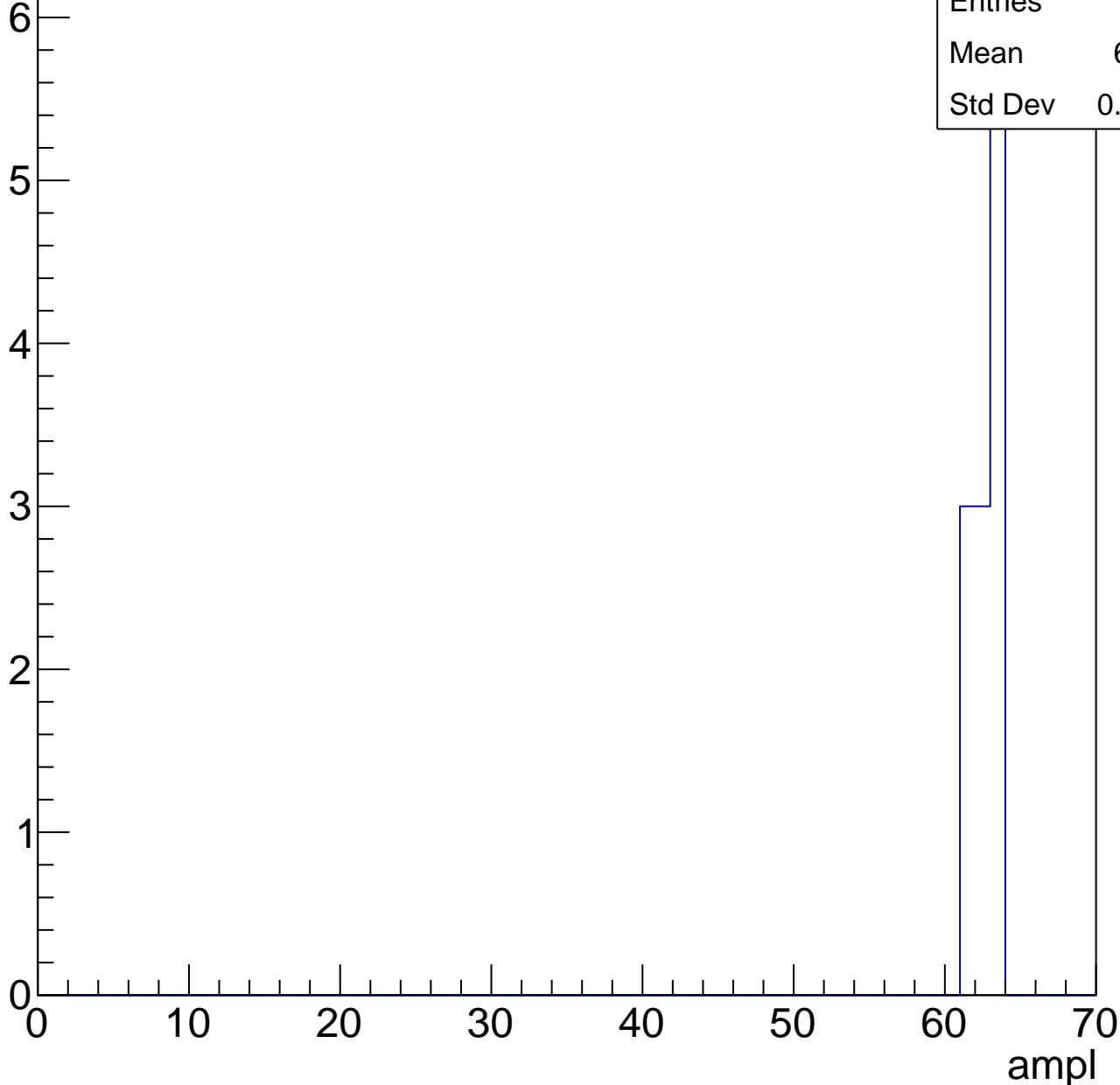


B1L001S, U25-ch32, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	62.25
Std Dev	0.8292



B1L001S, U25-ch32, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



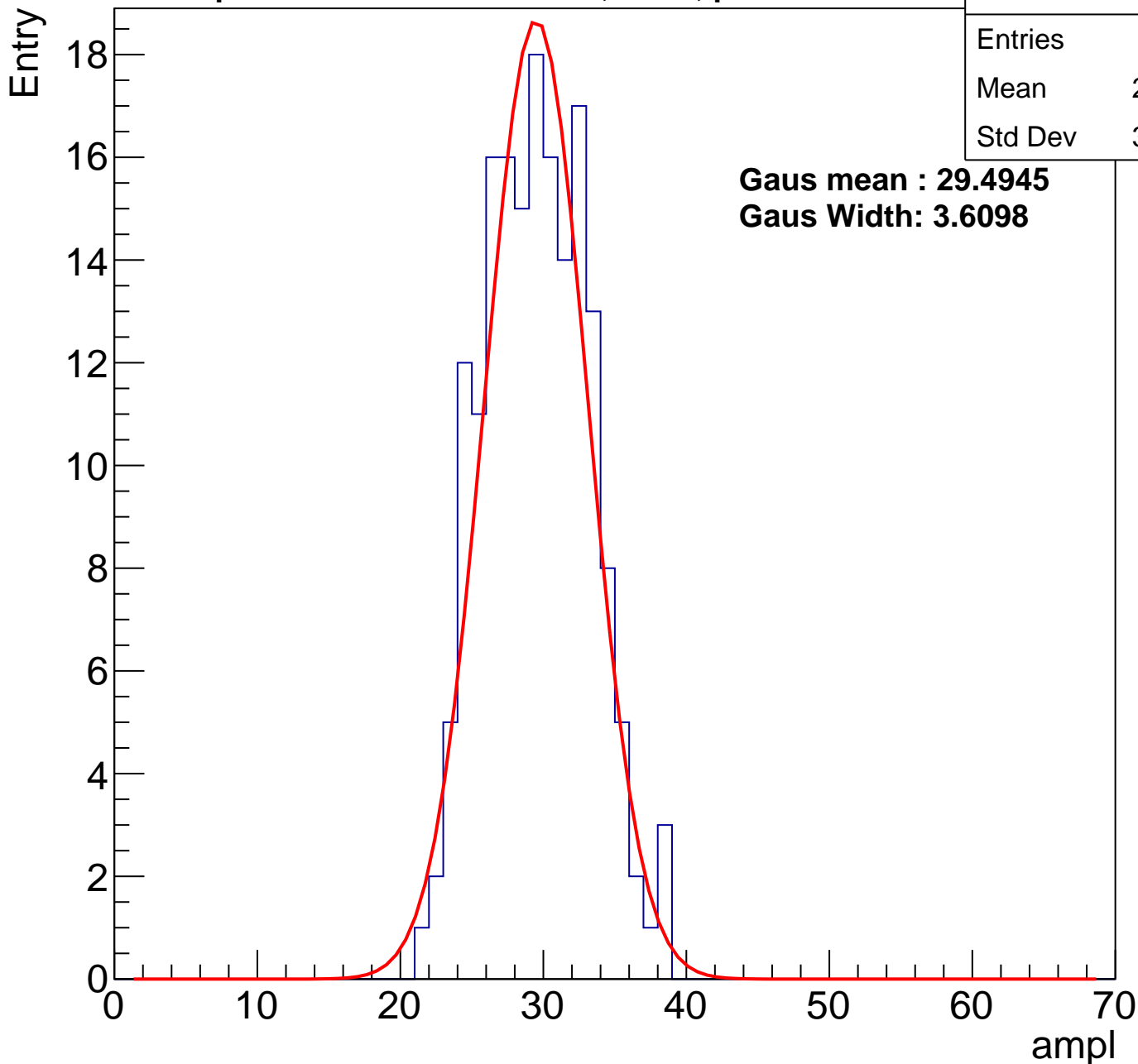
B1L001S, U25-ch33, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	175
Mean	29.09
Std Dev	3.592

Gaus mean : 29.4945

Gaus Width: 3.6098



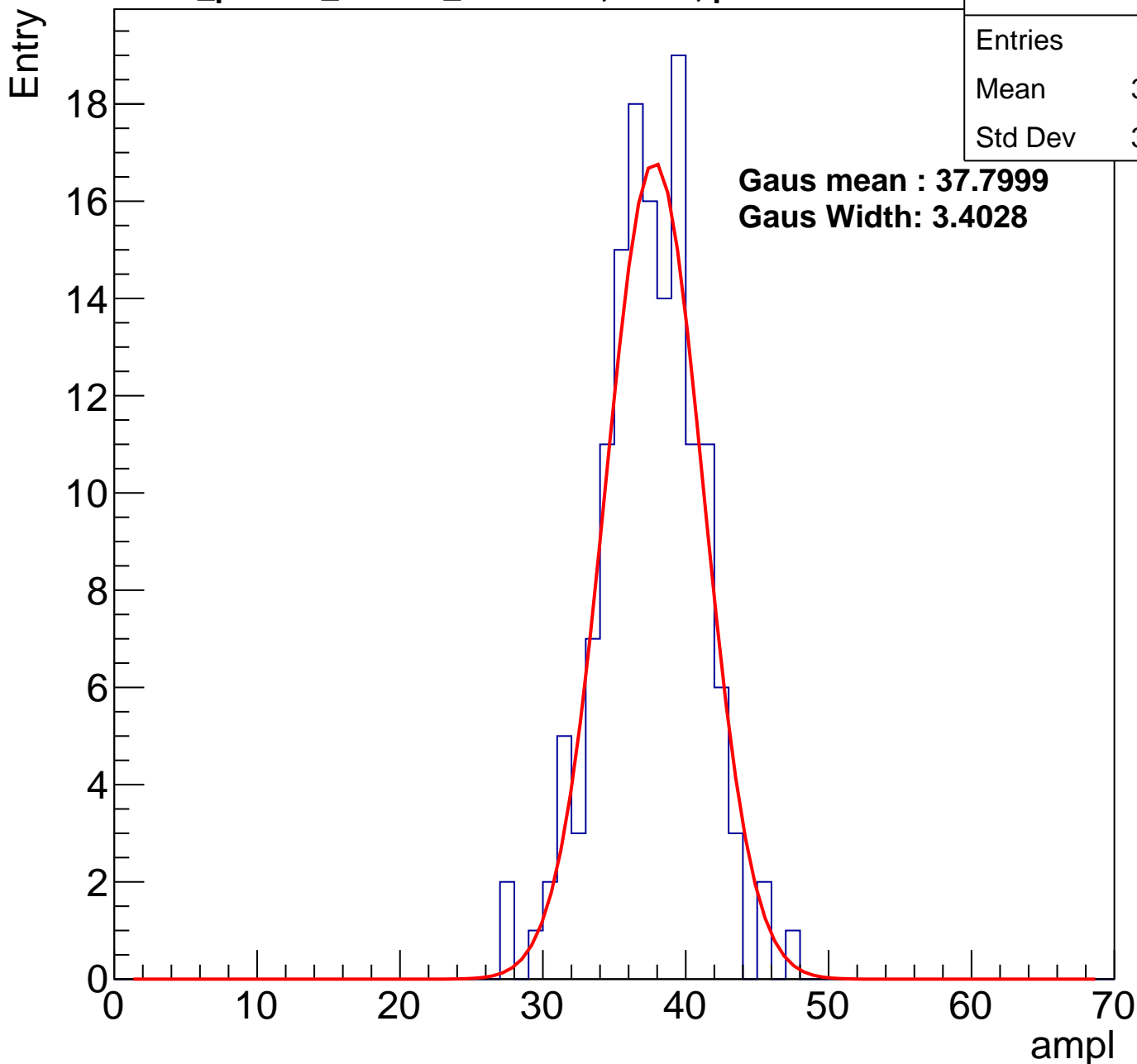
B1L001S, U25-ch33, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	37.05
Std Dev	3.457

Gaus mean : 37.7999

Gaus Width: 3.4028



B1L001S, U25-ch33, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	44.02
Std Dev	3.083

Gaus mean : 44.8942

Gaus Width: 2.7573

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

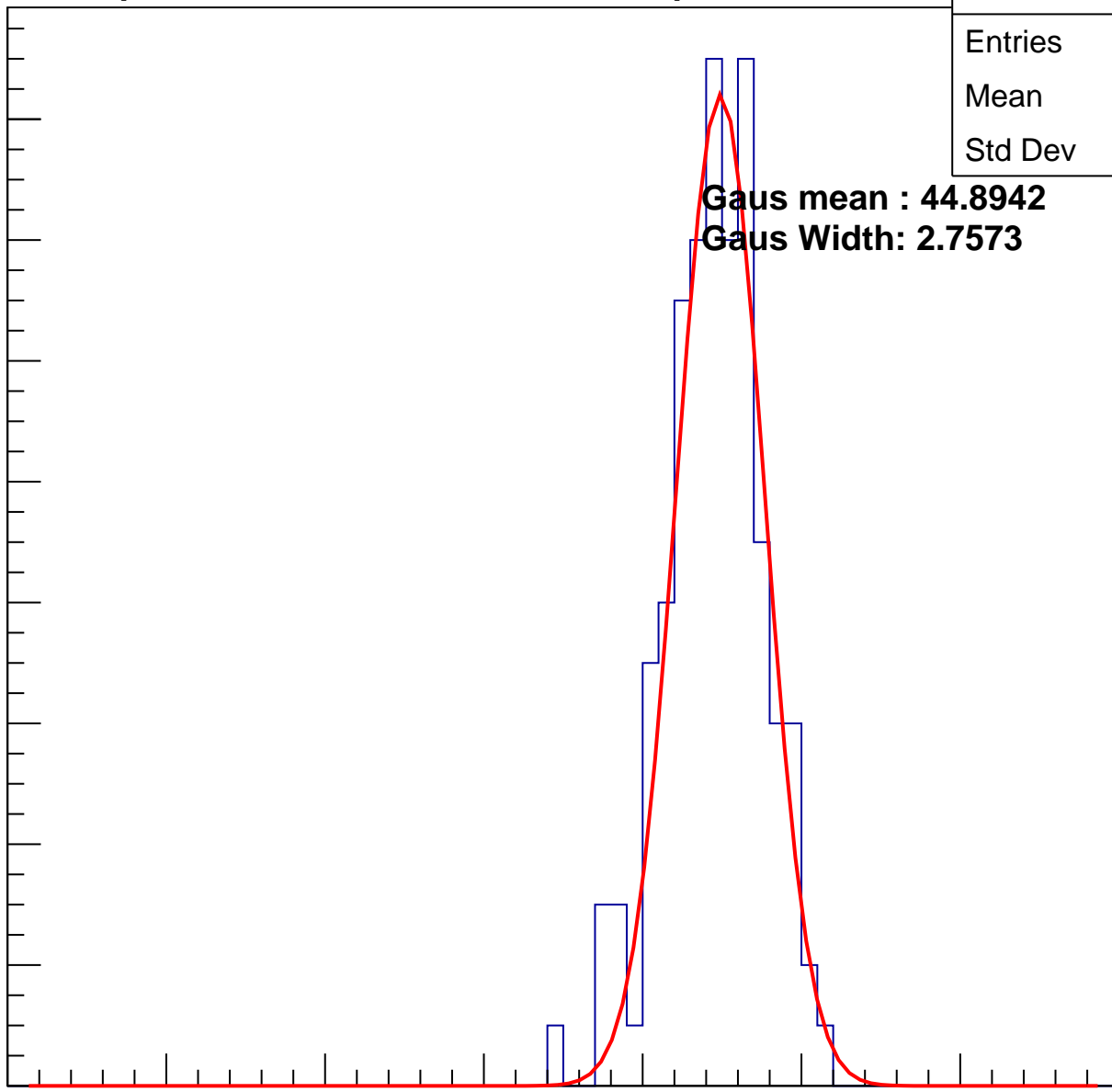
40

50

60

70

ampl



B1L001S, U25-ch33, adc3

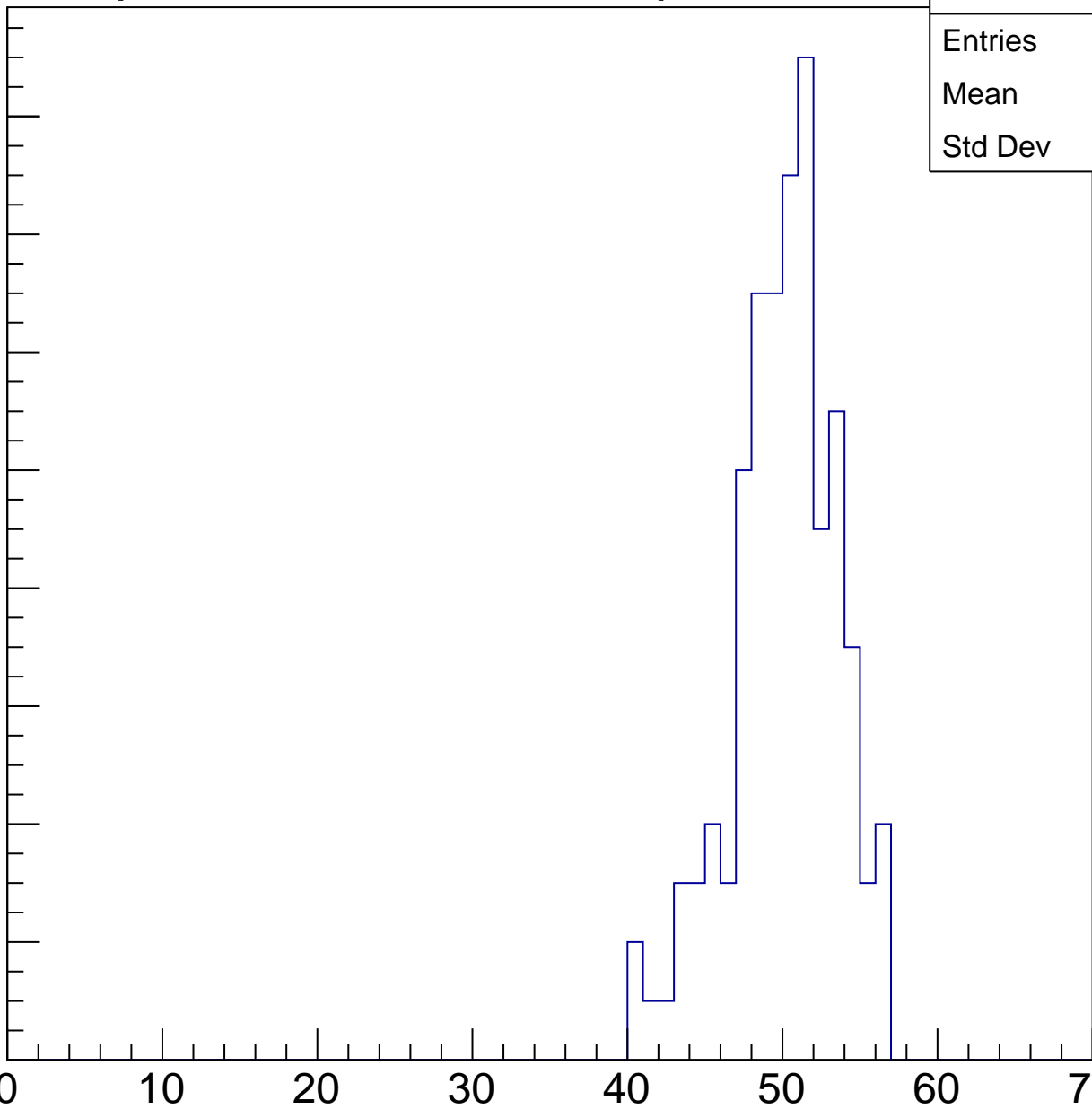
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	49.65
Std Dev	3.394

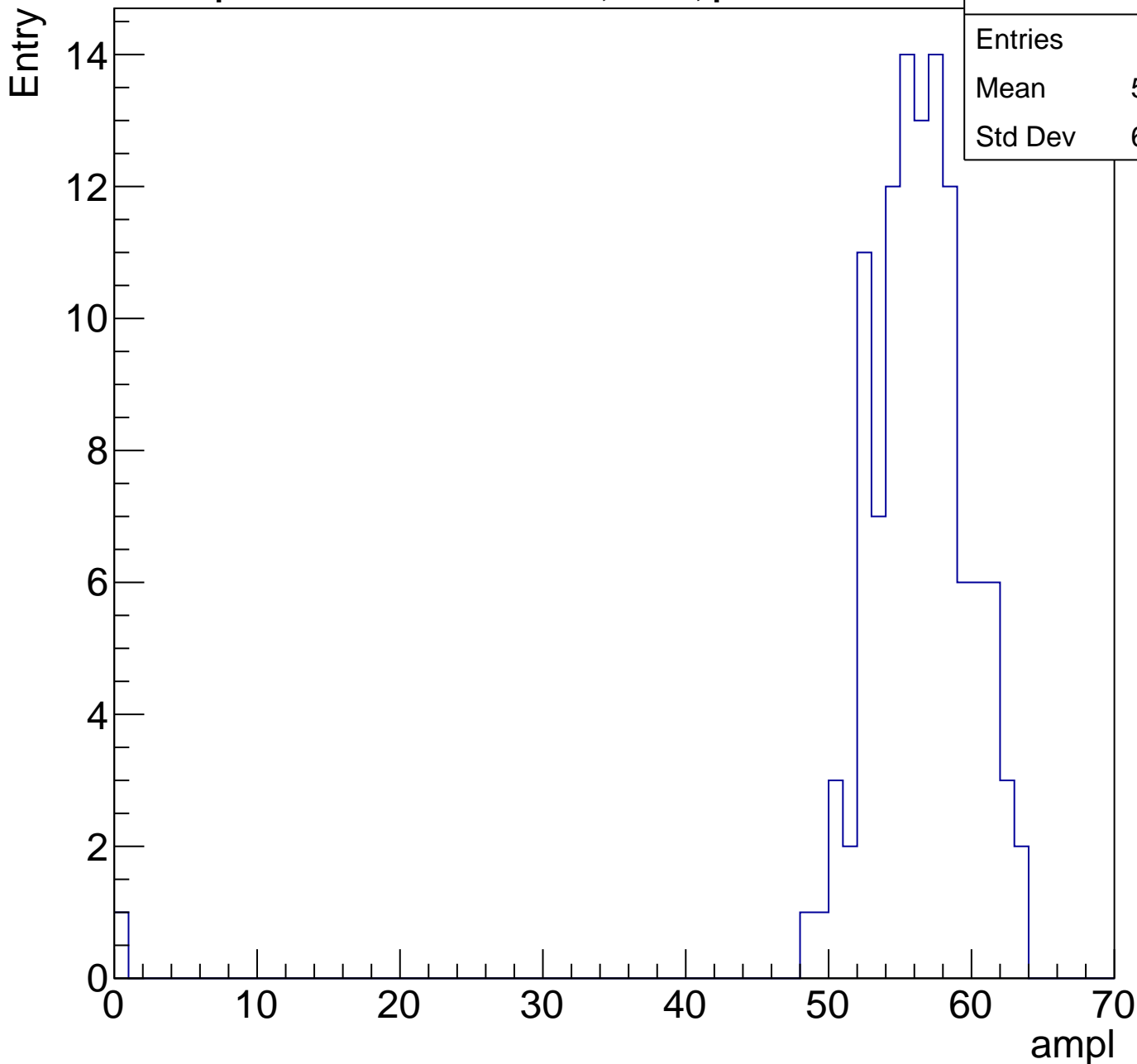
ampl



B1L001S, U25-ch33, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	55.47
Std Dev	6.102



B1L001S, U25-ch33, adc5

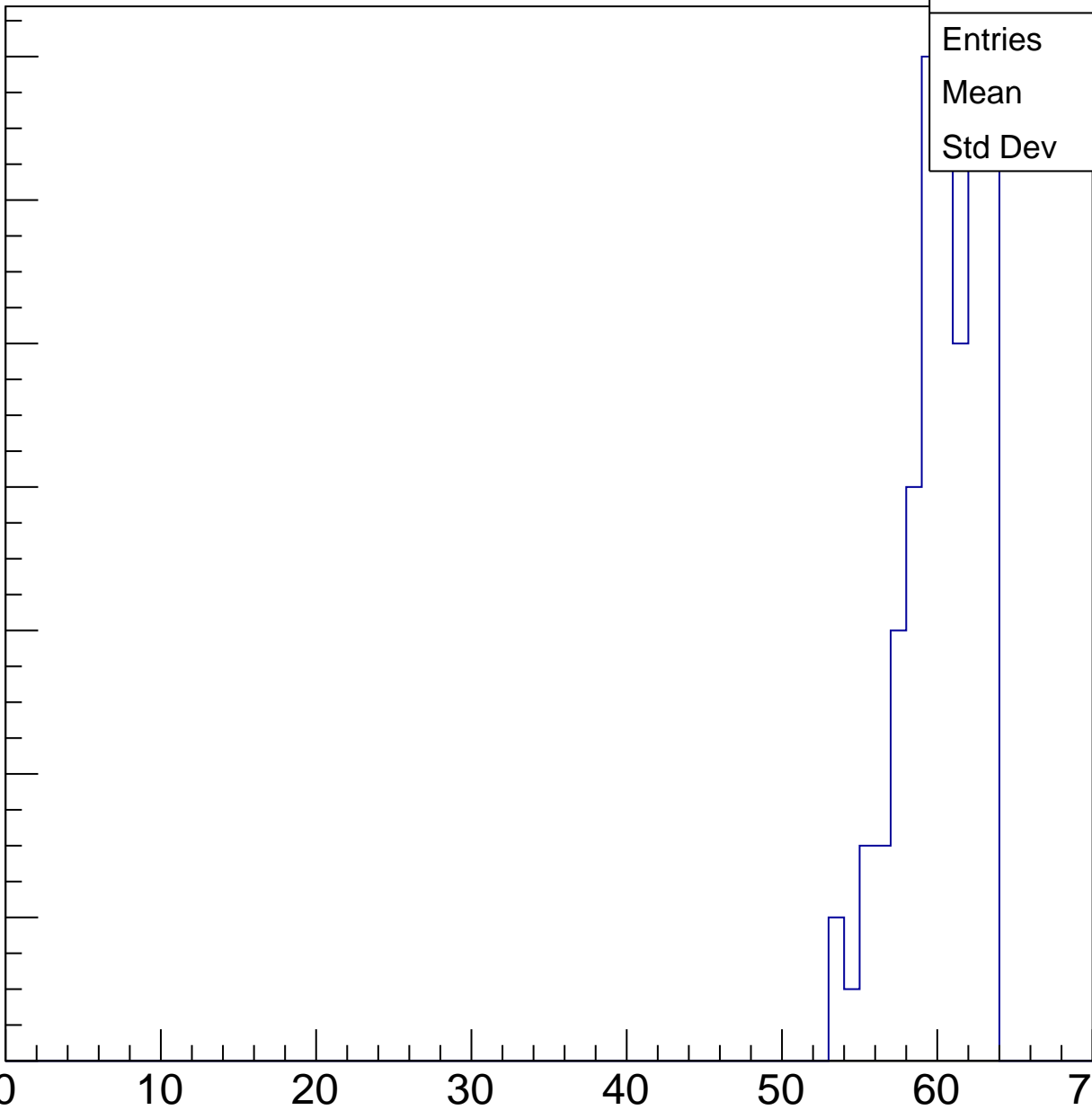
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	87
Mean	59.77
Std Dev	2.471

ampl

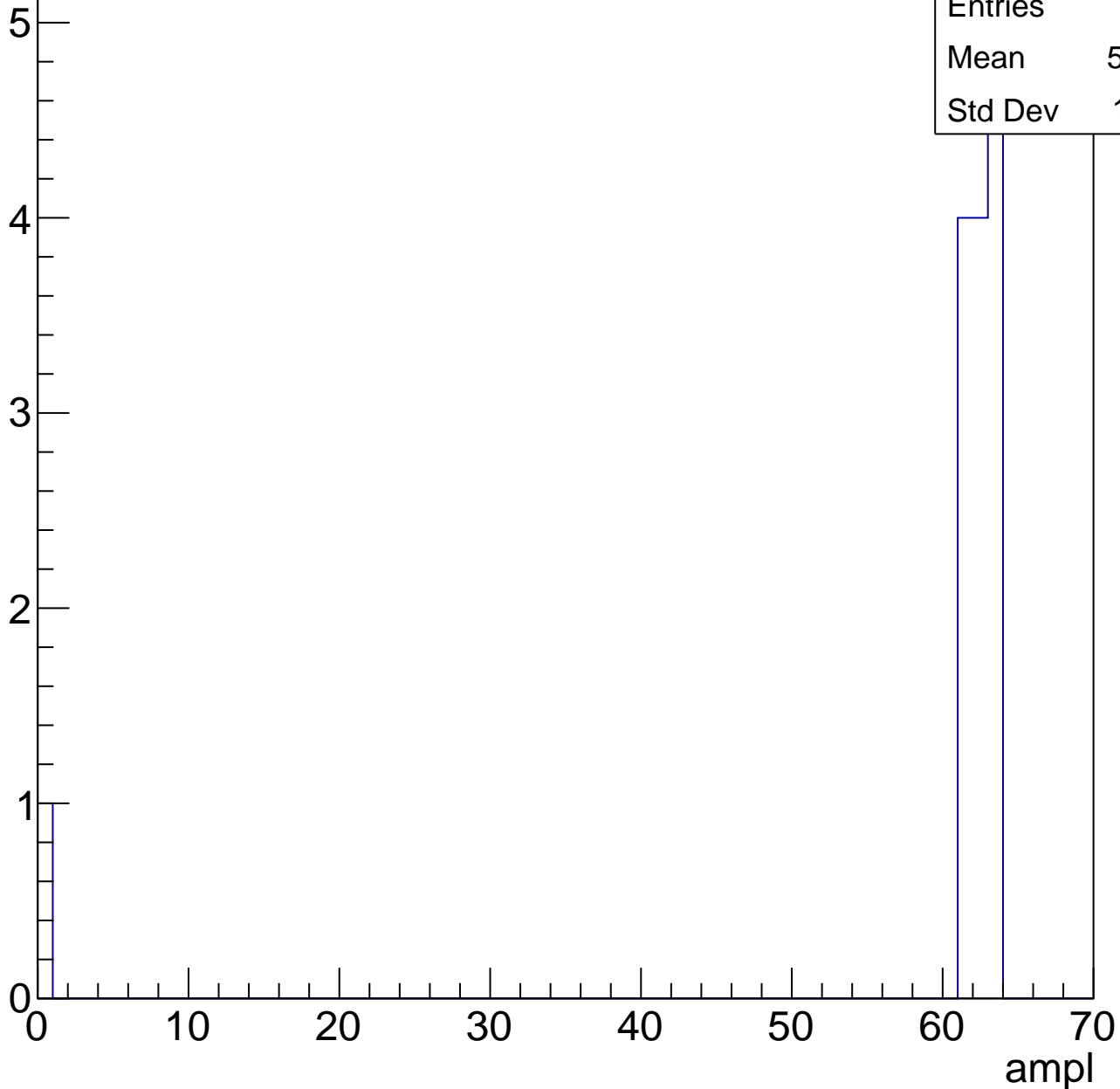


B1L001S, U25-ch33, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.64
Std Dev	16.01



B1L001S, U25-ch33, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

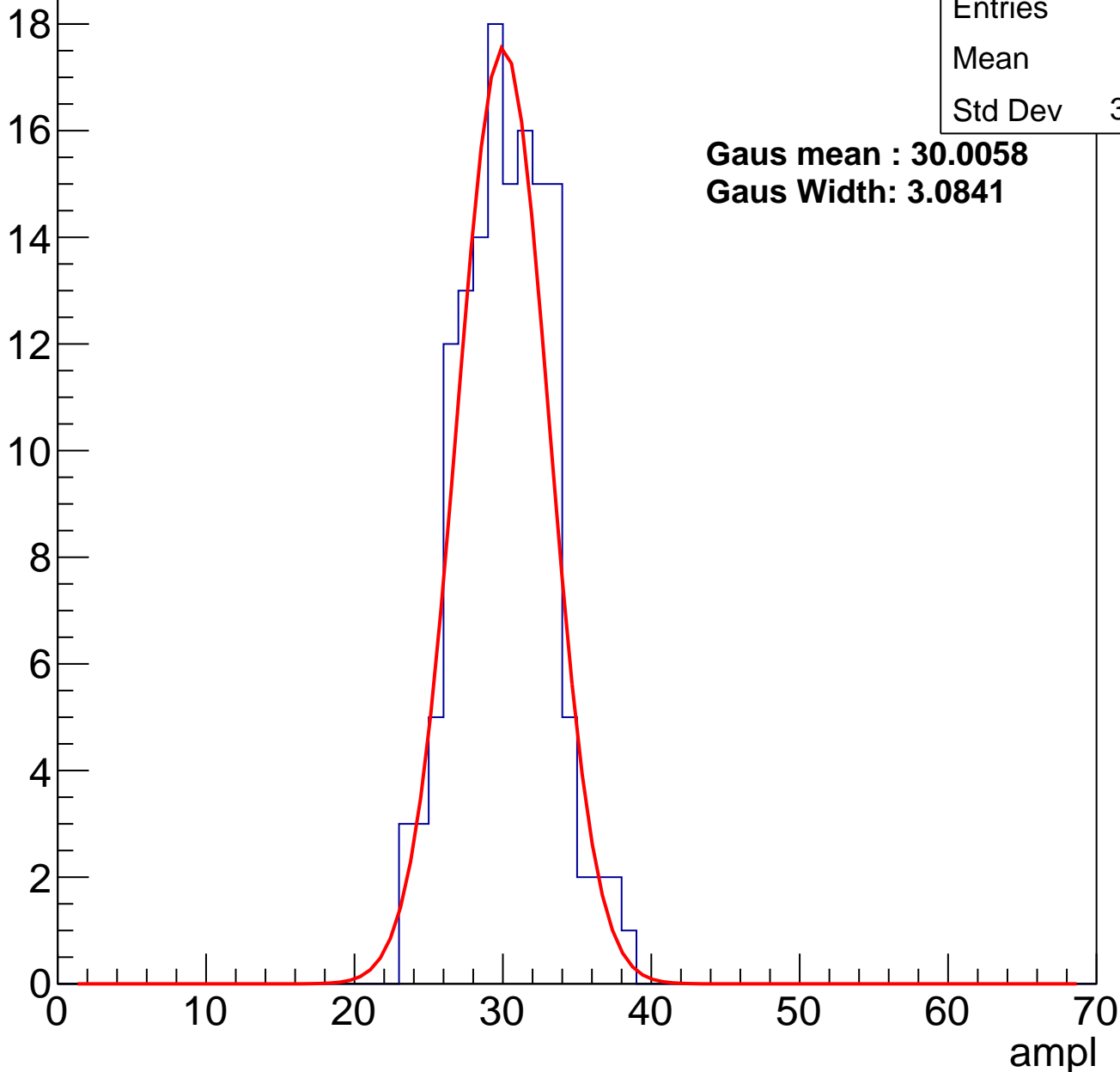


Entries	1
Mean	22
Std Dev	0

B1L001S, U25-ch34, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch34, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.62
Std Dev	3.076

Gaus mean : 37.0430

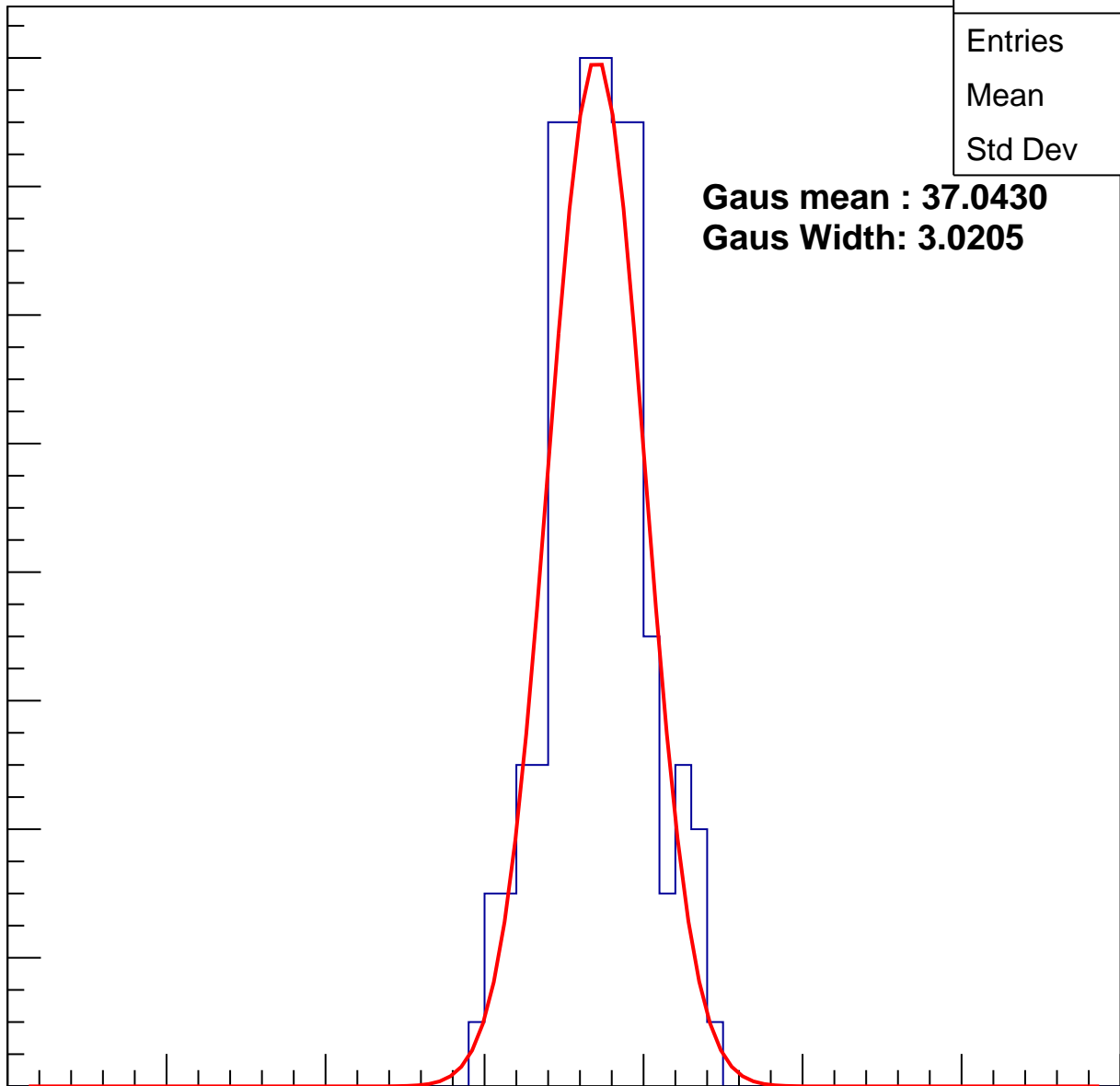
Gaus Width: 3.0205

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch34, adc2

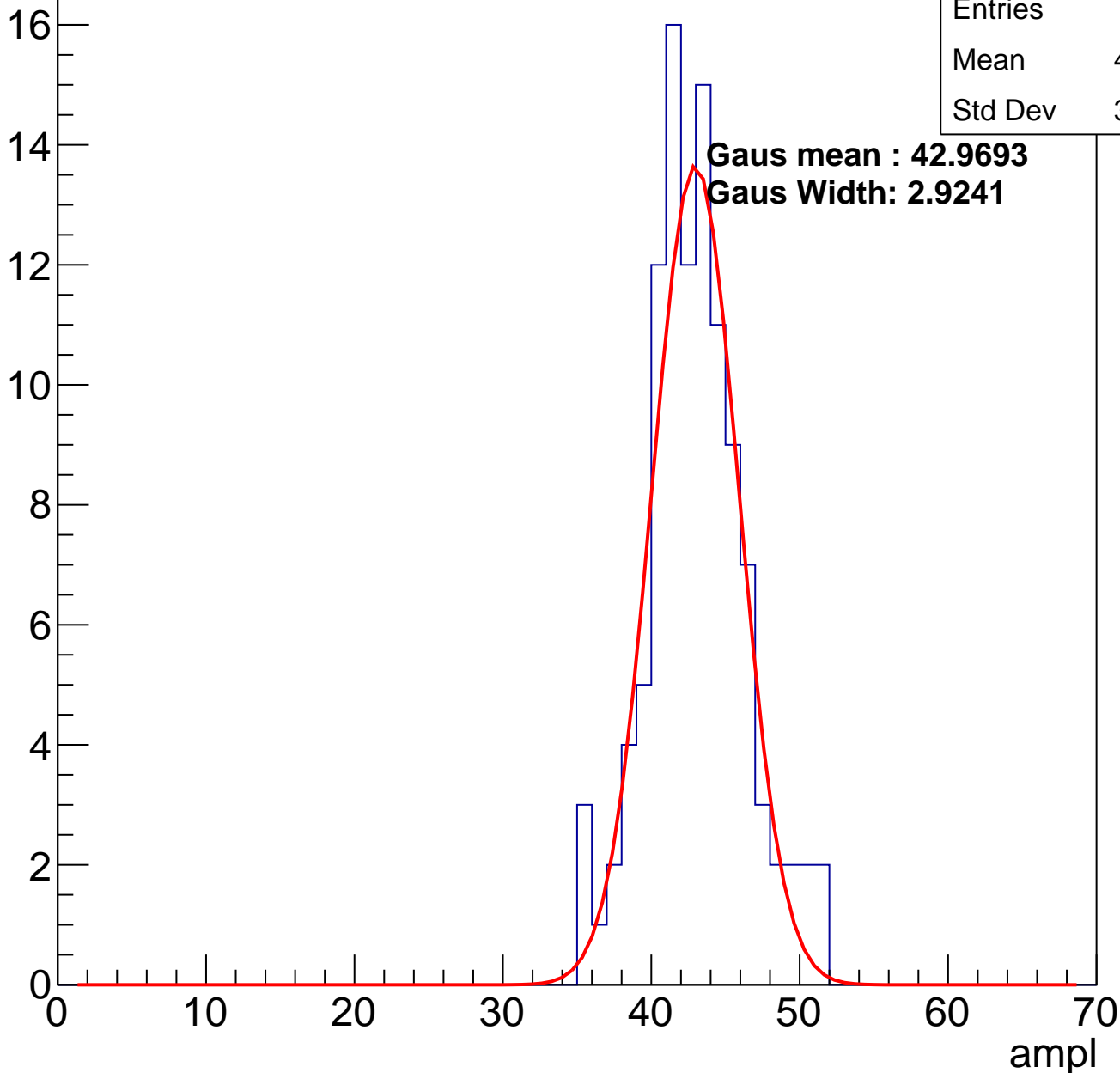
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	42.55
Std Dev	3.278

Gaus mean : 42.9693

Gaus Width: 2.9241

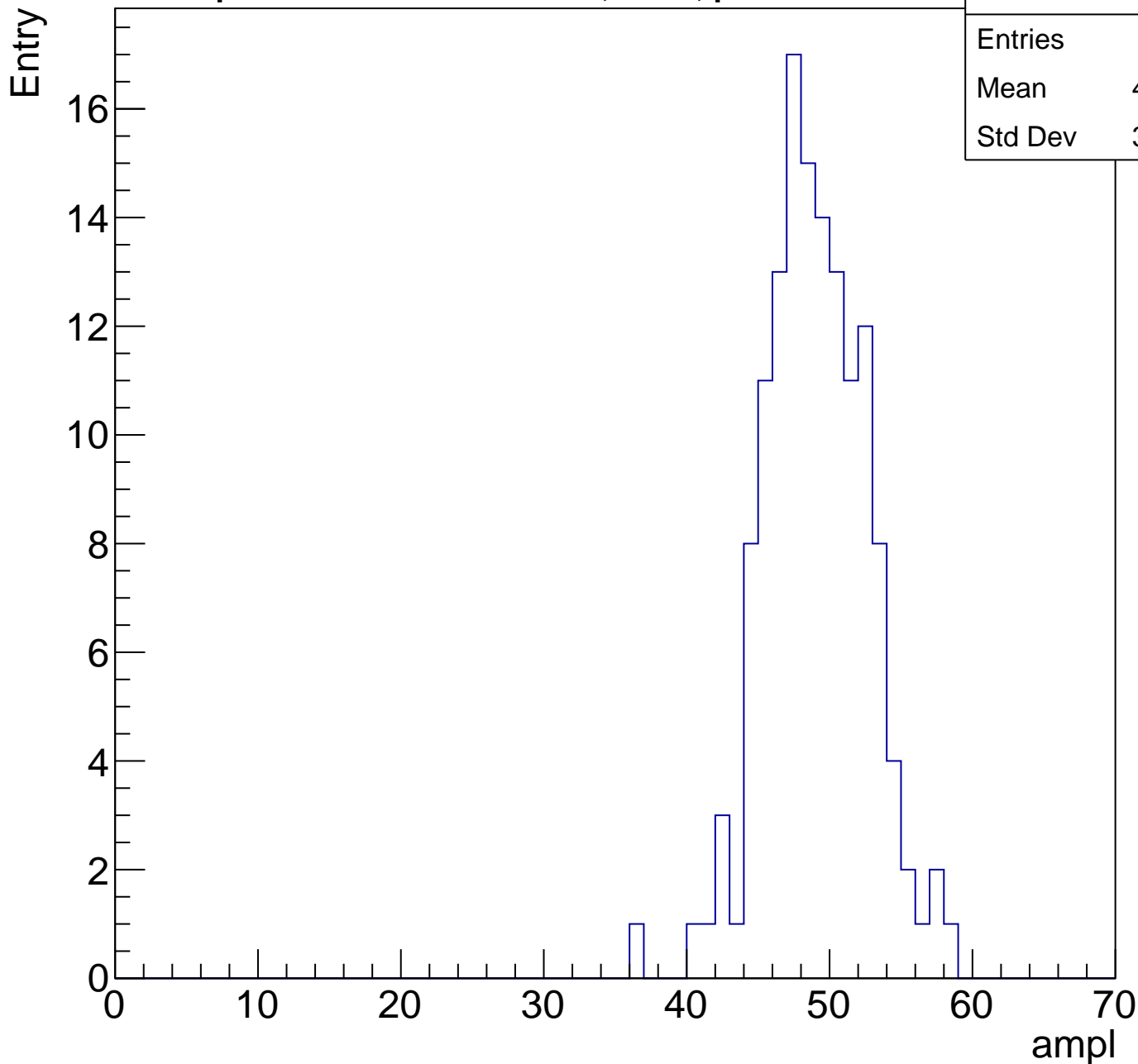
Entry



B1L001S, U25-ch34, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	48.55
Std Dev	3.567



B1L001S, U25-ch34, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	97
Mean	54.51
Std Dev	3.281

0

2

4

6

8

10

12

B1L001S, U25-ch34, adc5

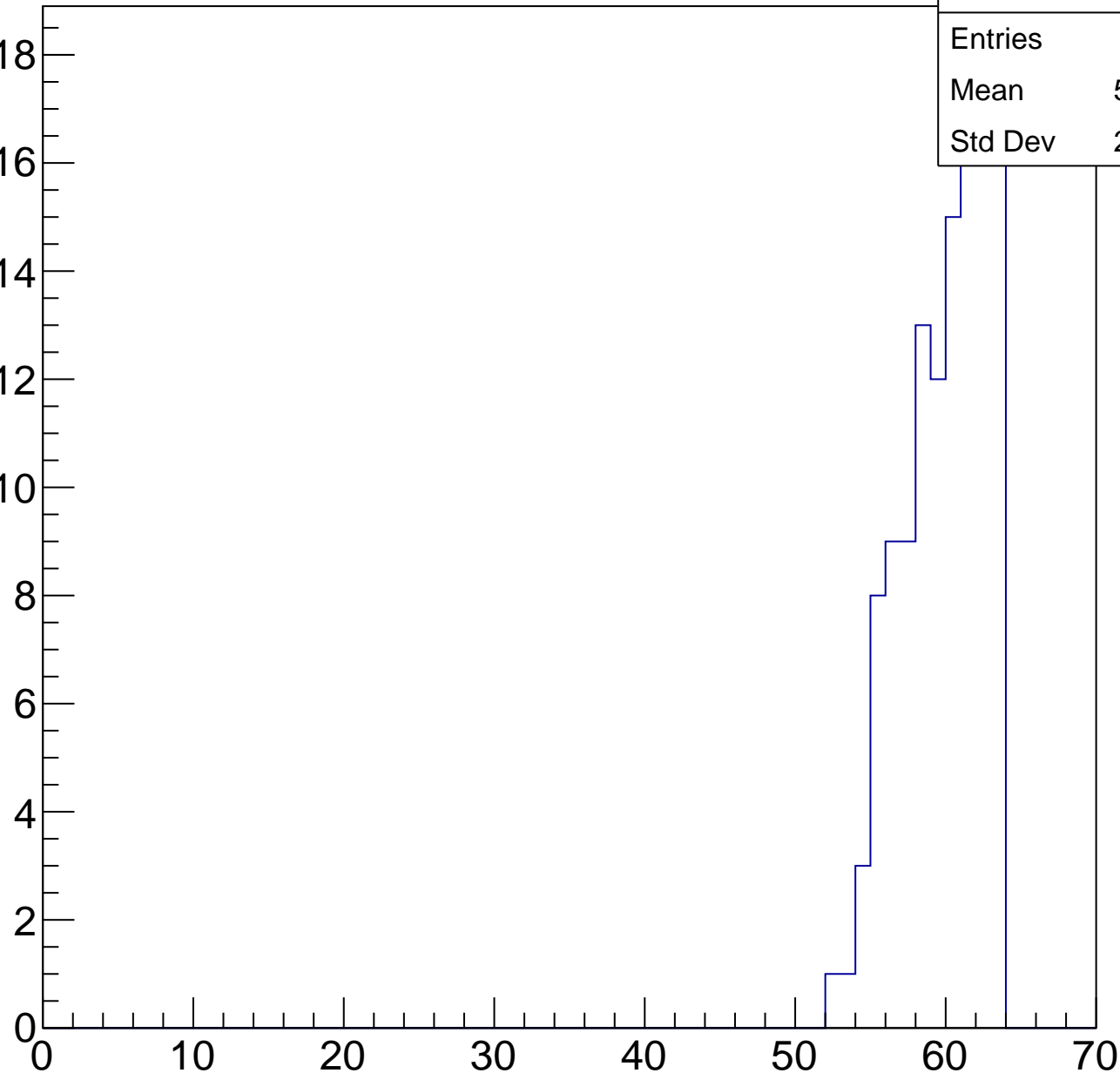
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	122
Mean	59.39
Std Dev	2.709

ampl



B1L001S, U25-ch34, adc6

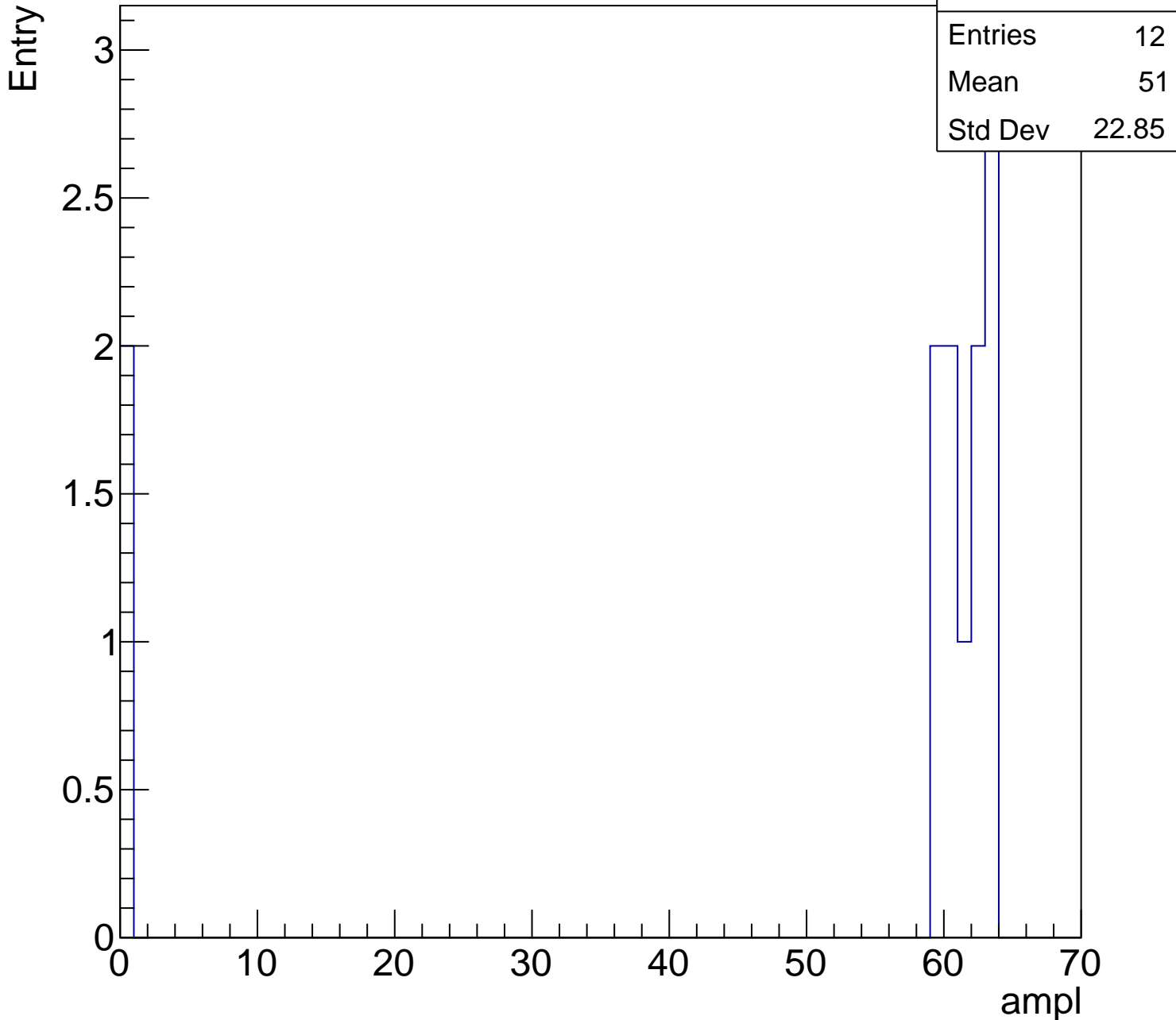
calib_packv5_042523_0143.root, FC#2, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	12
Mean	51
Std Dev	22.85

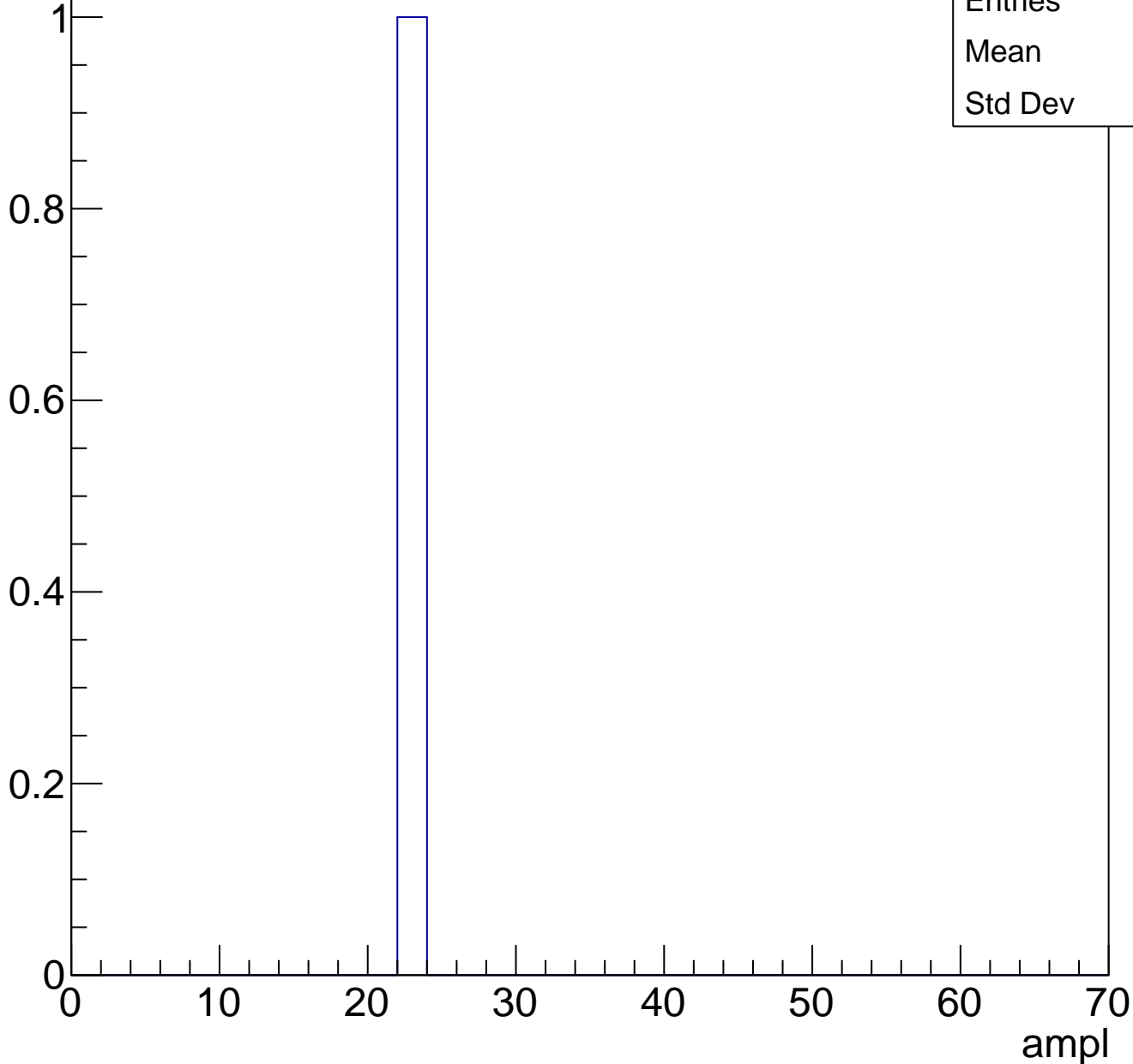
ampl



B1L001S, U25-ch34, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch35, adc0

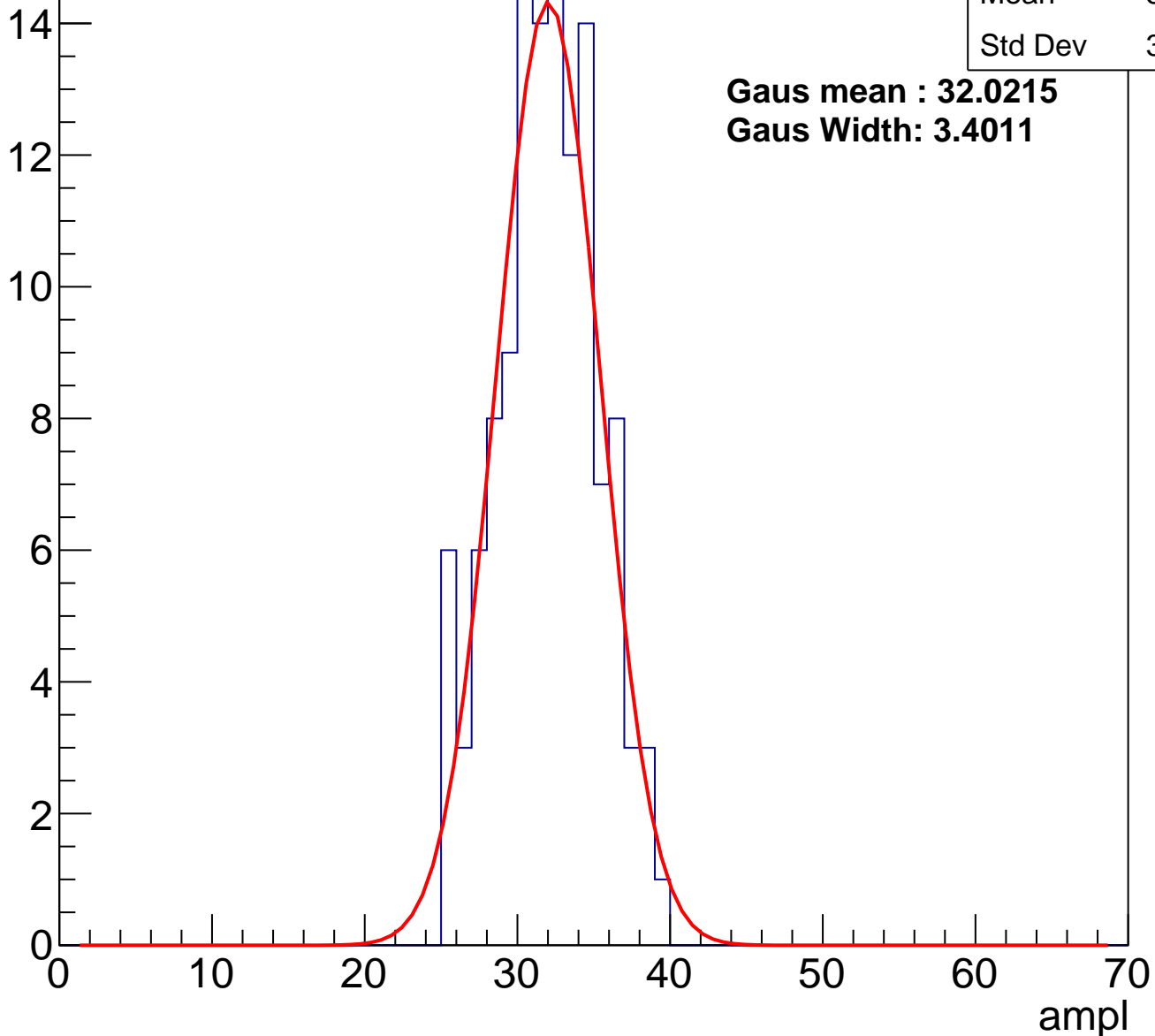
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	31.52
Std Dev	3.234

Gaus mean : 32.0215

Gaus Width: 3.4011

Entry



B1L001S, U25-ch35, adc1

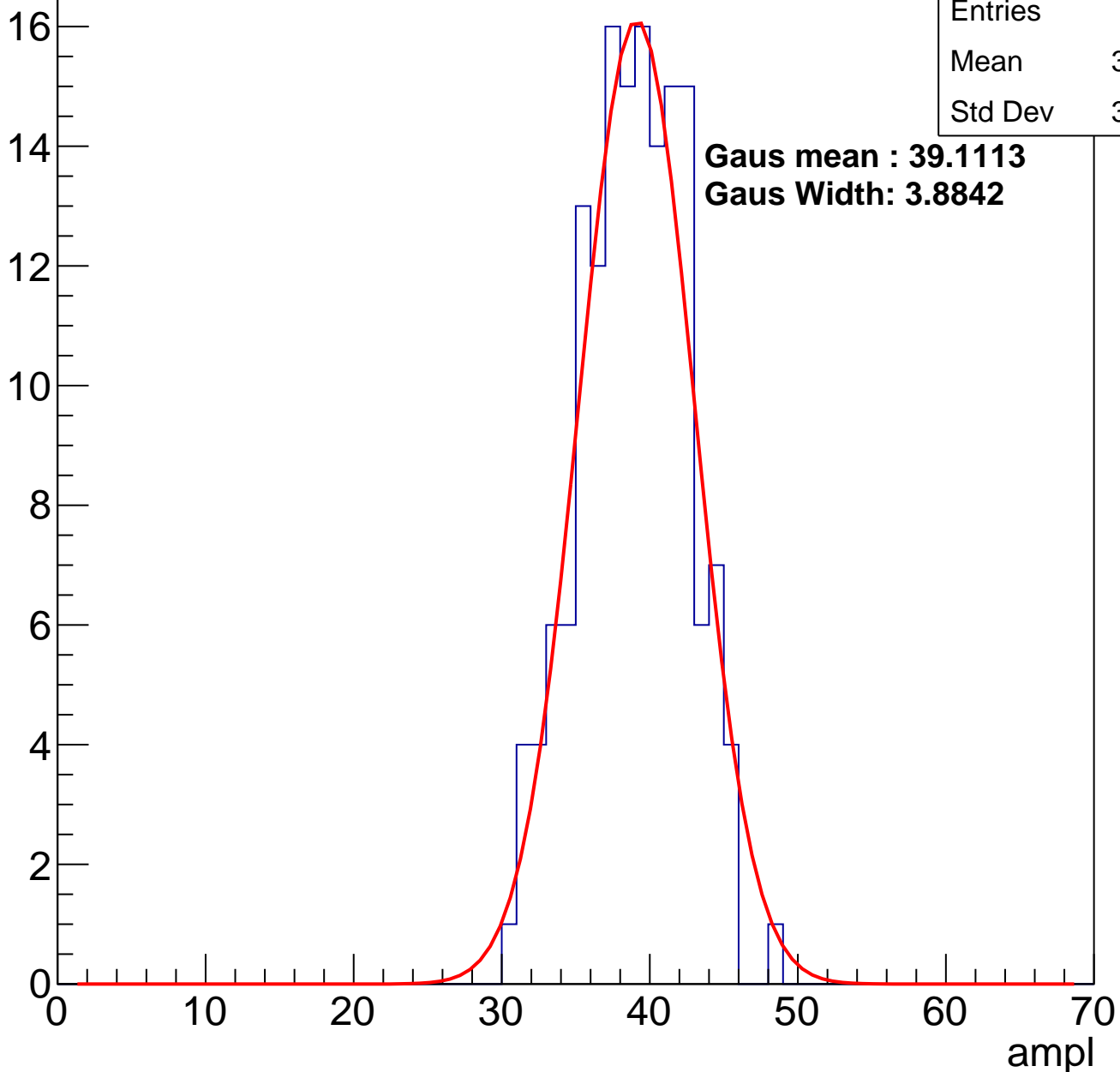
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	38.43
Std Dev	3.545

Gaus mean : 39.1113

Gaus Width: 3.8842

Entry



B1L001S, U25-ch35, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

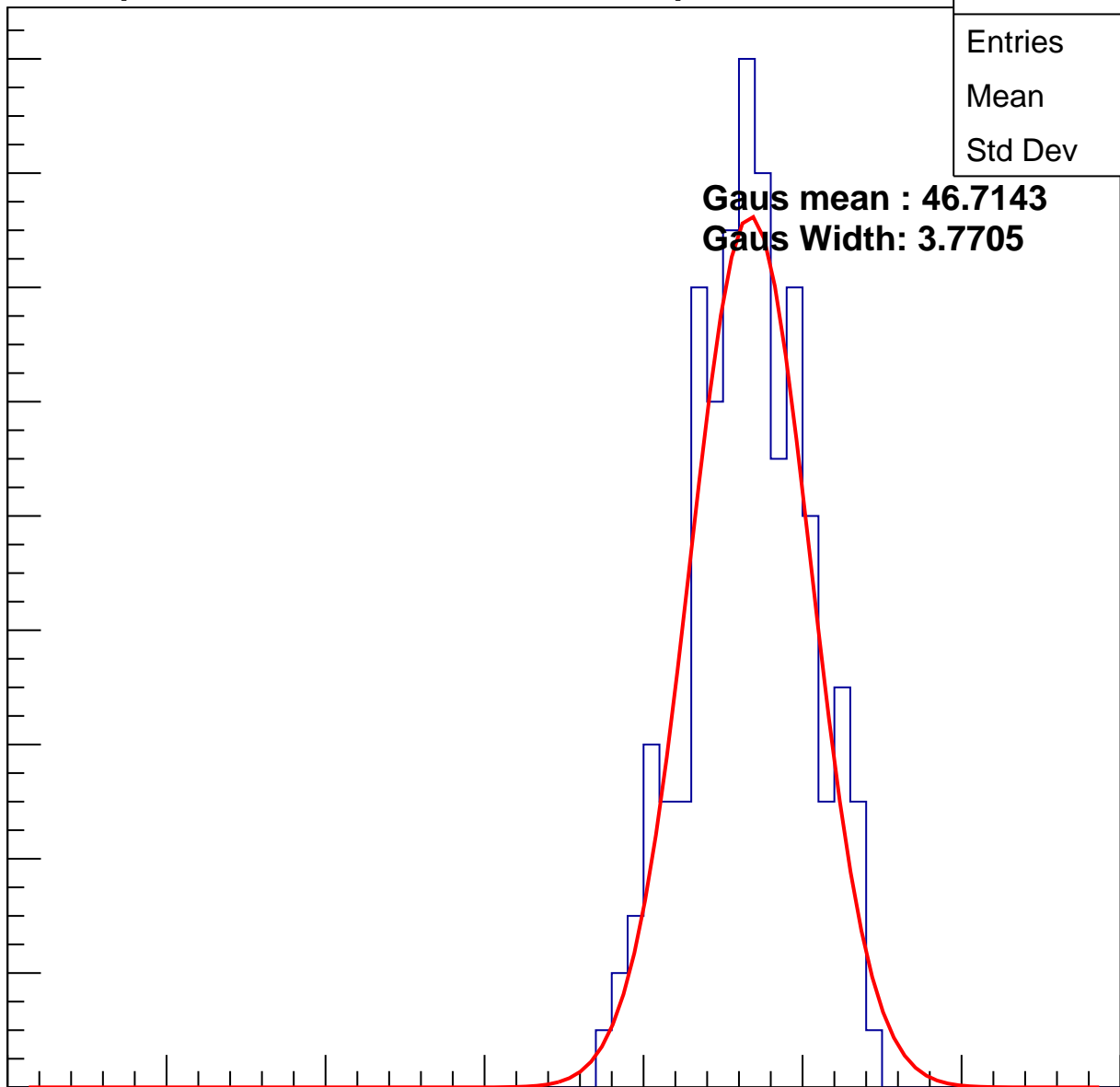
Entries	150
Mean	46.15
Std Dev	3.647

Gaus mean : 46.7143

Gaus Width: 3.7705

0 10 20 30 40 50 60 70

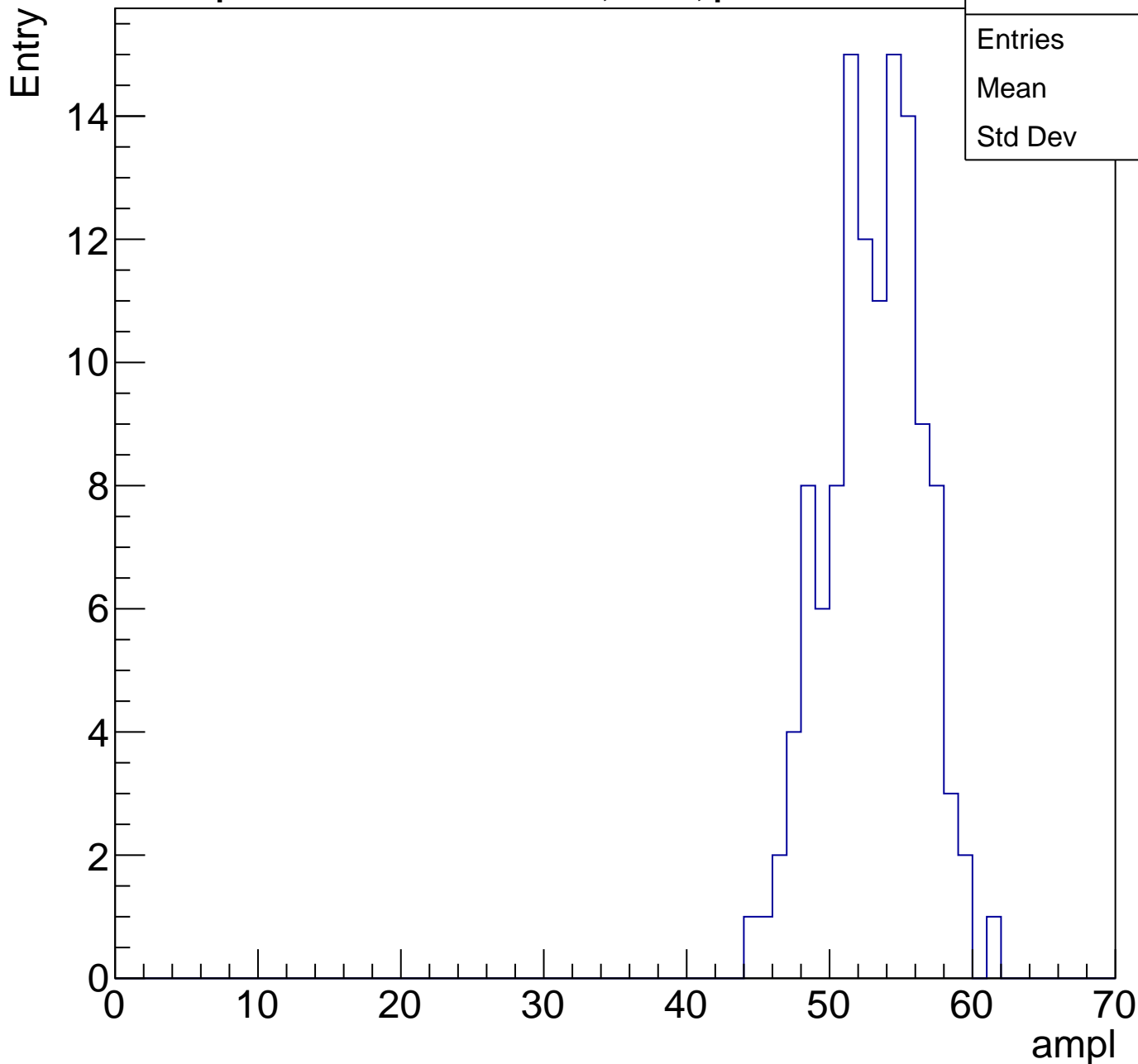
ampl



B1L001S, U25-ch35, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	52.6
Std Dev	3.29



B1L001S, U25-ch35, adc4

calib_packv5_042523_0143.root, FC#2, port C2

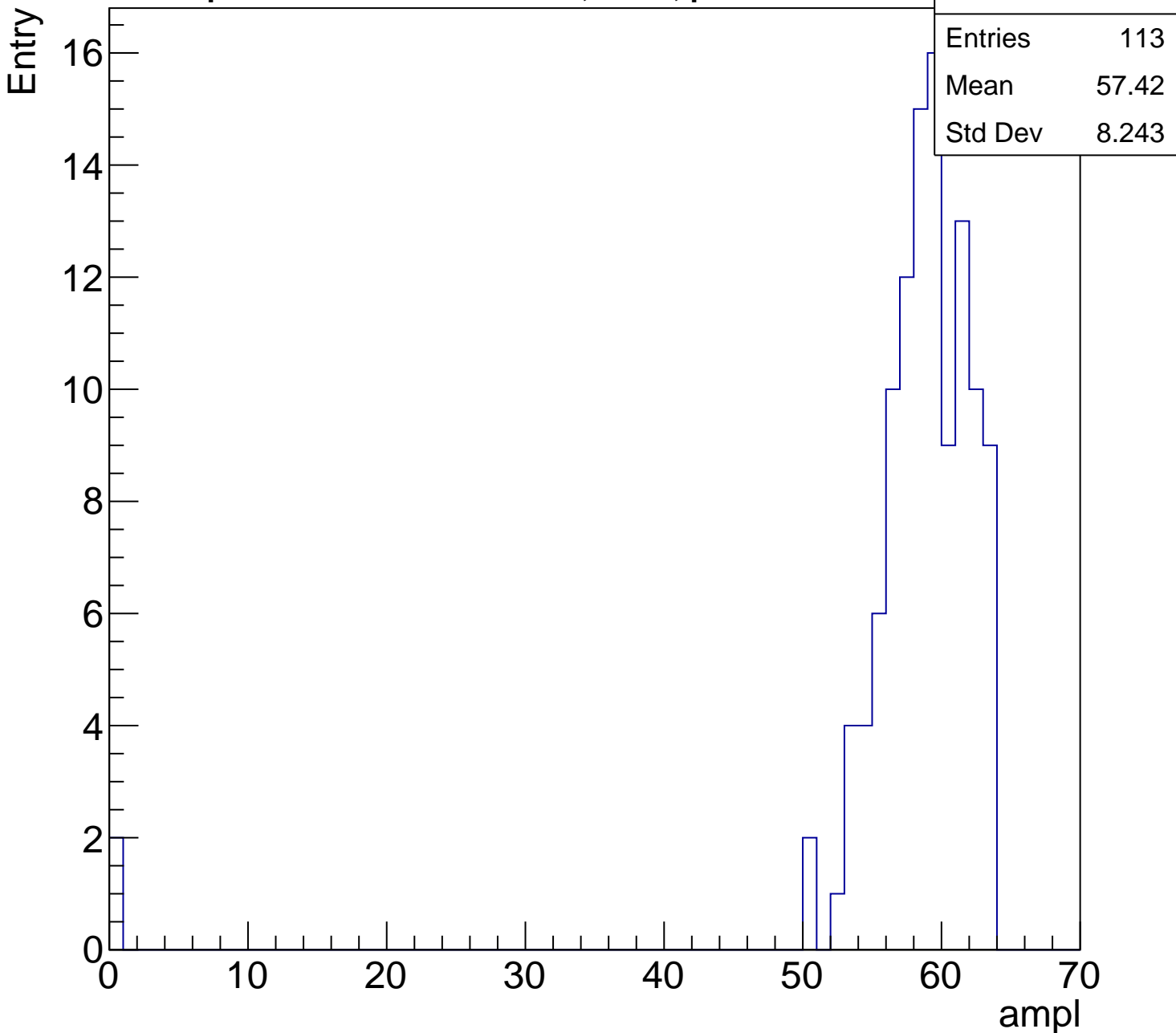
Entries	113
Mean	57.42
Std Dev	8.243

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch35, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	43
Mean	61.14
Std Dev	1.637

B1L001S, U25-ch35, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch35, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch36, adc0

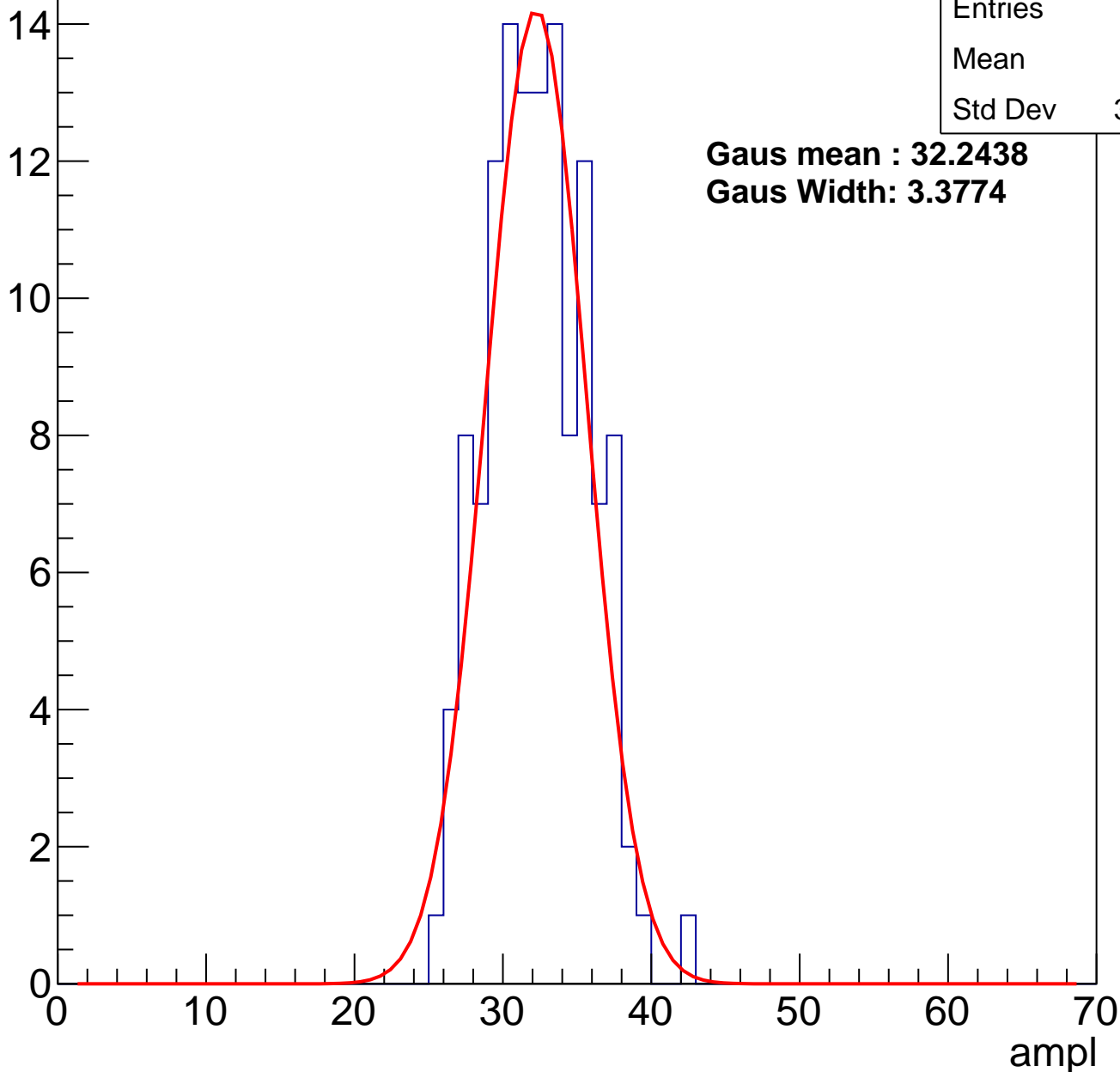
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	125
Mean	31.9
Std Dev	3.308

Gaus mean : 32.2438

Gaus Width: 3.3774

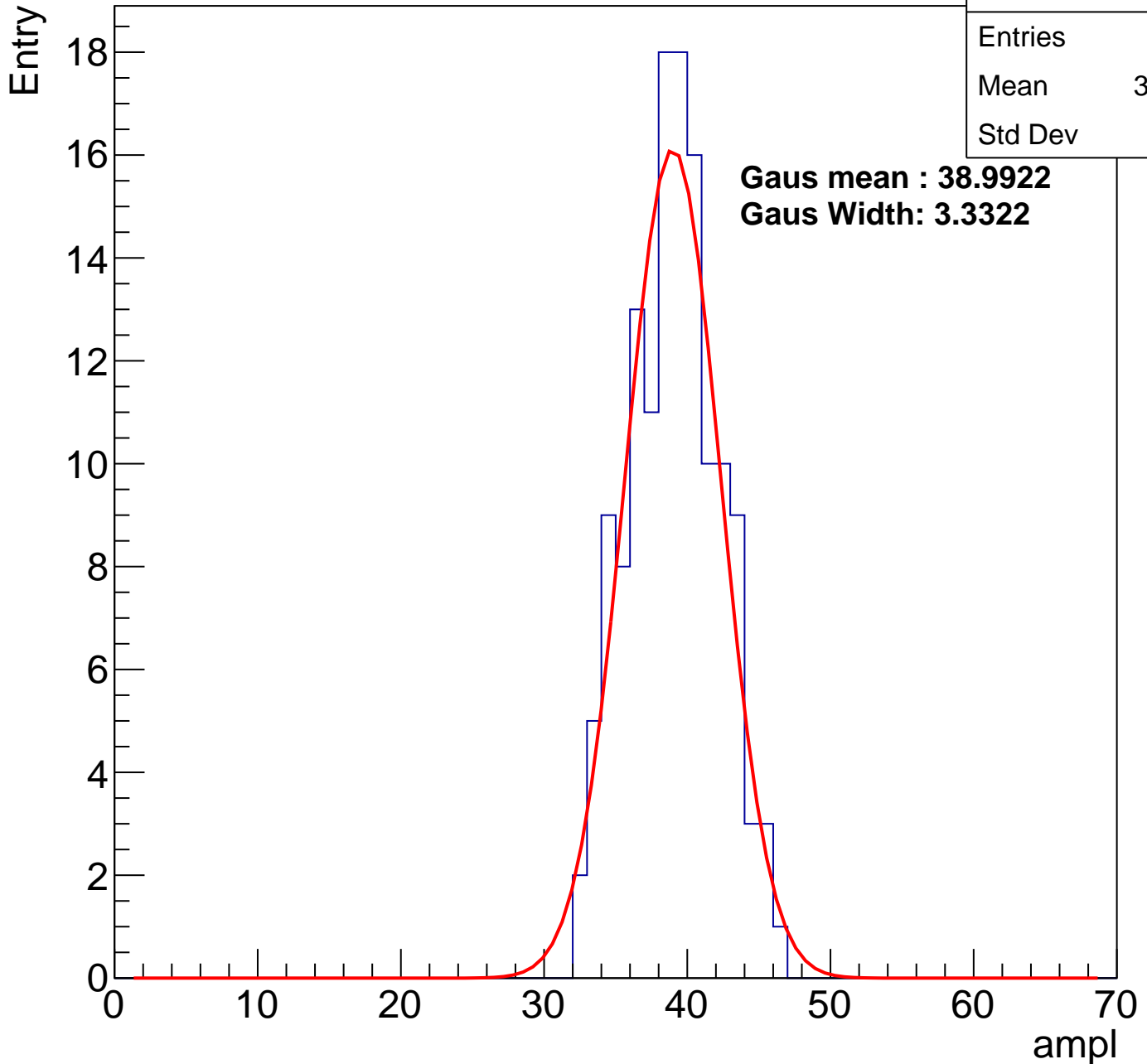


B1L001S, U25-ch36, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	38.57
Std Dev	3.1

Gaus mean : 38.9922
Gaus Width: 3.3322



B1L001S, U25-ch36, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	45.4
Std Dev	3.267

Gaus mean : 45.9627

Gaus Width: 3.1166

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch36, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	51.74
Std Dev	3.383

Entry

14

12

10

8

6

4

2

0

0

10

20

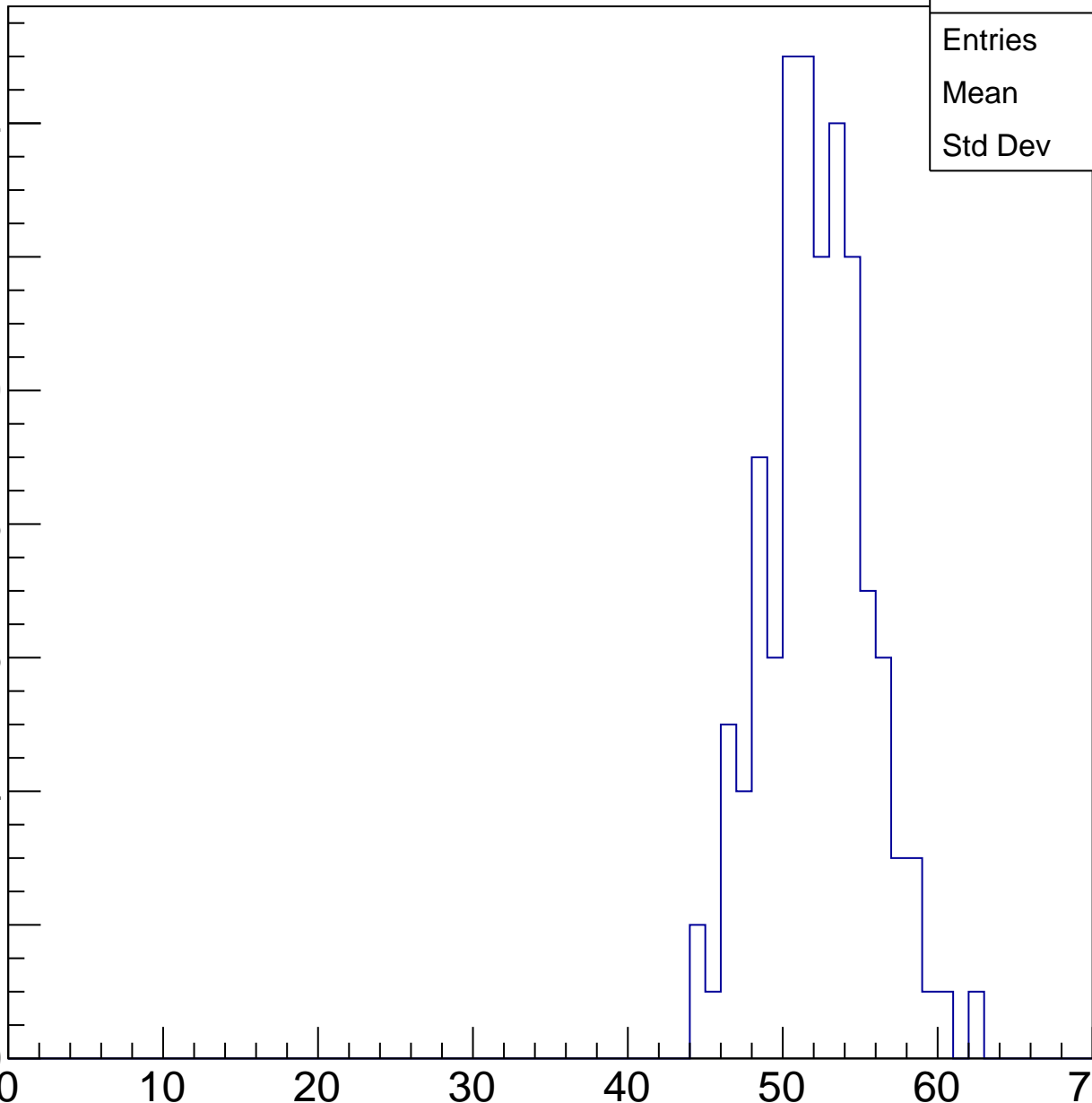
30

40

50

60

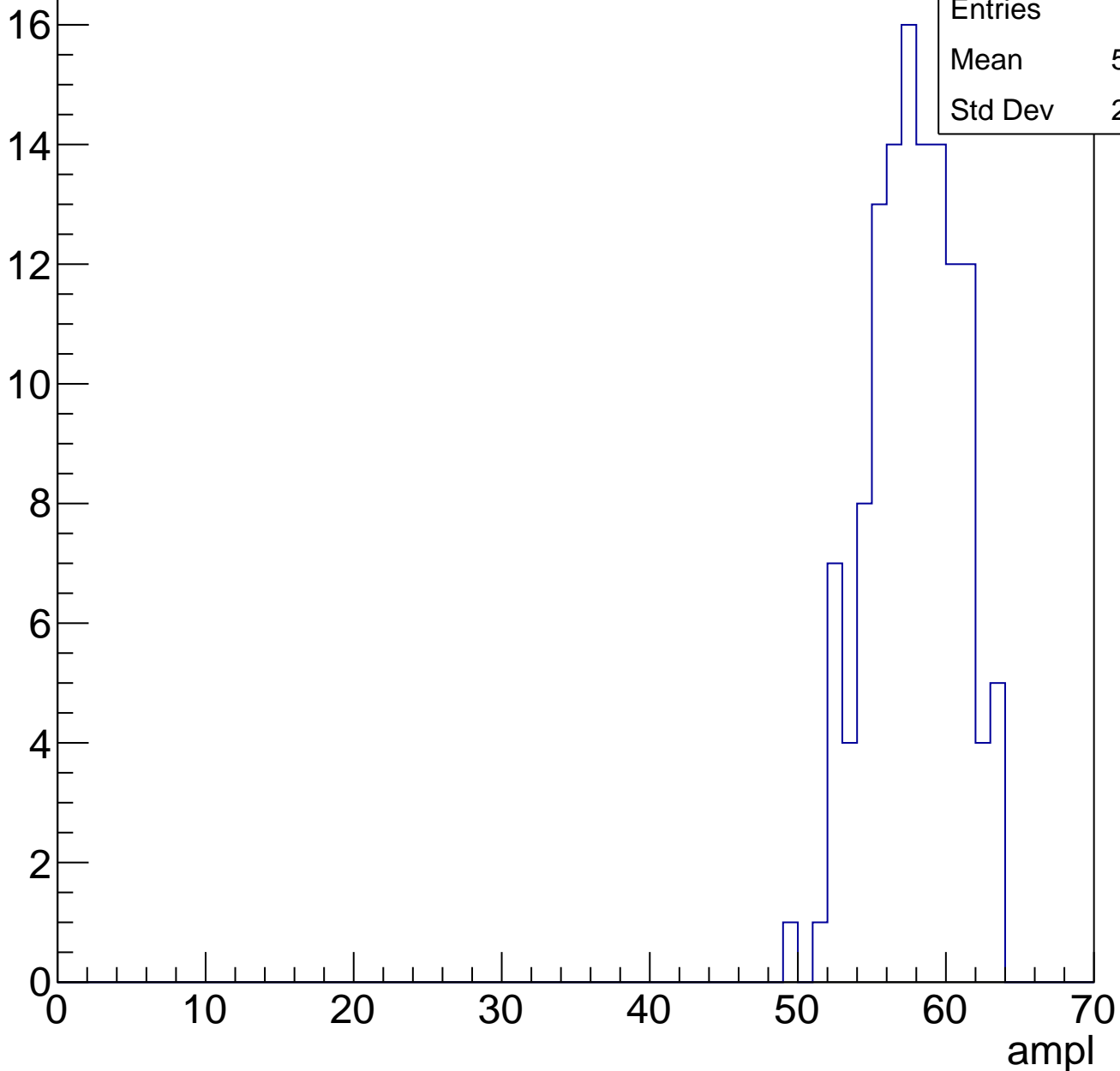
ampl



B1L001S, U25-ch36, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch36, adc5

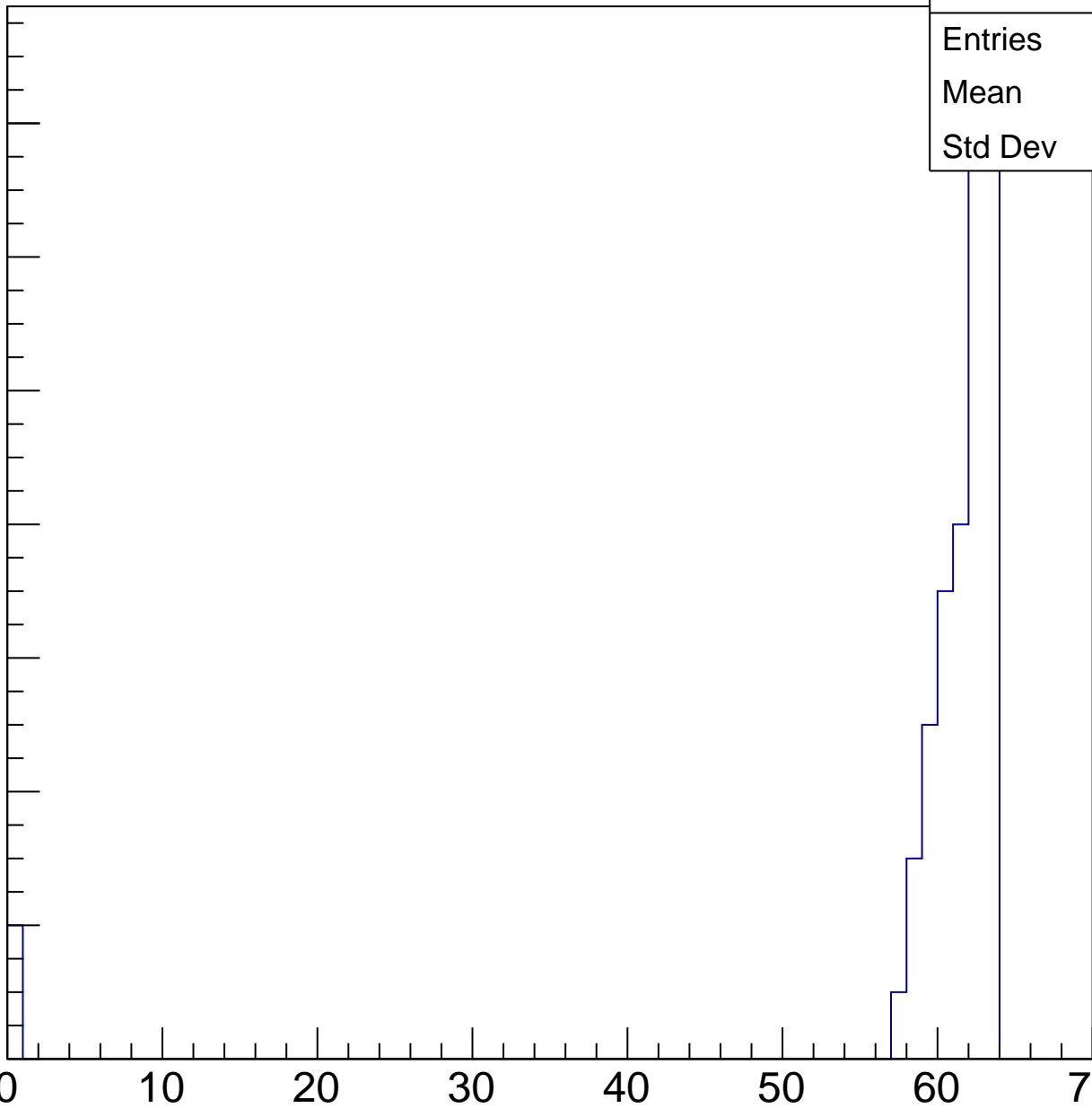
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	56
Mean	59.09
Std Dev	11.48

ampl



B1L001S, U25-ch36, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch36, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	24
Std Dev	0

B1L001S, U25-ch37, adc0

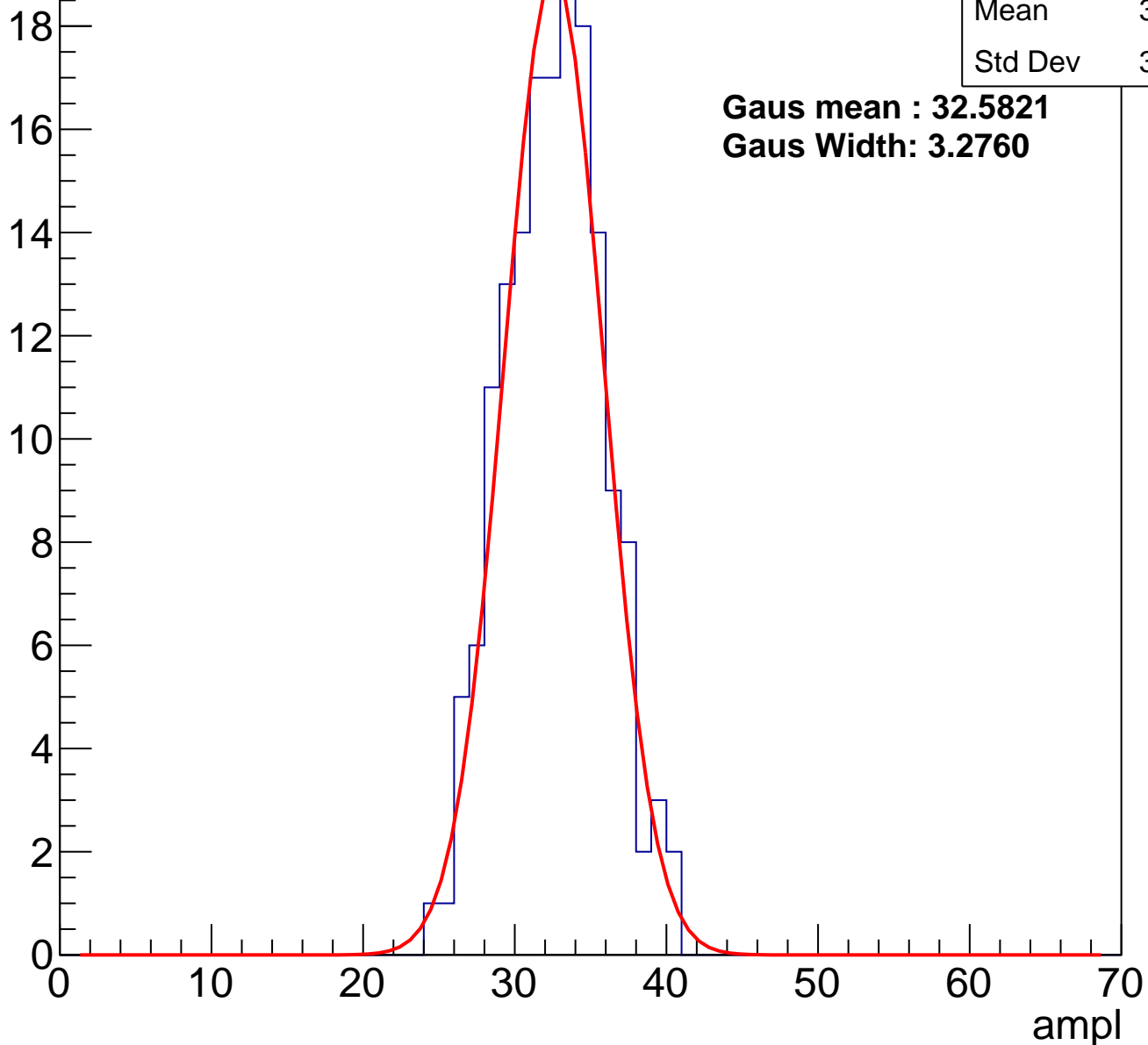
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	32.12
Std Dev	3.274

Gaus mean : 32.5821

Gaus Width: 3.2760

Entry

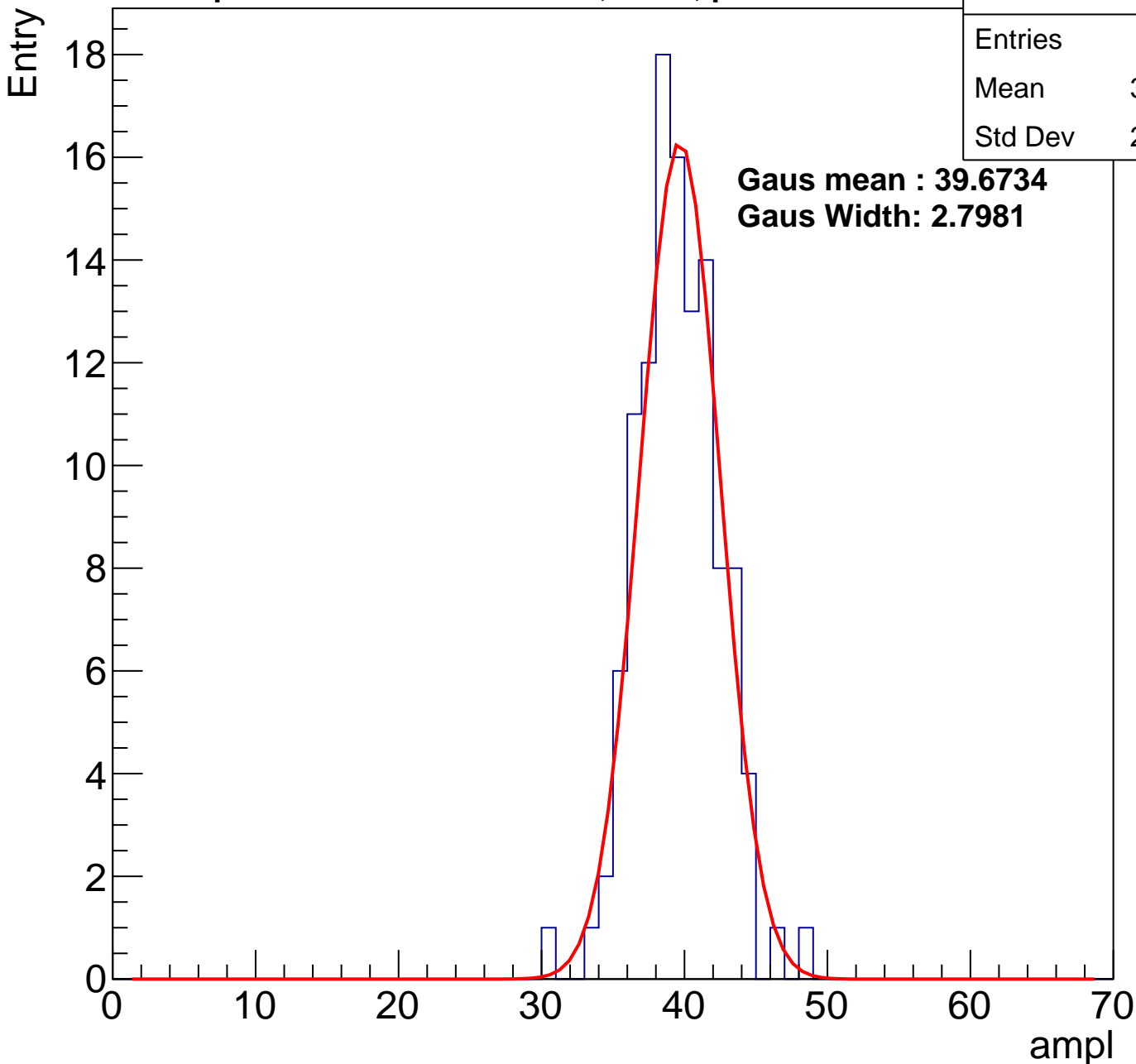


B1L001S, U25-ch37, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	39.08
Std Dev	2.826

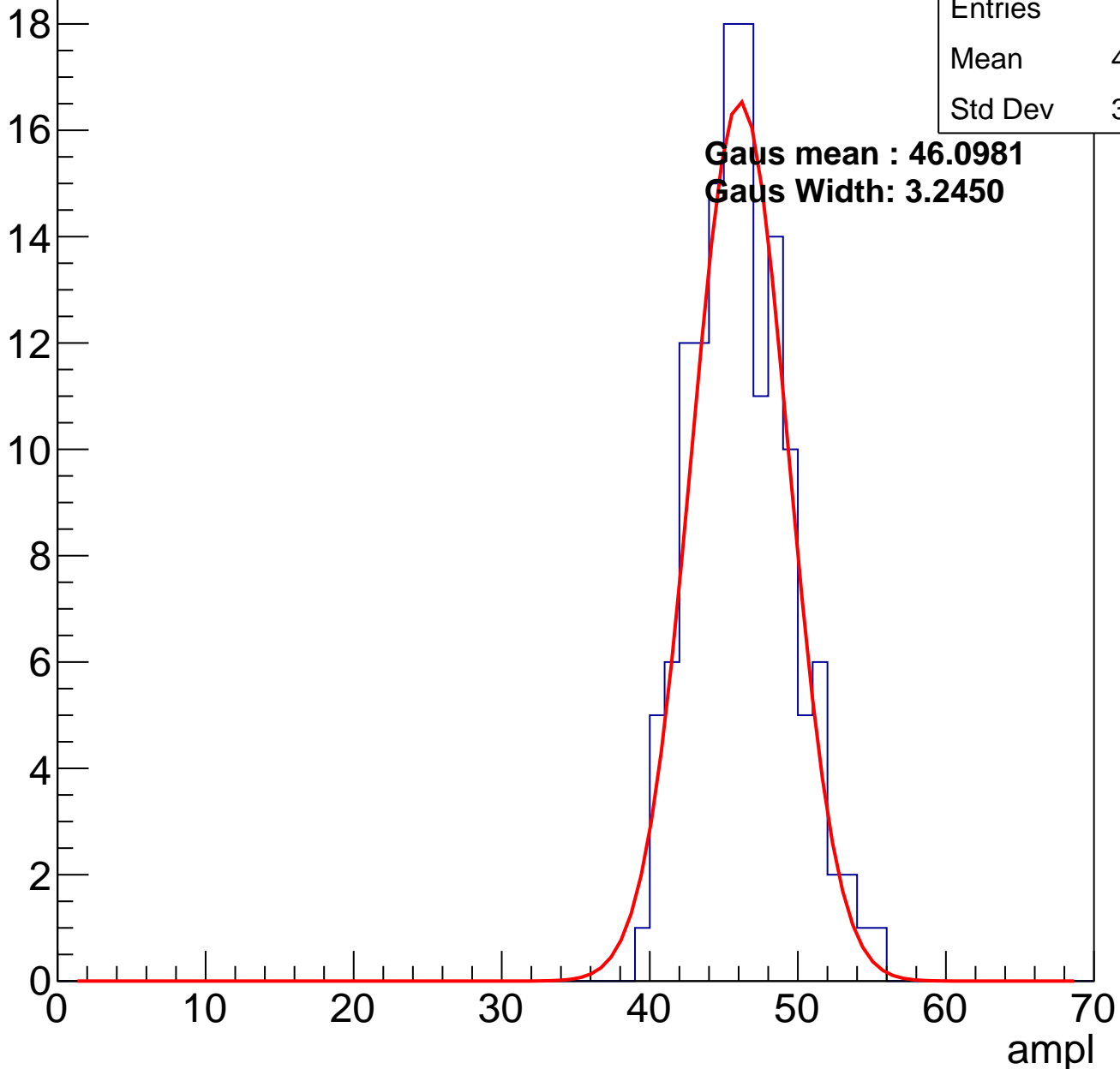
Gaus mean : 39.6734
Gaus Width: 2.7981



B1L001S, U25-ch37, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	139
Mean	45.73
Std Dev	3.229

Gaus mean : 46.0981

Gaus Width: 3.2450

B1L001S, U25-ch37, adc3

calib_packv5_042523_0143.root, FC#2, port C2

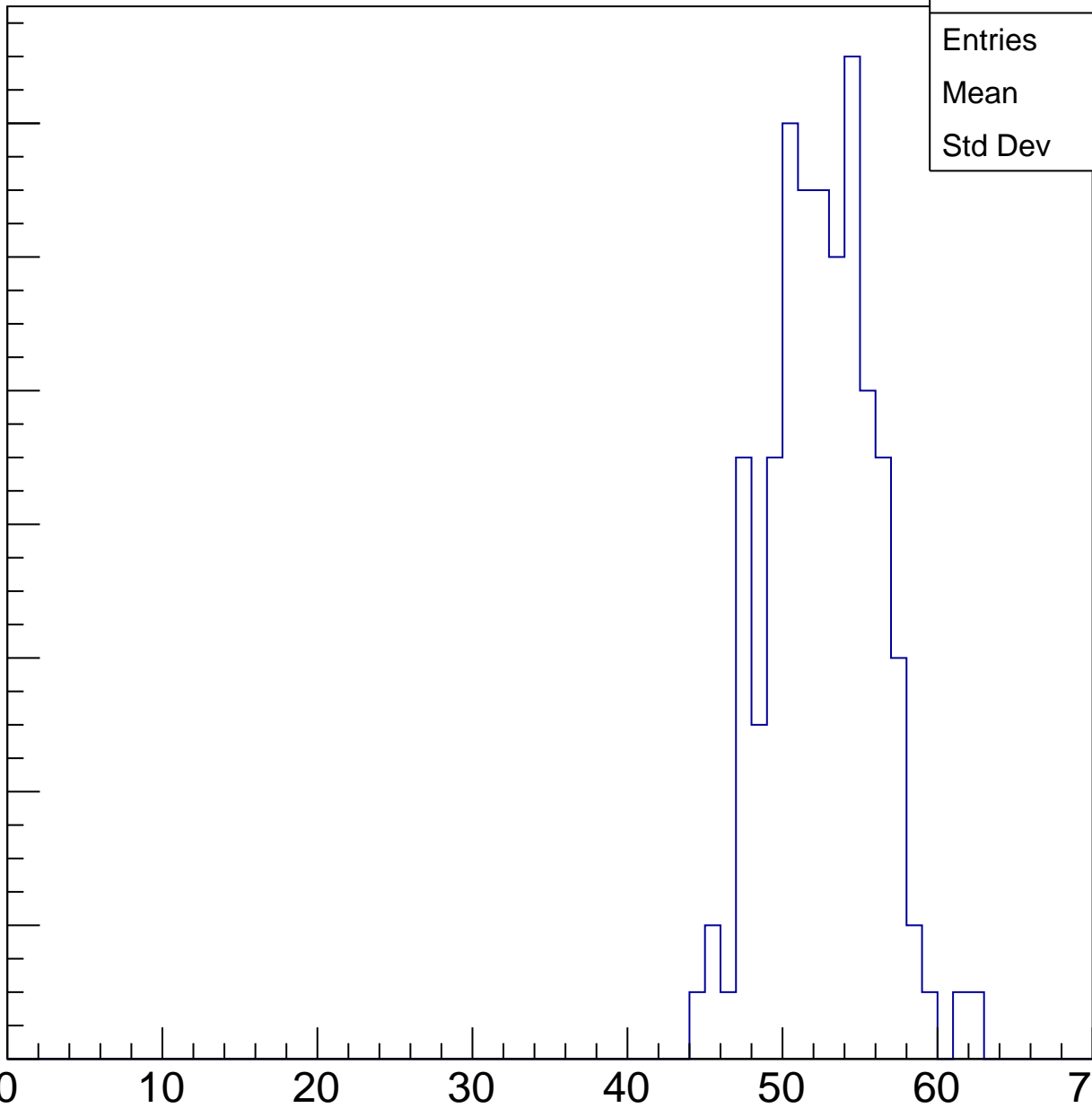
Entries	124
Mean	52.12
Std Dev	3.371

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

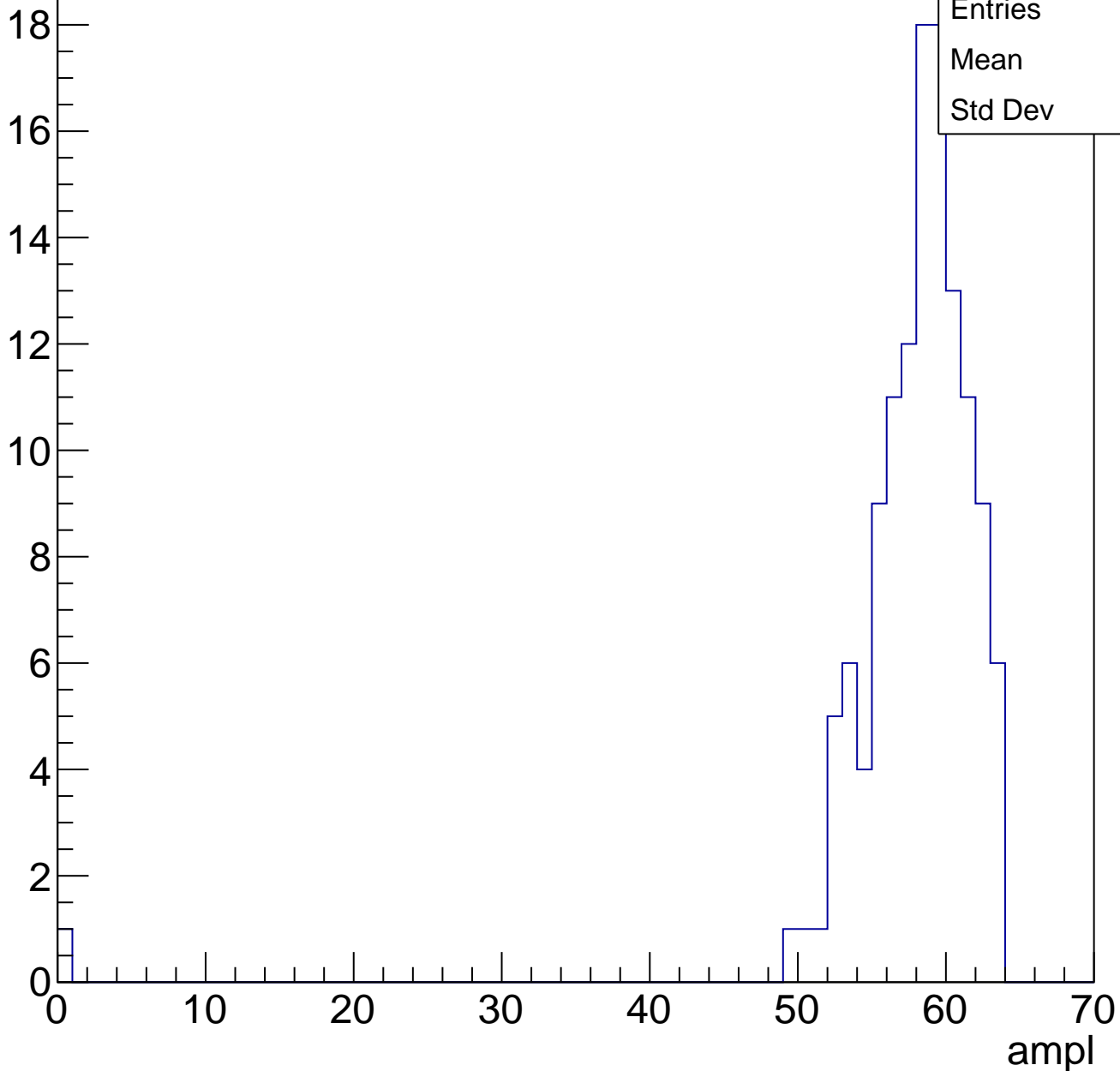
ampl



B1L001S, U25-ch37, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

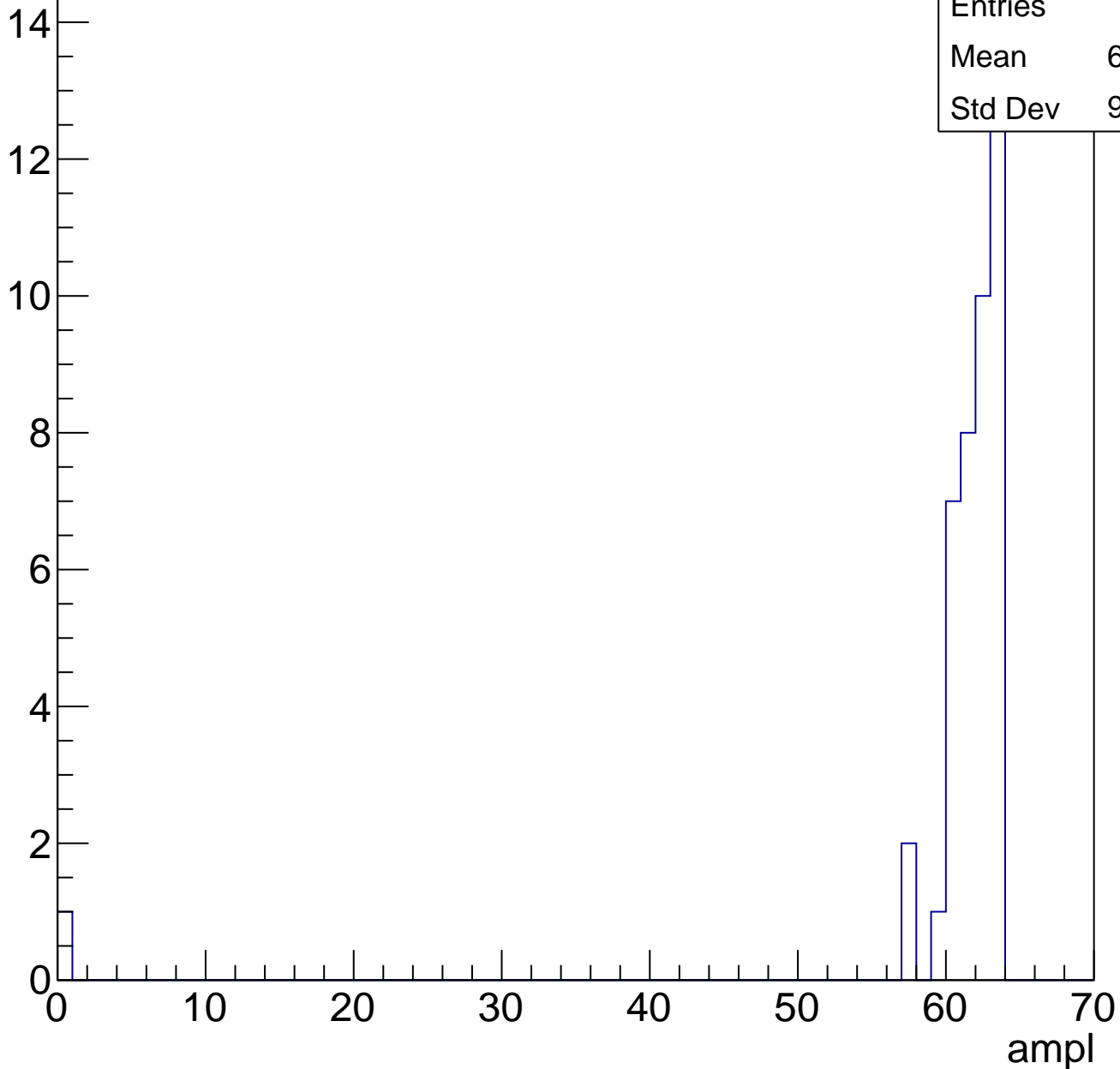


B1L001S, U25-ch37, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	43
Mean	60.07
Std Dev	9.392

Entry



B1L001S, U25-ch37, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch37, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	24
Std Dev	0

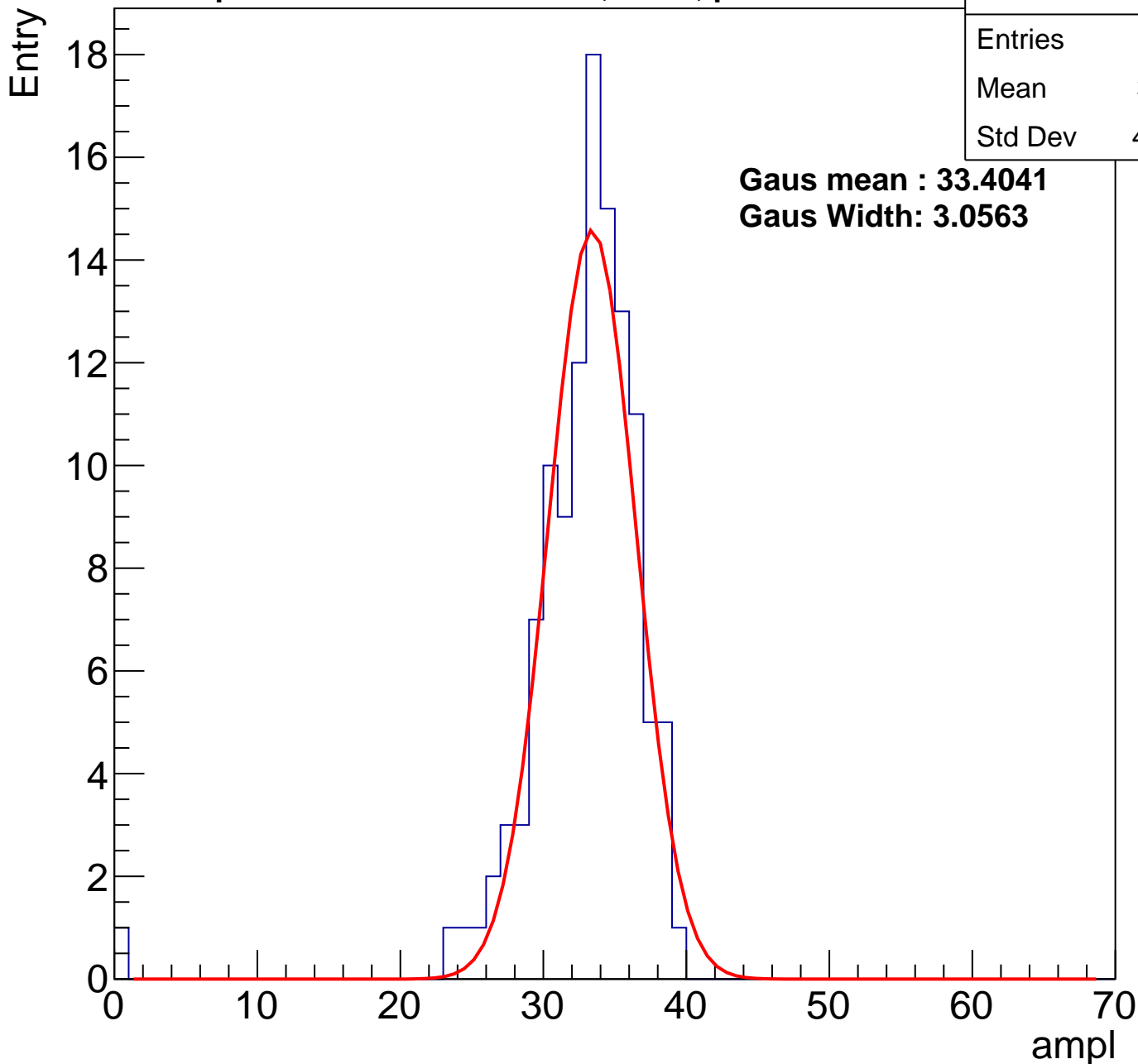
B1L001S, U25-ch38, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	32.41
Std Dev	4.348

Gaus mean : 33.4041

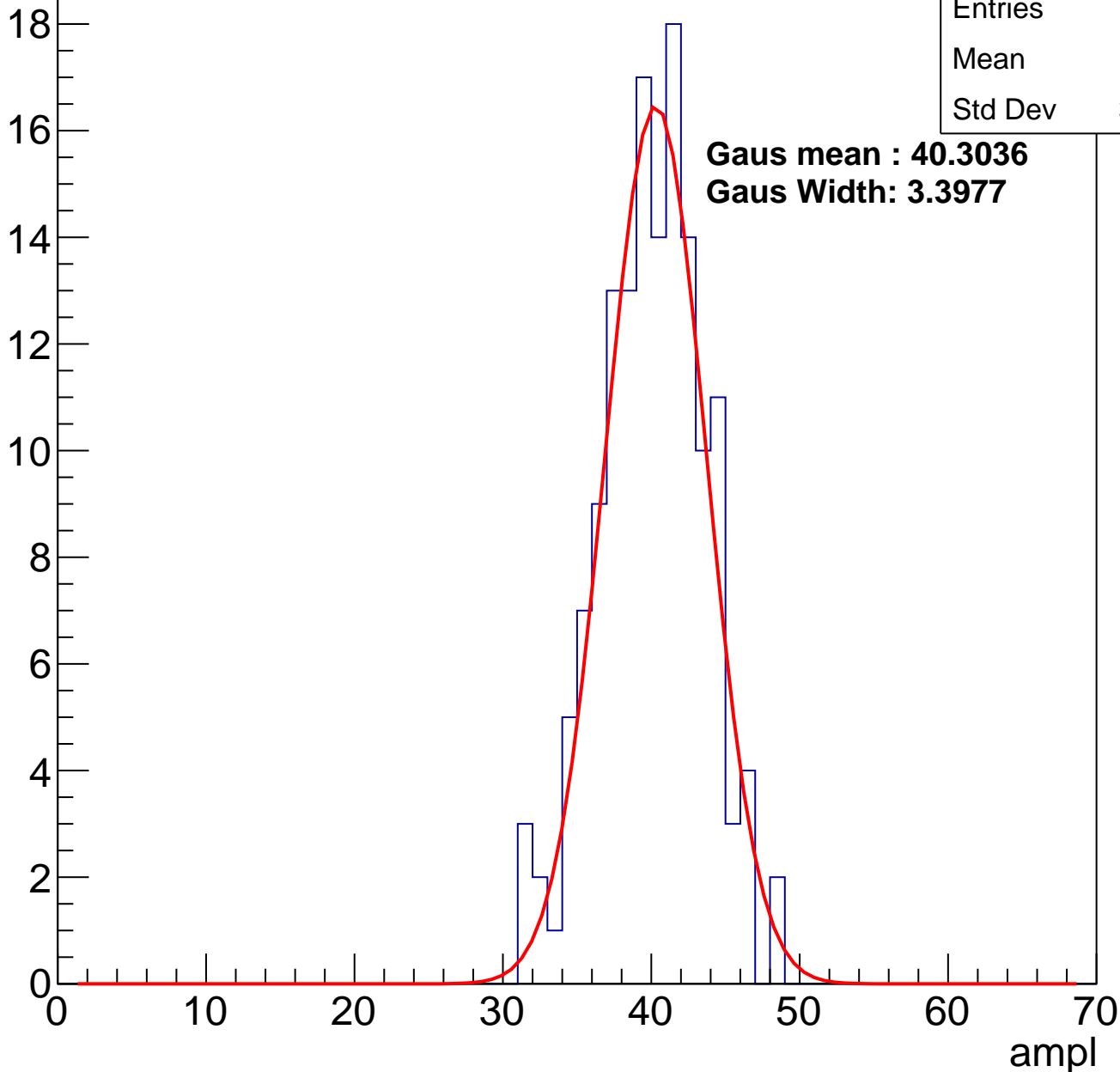
Gaus Width: 3.0563



B1L001S, U25-ch38, adc1

calib_packv5_042523_0143.root, FC#2, port C2

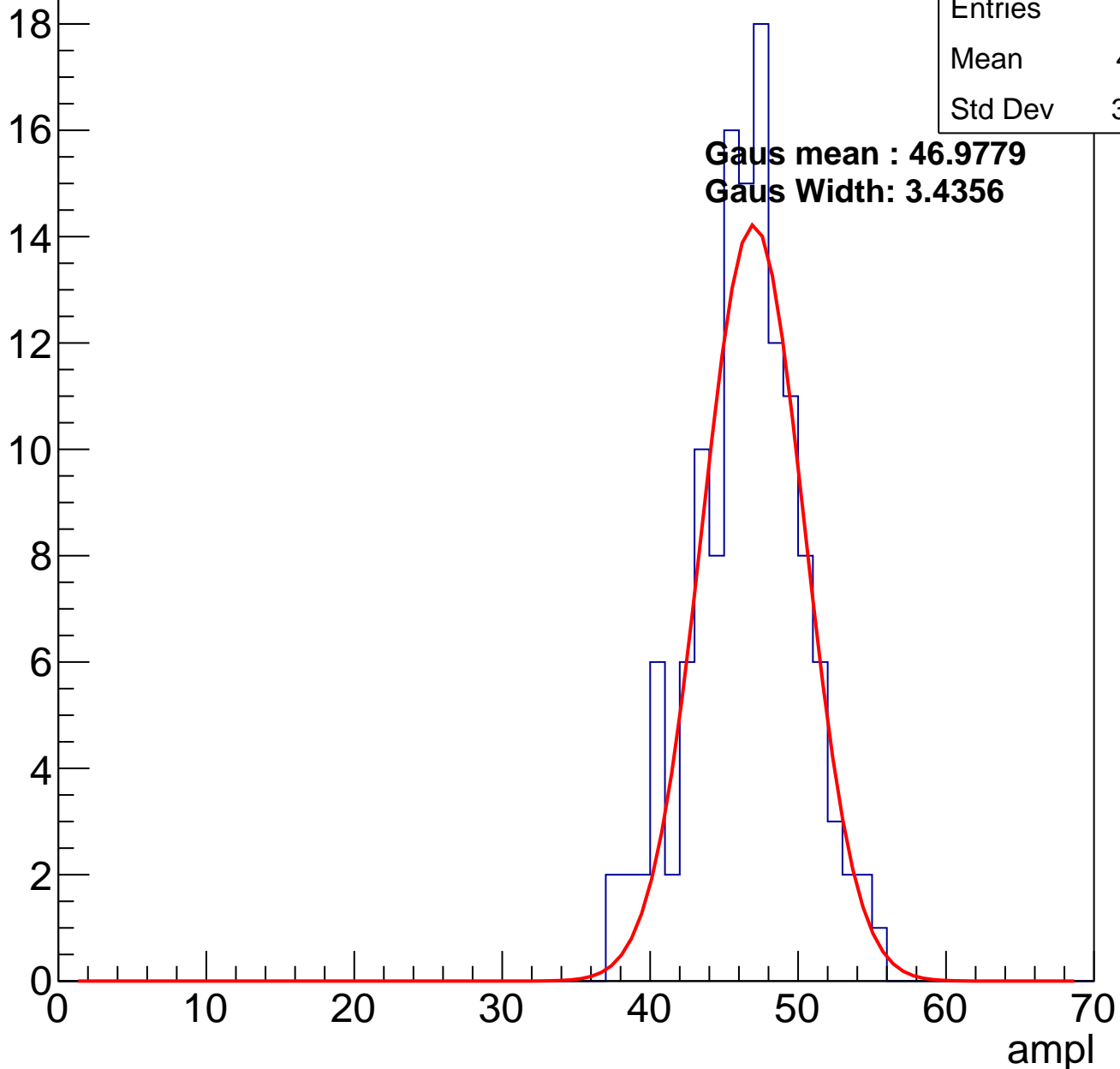
Entry



B1L001S, U25-ch38, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	132
Mean	46.11
Std Dev	3.635

Gaus mean : 46.9779

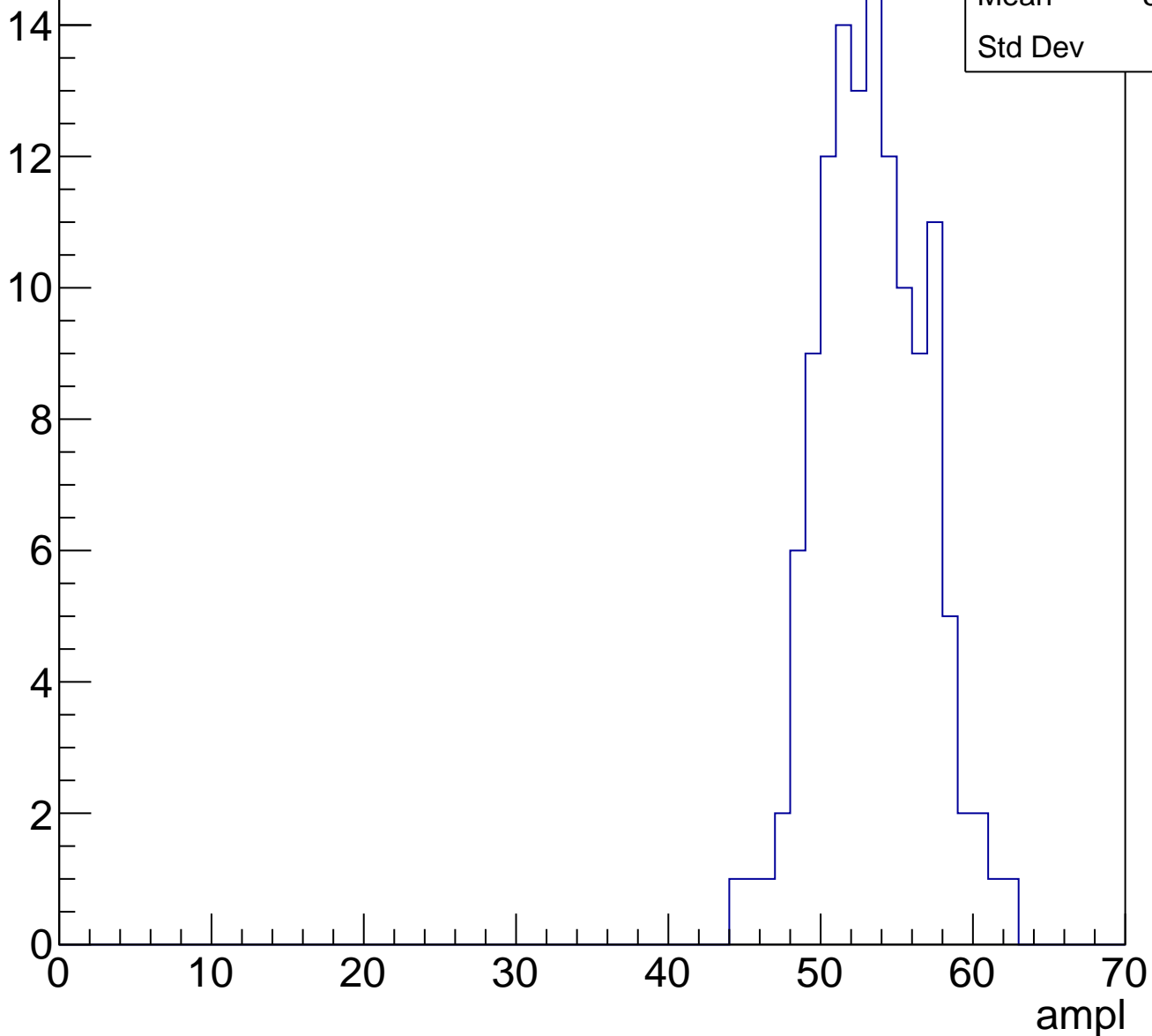
Gaus Width: 3.4356

B1L001S, U25-ch38, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	52.94
Std Dev	3.42

Entry

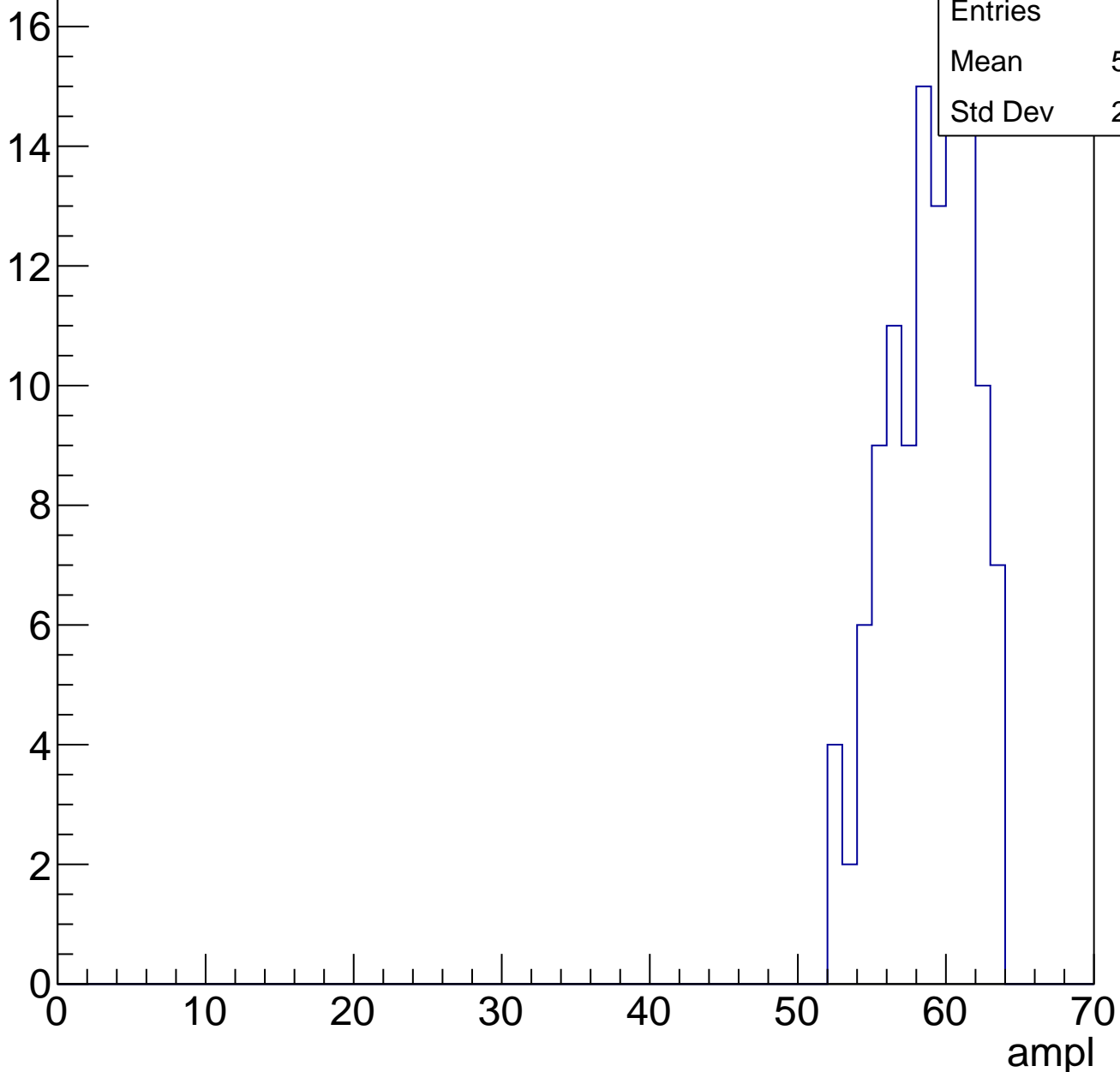


B1L001S, U25-ch38, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	58.44
Std Dev	2.848

Entry



B1L001S, U25-ch38, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	34
Mean	57.82
Std Dev	14.6

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch38, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch38, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

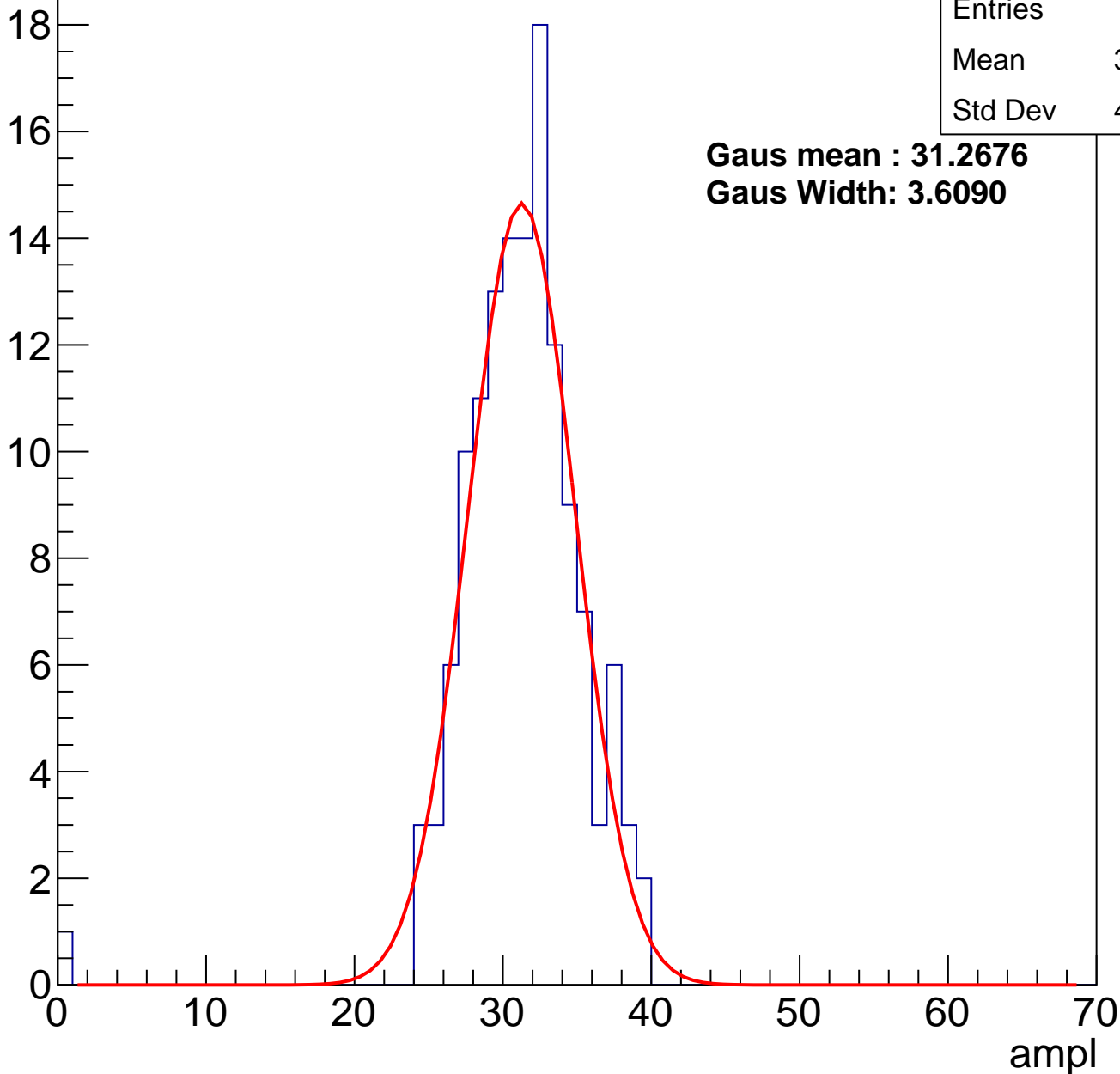


Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch39, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch39, adc1

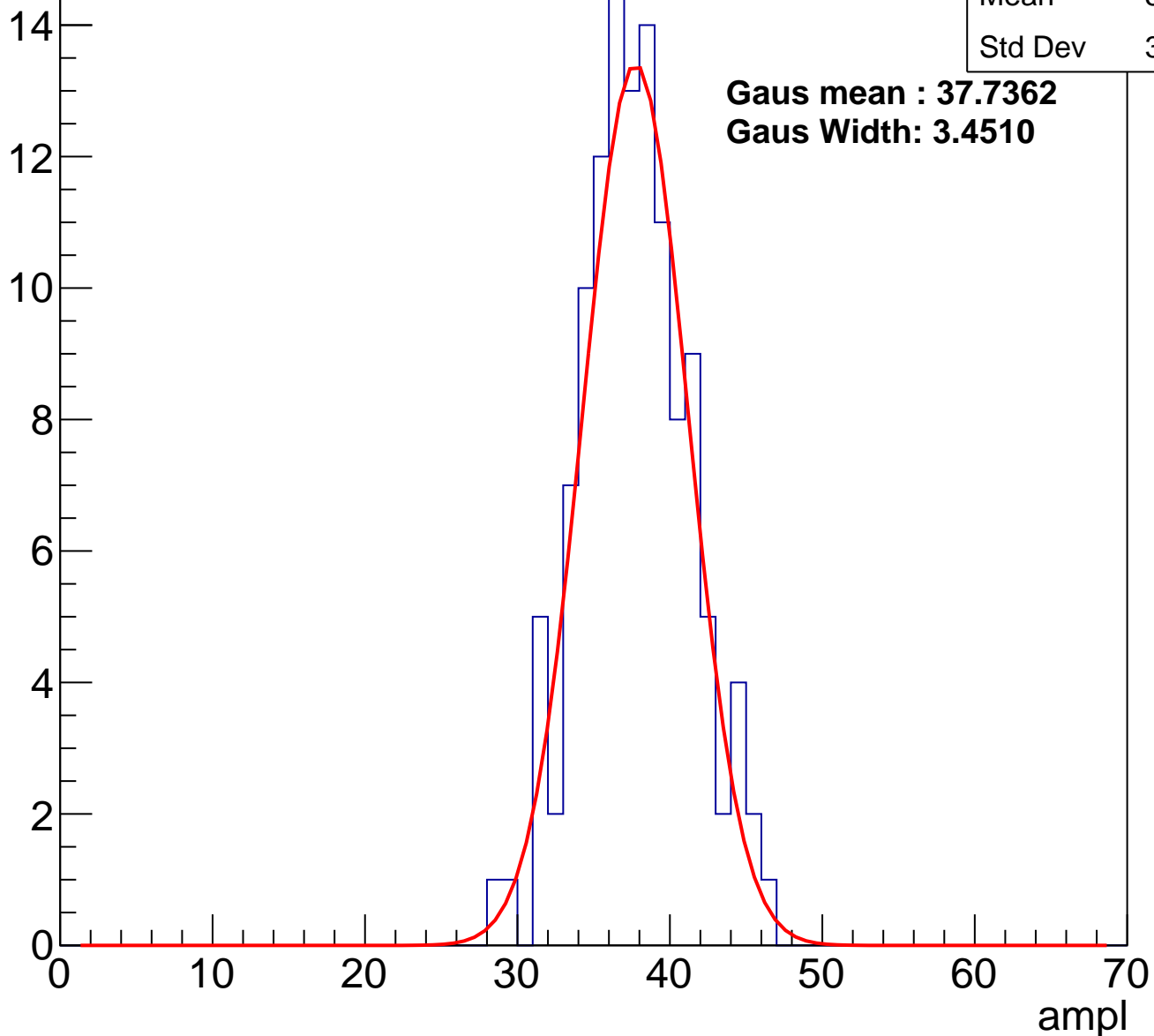
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	37.26
Std Dev	3.515

Gaus mean : 37.7362

Gaus Width: 3.4510

Entry



B1L001S, U25-ch39, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

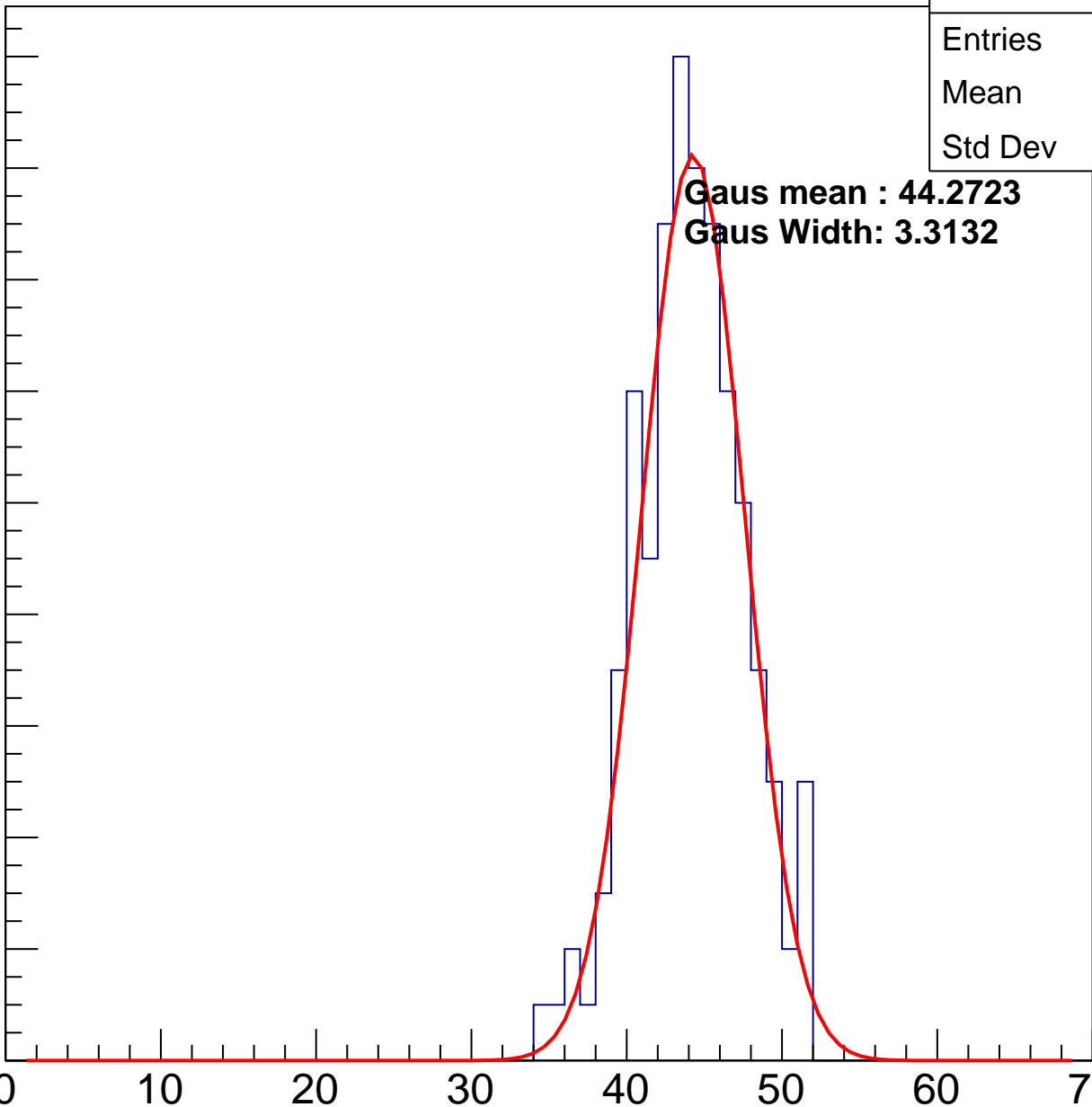
Entries	141
Mean	43.65
Std Dev	3.454

Gaus mean : 44.2723

Gaus Width: 3.3132

0 10 20 30 40 50 60 70

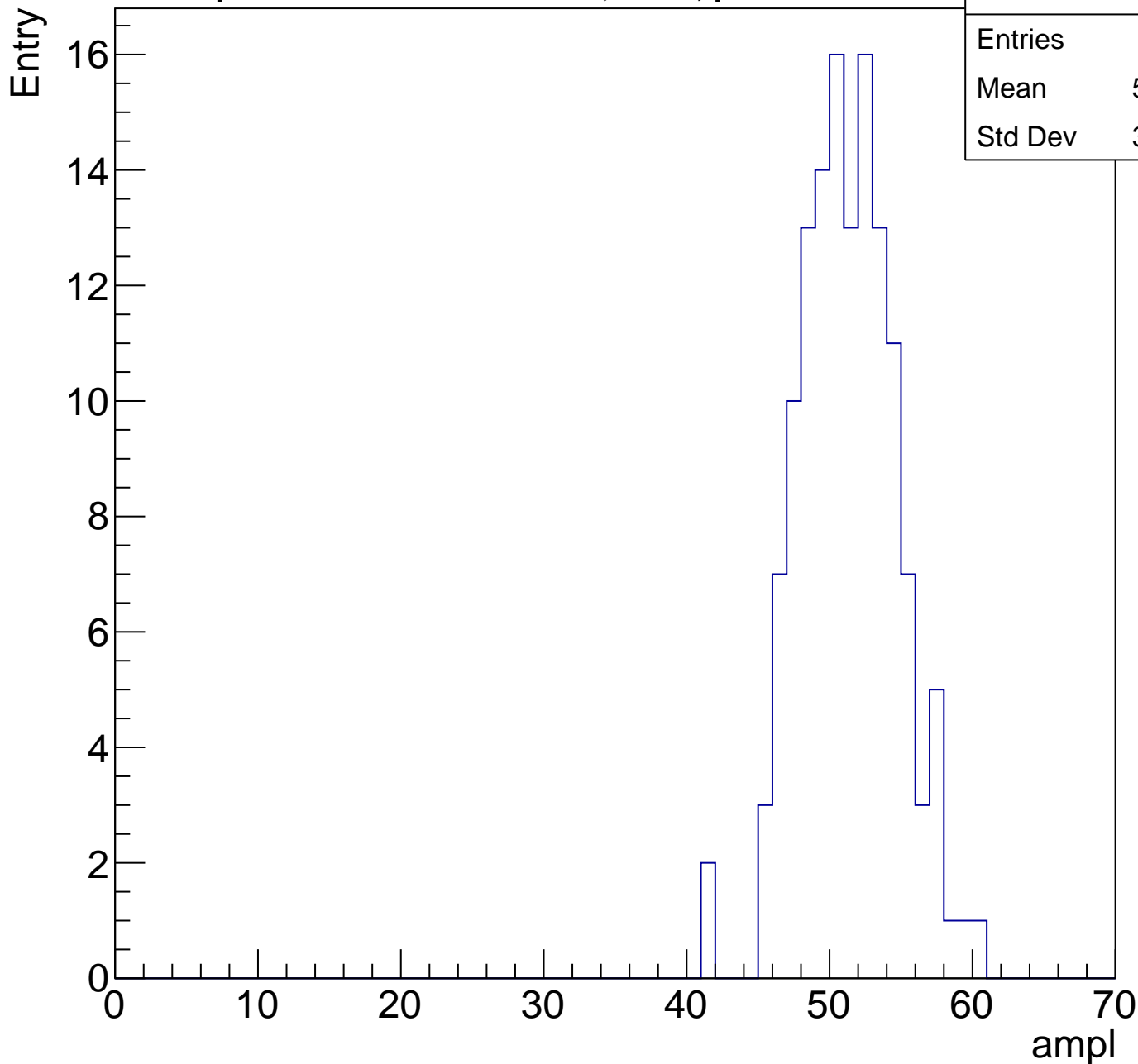
ampl



B1L001S, U25-ch39, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	50.82
Std Dev	3.382



B1L001S, U25-ch39, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	56.66
Std Dev	6.107

Entry

12

10

8

6

4

2

0

0

10

20

30

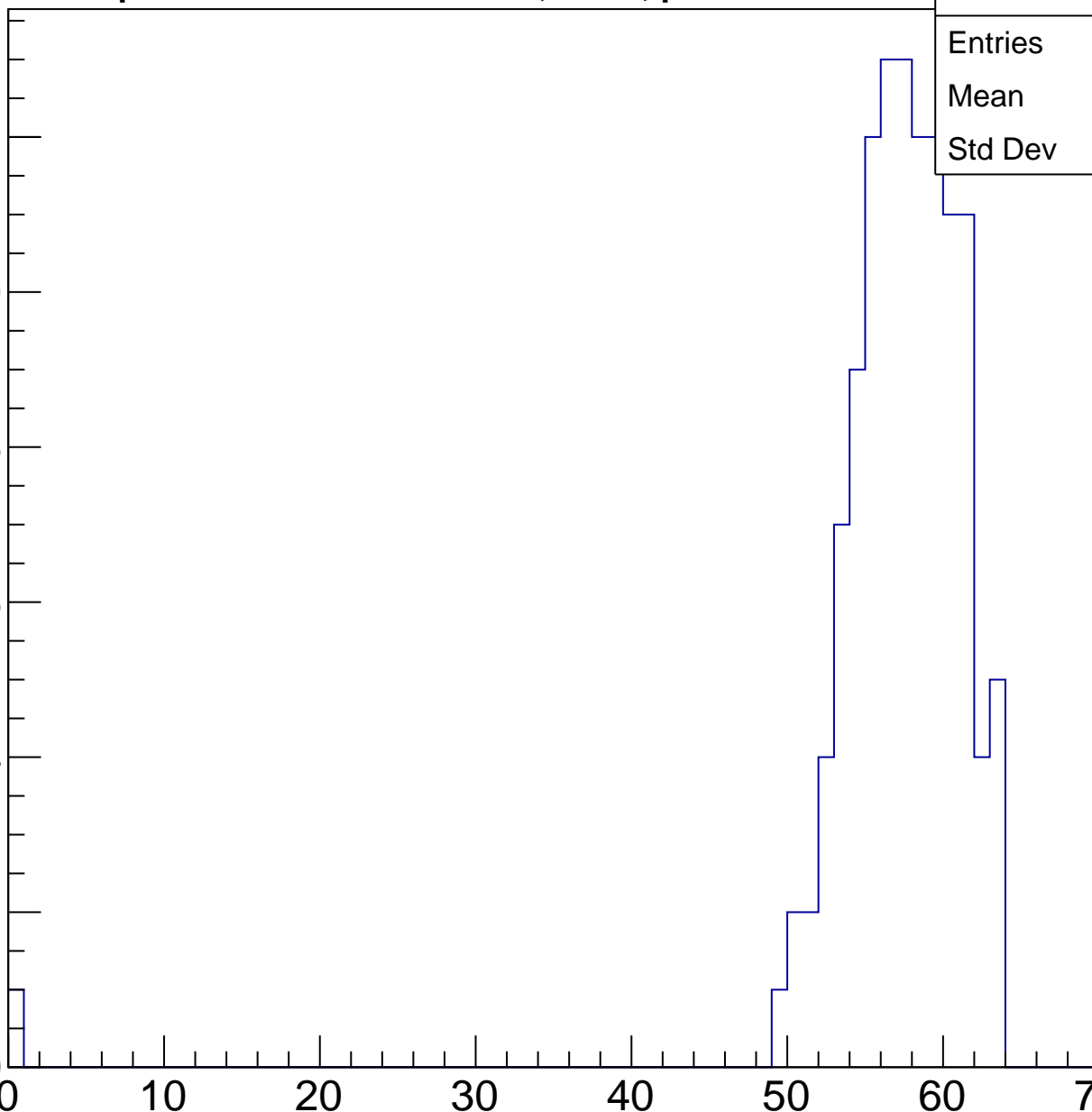
40

50

60

70

ampl



B1L001S, U25-ch39, adc5

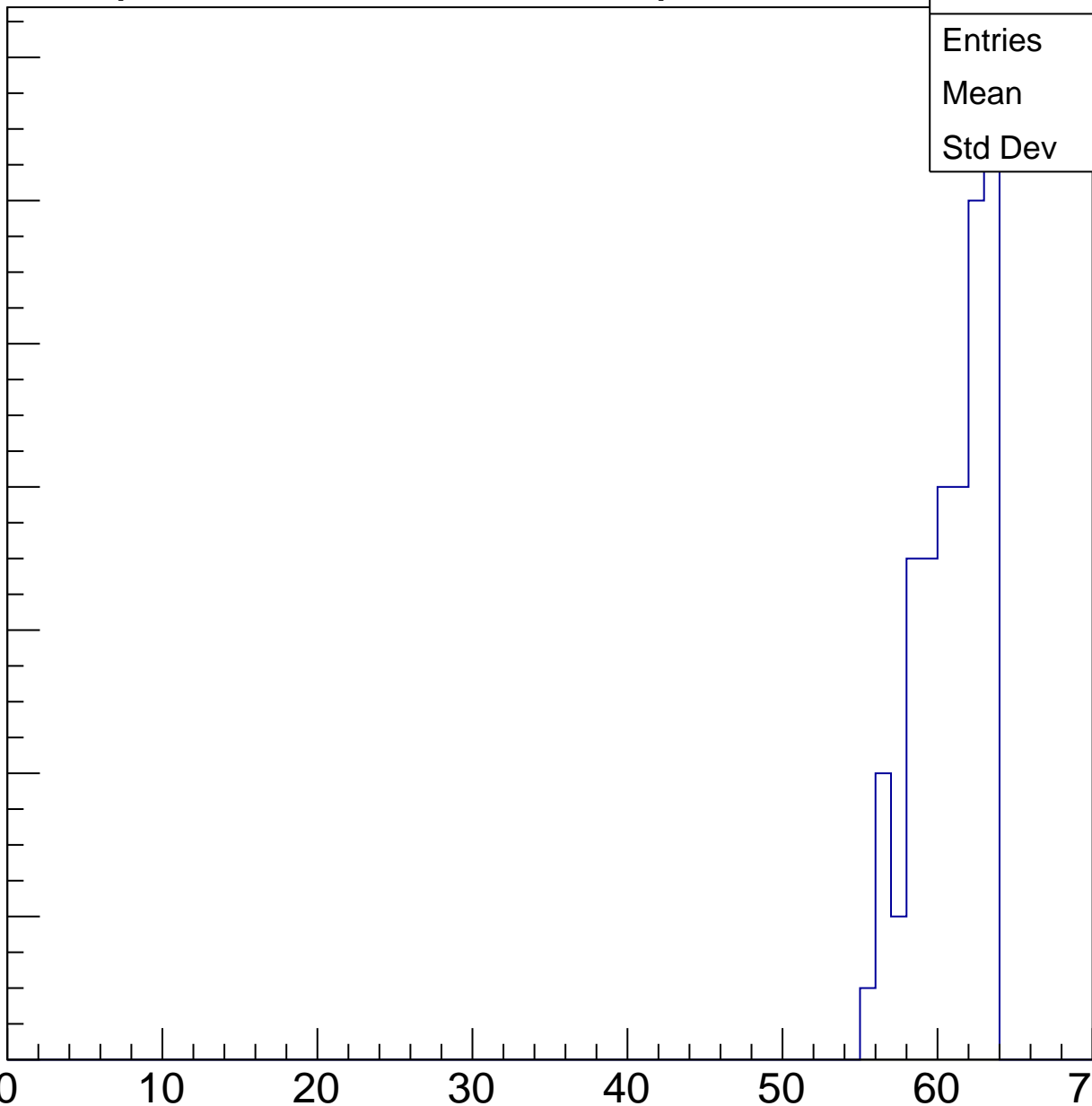
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	63
Mean	60.41
Std Dev	2.23

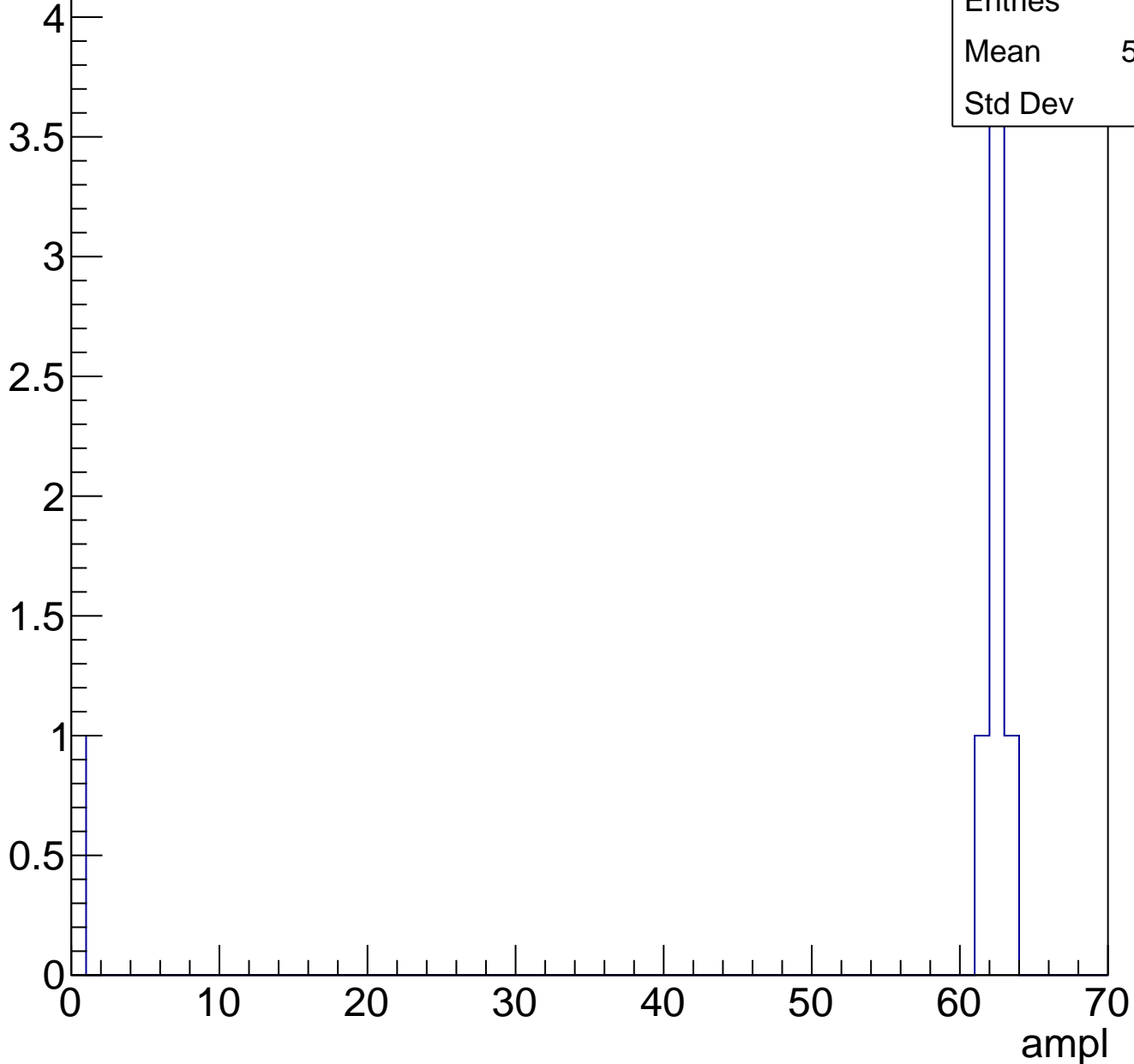
ampl



B1L001S, U25-ch39, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch39, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



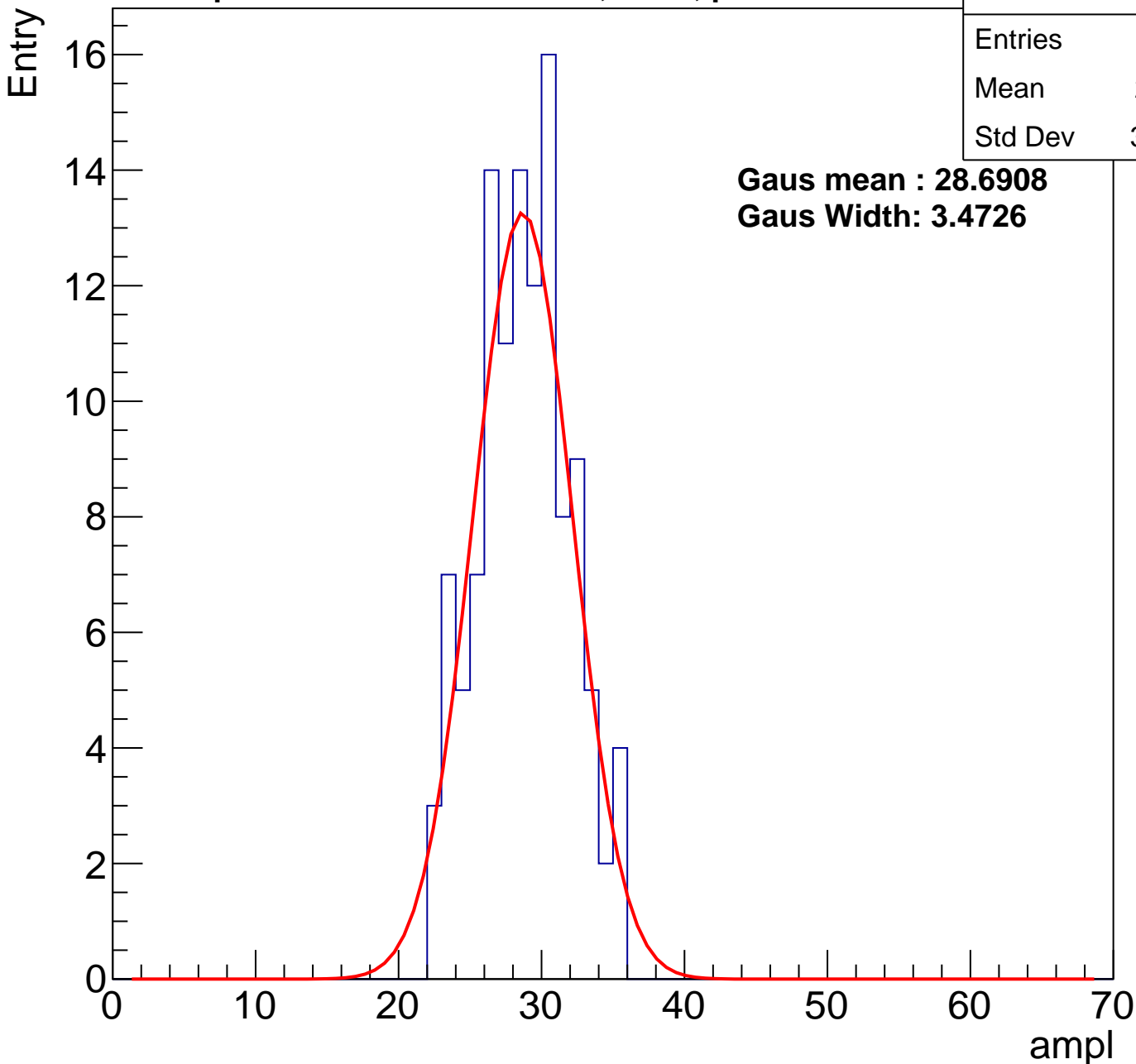
B1L001S, U25-ch40, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	28.31
Std Dev	3.158

Gaus mean : 28.6908

Gaus Width: 3.4726



B1L001S, U25-ch40, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	34.82
Std Dev	3.519

Gaus mean : 35.6463

Gaus Width: 3.6265

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch40, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

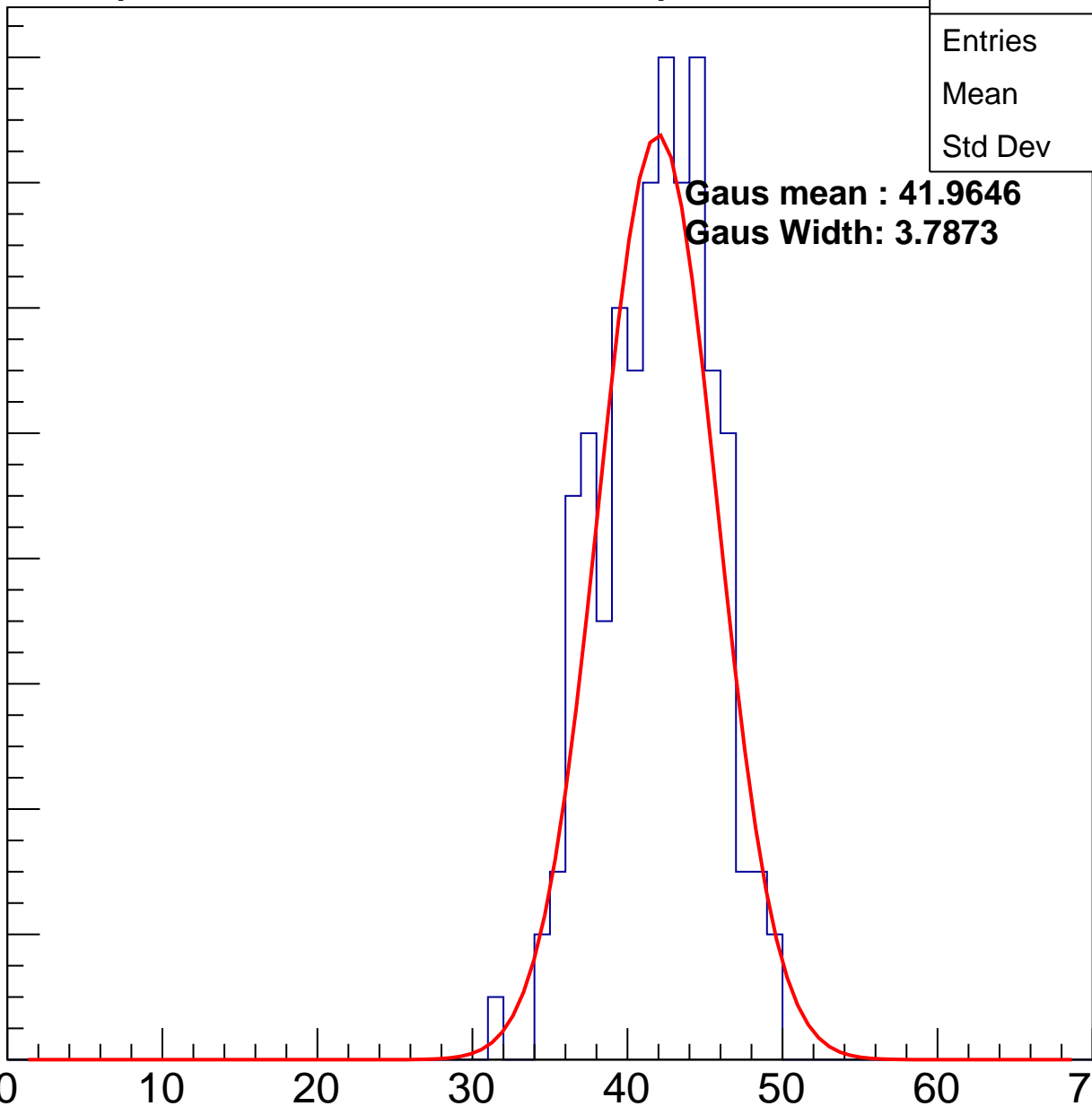
16
14
12
10
8
6
4
2
0

Entries	144
Mean	41.4
Std Dev	3.55

Gaus mean : 41.9646

Gaus Width: 3.7873

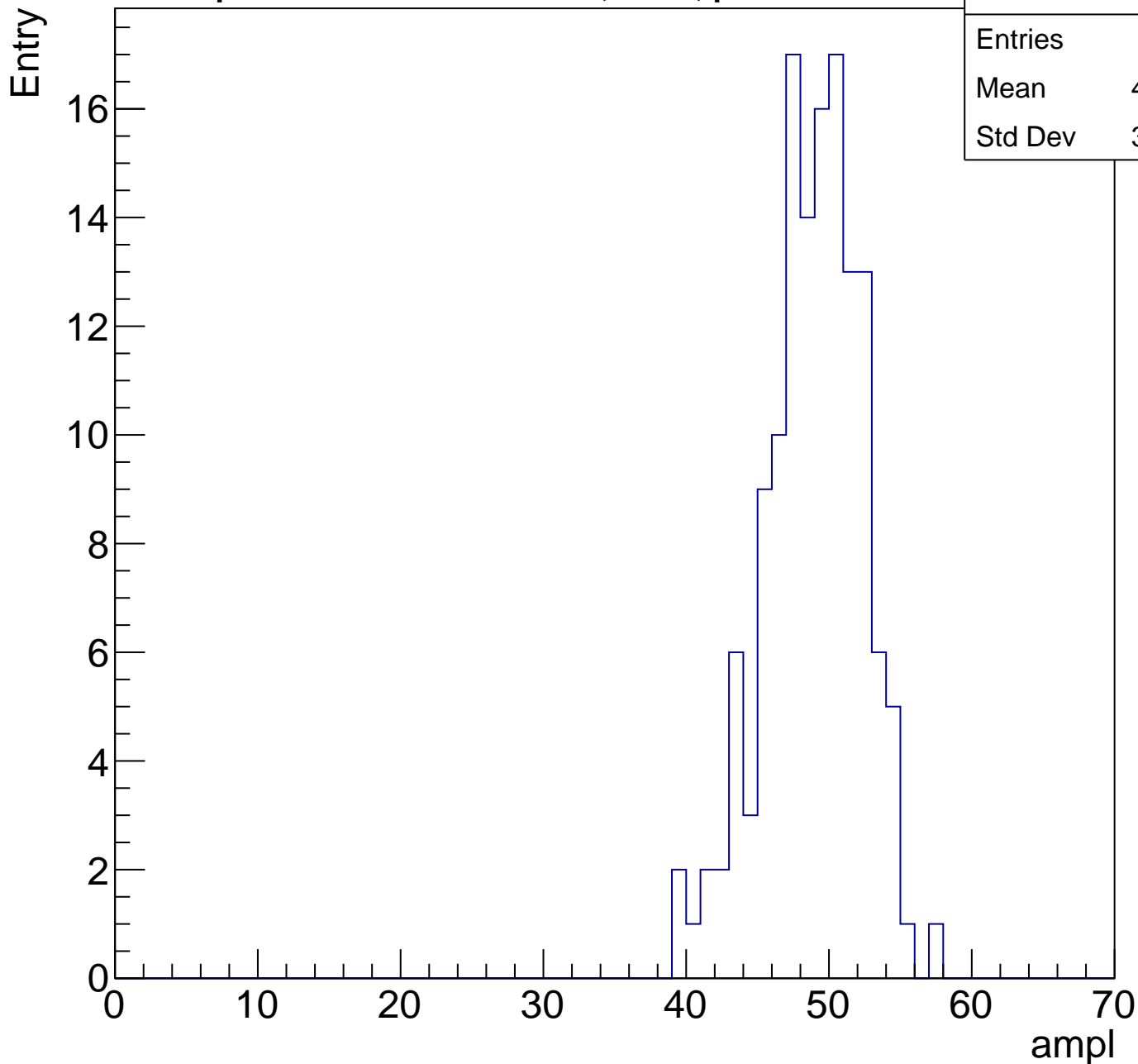
ampl



B1L001S, U25-ch40, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	48.43
Std Dev	3.379



B1L001S, U25-ch40, adc4

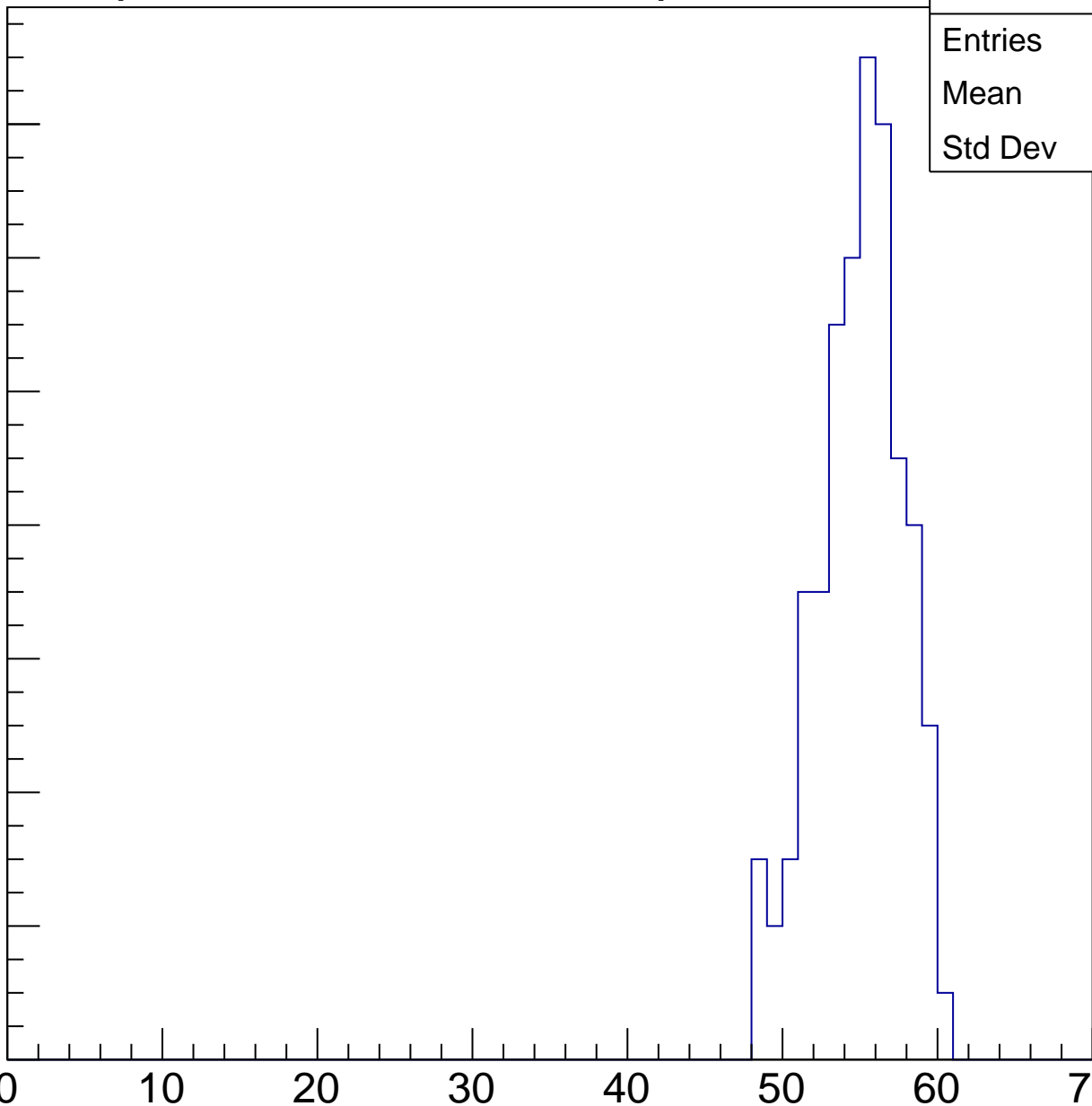
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	54.48
Std Dev	2.736

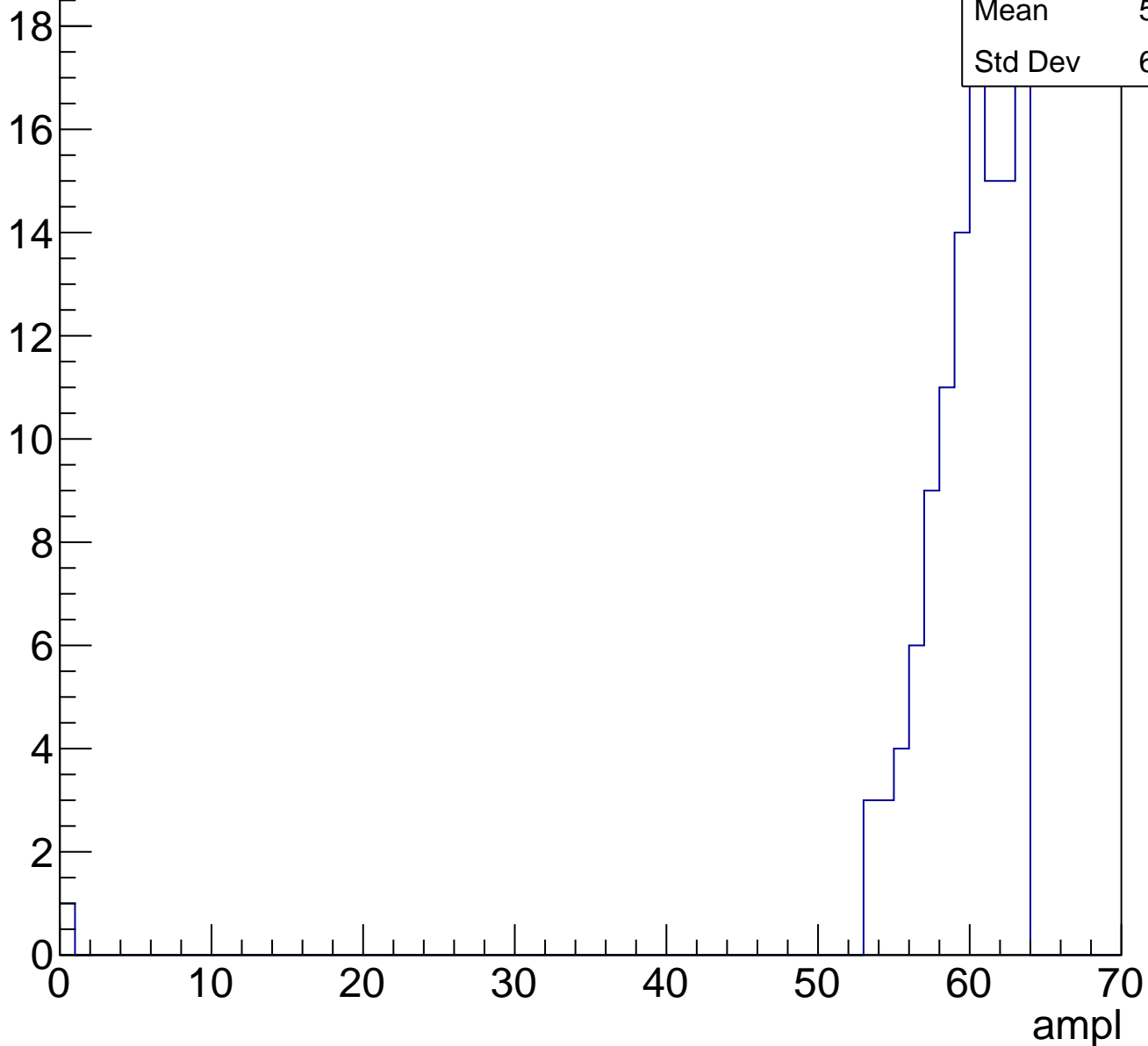
ampl



B1L001S, U25-ch40, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

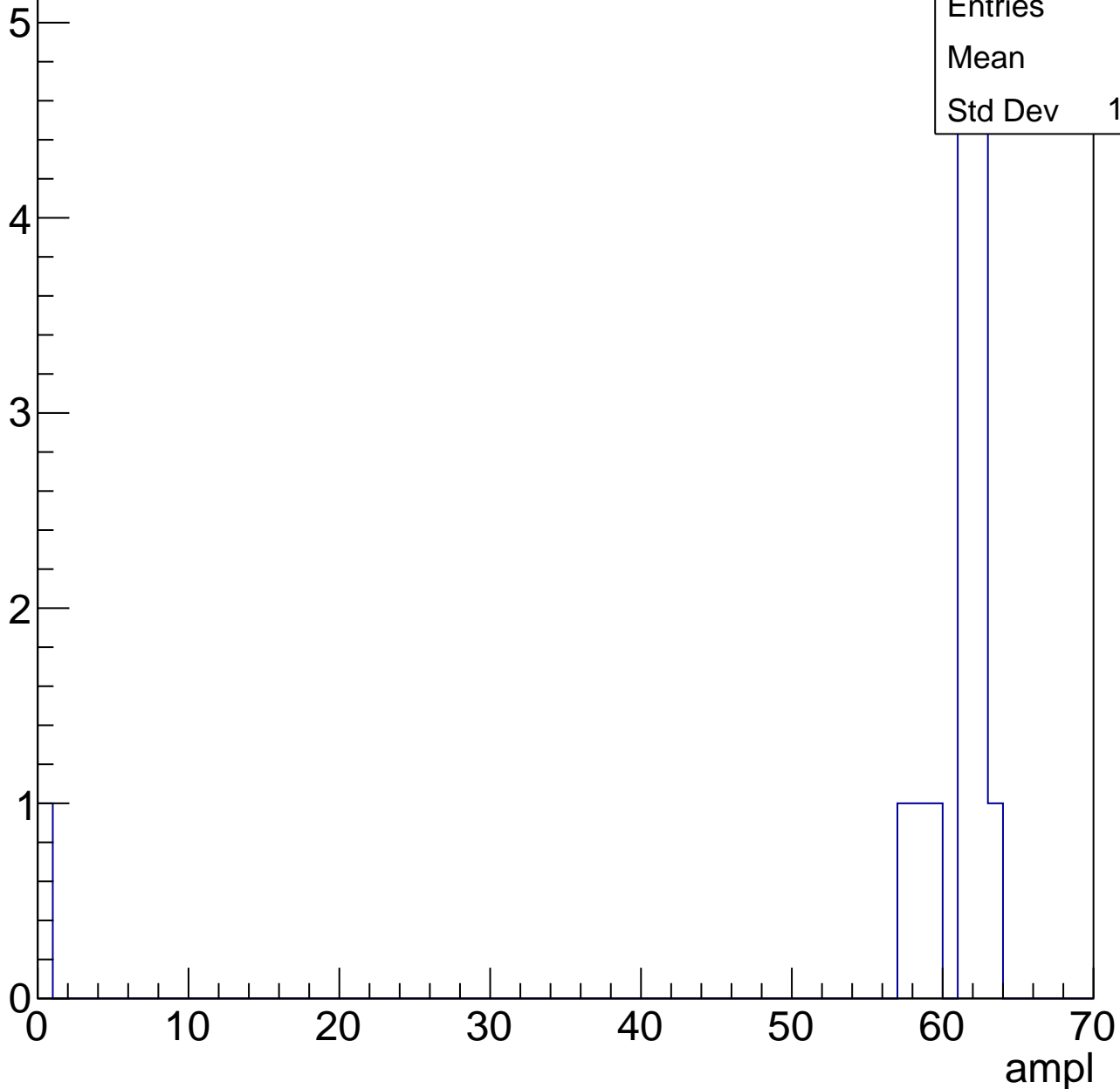


B1L001S, U25-ch40, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

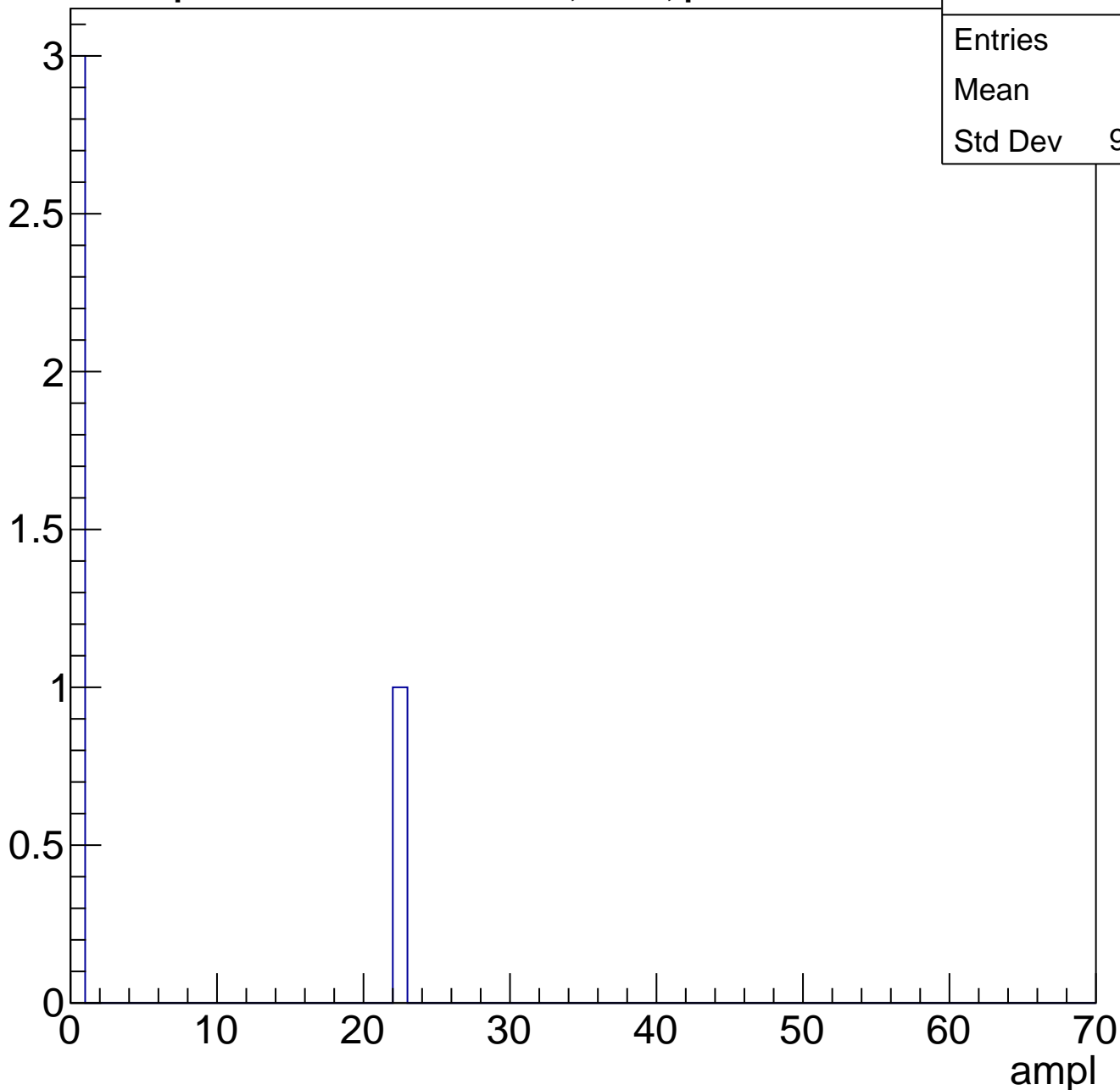
Entries	15
Mean	56.8
Std Dev	15.26



B1L001S, U25-ch40, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	5.5
Std Dev	9.526

B1L001S, U25-ch41, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	30.98
Std Dev	3.273

Gaus mean : 31.5288

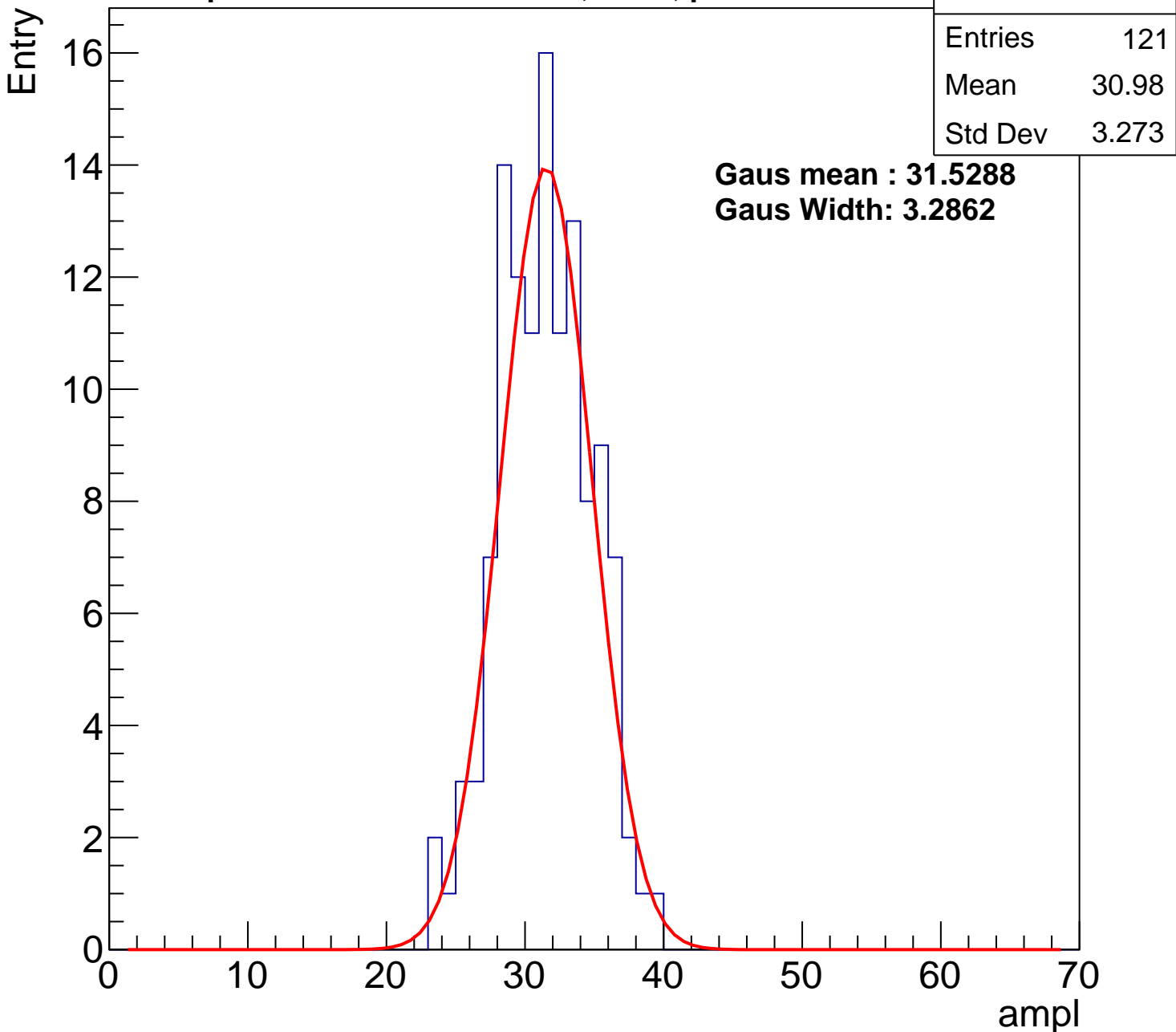
Gaus Width: 3.2862

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch41, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	37.52
Std Dev	3.517

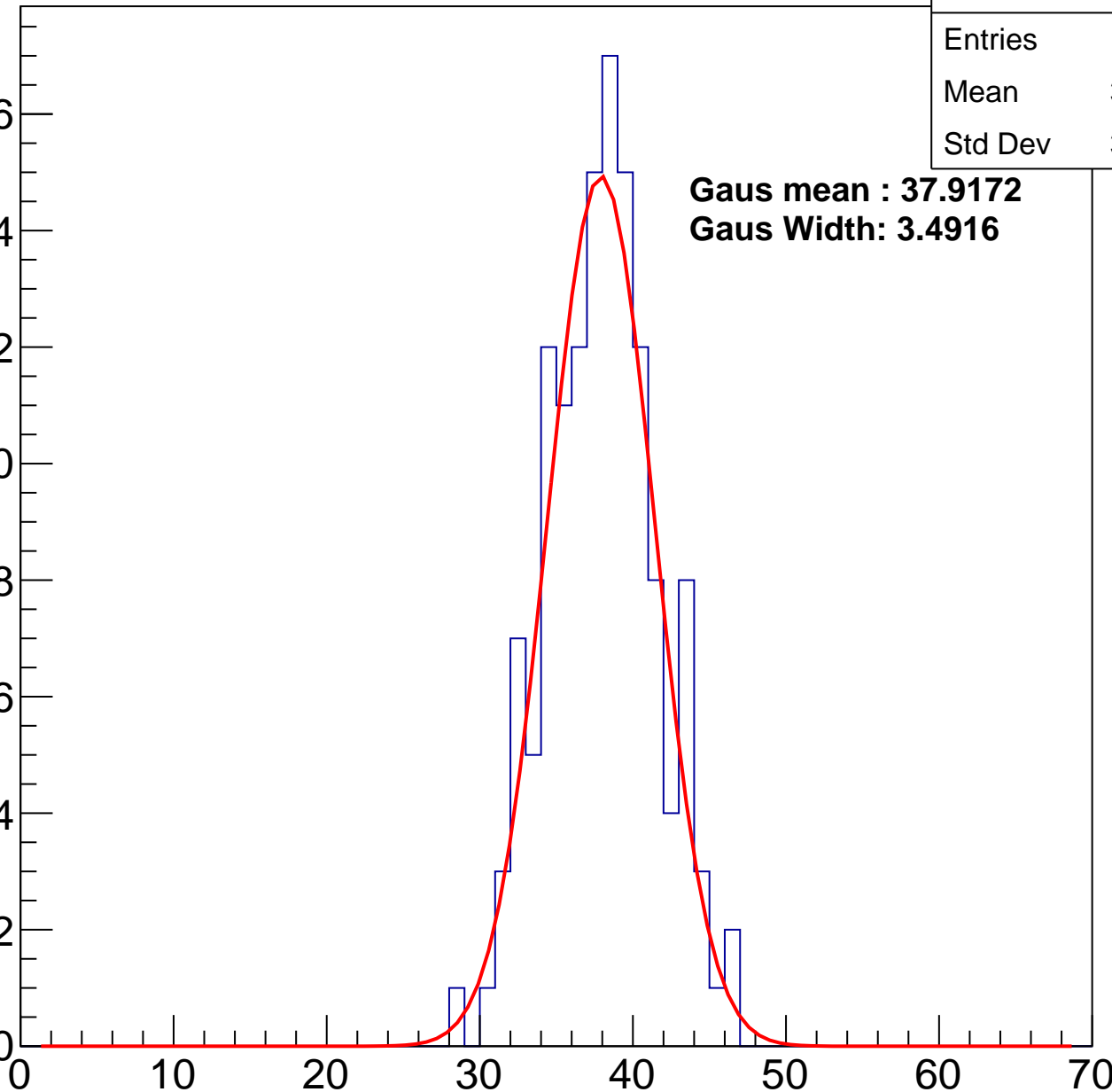
Gaus mean : 37.9172

Gaus Width: 3.4916

Entry

16
14
12
10
8
6
4
2
0

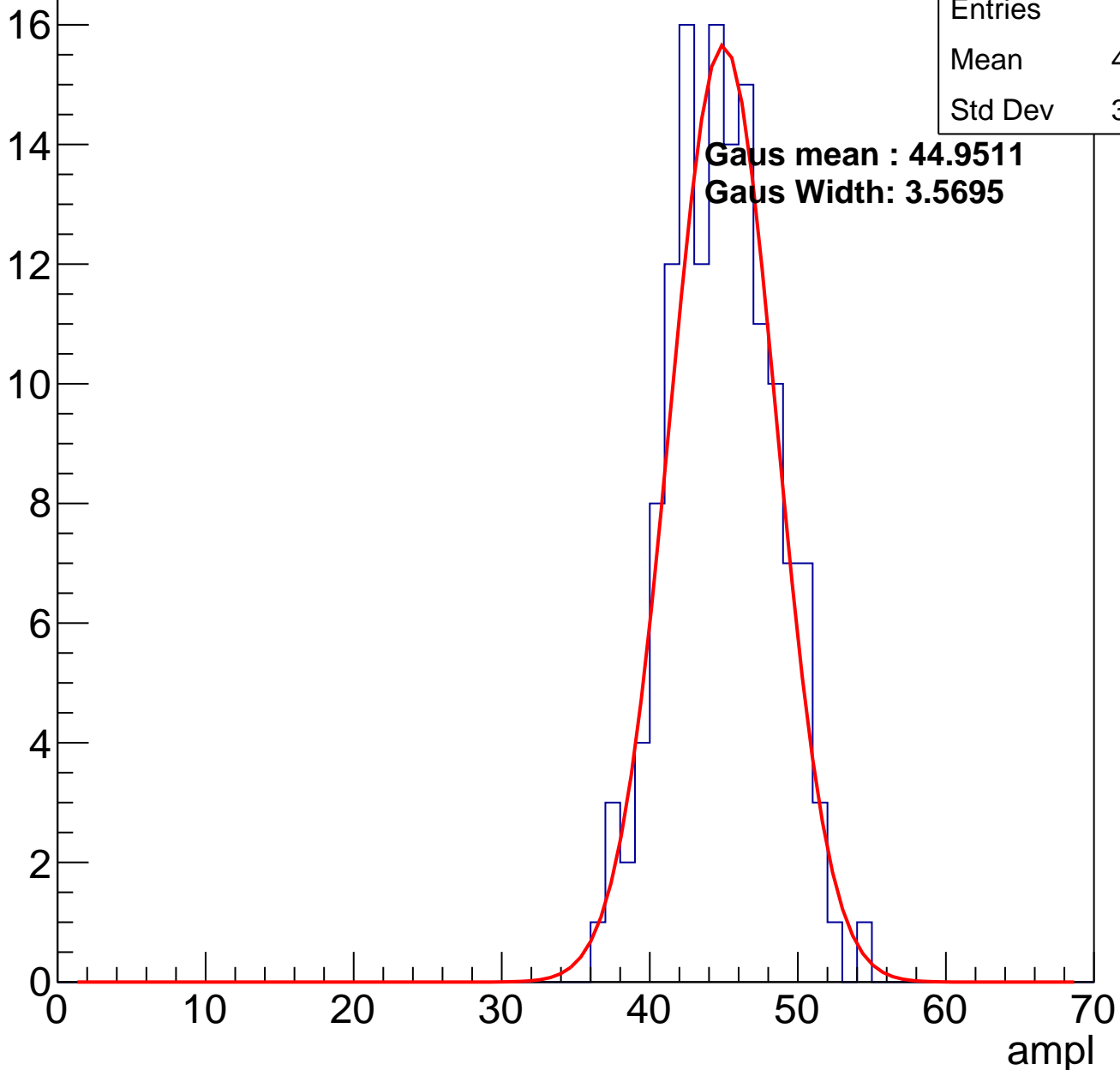
ampl



B1L001S, U25-ch41, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch41, adc3

calib_packv5_042523_0143.root, FC#2, port C2

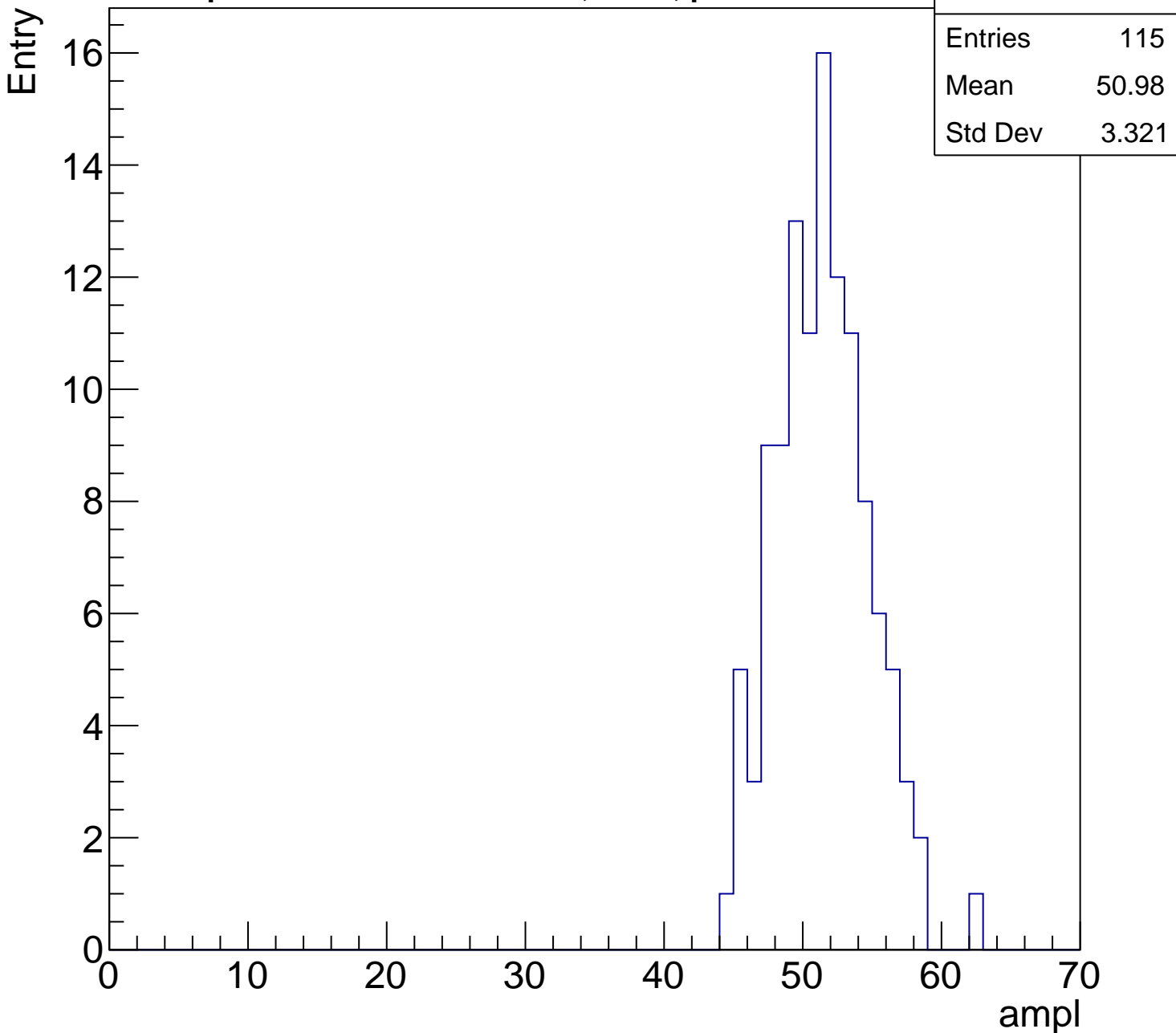
Entries	115
Mean	50.98
Std Dev	3.321

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch41, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

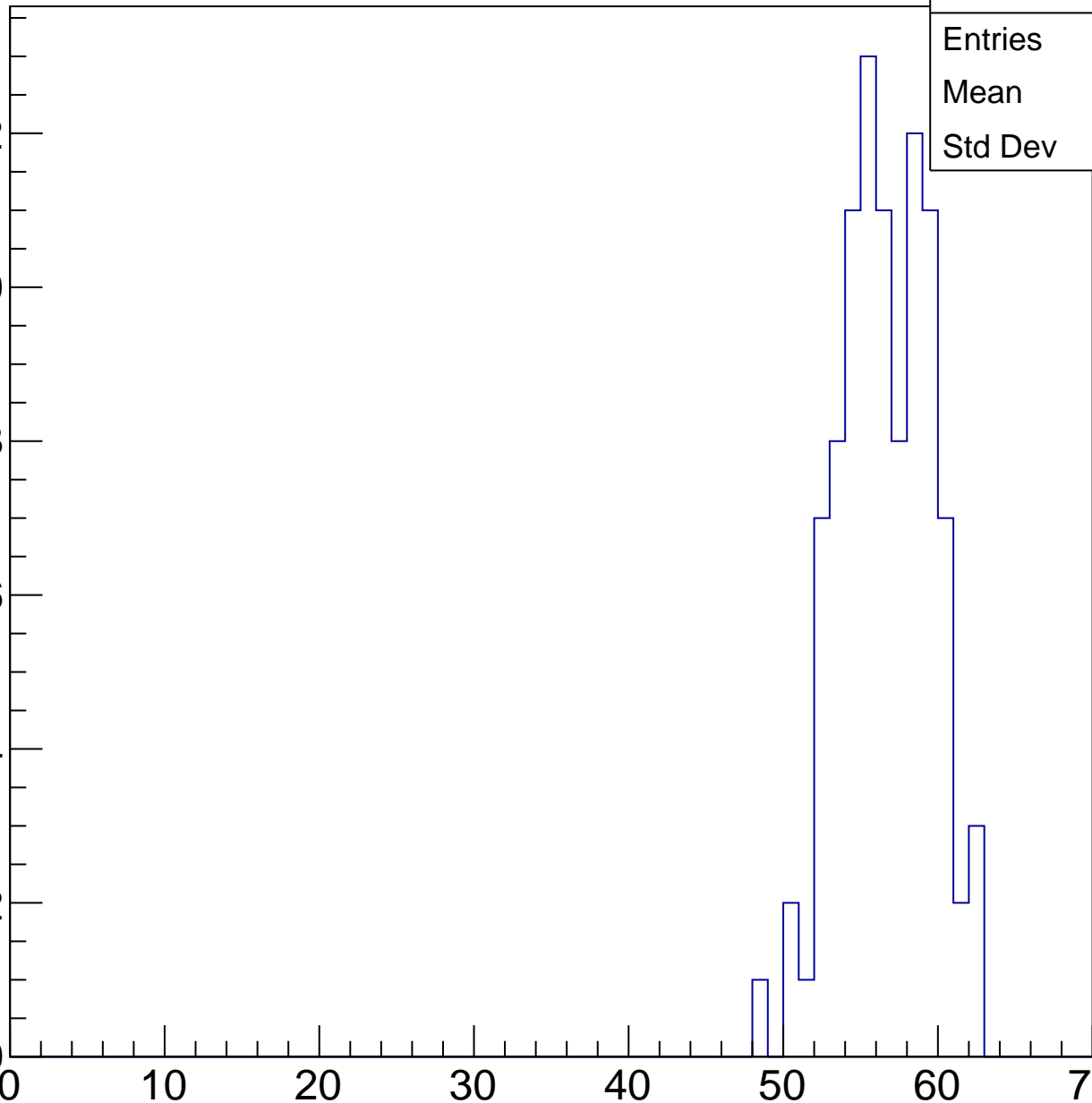
40

50

60

ampl

Entries	97
Mean	56.09
Std Dev	2.918



B1L001S, U25-ch41, adc5

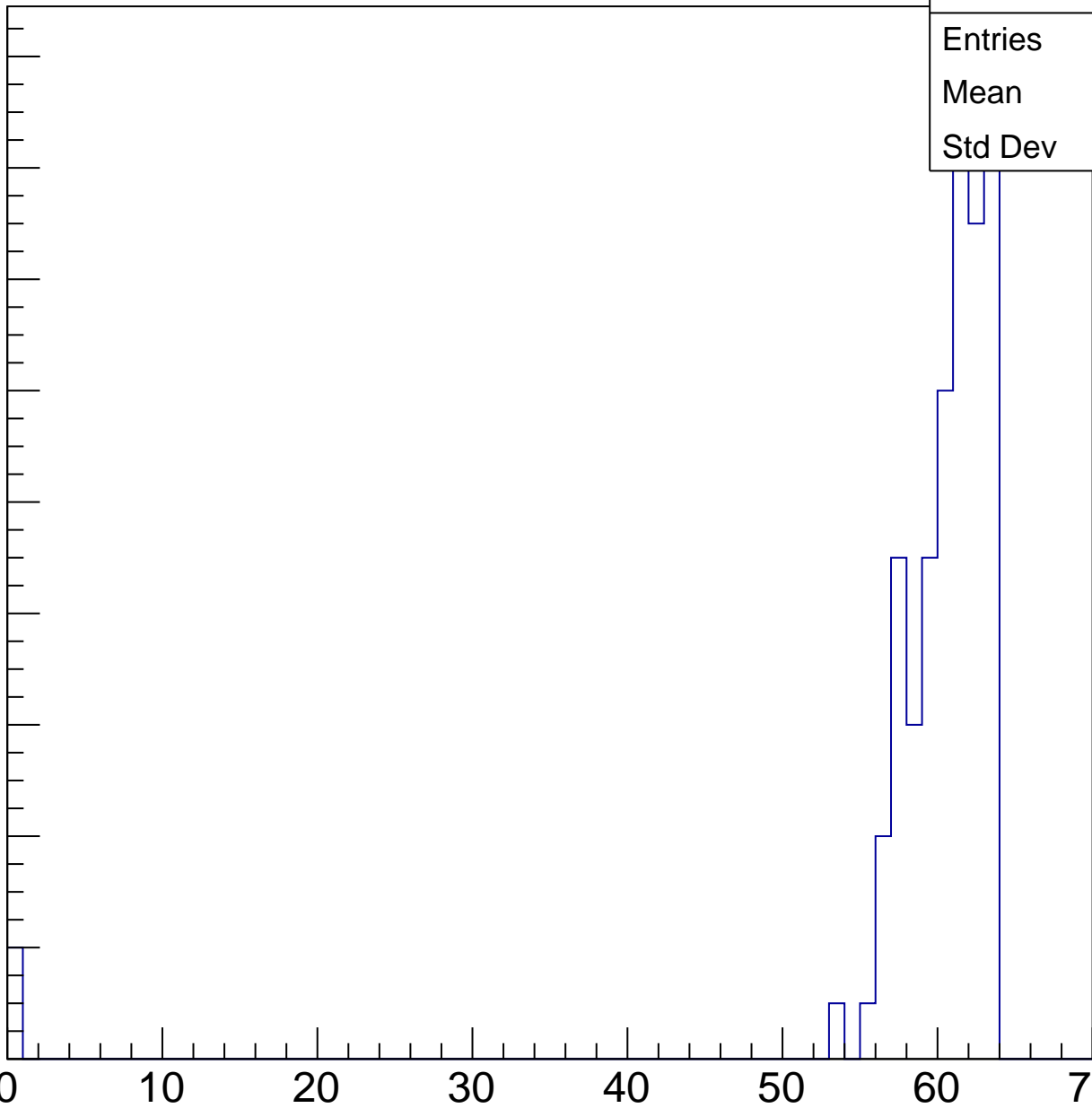
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	94
Mean	58.97
Std Dev	8.982

ampl



B1L001S, U25-ch41, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch41, adc7

calib_packv5_042523_0143.root, FC#2, port C2

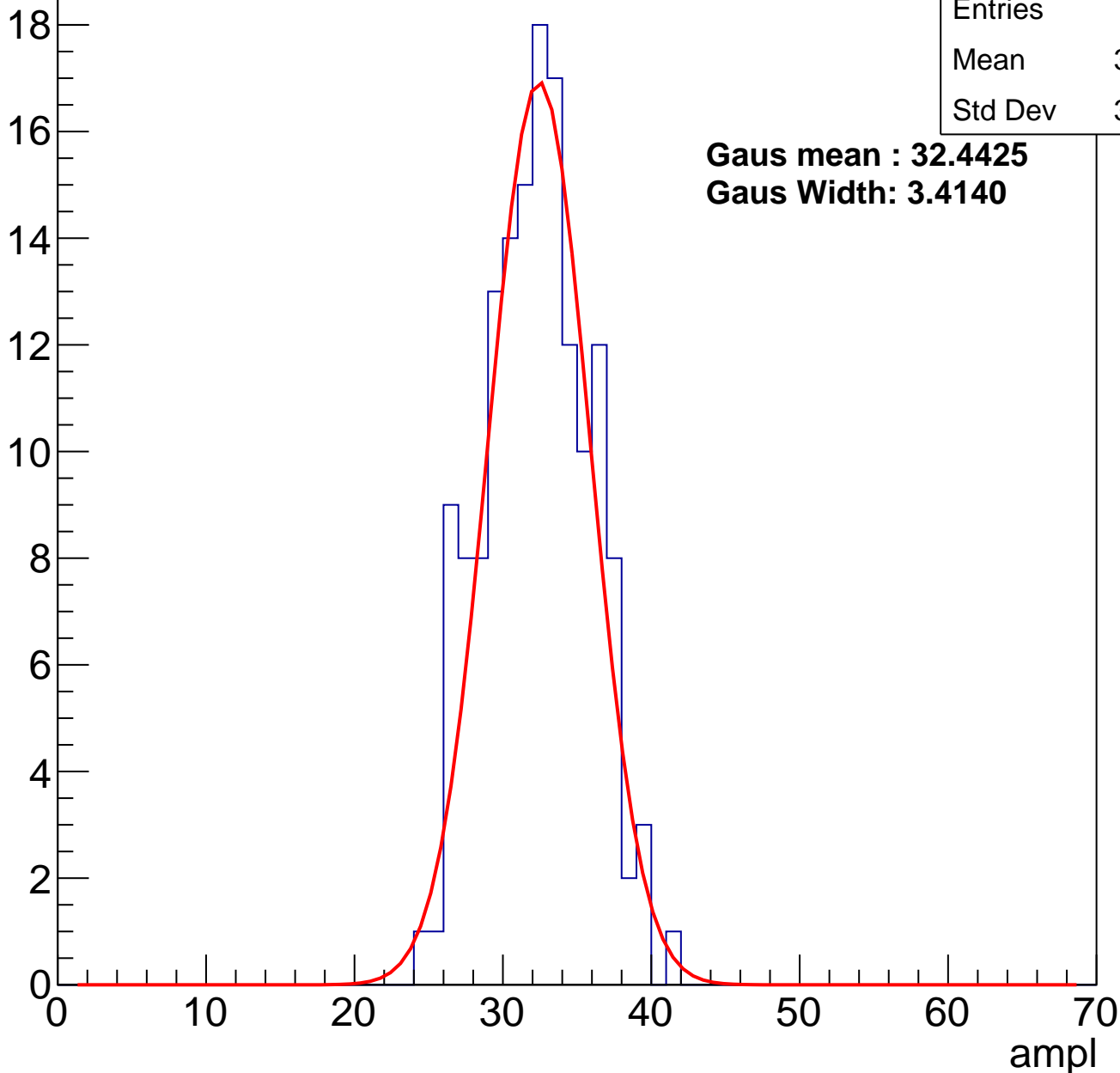
Entry



B1L001S, U25-ch42, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch42, adc1

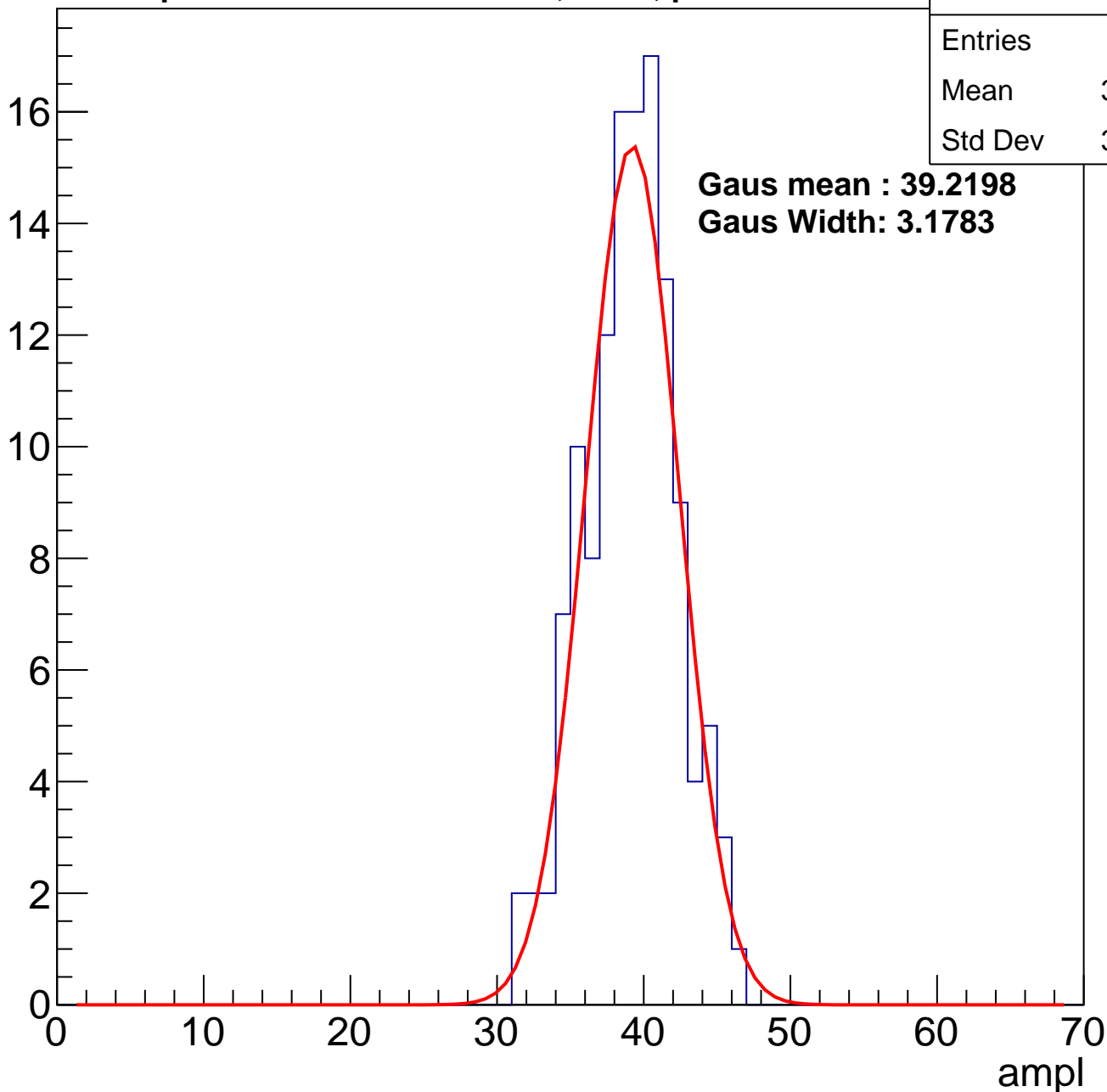
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	38.65
Std Dev	3.136

Gaus mean : 39.2198

Gaus Width: 3.1783

Entry



B1L001S, U25-ch42, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

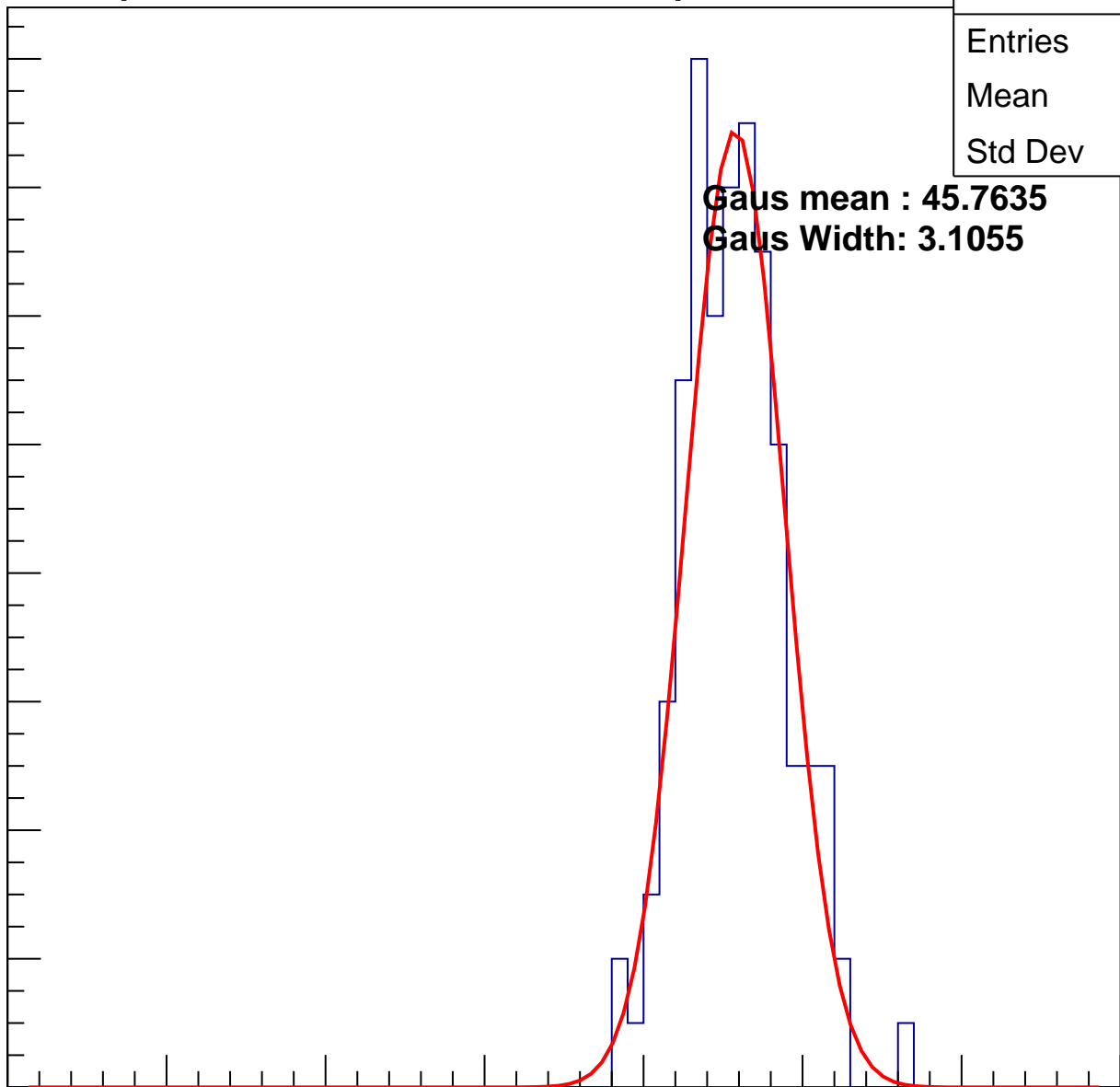
Entries	121
Mean	45.29
Std Dev	3.189

Gaus mean : 45.7635

Gaus Width: 3.1055

ampl

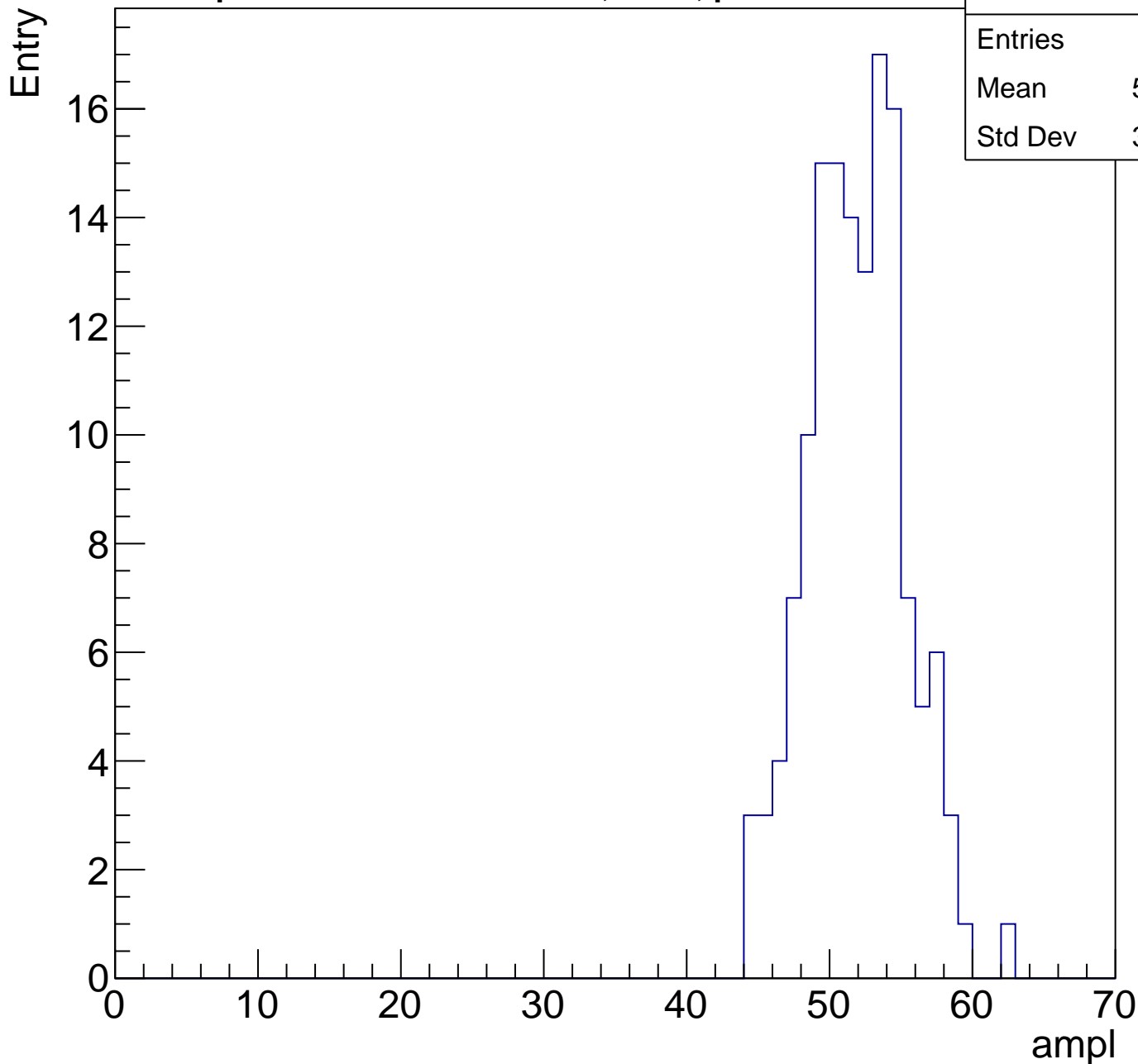
0 10 20 30 40 50 60 70



B1L001S, U25-ch42, adc3

calib_packv5_042523_0143.root, FC#2, port C2

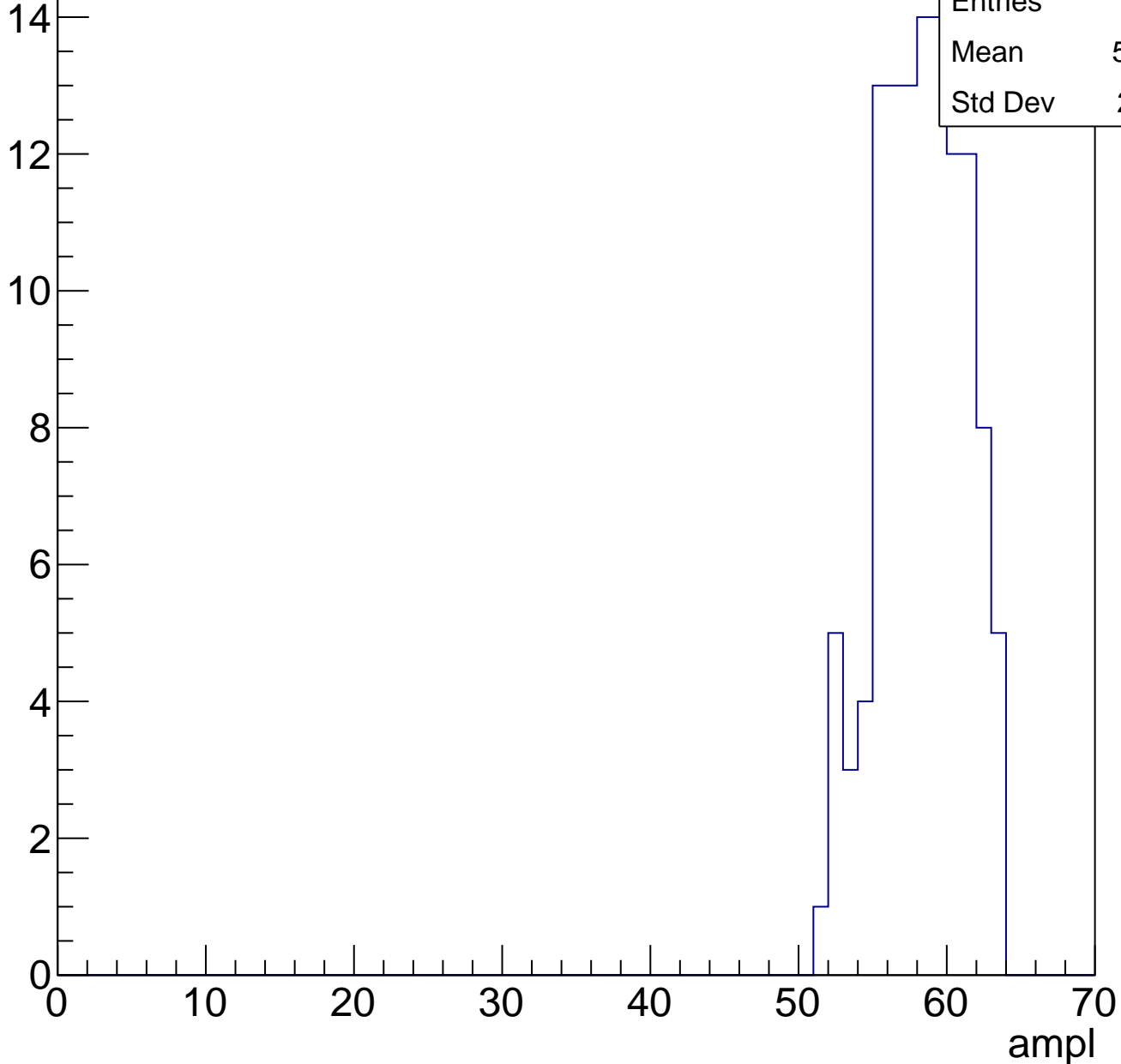
Entries	140
Mean	51.44
Std Dev	3.396



B1L001S, U25-ch42, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch42, adc5

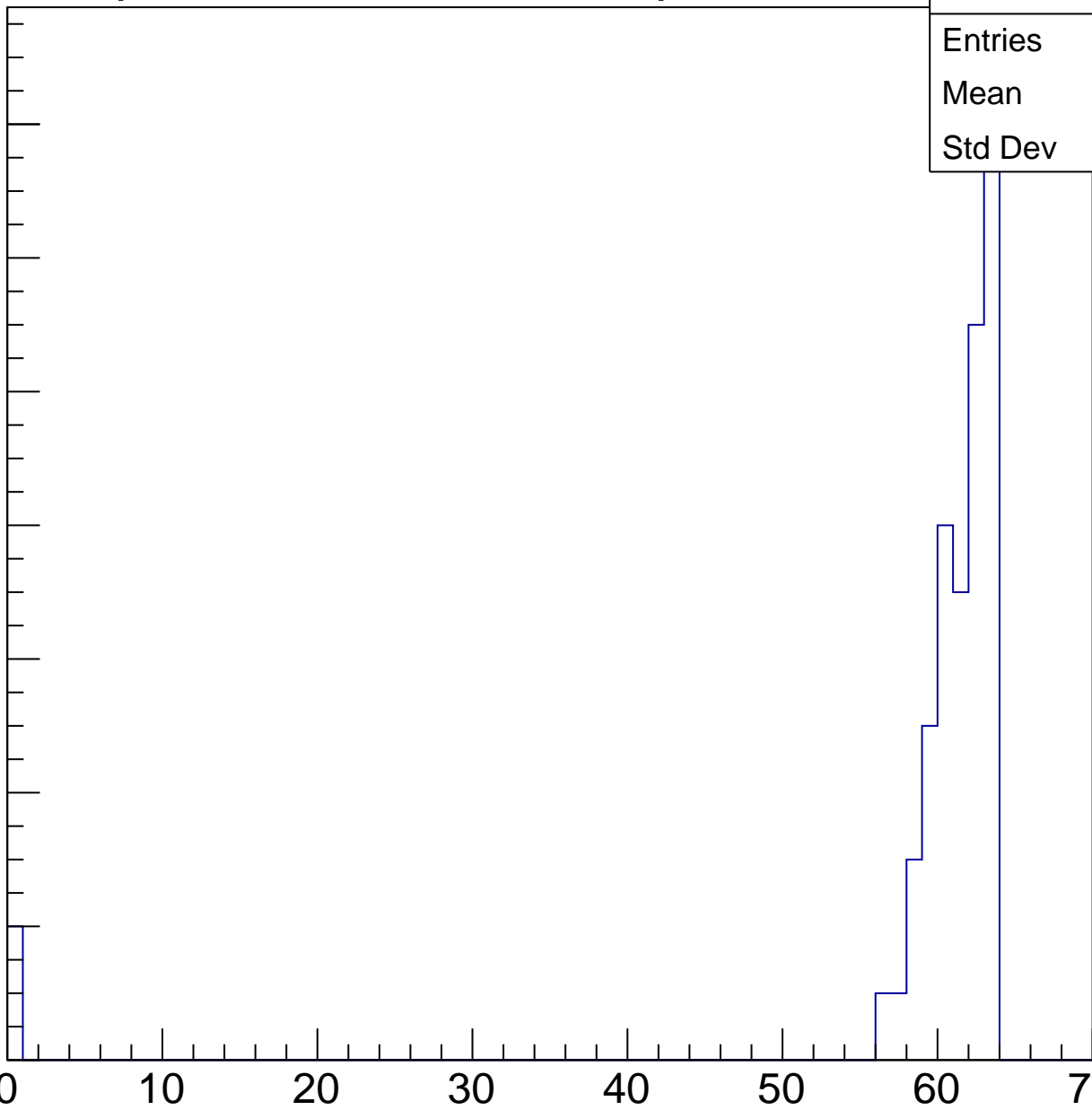
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	53
Mean	58.79
Std Dev	11.78

ampl



B1L001S, U25-ch42, adc6

calib_packv5_042523_0143.root, FC#2, port C2

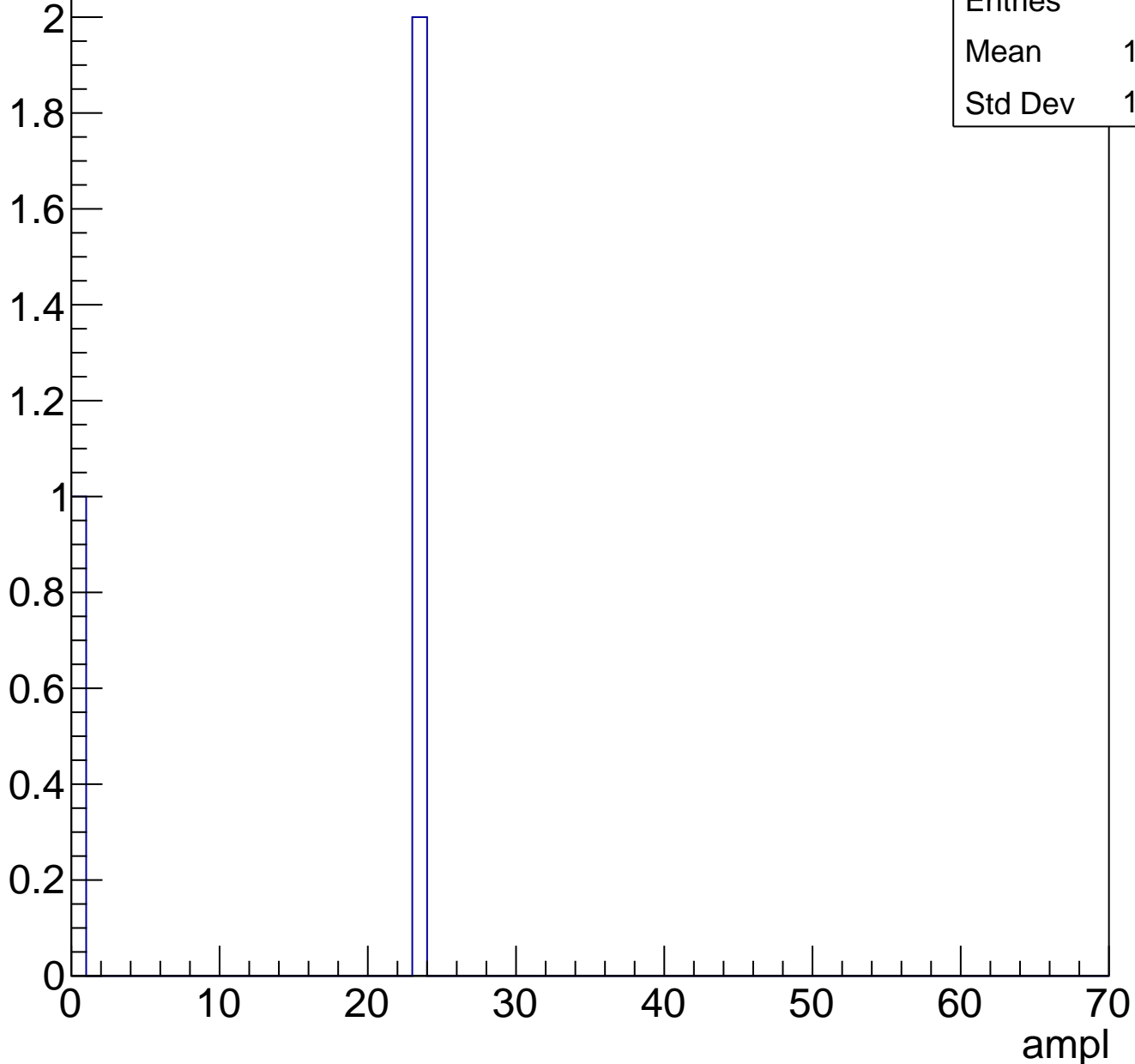
Entry



B1L001S, U25-ch42, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	15.33
Std Dev	10.84

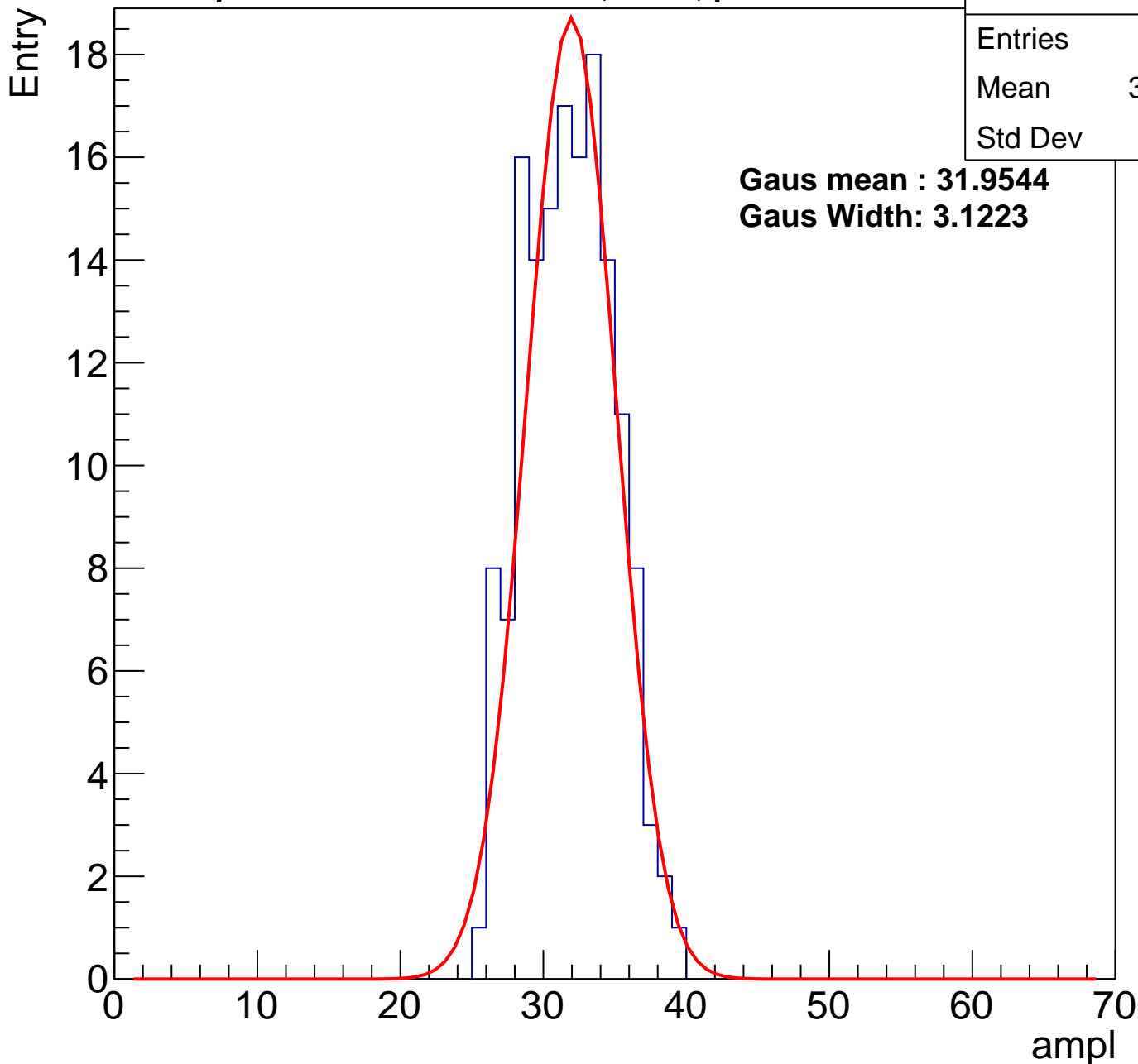
B1L001S, U25-ch43, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	31.35
Std Dev	3.05

Gaus mean : 31.9544

Gaus Width: 3.1223

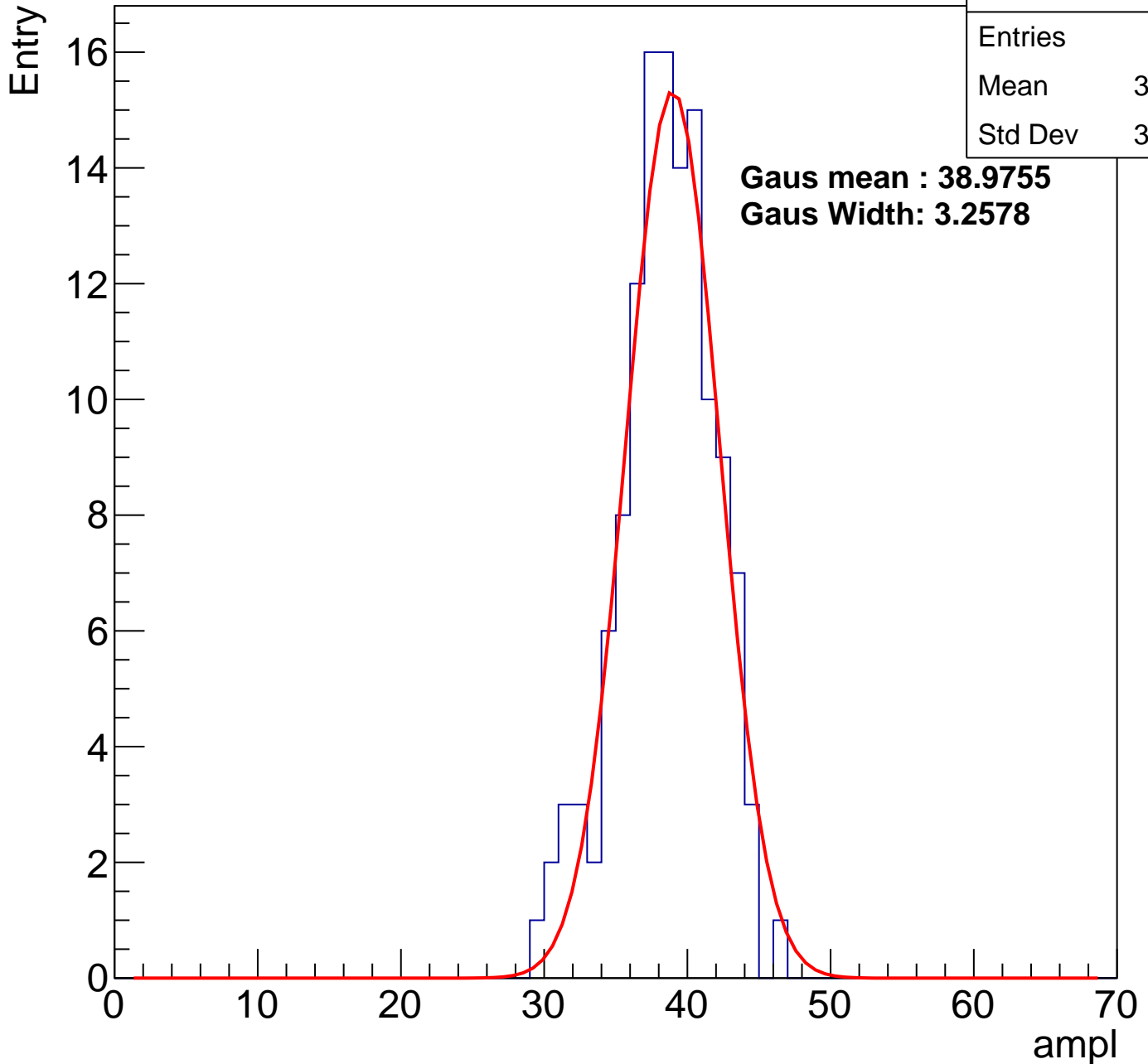


B1L001S, U25-ch43, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	38.07
Std Dev	3.308

Gaus mean : 38.9755
Gaus Width: 3.2578



B1L001S, U25-ch43, adc2

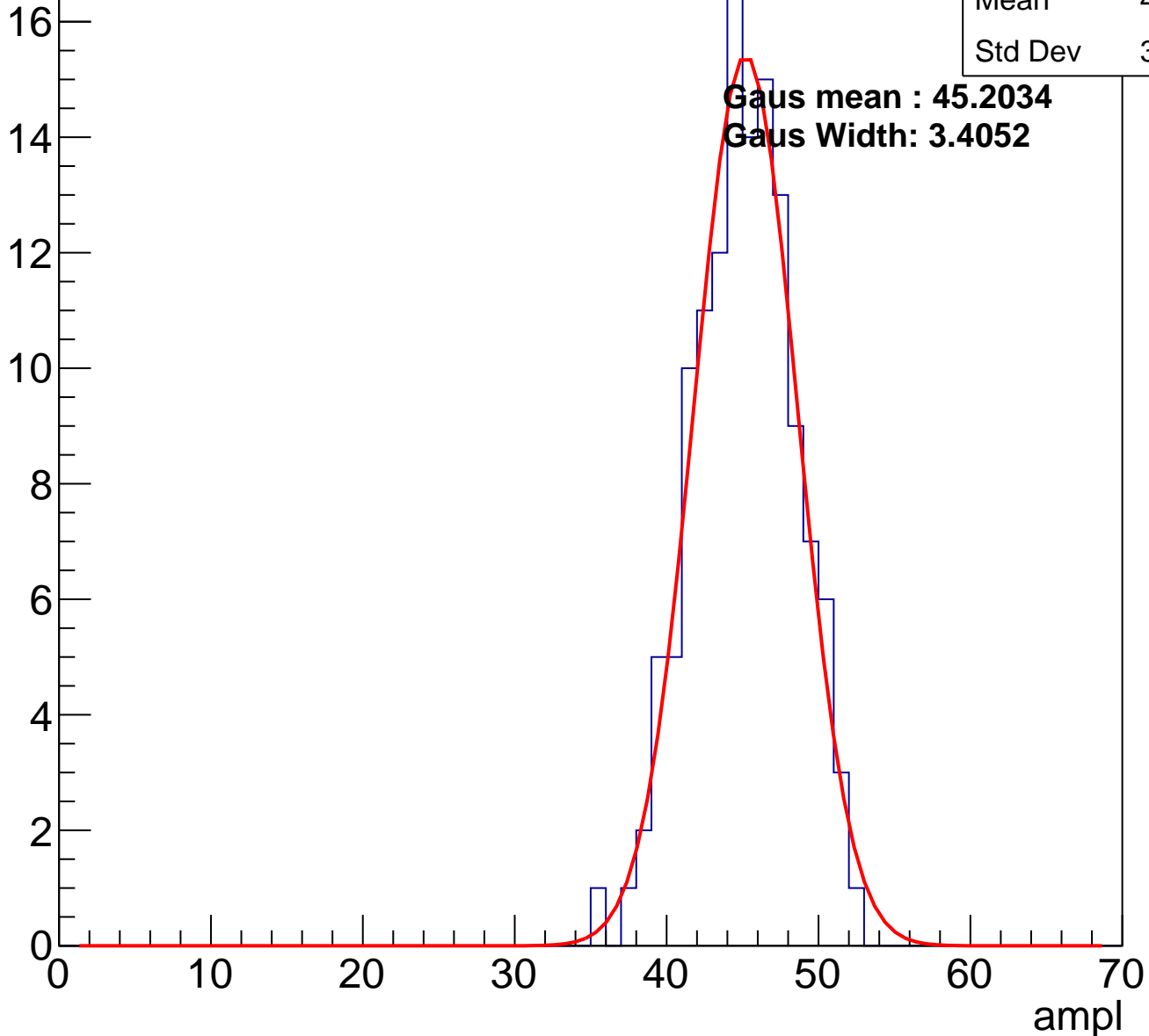
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	132
Mean	44.62
Std Dev	3.302

Gaus mean : 45.2034

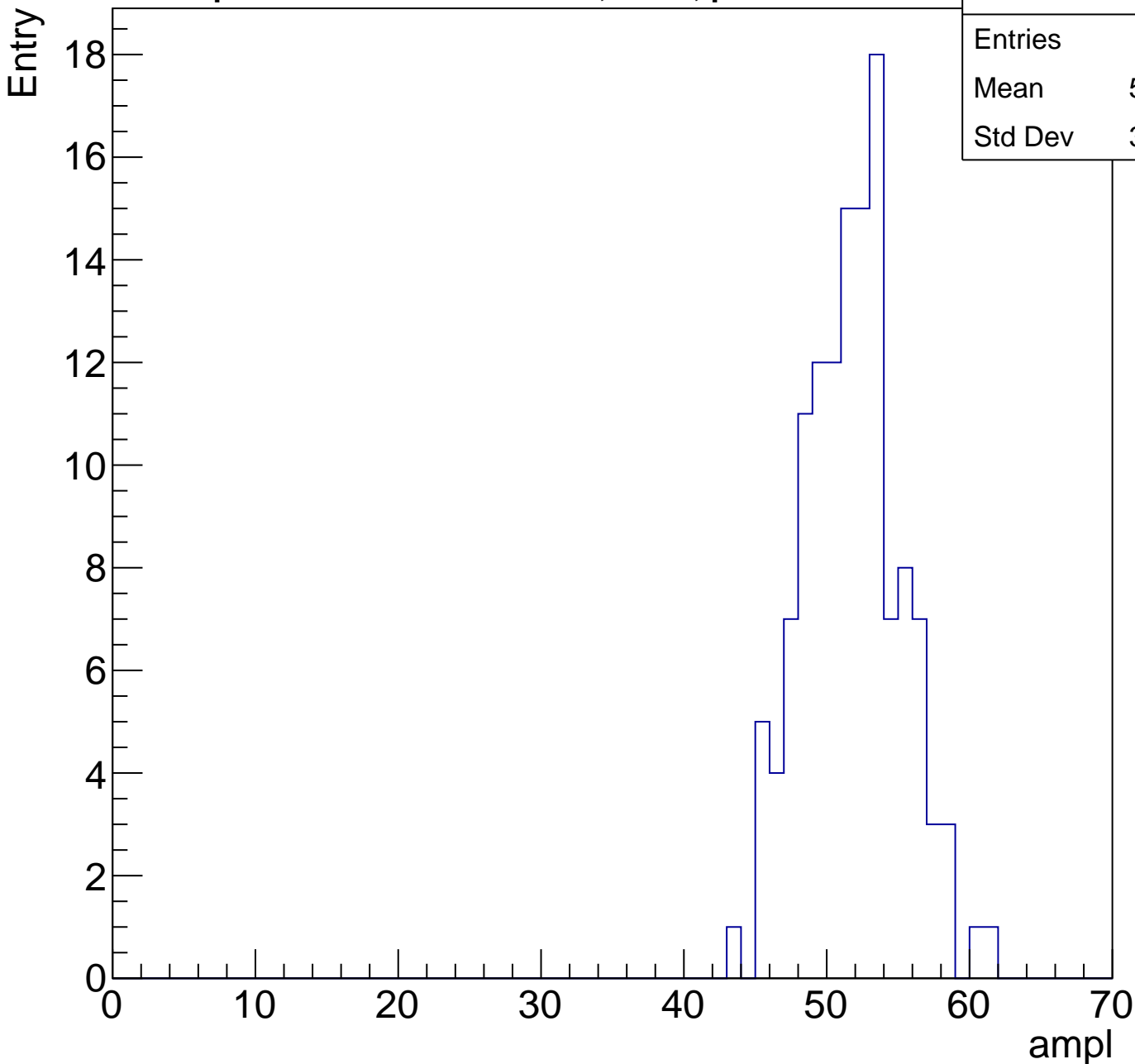
Gaus Width: 3.4052



B1L001S, U25-ch43, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	51.32
Std Dev	3.386



B1L001S, U25-ch43, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	56.8
Std Dev	3.16

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch43, adc5

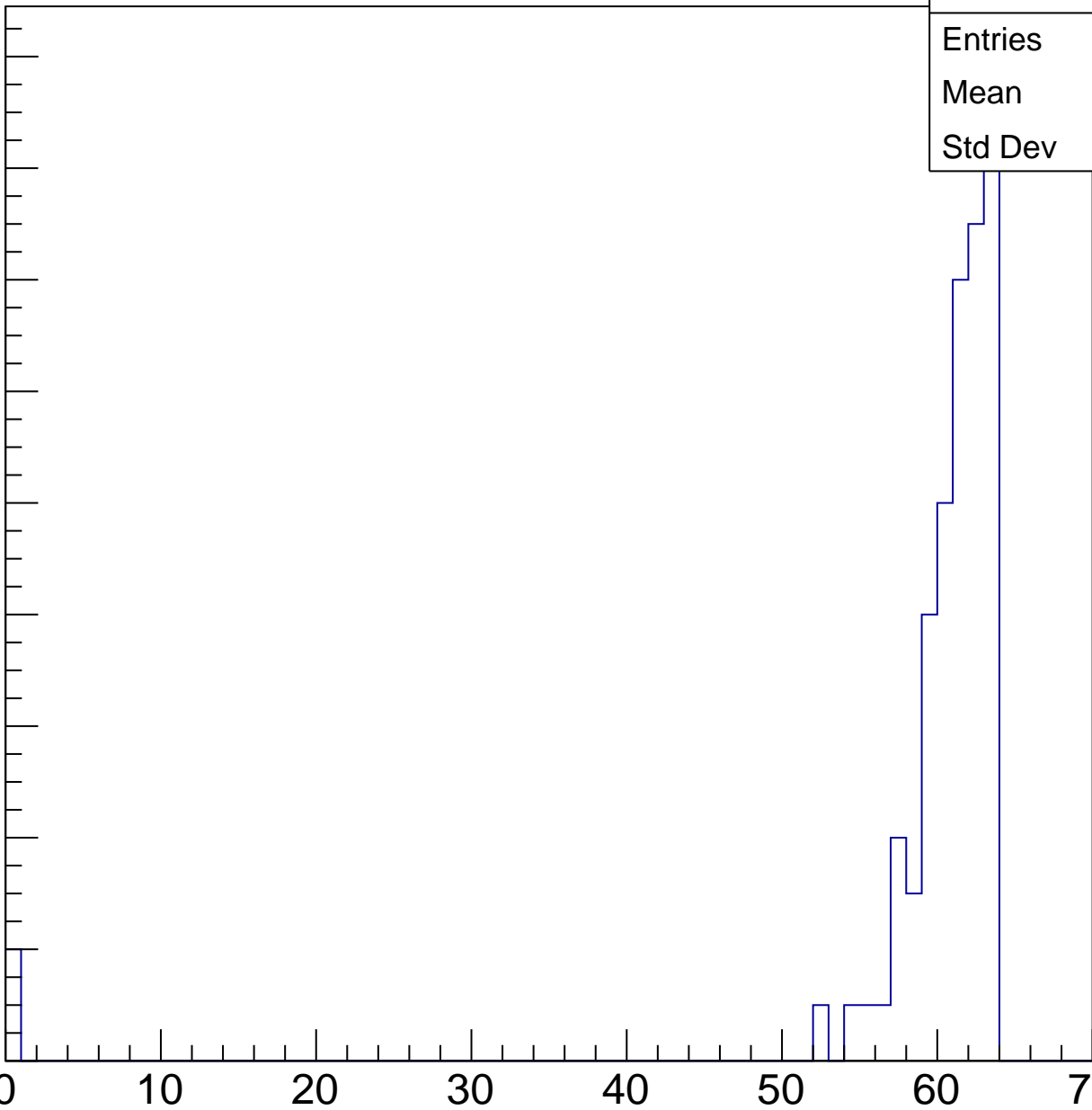
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.09
Std Dev	9.85

ampl



B1L001S, U25-ch43, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch43, adc7

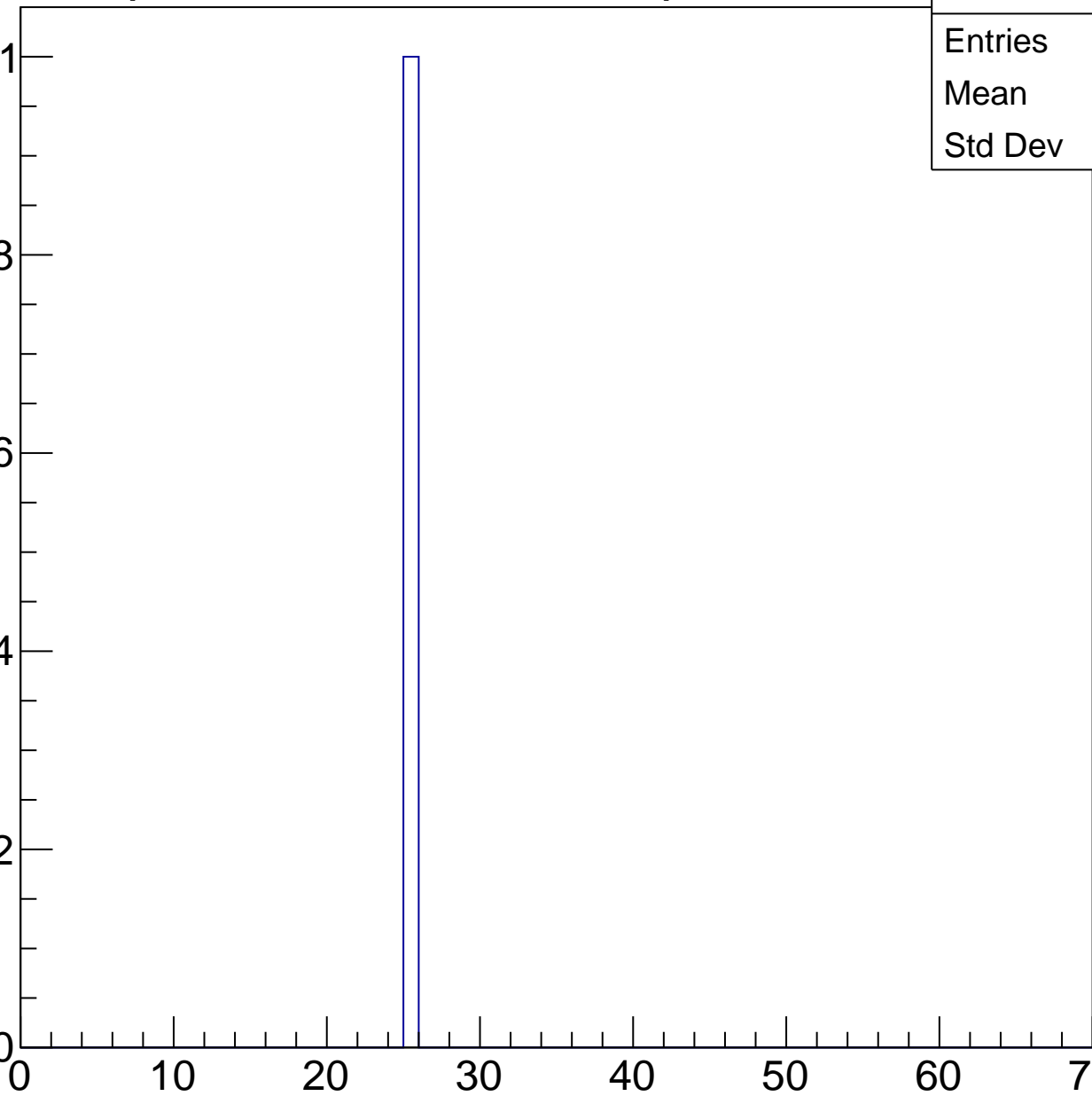
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

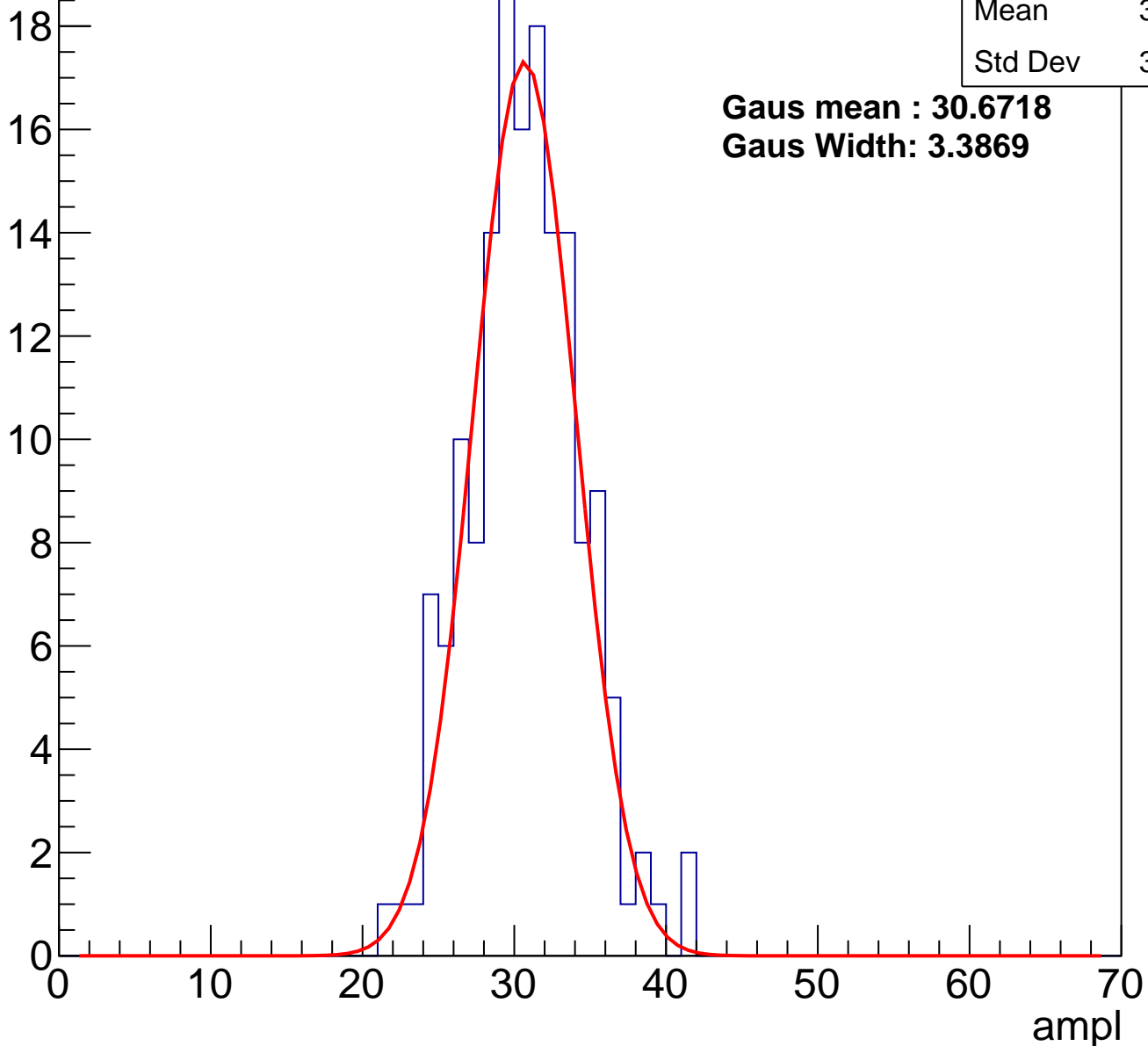
ampl



B1L001S, U25-ch44, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch44, adc1

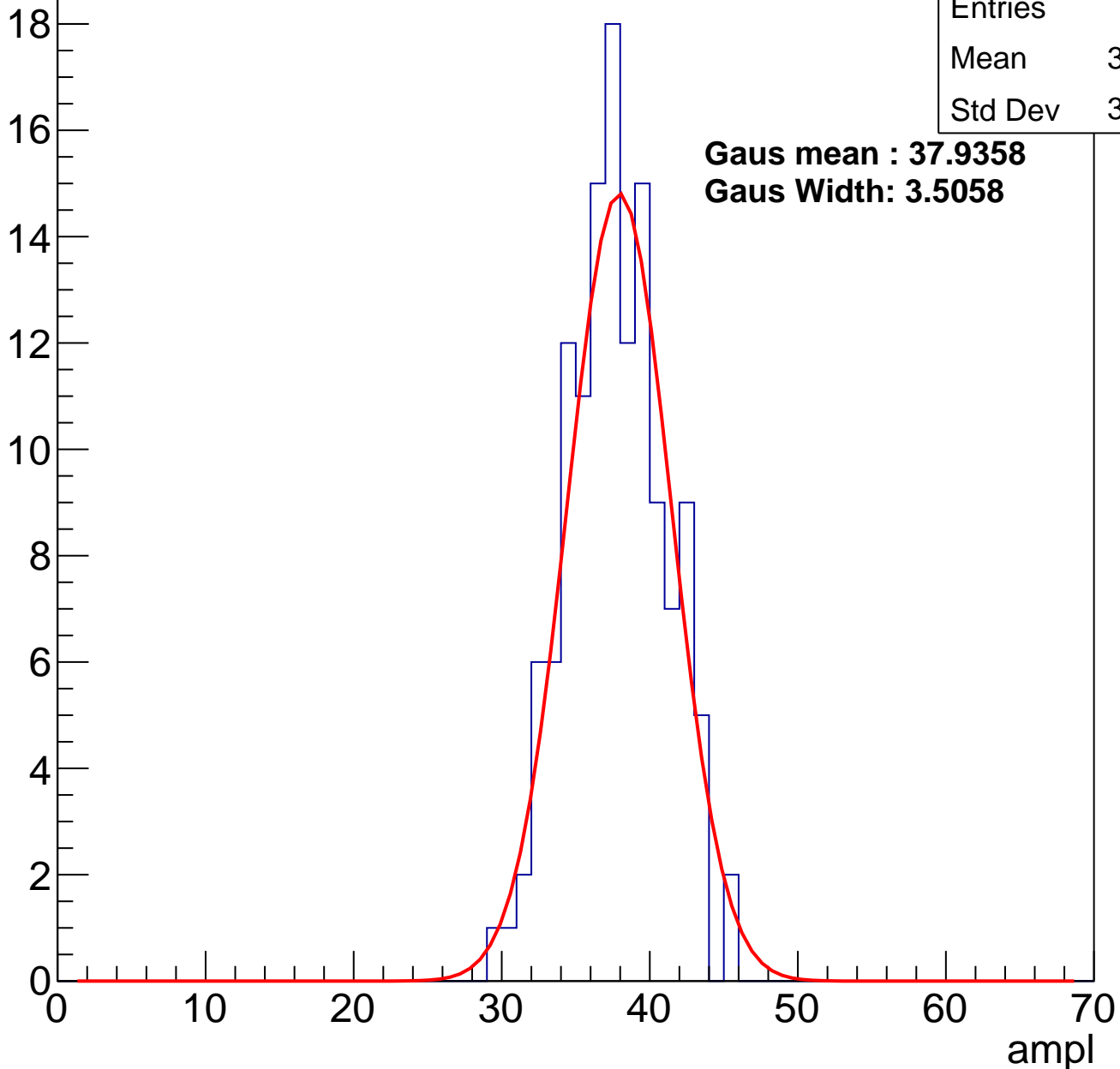
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	37.26
Std Dev	3.254

Gaus mean : 37.9358

Gaus Width: 3.5058

Entry



B1L001S, U25-ch44, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

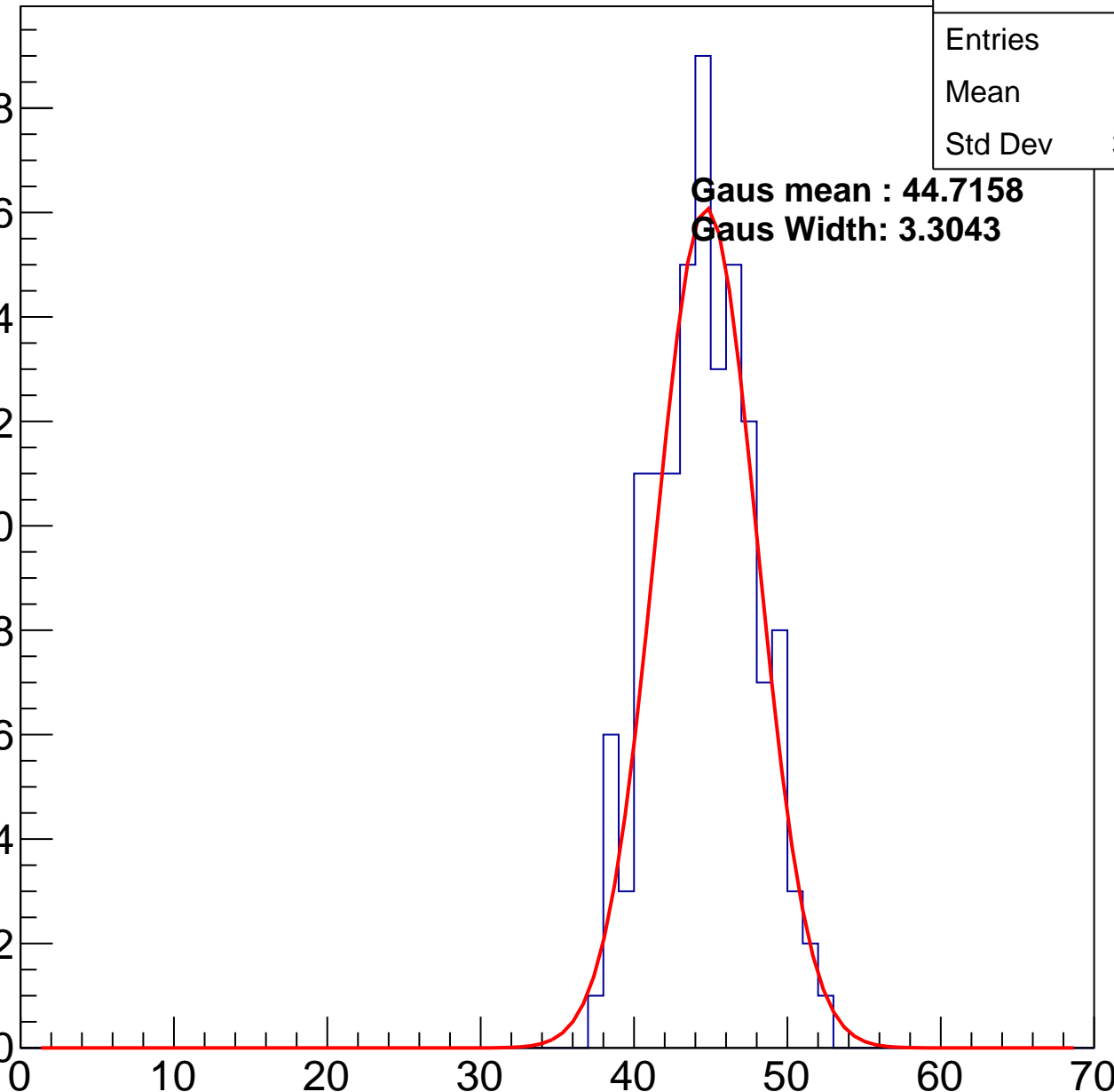
18
16
14
12
10
8
6
4
2
0

Entries	138
Mean	44.11
Std Dev	3.225

Gaus mean : 44.7158

Gaus Width: 3.3043

ampl

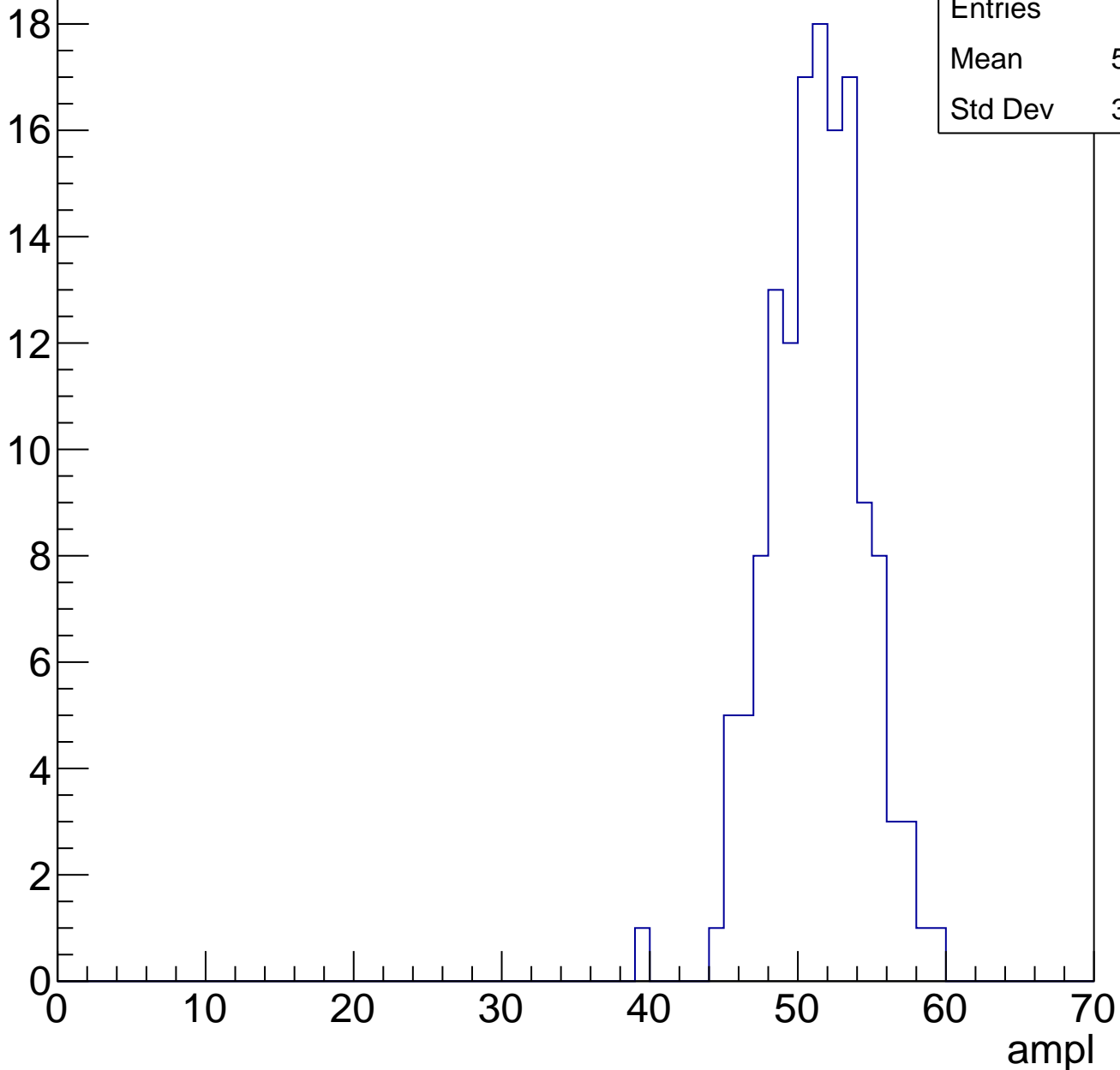


B1L001S, U25-ch44, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	50.79
Std Dev	3.175

Entry



B1L001S, U25-ch44, adc4

calib_packv5_042523_0143.root, FC#2, port C2

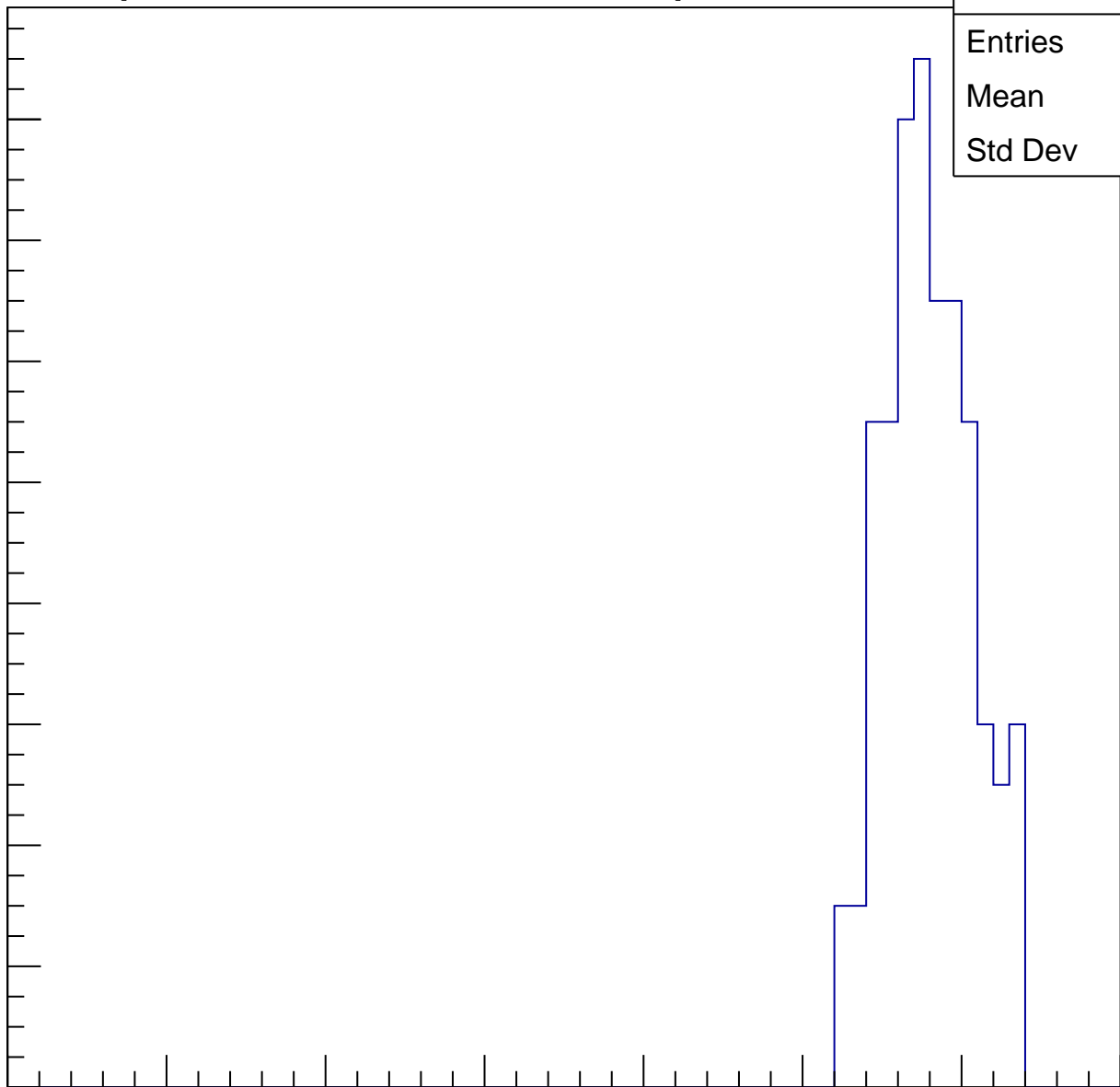
Entries	115
Mean	57.51
Std Dev	2.723

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch44, adc5

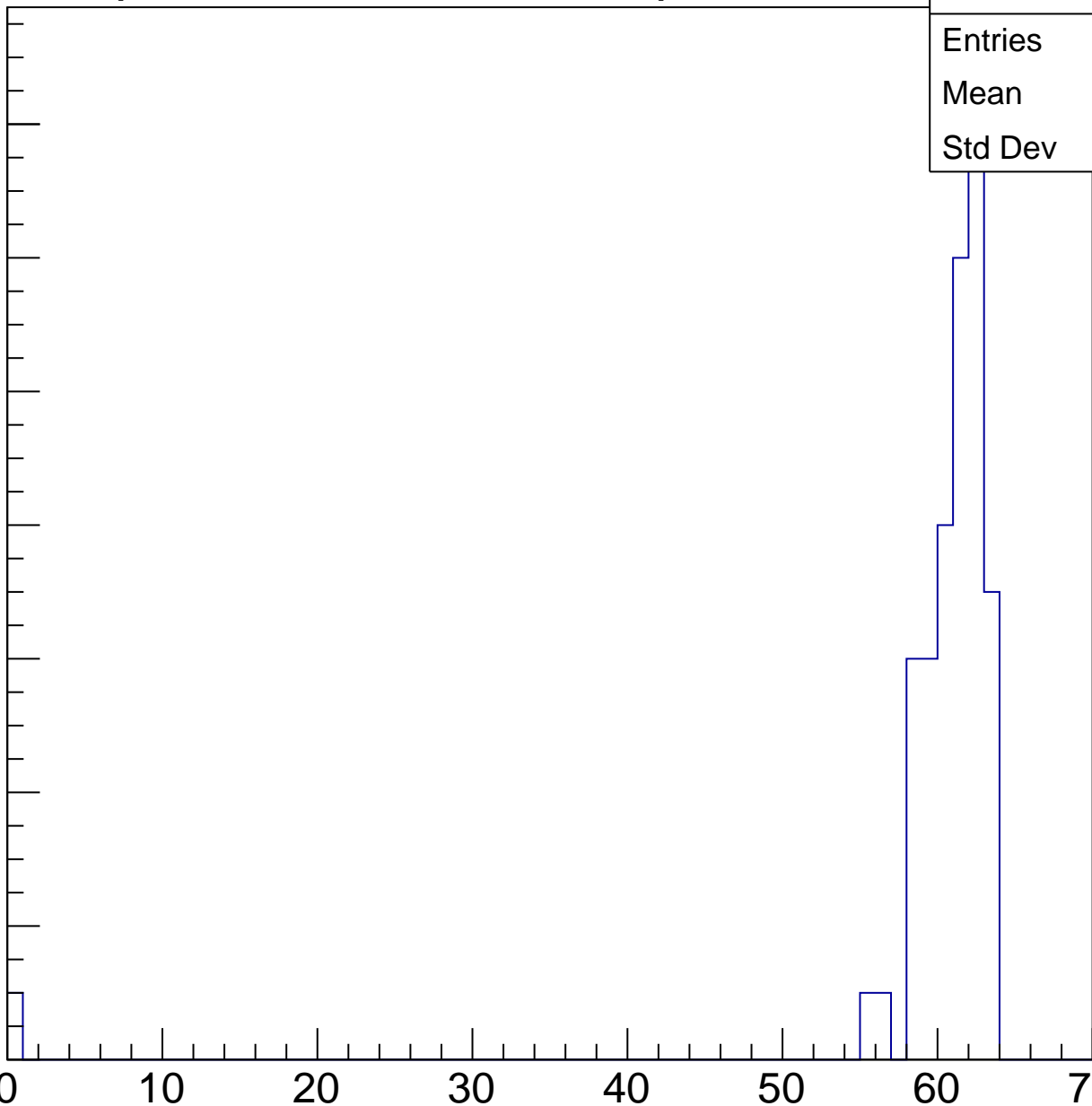
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	57
Mean	59.58
Std Dev	8.161

ampl

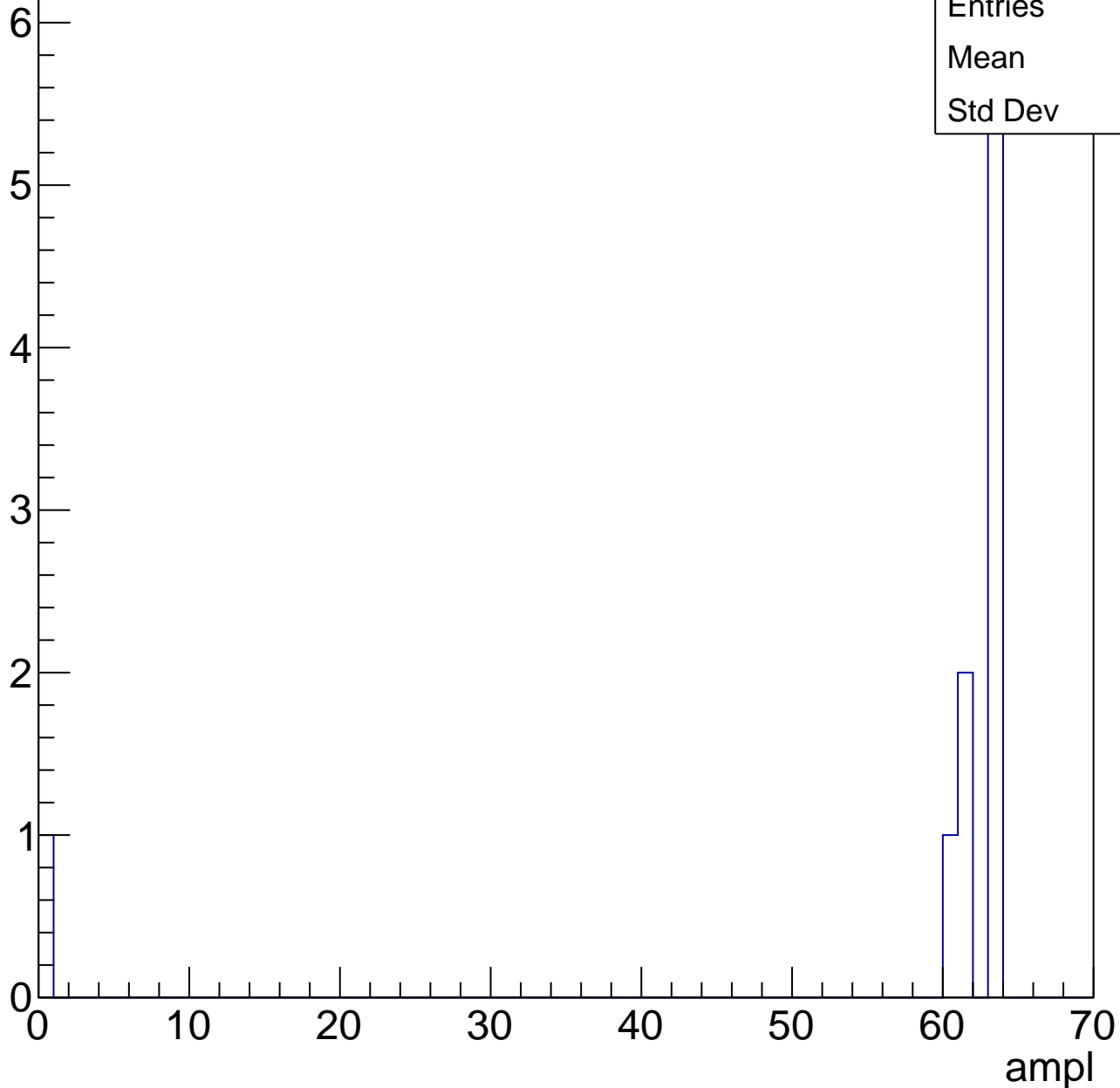


B1L001S, U25-ch44, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	56
Std Dev	18.7



B1L001S, U25-ch44, adc7

calib_packv5_042523_0143.root, FC#2, port C2

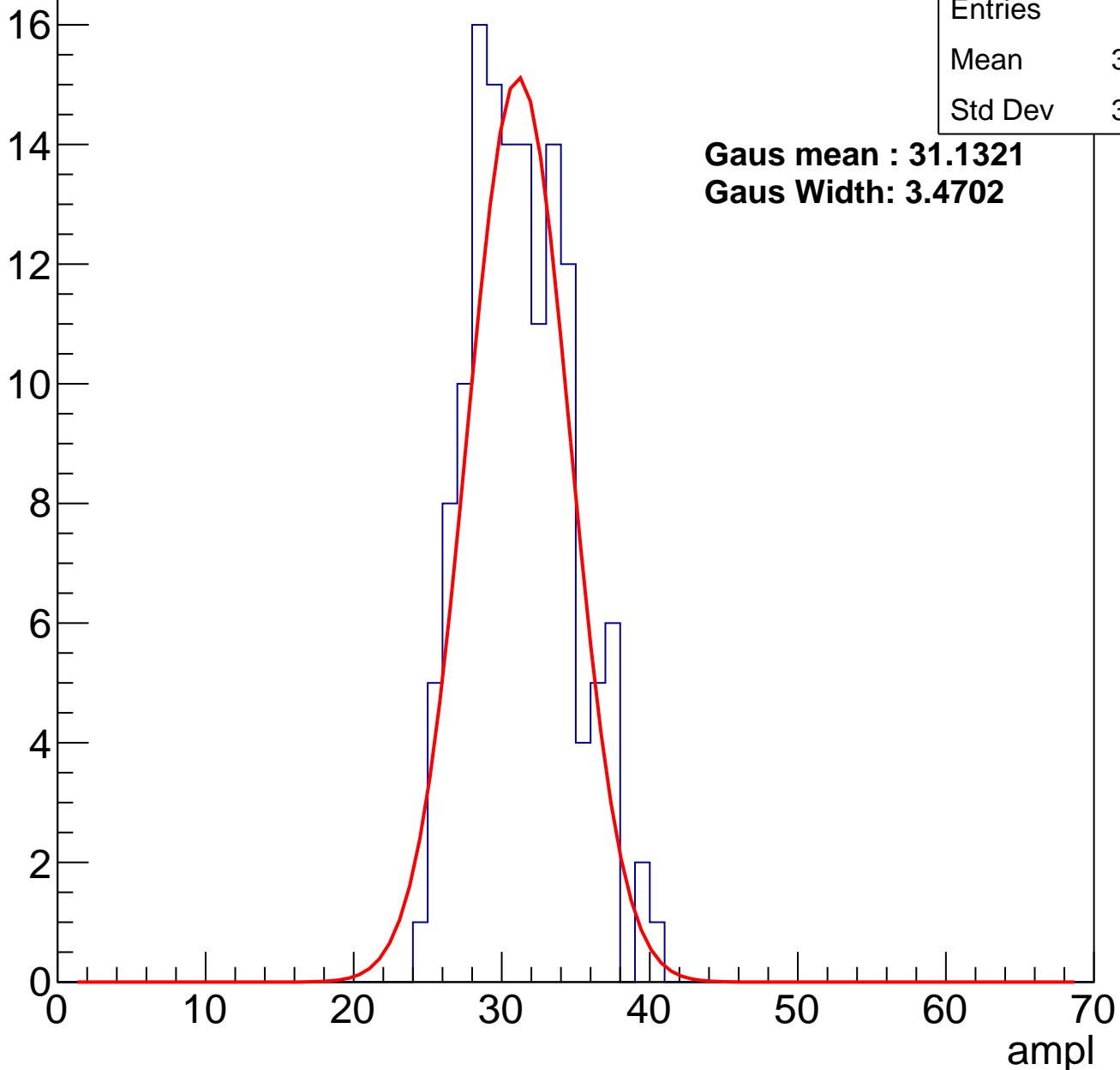
Entry



B1L001S, U25-ch45, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch45, adc1

calib_packv5_042523_0143.root, FC#2, port C2

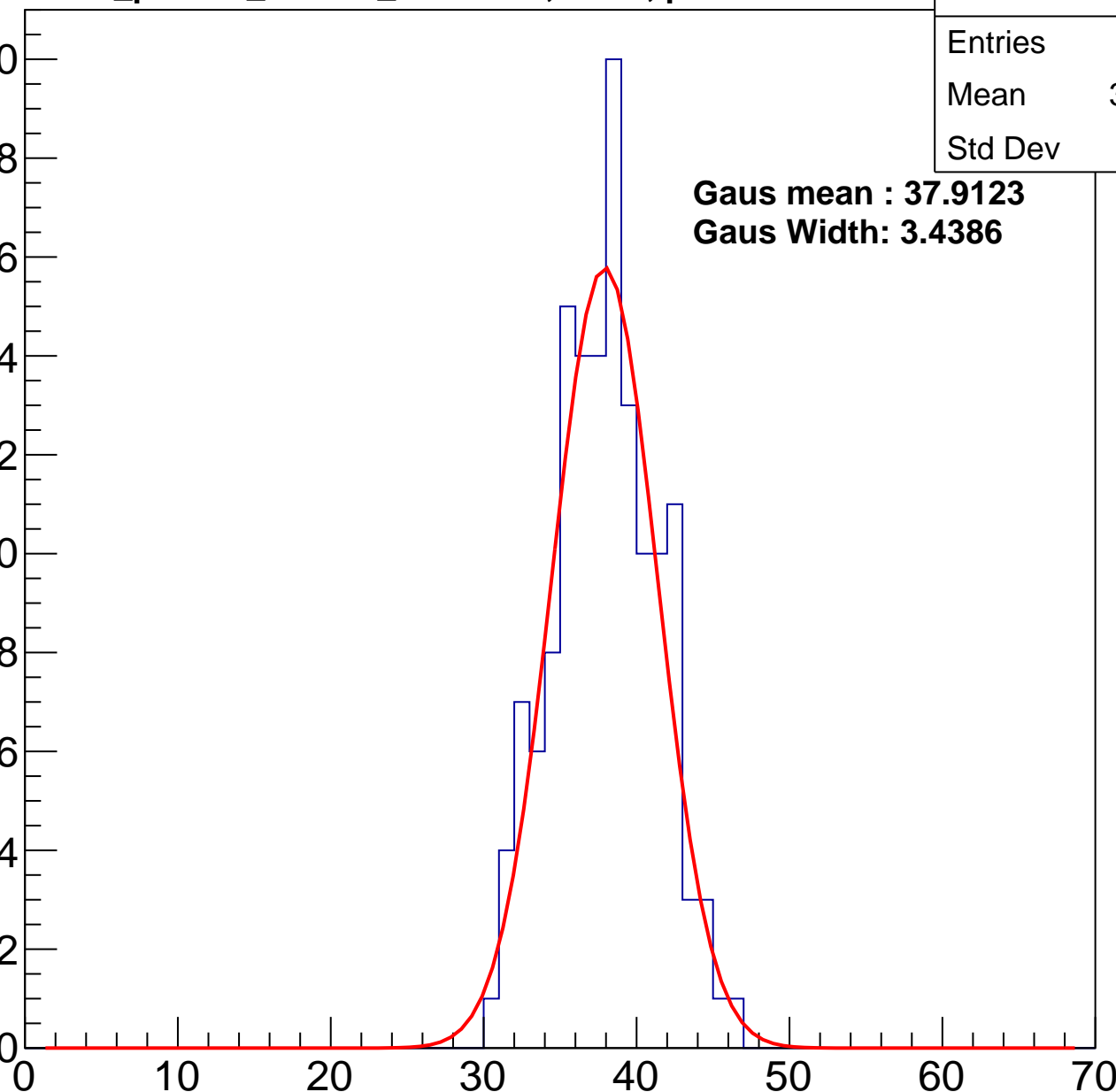
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	141
Mean	37.49
Std Dev	3.34

Gaus mean : 37.9123

Gaus Width: 3.4386



ampl

B1L001S, U25-ch45, adc2

calib_packv5_042523_0143.root, FC#2, port C2

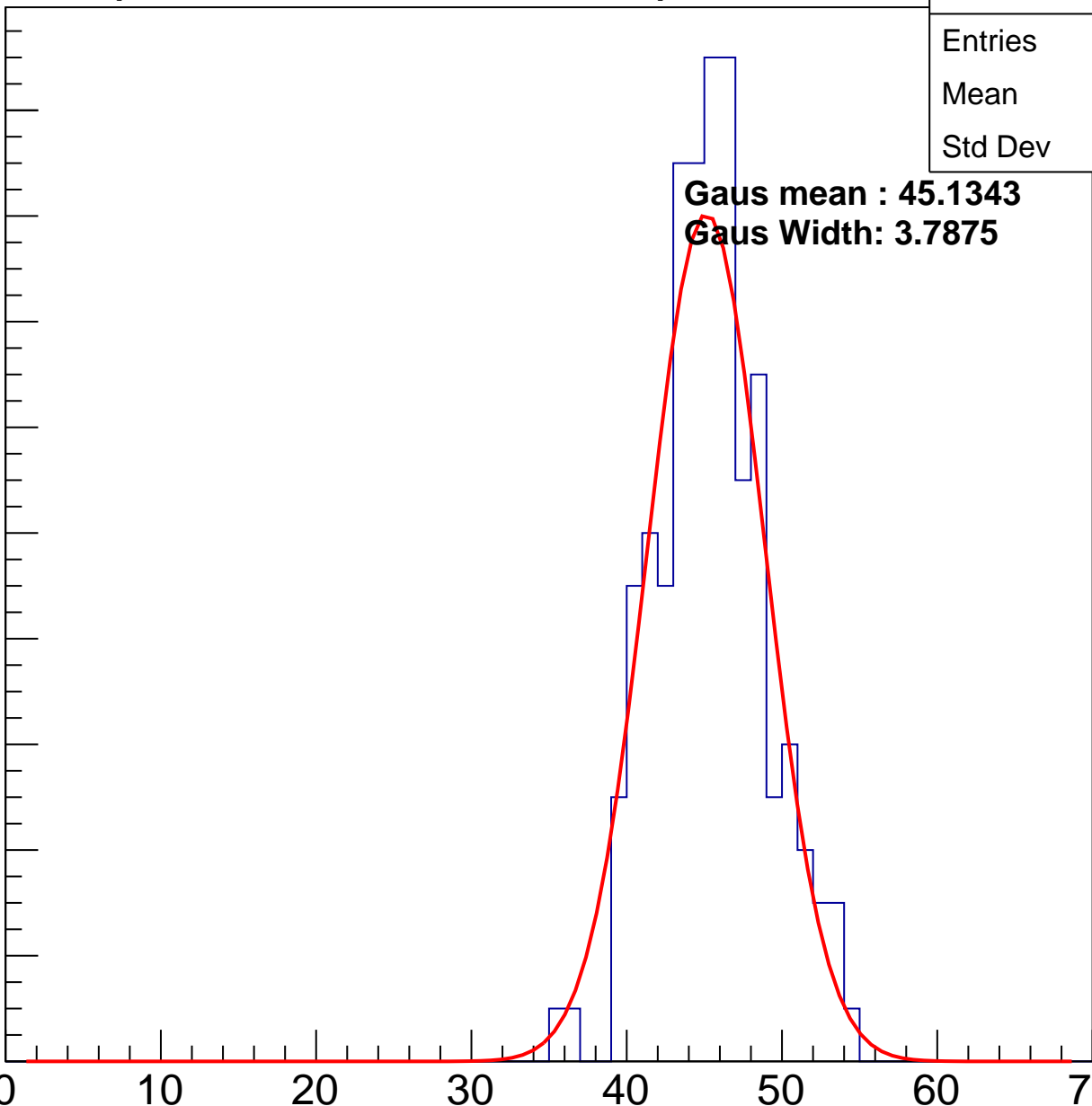
Entry

18
16
14
12
10
8
6
4
2
0

Entries	153
Mean	44.97
Std Dev	3.511

Gaus mean : 45.1343
Gaus Width: 3.7875

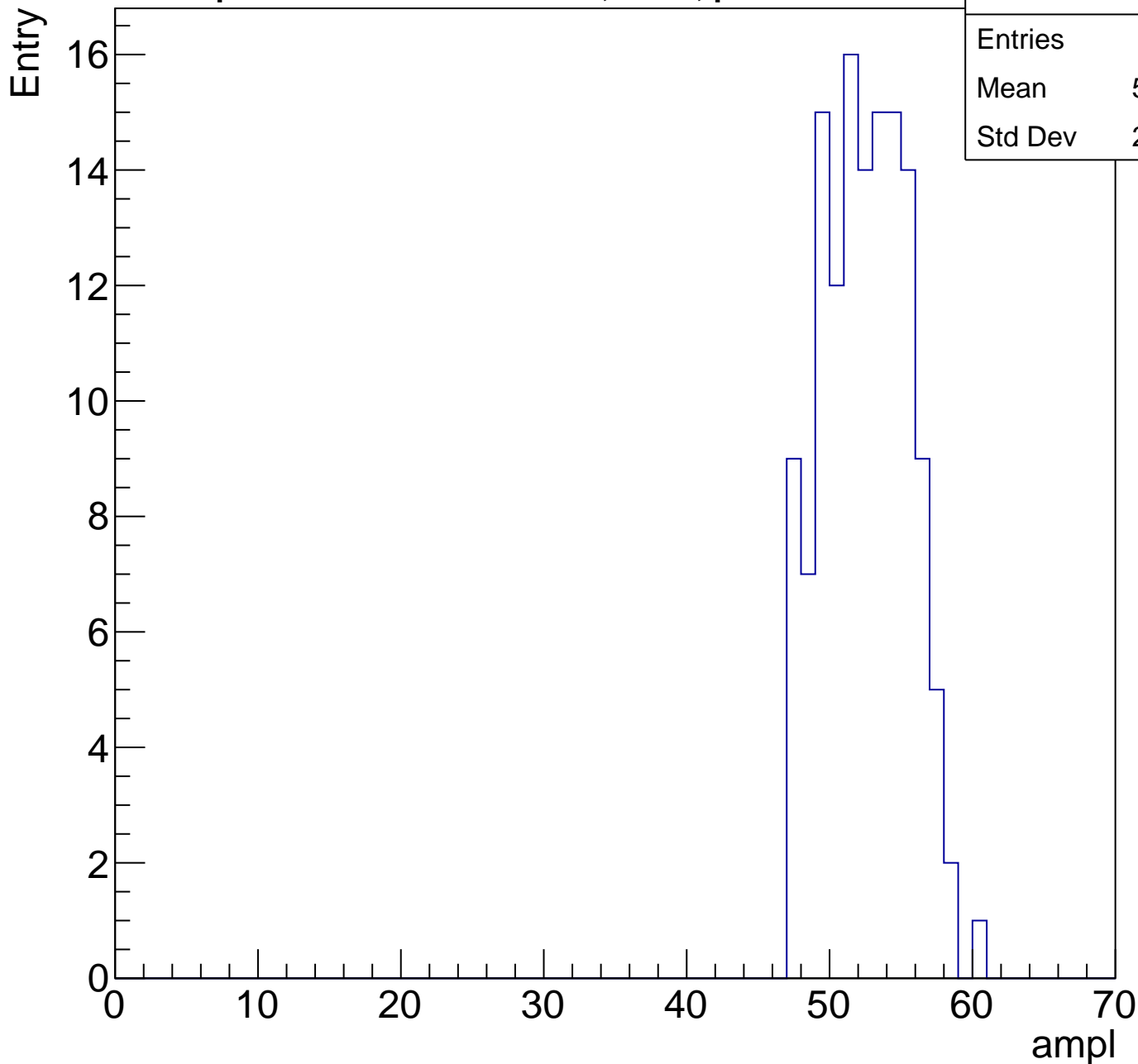
0 10 20 30 40 50 60 70
ampl



B1L001S, U25-ch45, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	52.07
Std Dev	2.918



B1L001S, U25-ch45, adc4

calib_packv5_042523_0143.root, FC#2, port C2

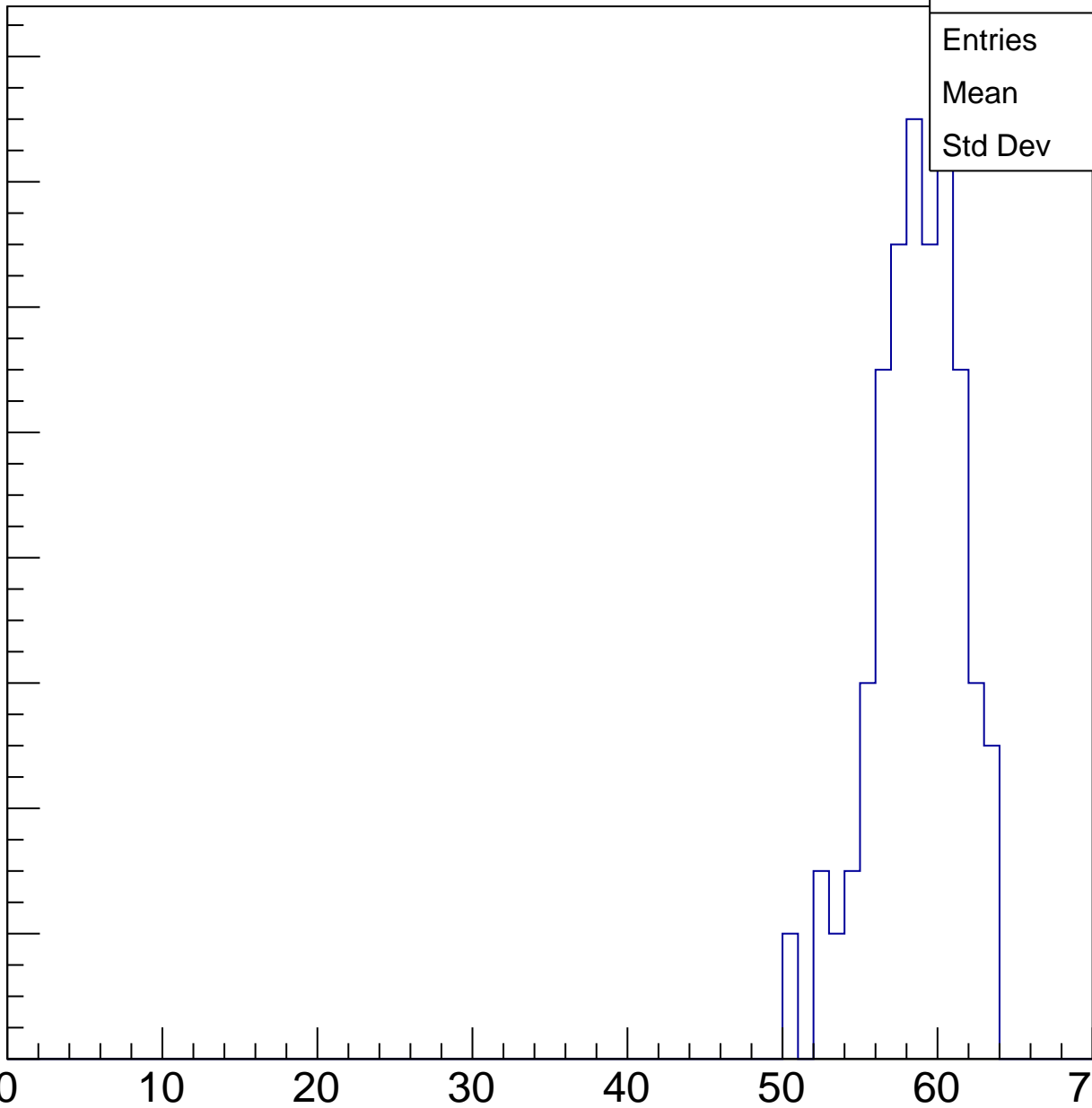
Entries	106
Mean	58.17
Std Dev	2.813

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch45, adc5

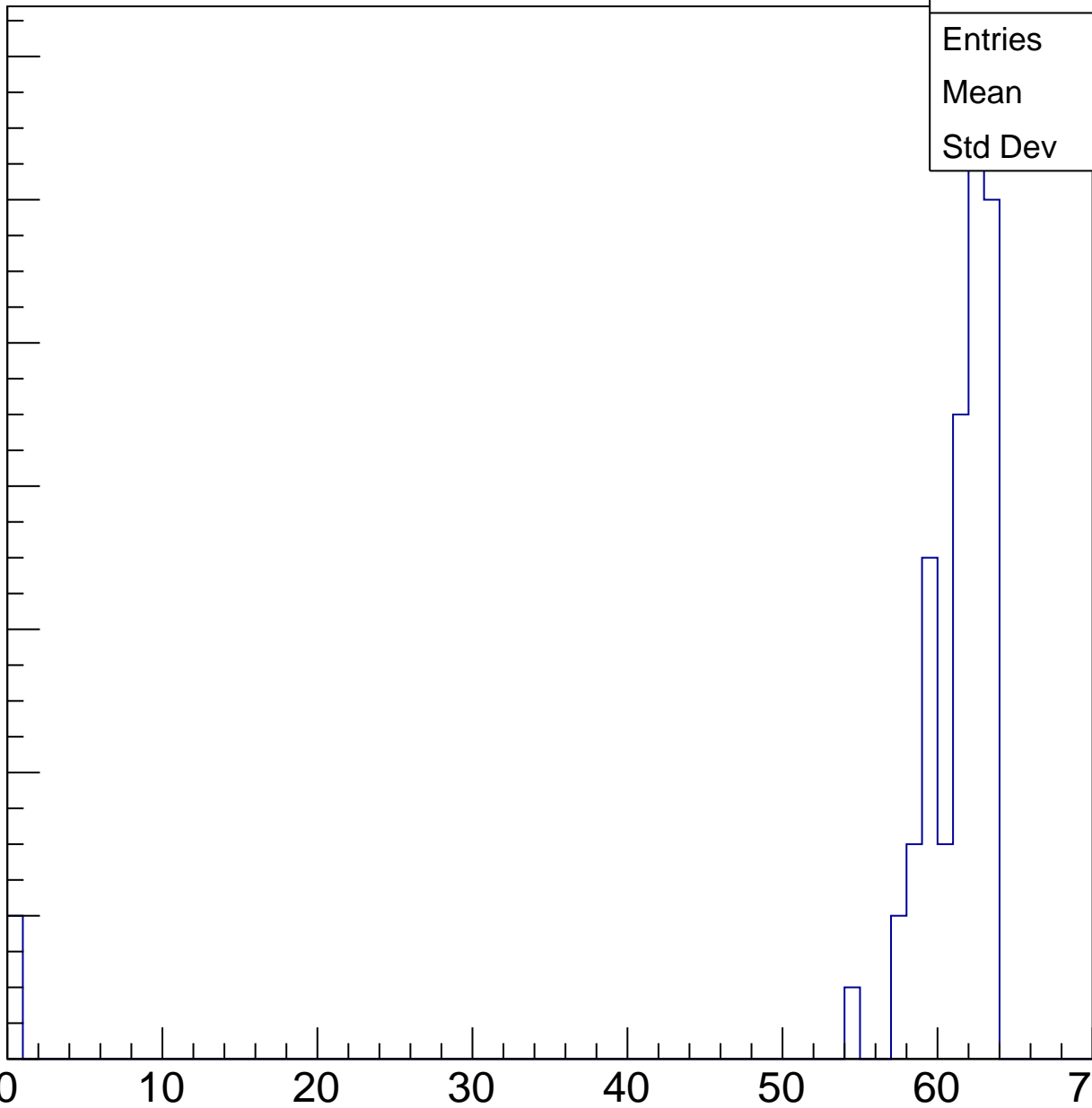
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	53
Mean	58.64
Std Dev	11.77

ampl



B1L001S, U25-ch45, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch45, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	24
Std Dev	0

B1L001S, U25-ch46, adc0

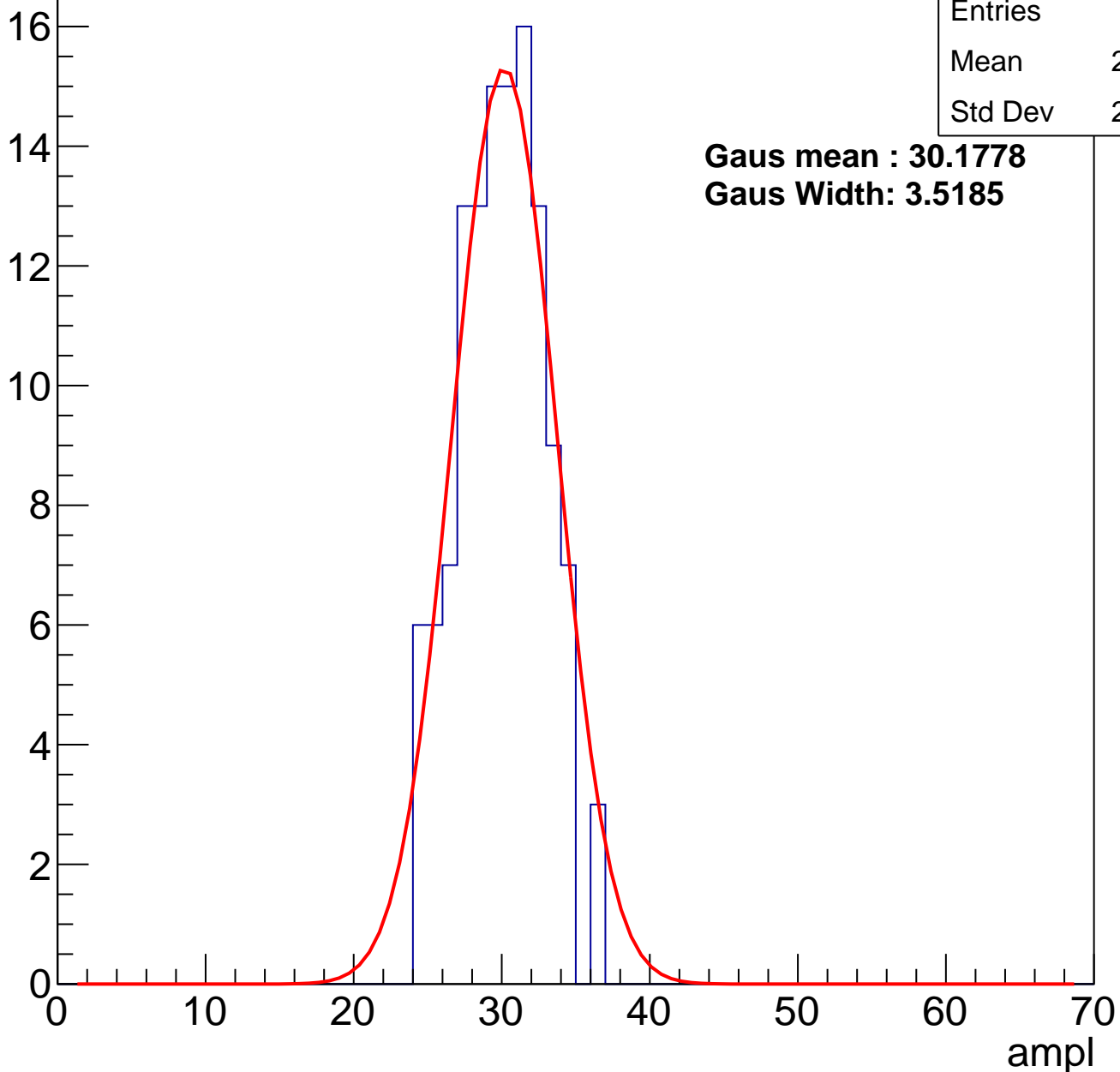
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	29.52
Std Dev	2.855

Gaus mean : 30.1778

Gaus Width: 3.5185

Entry



B1L001S, U25-ch46, adc1

calib_packv5_042523_0143.root, FC#2, port C2

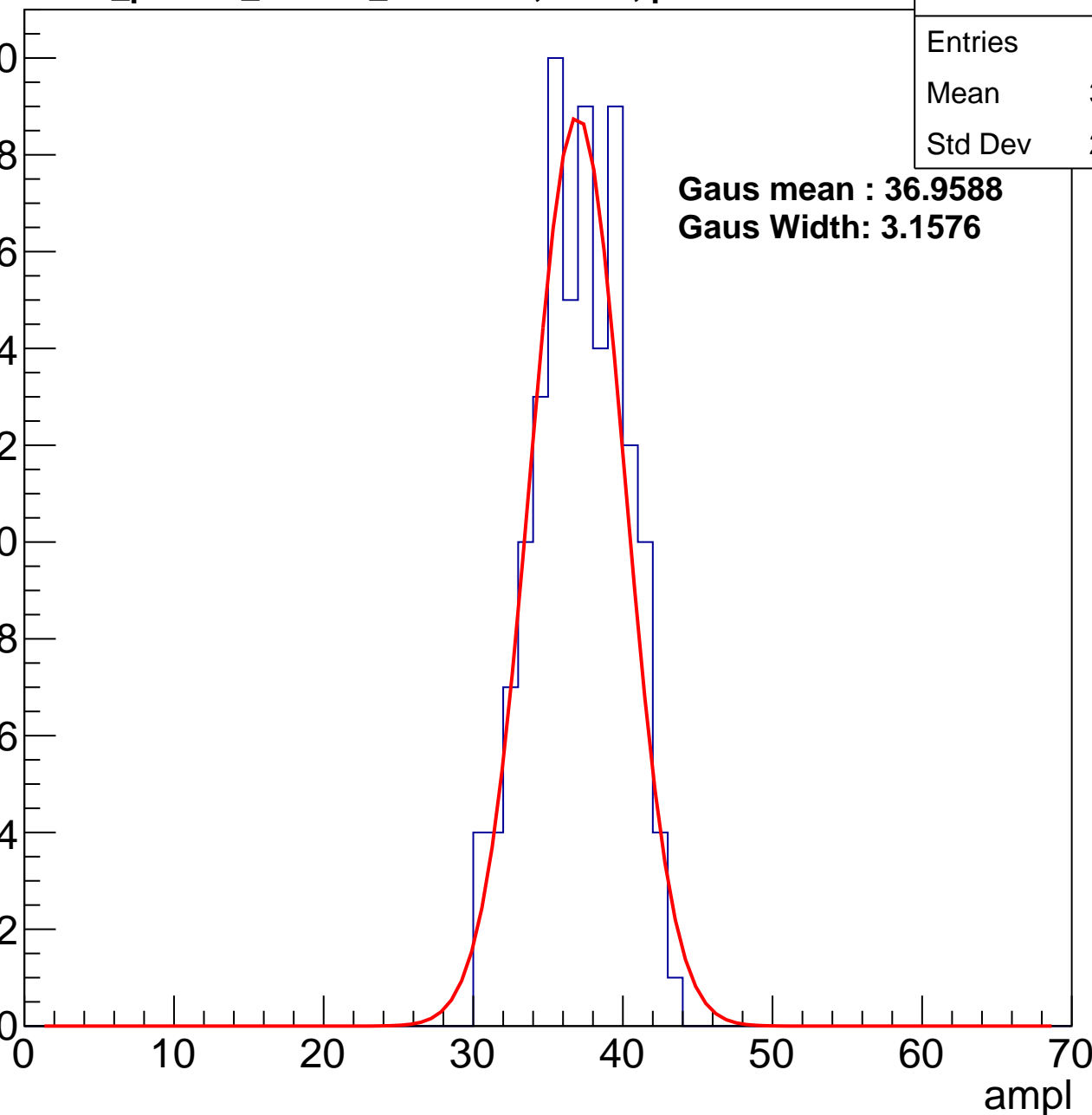
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	152
Mean	36.56
Std Dev	2.982

Gaus mean : 36.9588

Gaus Width: 3.1576



B1L001S, U25-ch46, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

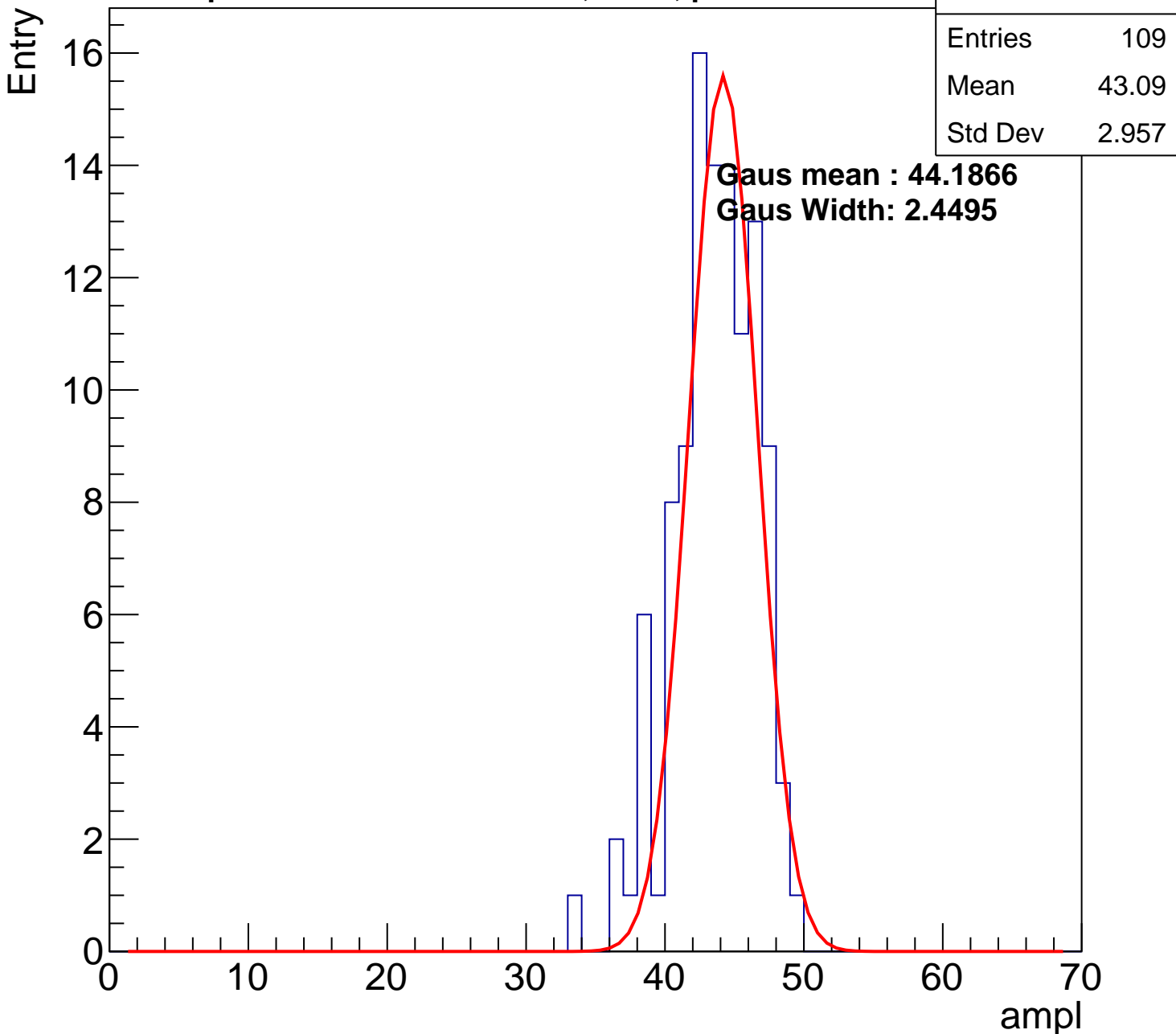
Entries	109
Mean	43.09
Std Dev	2.957

Gaus mean : 44.1866

Gaus Width: 2.4495

0 10 20 30 40 50 60 70

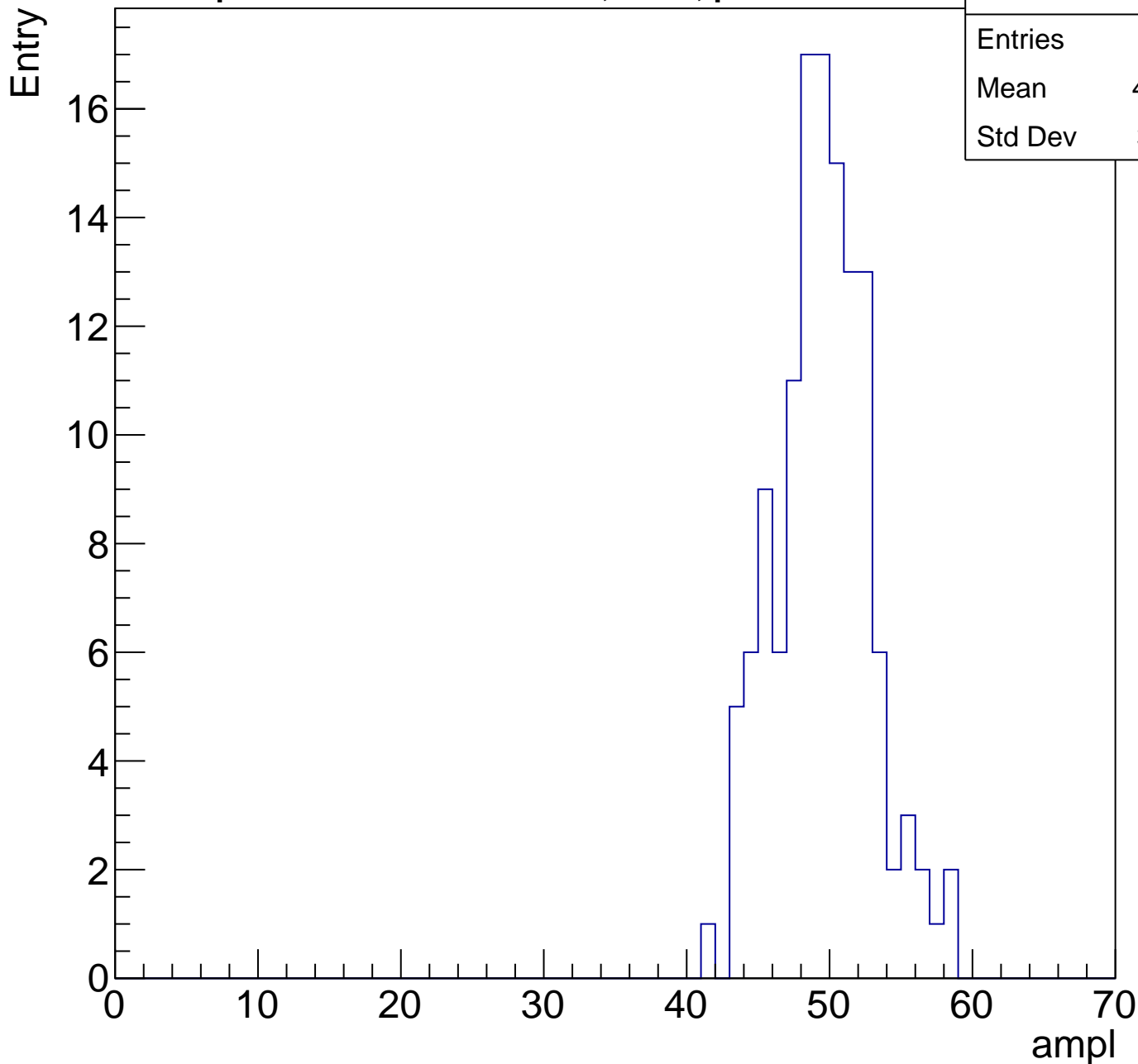
ampl



B1L001S, U25-ch46, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	49.09
Std Dev	3.311



B1L001S, U25-ch46, adc4

calib_packv5_042523_0143.root, FC#2, port C2

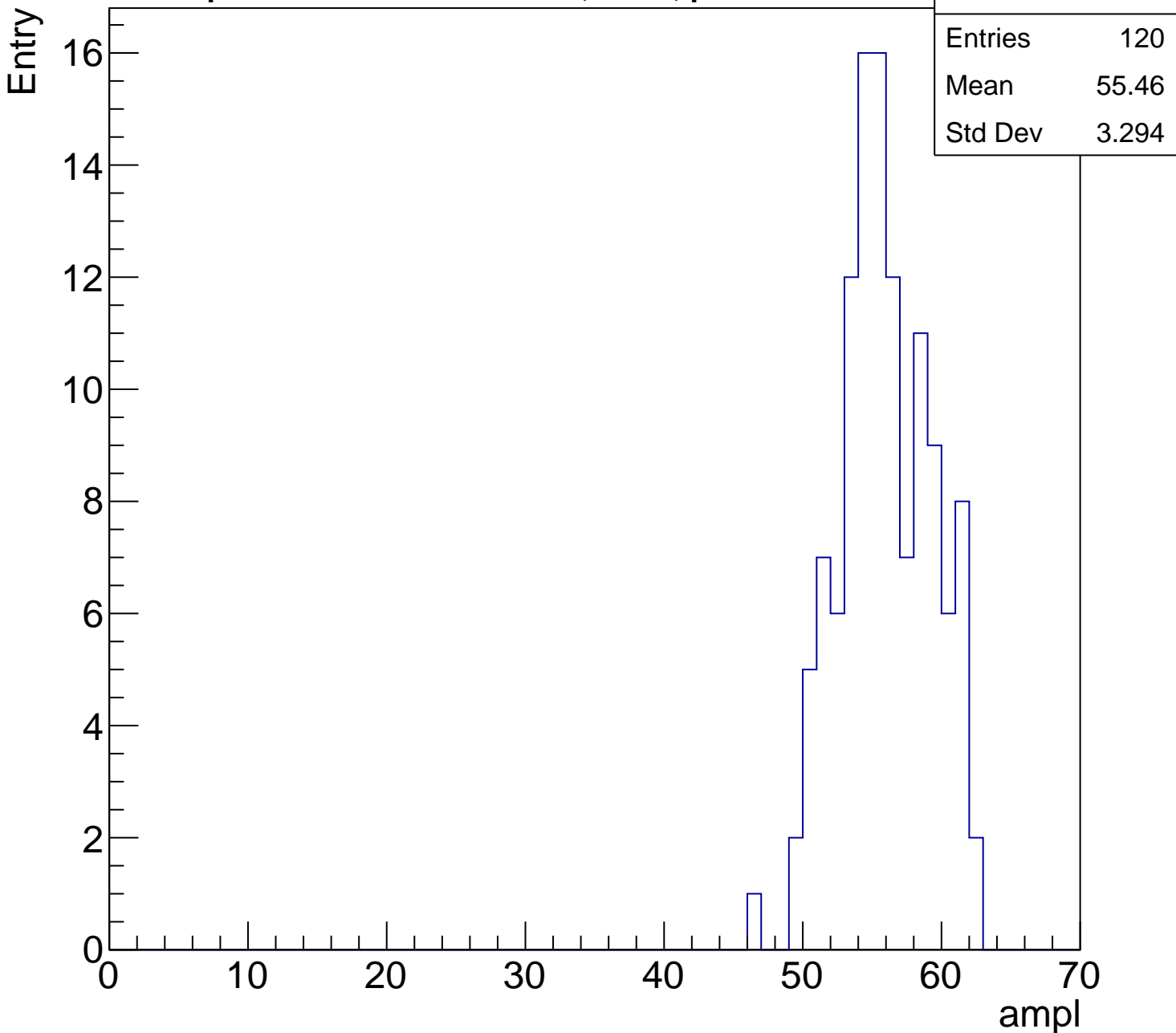
Entries	120
Mean	55.46
Std Dev	3.294

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch46, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	91
Mean	58.82
Std Dev	6.748

0

10

20

30

40

50

60

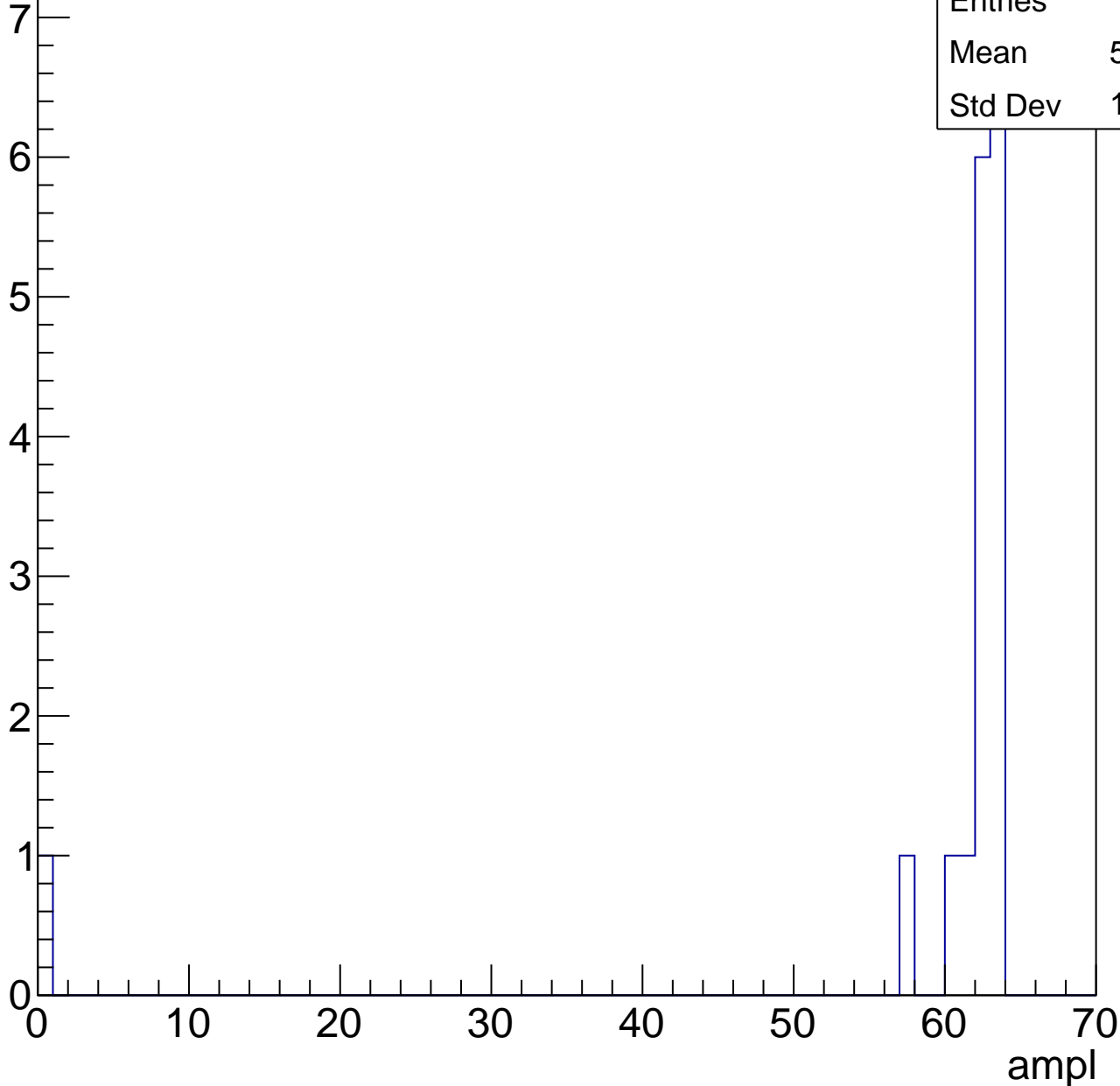
70

B1L001S, U25-ch46, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	58.29
Std Dev	14.65



B1L001S, U25-ch46, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



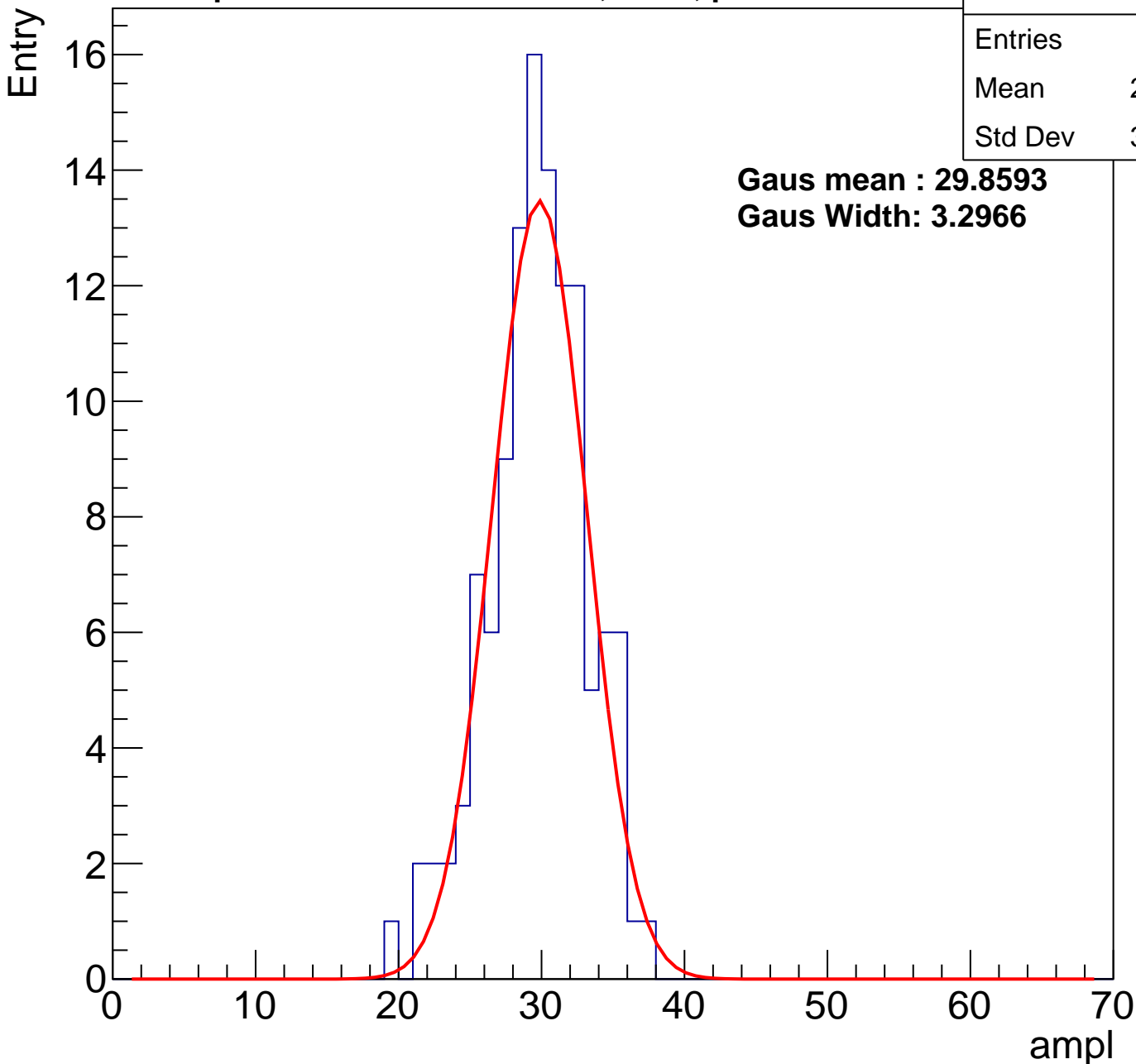
B1L001S, U25-ch47, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	29.26
Std Dev	3.455

Gaus mean : 29.8593

Gaus Width: 3.2966



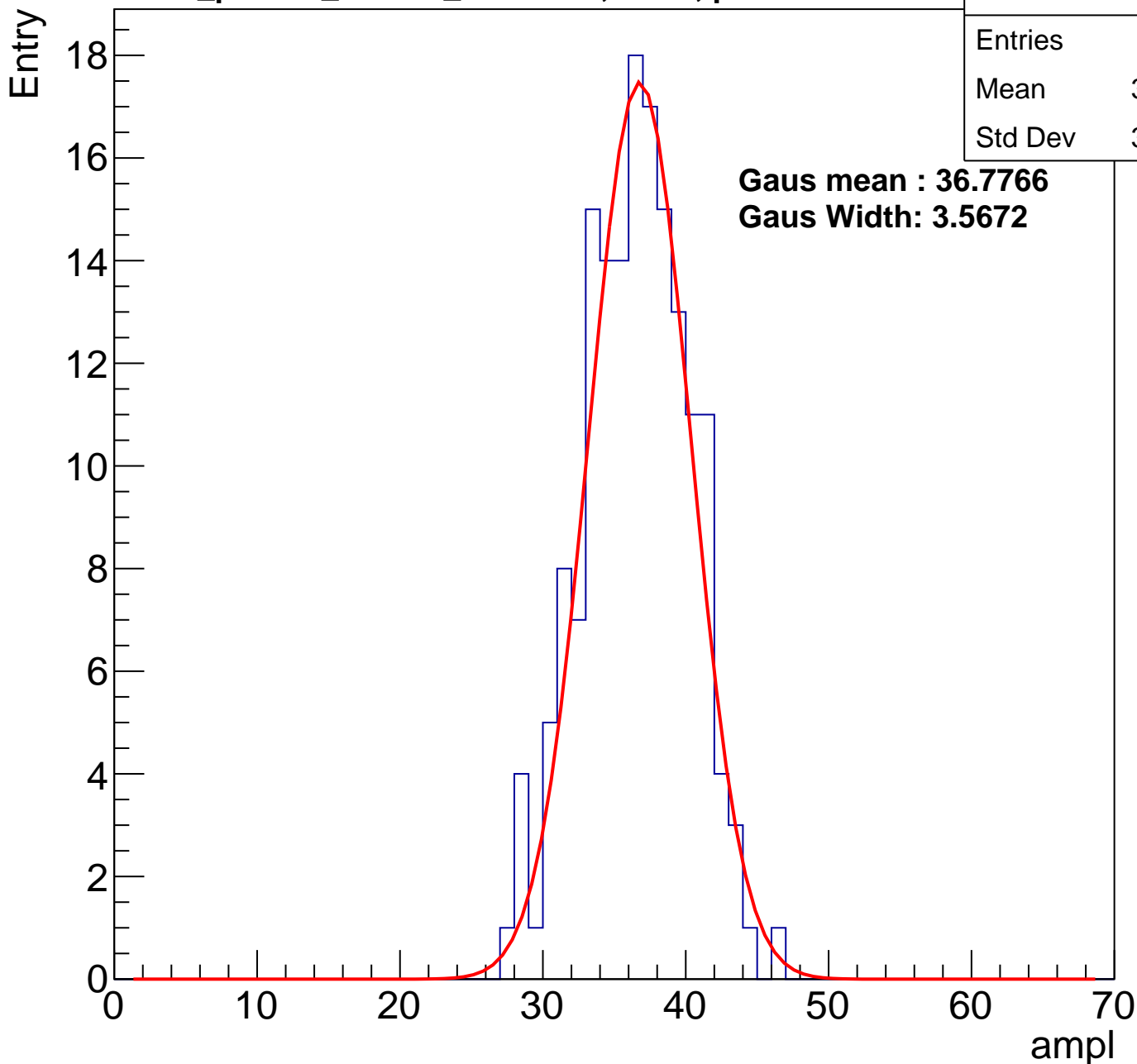
B1L001S, U25-ch47, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	36.09
Std Dev	3.658

Gaus mean : 36.7766

Gaus Width: 3.5672



B1L001S, U25-ch47, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

126

Mean

43.37

Std Dev

3.057

Gaus mean : 43.7774

Gaus Width: 3.1148

0

10

20

30

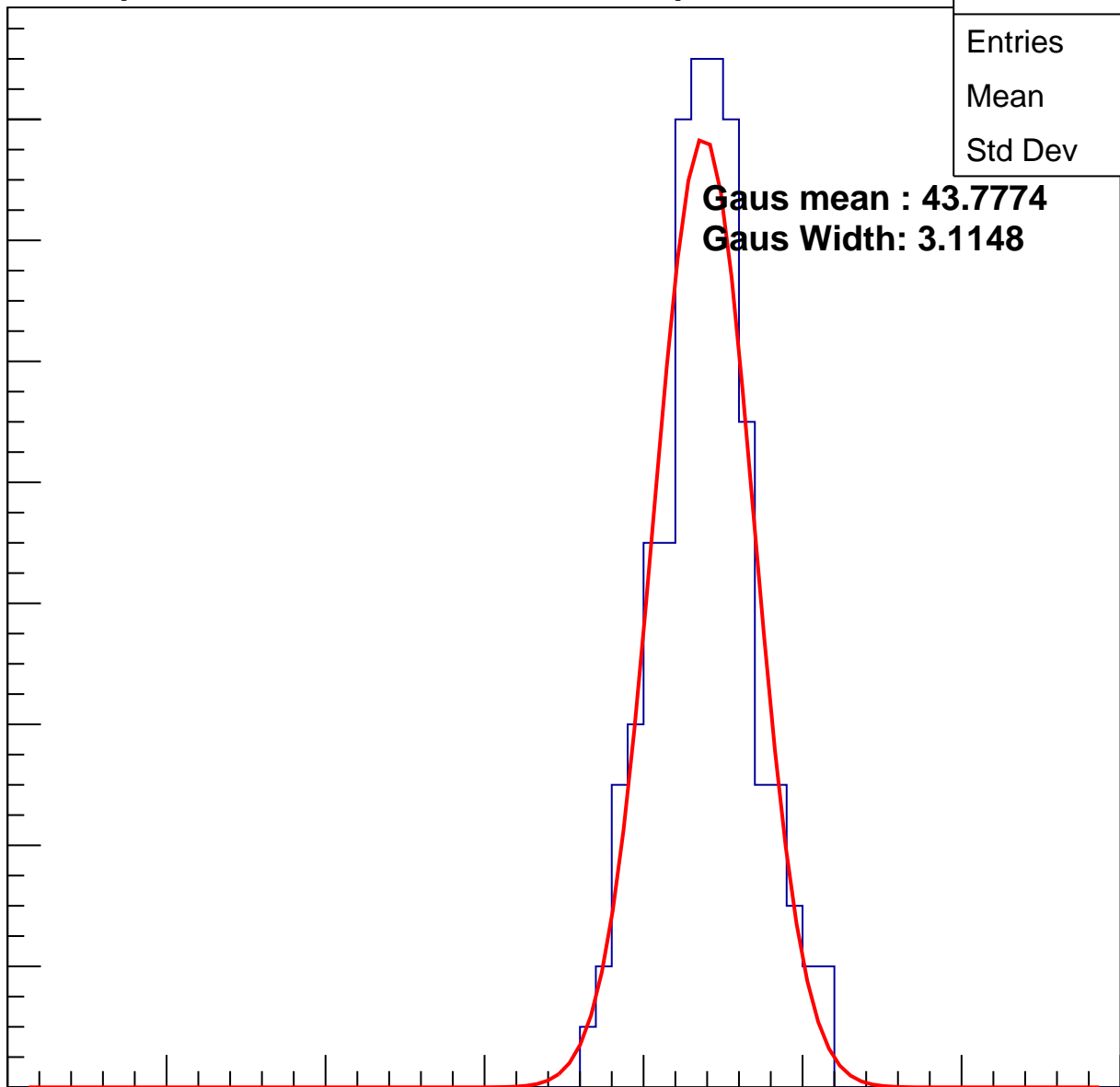
40

50

60

70

ampl



B1L001S, U25-ch47, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	50.39
Std Dev	3.302

Entry

16

14

12

10

8

6

4

2

0

0

10

20

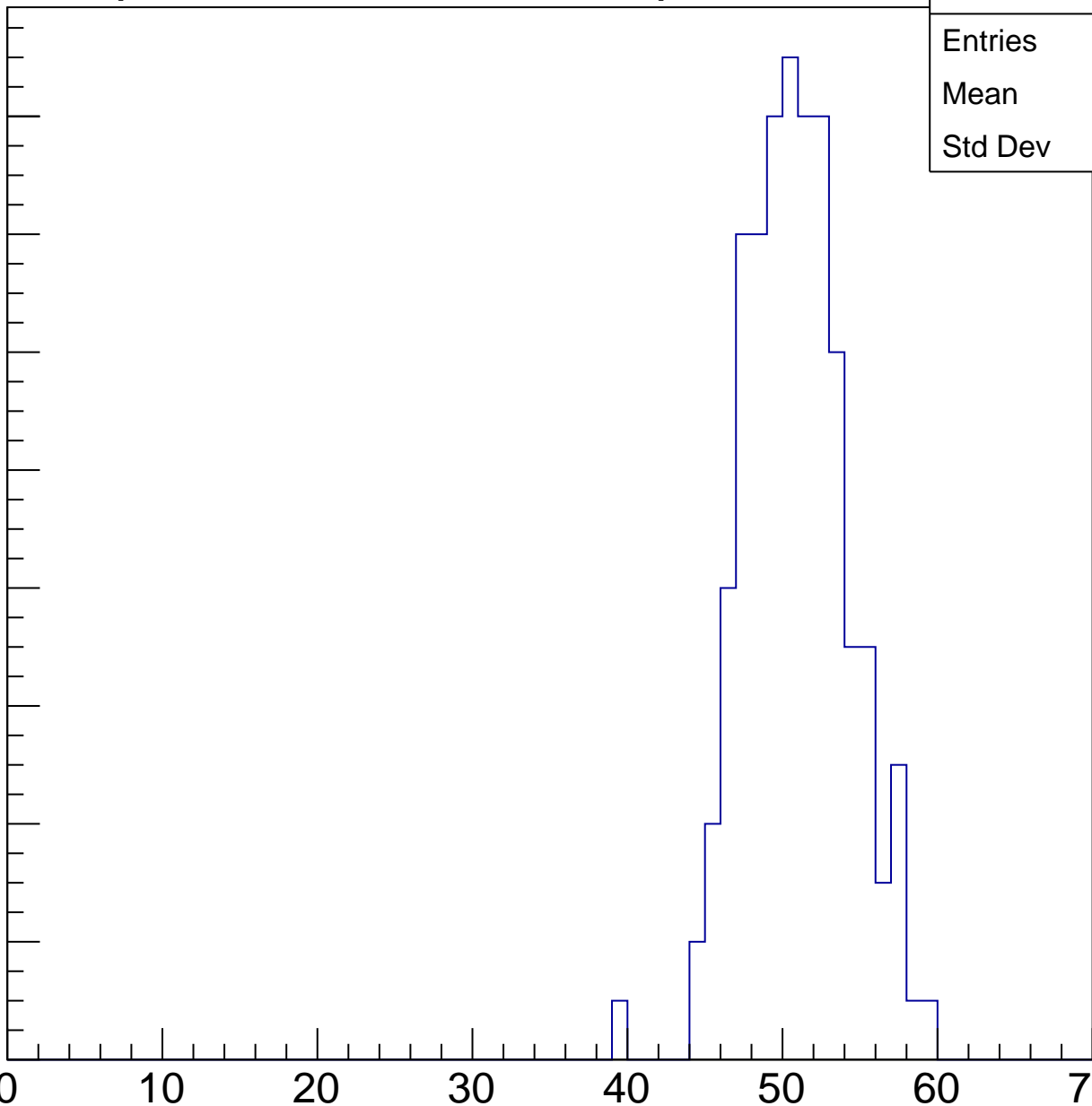
30

40

50

60

ampl



B1L001S, U25-ch47, adc4

calib_packv5_042523_0143.root, FC#2, port C2

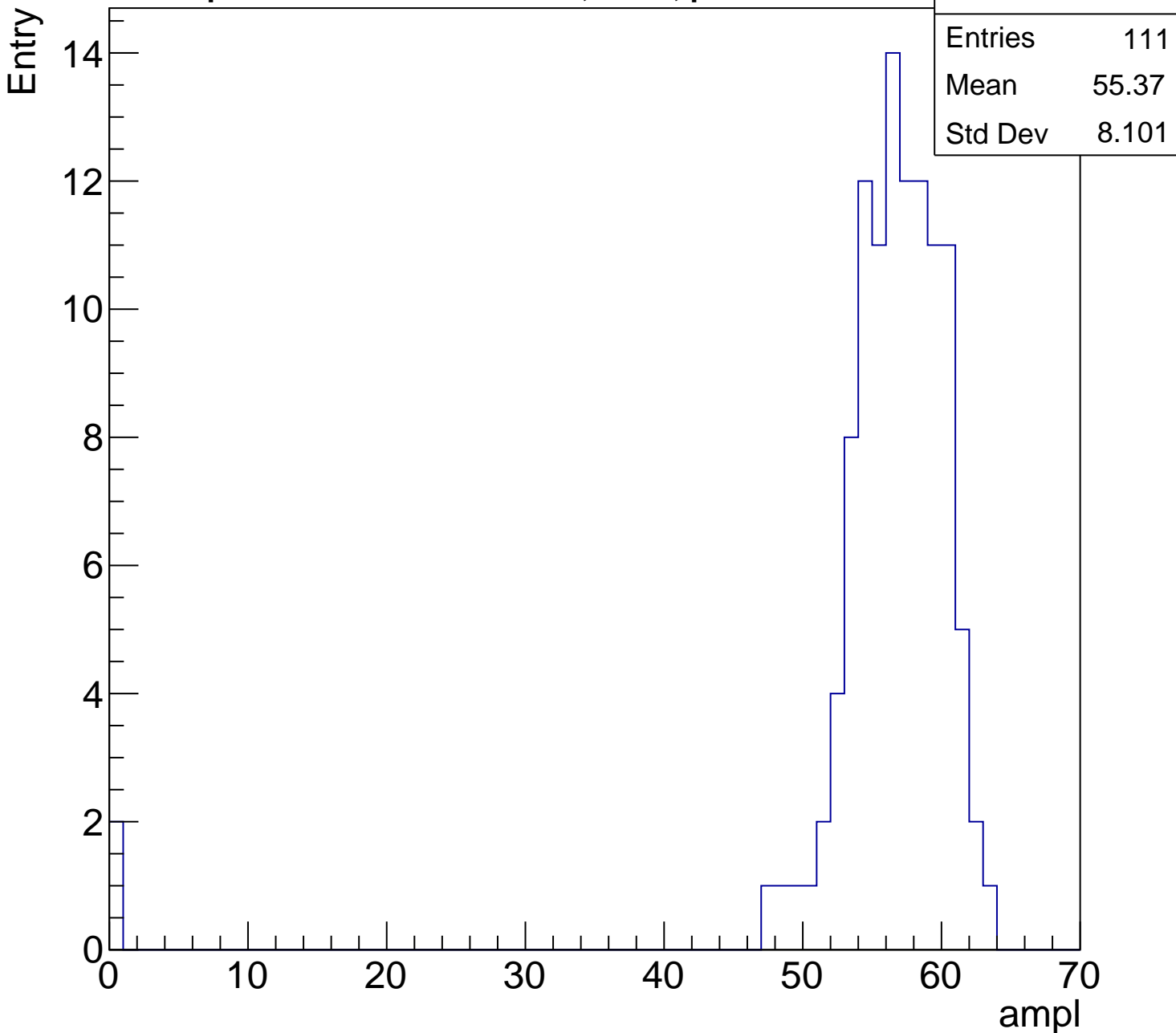
Entries	111
Mean	55.37
Std Dev	8.101

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch47, adc5

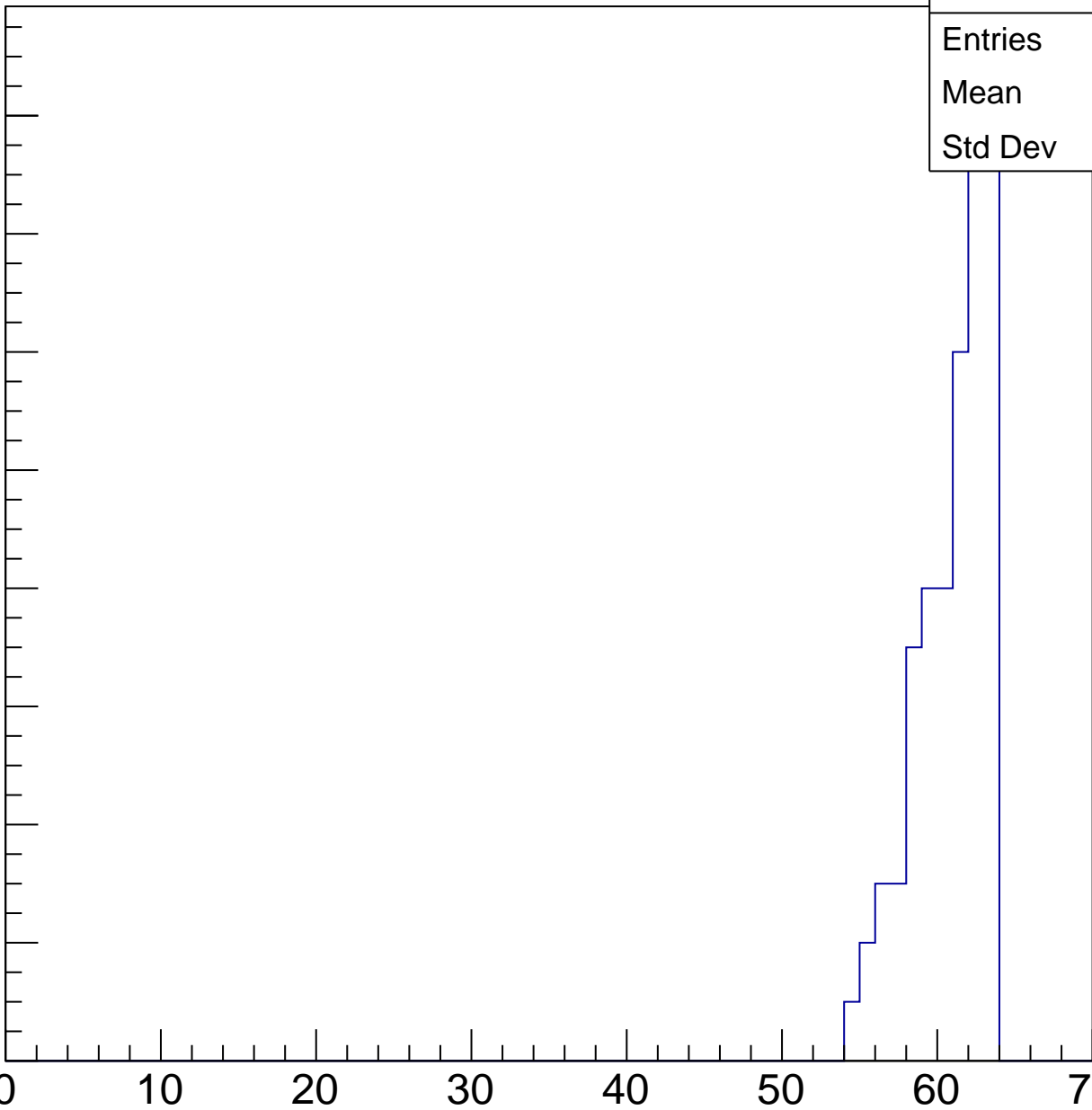
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	60.47
Std Dev	2.305

ampl



B1L001S, U25-ch47, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

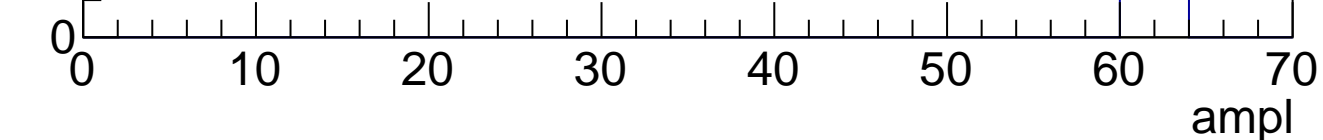
7

Mean

61.43

Std Dev

0.9035



B1L001S, U25-ch47, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



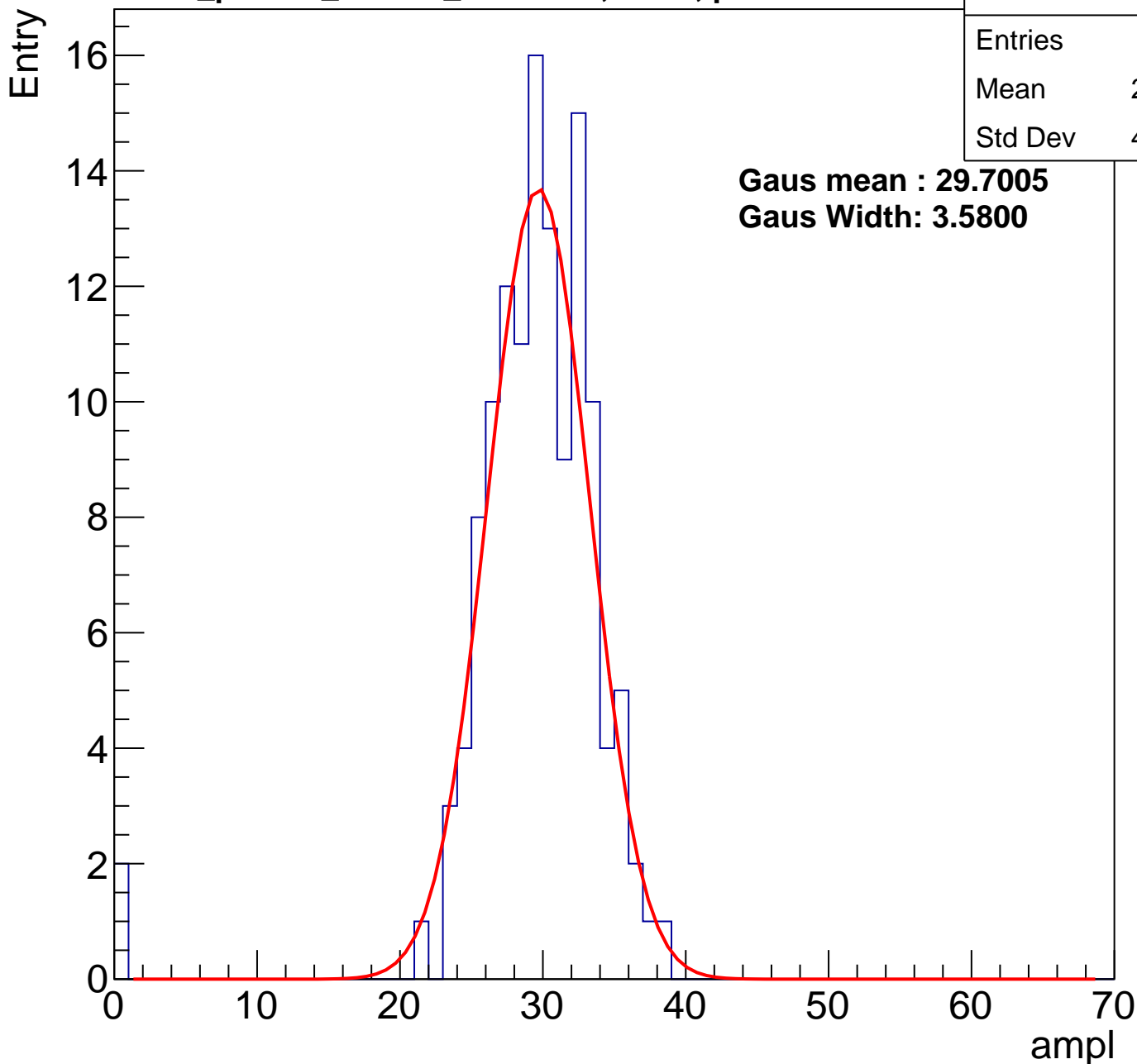
B1L001S, U25-ch48, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	28.97
Std Dev	4.939

Gaus mean : 29.7005

Gaus Width: 3.5800



B1L001S, U25-ch48, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	34.84
Std Dev	3.554

Gaus mean : 35.6343

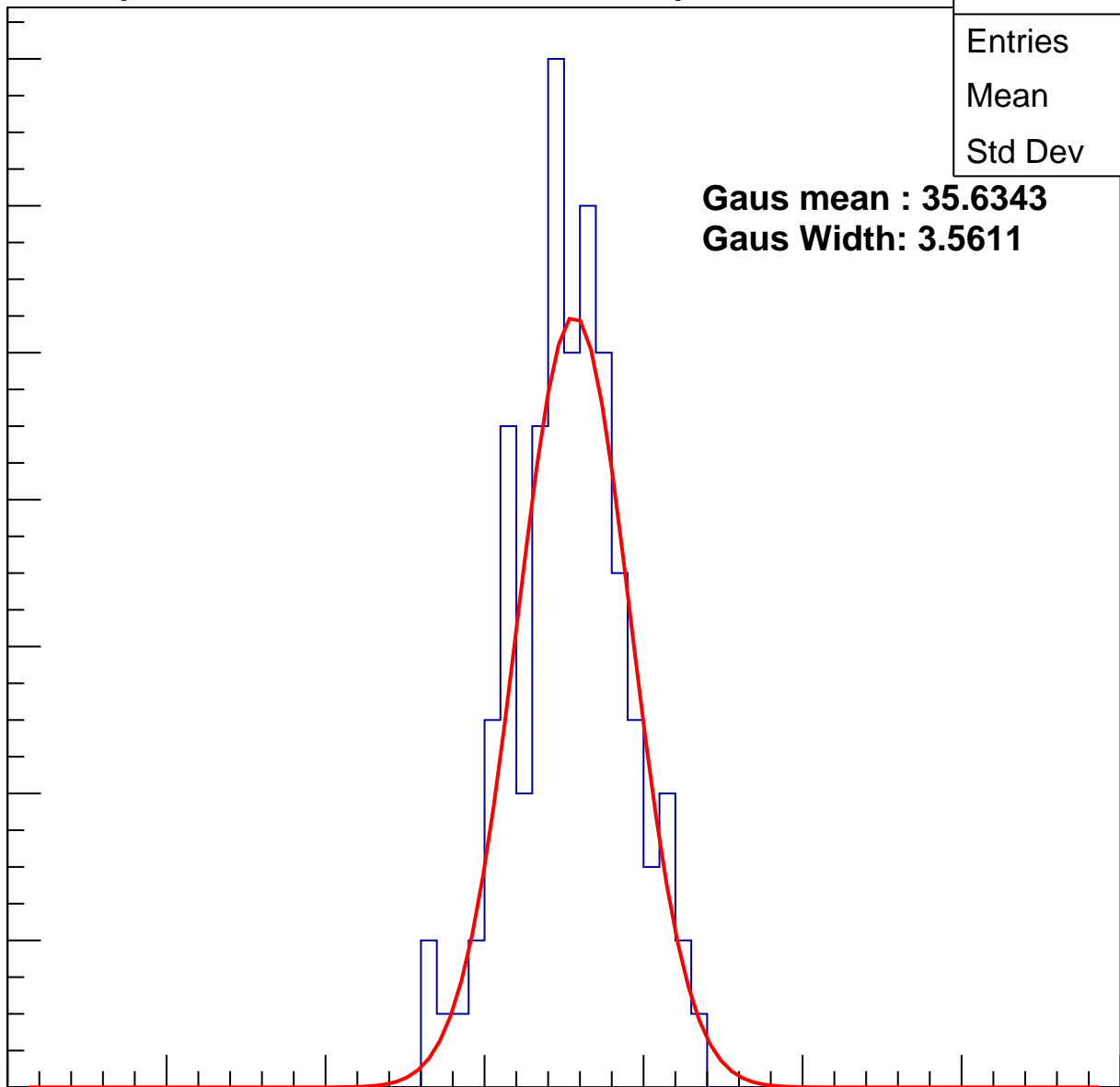
Gaus Width: 3.5611

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch48, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

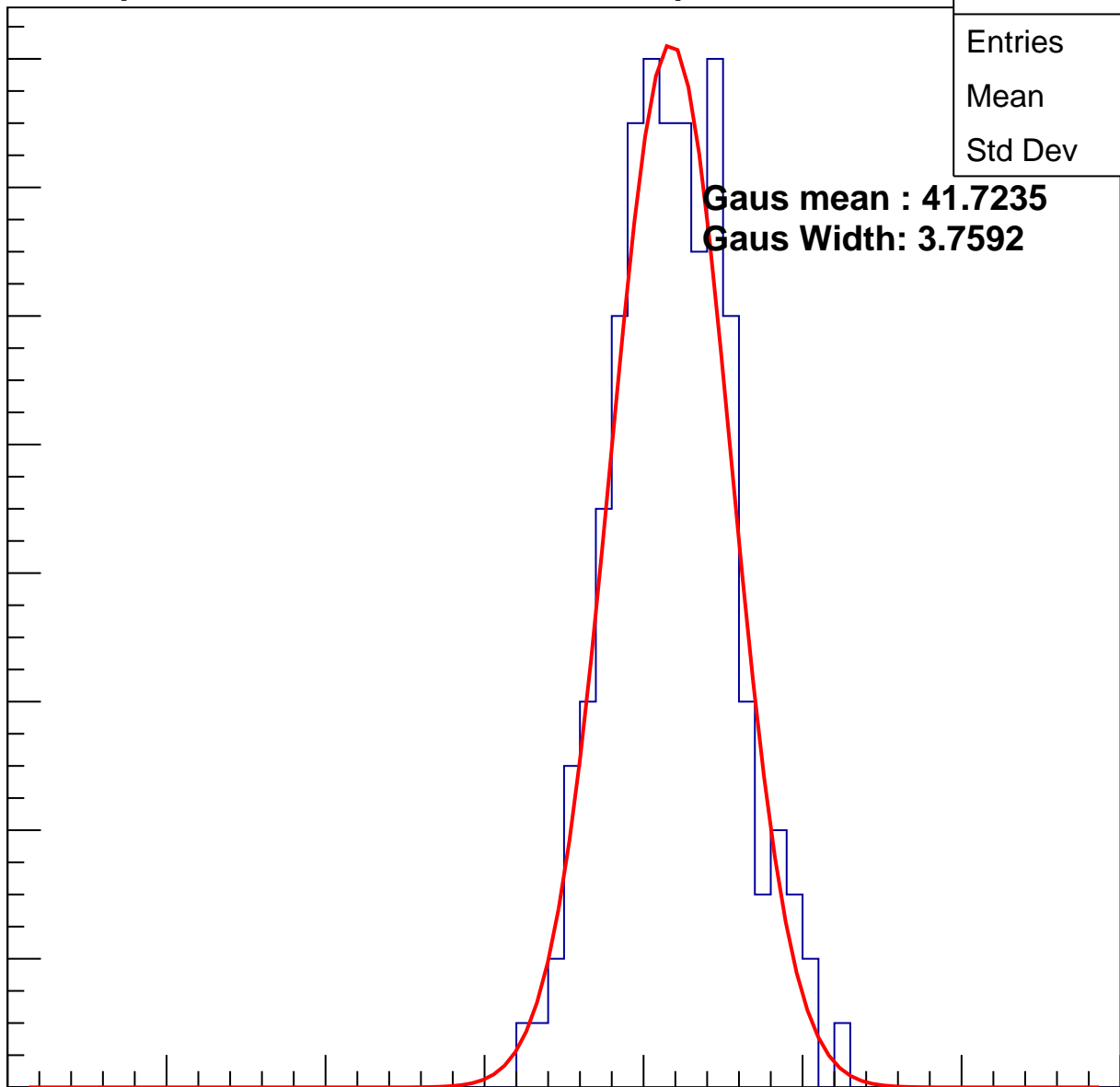
Entries	157
Mean	41.36
Std Dev	3.754

Gaus mean : 41.7235

Gaus Width: 3.7592

0 10 20 30 40 50 60 70

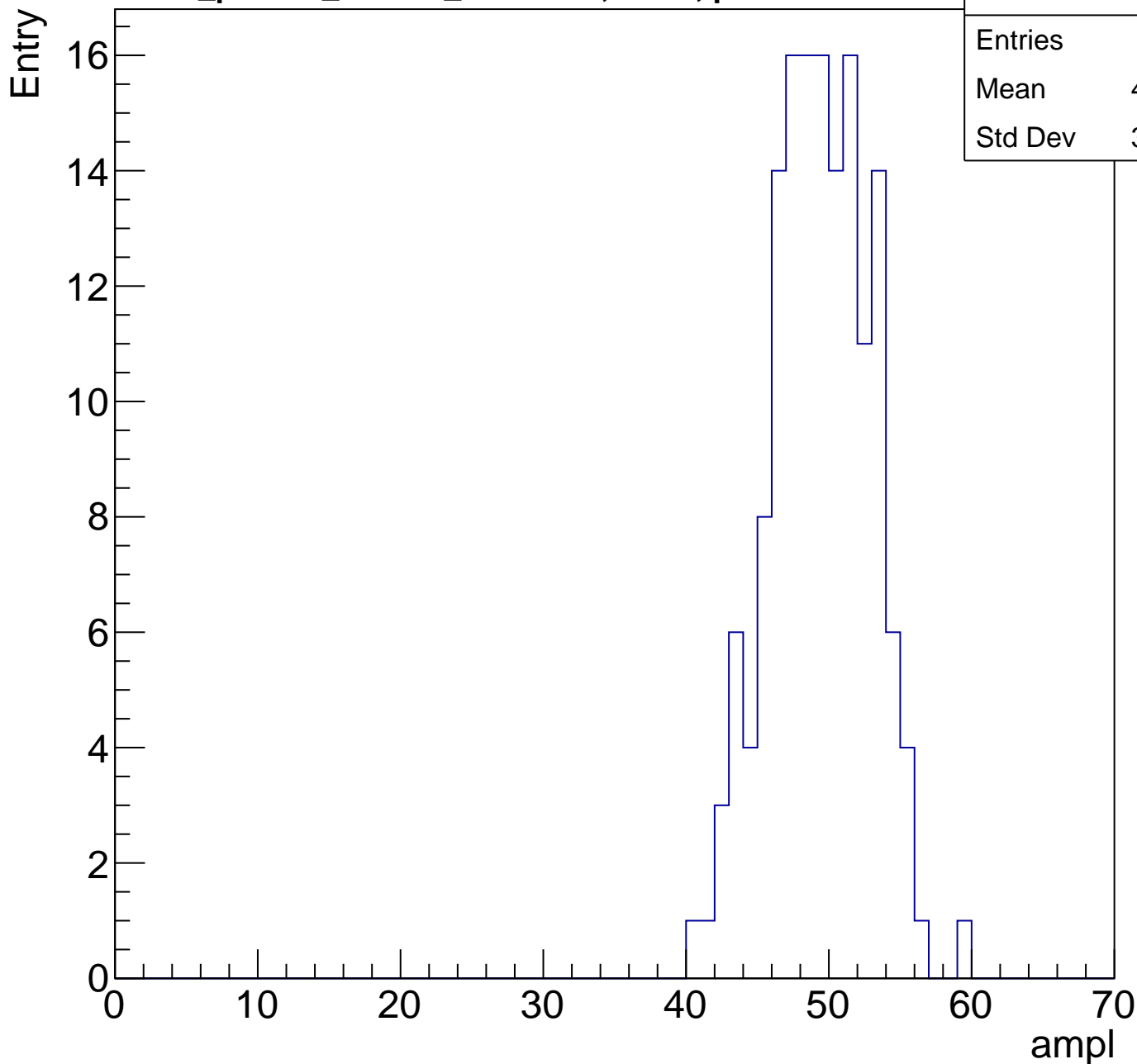
ampl



B1L001S, U25-ch48, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	48.93
Std Dev	3.427



B1L001S, U25-ch48, adc4

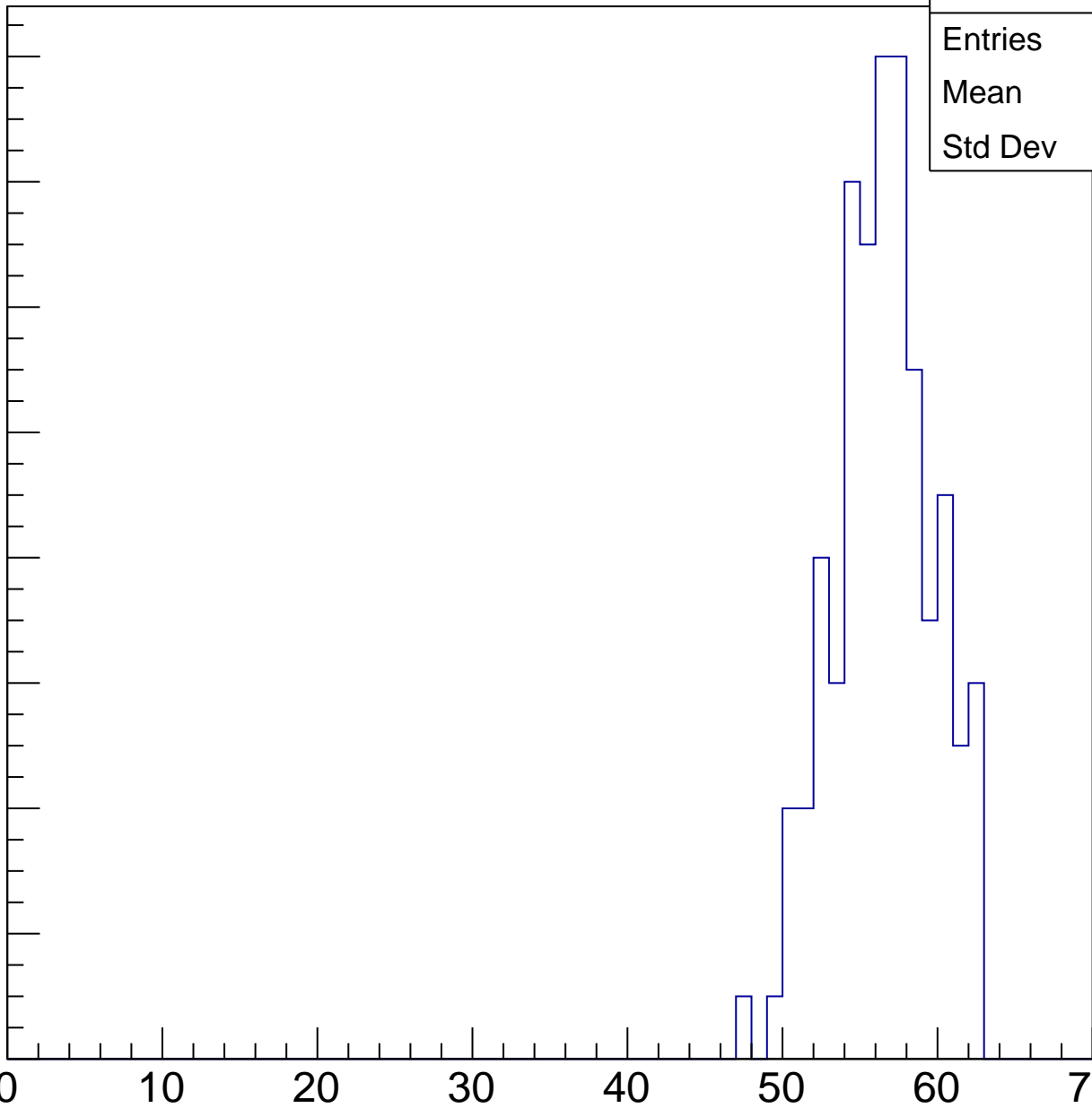
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	121
Mean	56.04
Std Dev	3.192

ampl



B1L001S, U25-ch48, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

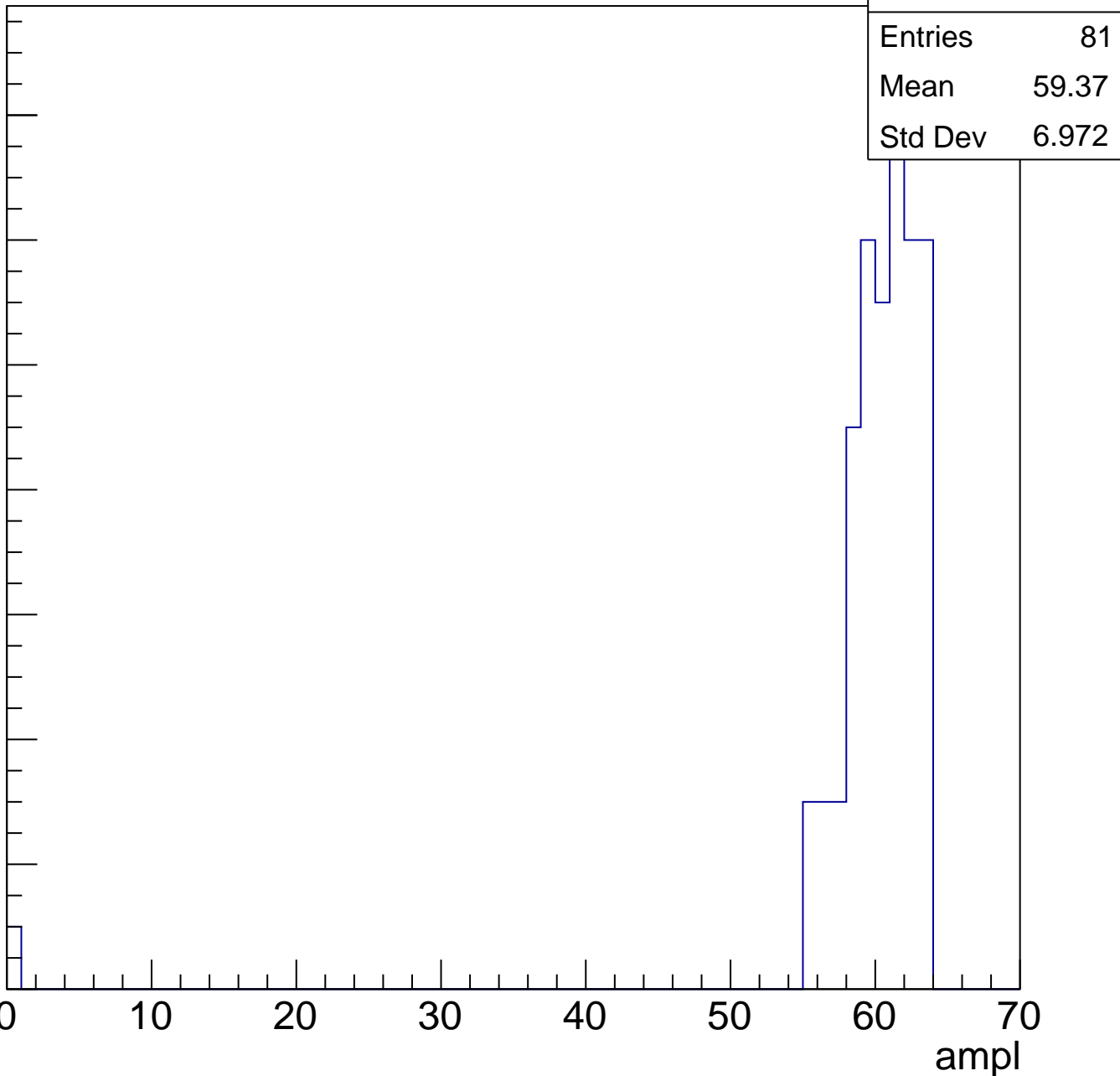
40

50

60

ampl

Entries	81
Mean	59.37
Std Dev	6.972

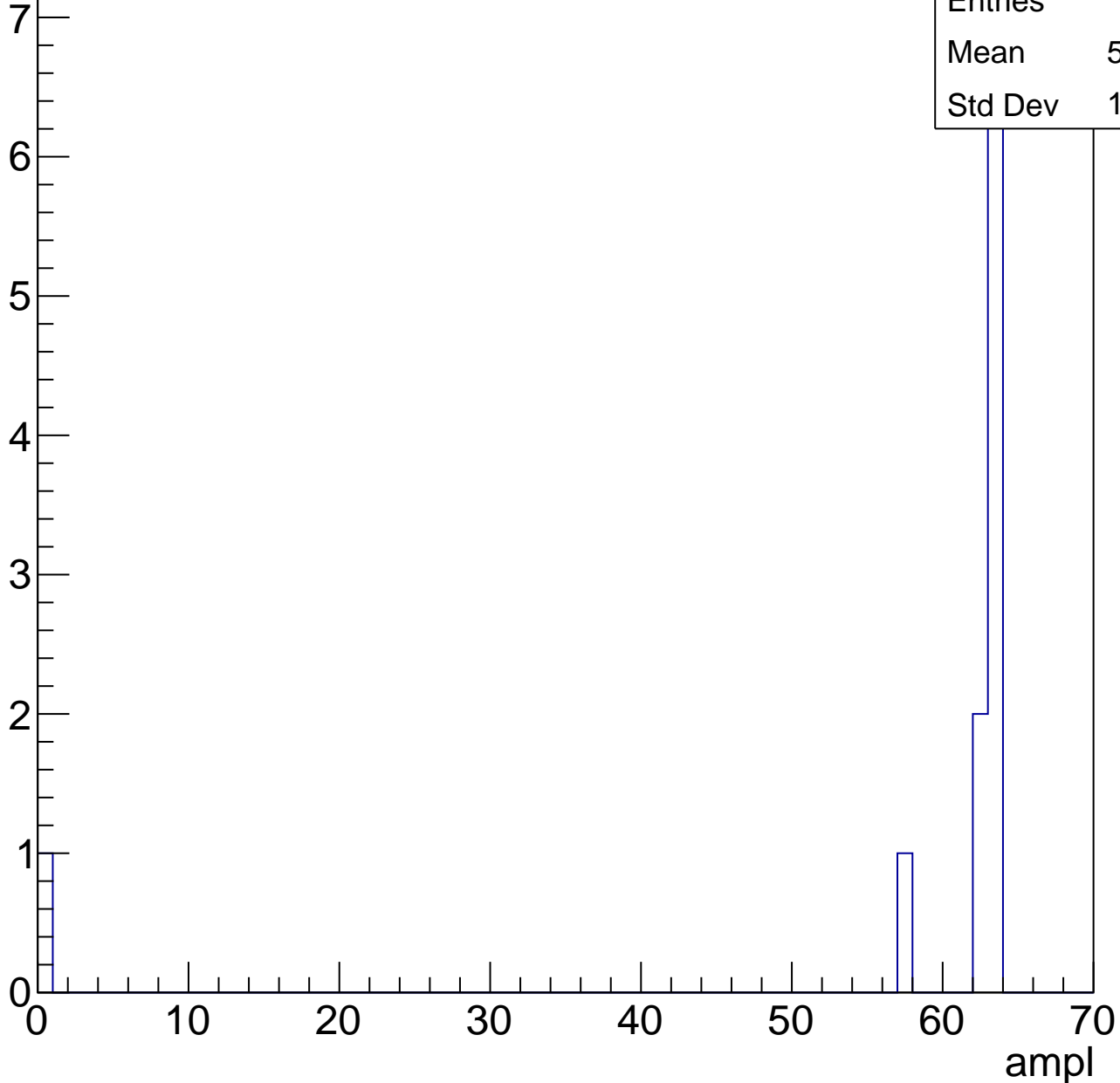


B1L001S, U25-ch48, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.55
Std Dev	17.96



B1L001S, U25-ch48, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch49, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

145

Mean

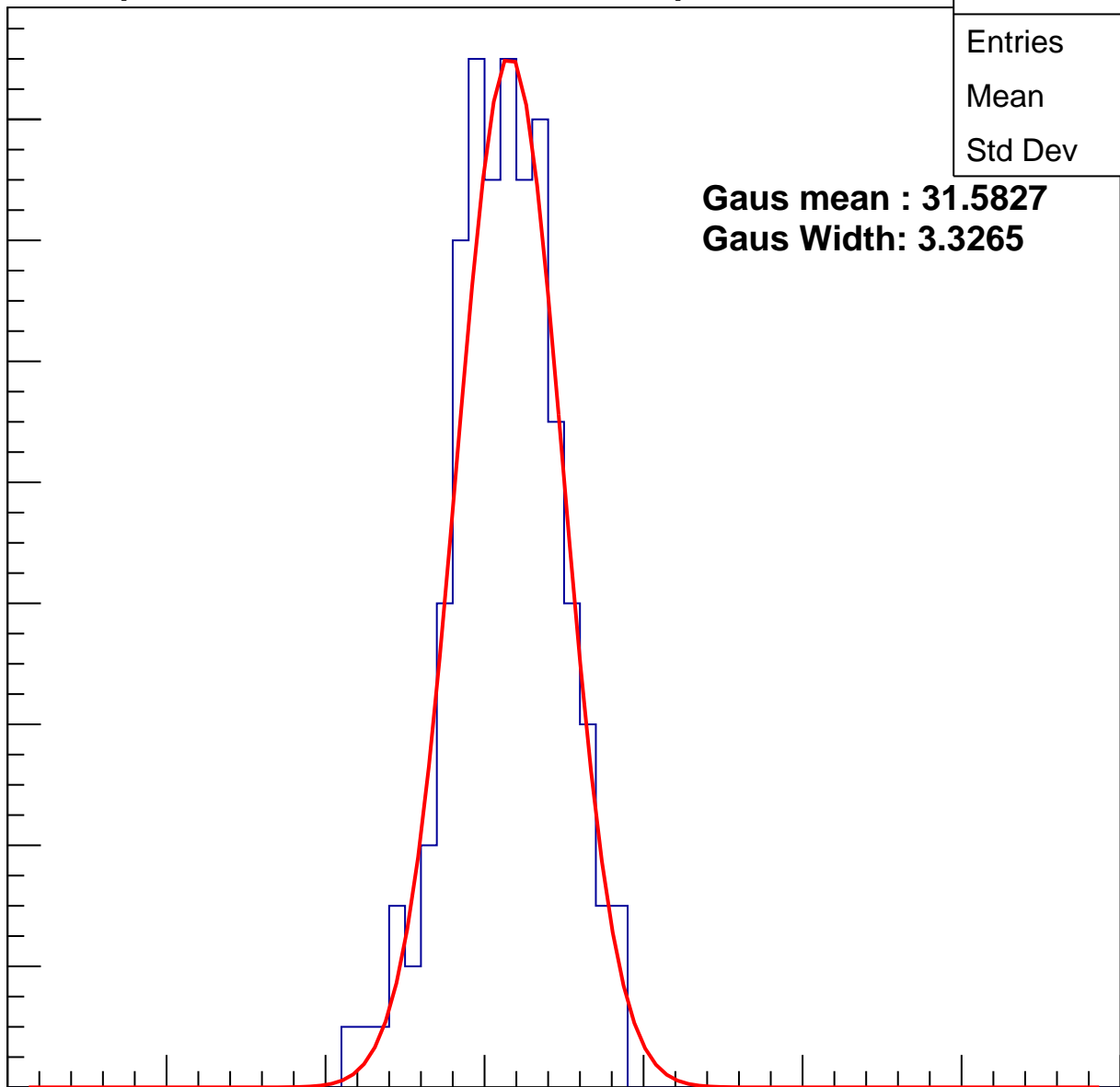
30.85

Std Dev

3.331

Gaus mean : 31.5827

Gaus Width: 3.3265



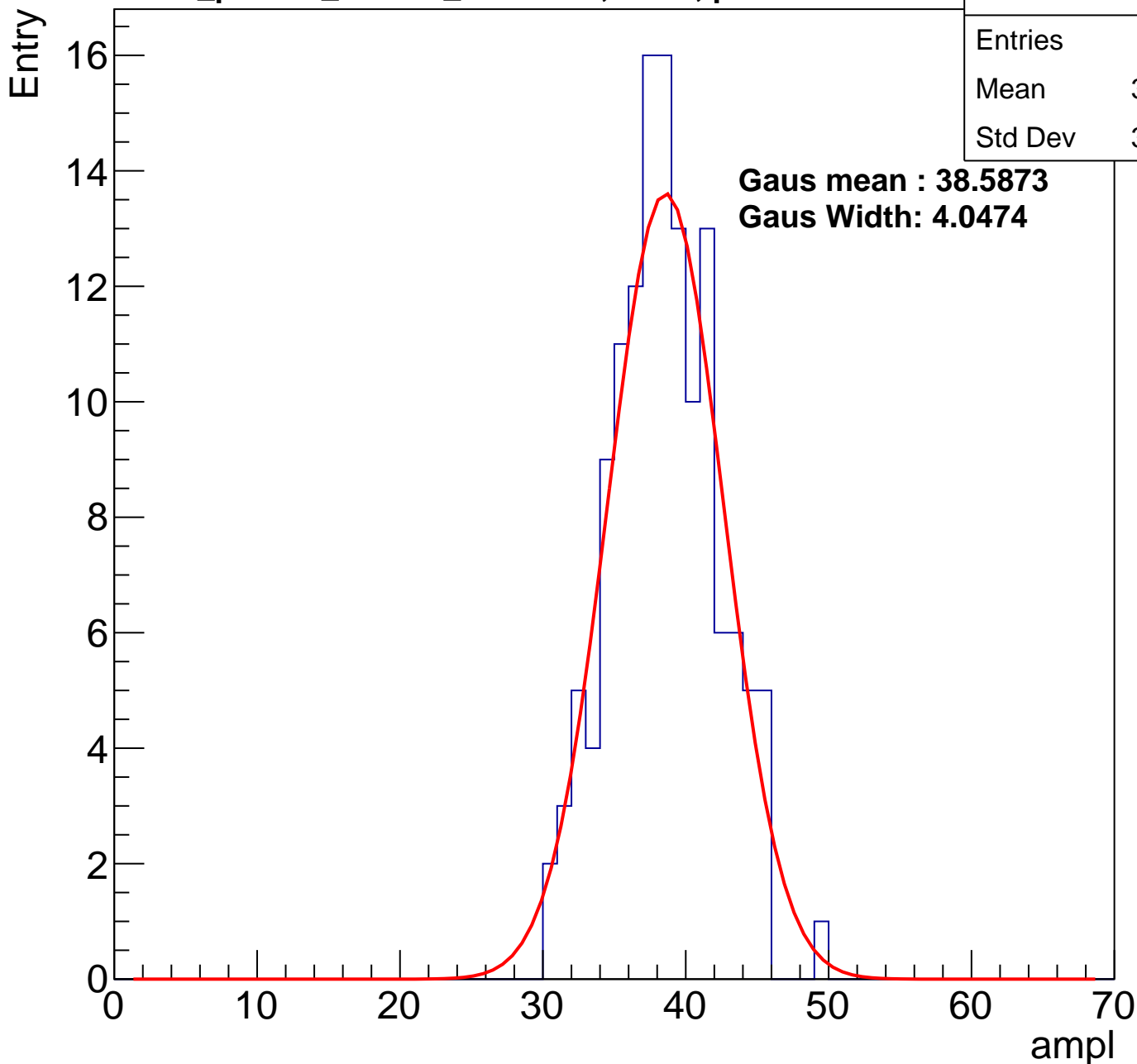
B1L001S, U25-ch49, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	38.04
Std Dev	3.649

Gaus mean : 38.5873

Gaus Width: 4.0474



B1L001S, U25-ch49, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	44.08
Std Dev	3.399

Gaus mean : 44.7937

Gaus Width: 3.1898

Entry

16
14
12
10
8
6
4
2
0

ampl

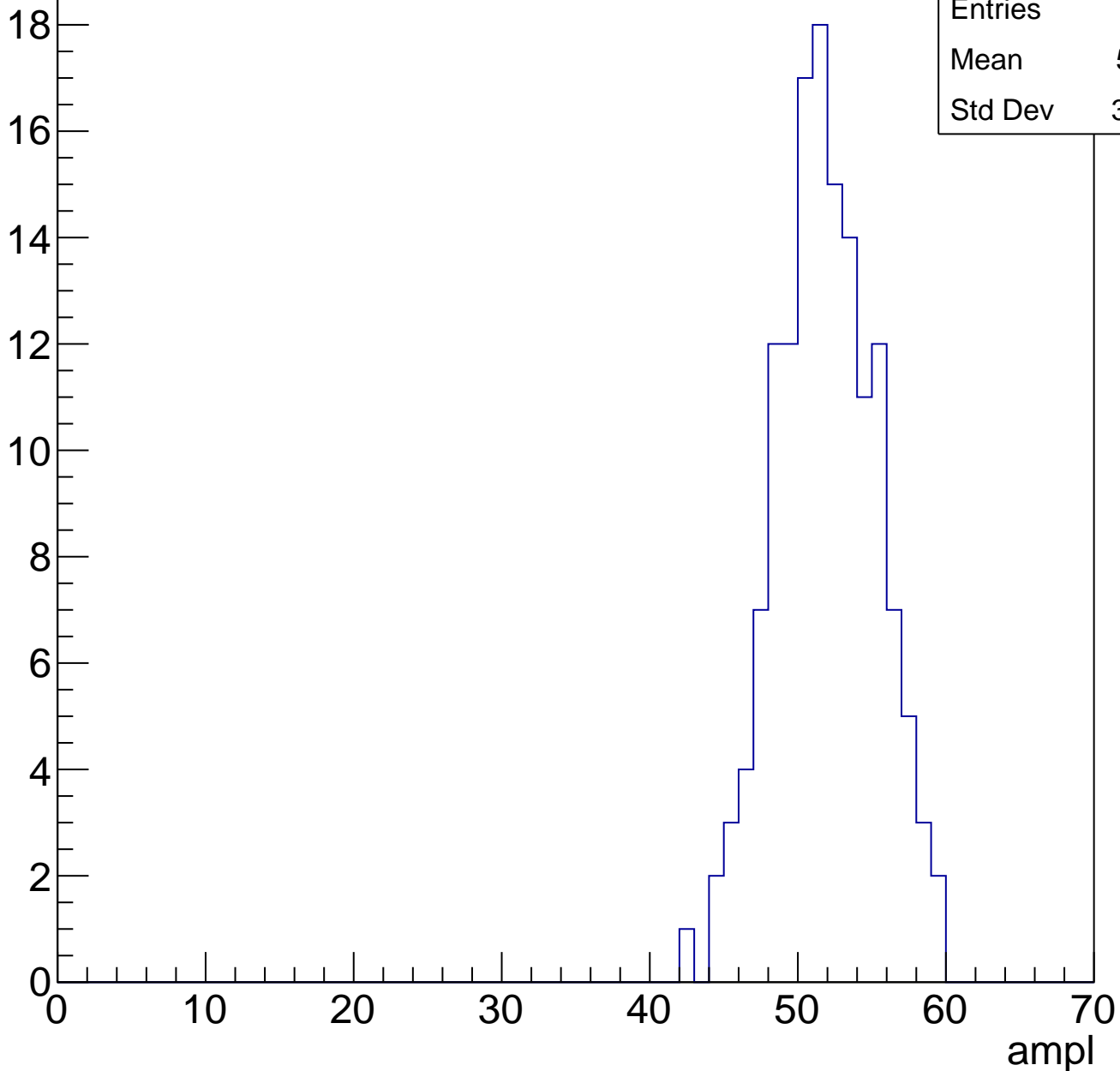
0 10 20 30 40 50 60 70

B1L001S, U25-ch49, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	51.41
Std Dev	3.362

Entry



B1L001S, U25-ch49, adc4

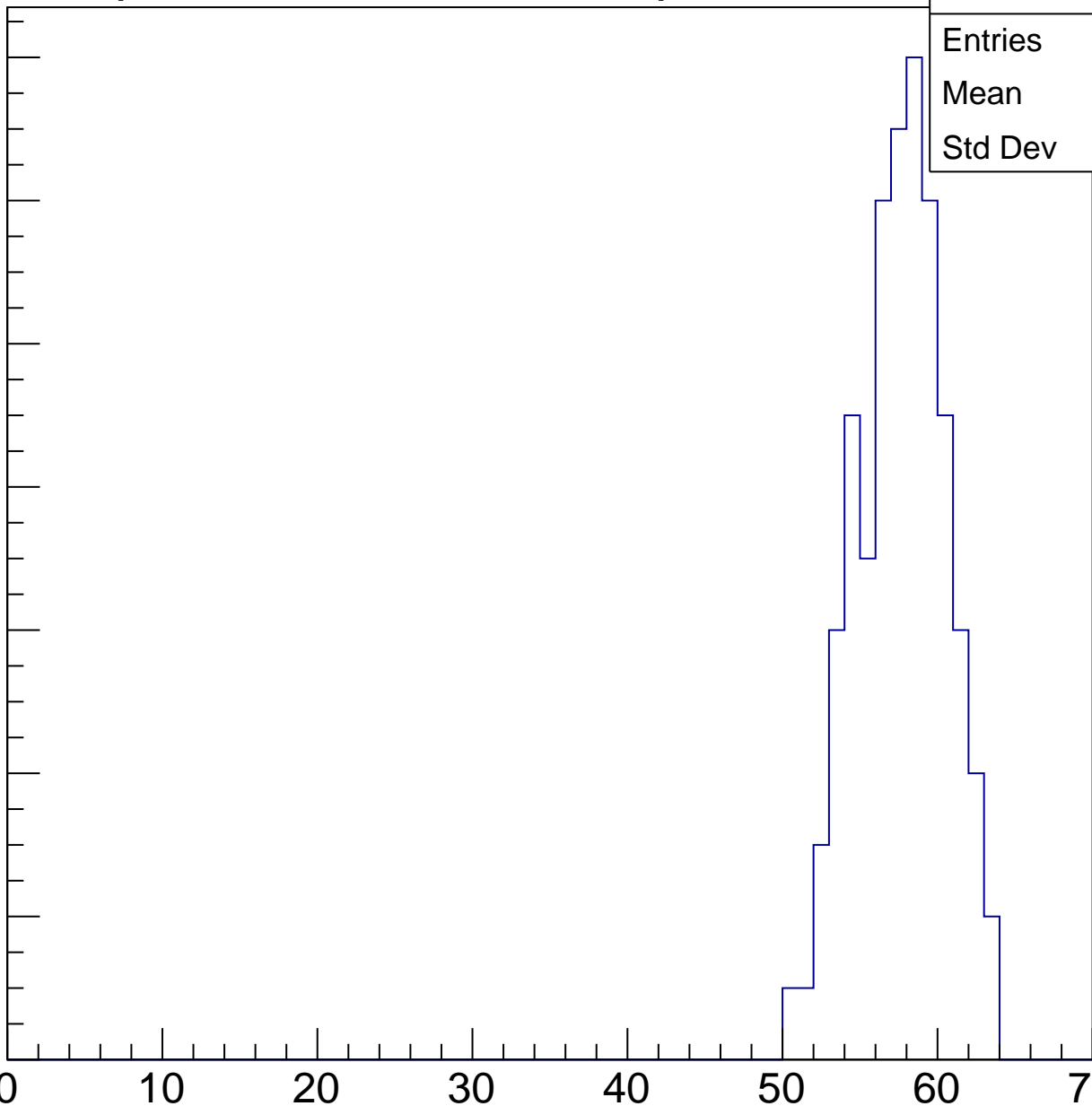
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	57.16
Std Dev	2.817

ampl



B1L001S, U25-ch49, adc5

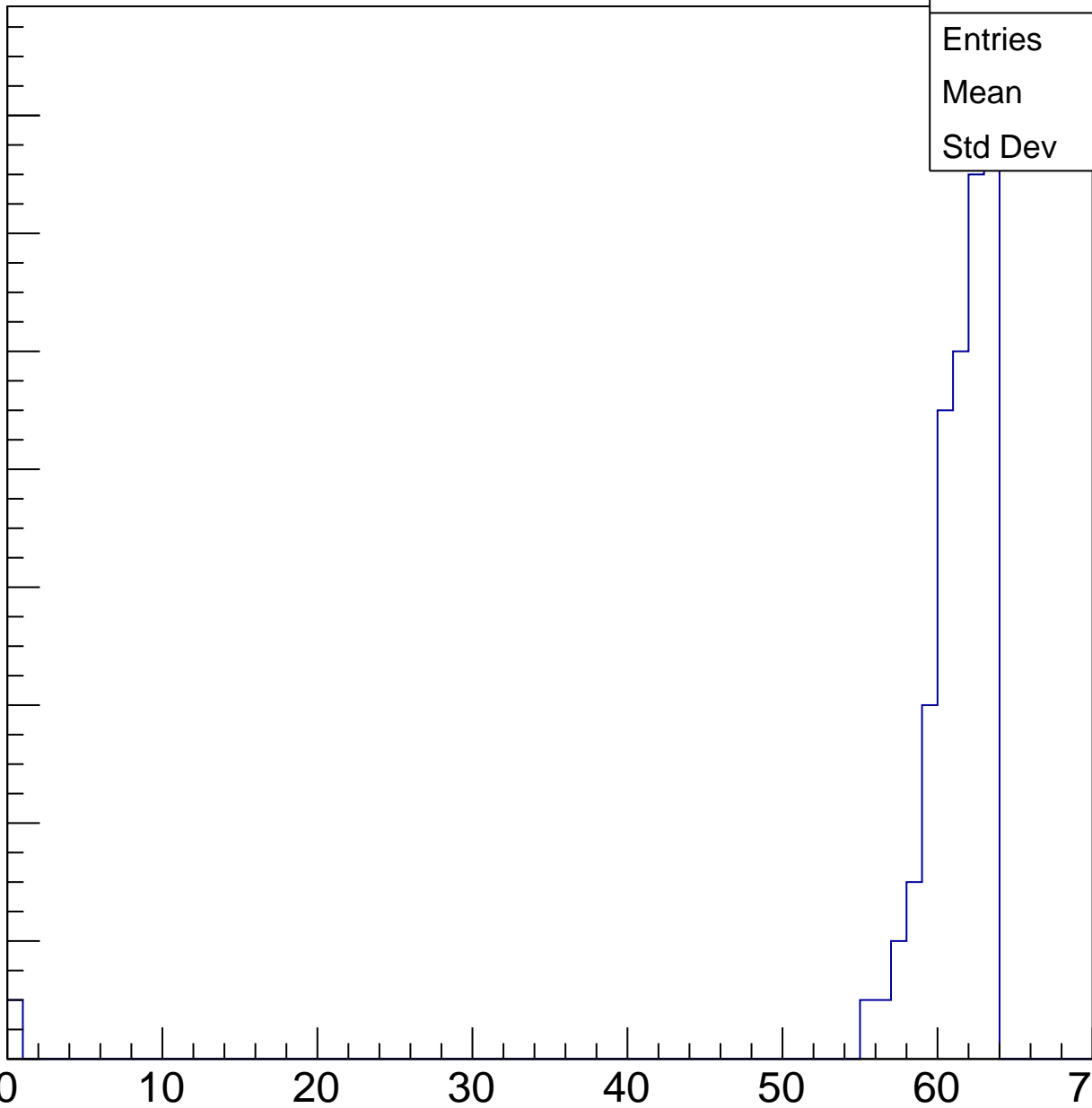
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	60.09
Std Dev	7.52

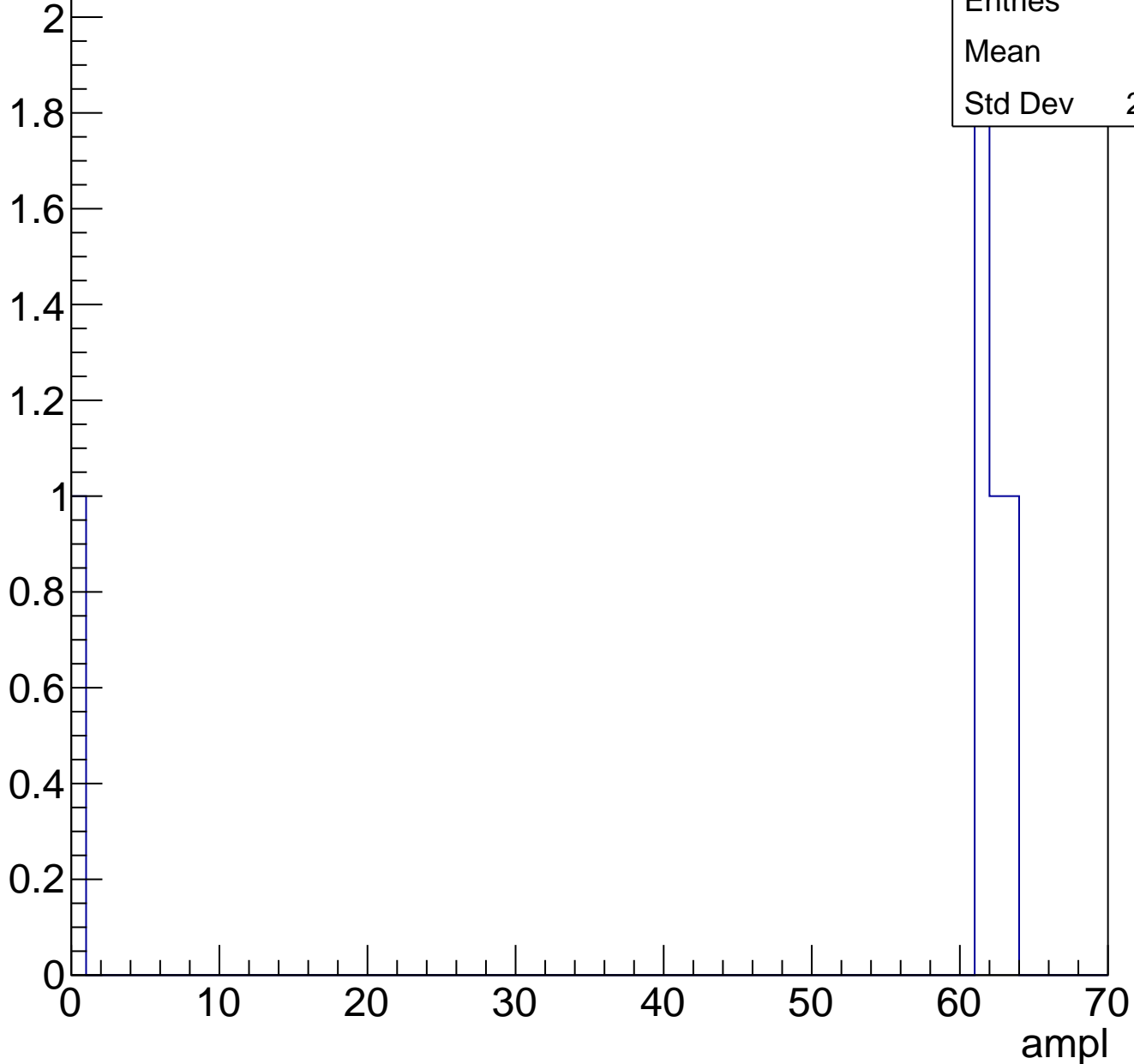
ampl



B1L001S, U25-ch49, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch49, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



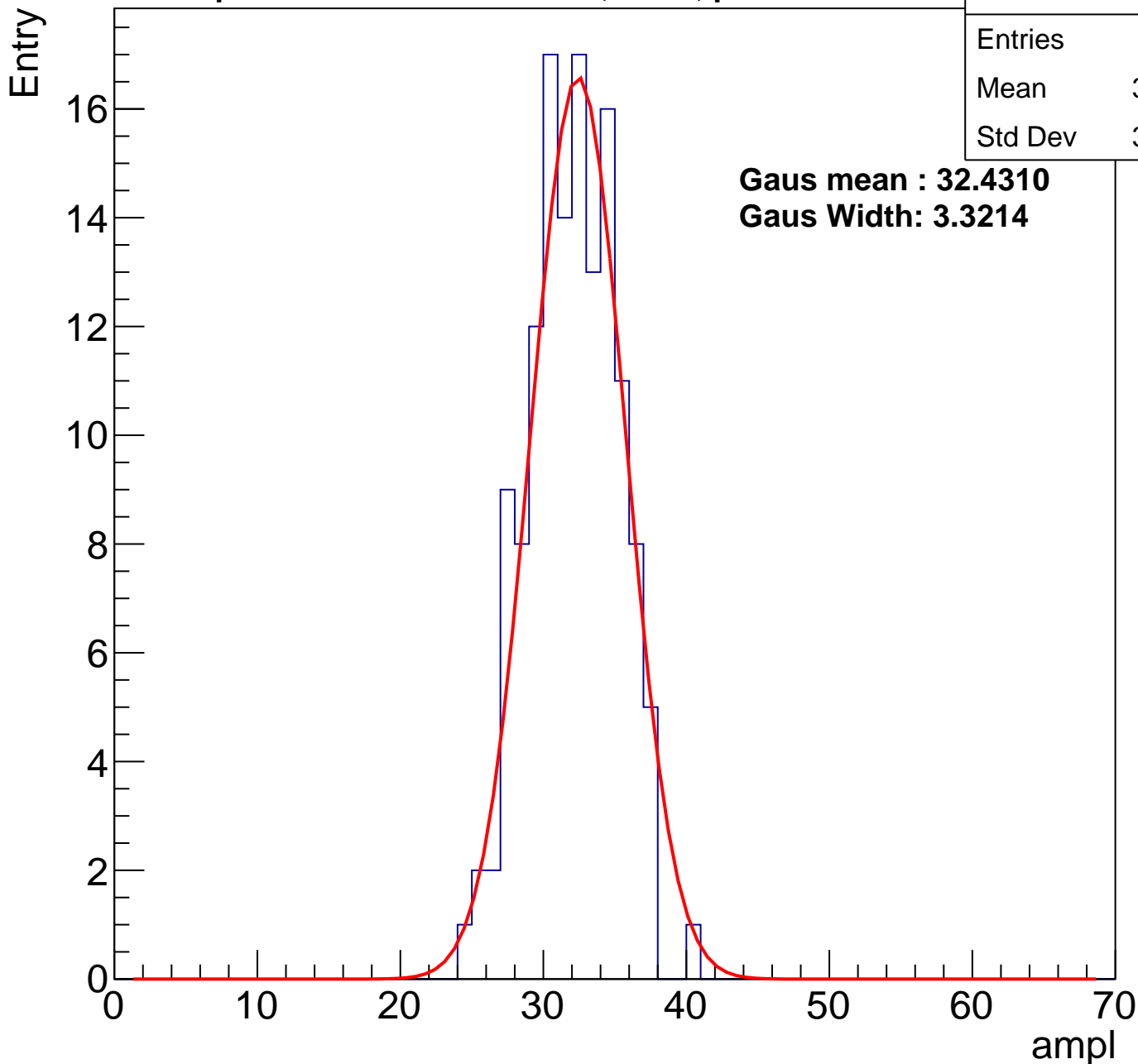
B1L001S, U25-ch50, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	31.62
Std Dev	3.032

Gaus mean : 32.4310

Gaus Width: 3.3214



B1L001S, U25-ch50, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	38.03
Std Dev	3.124

Gaus mean : 38.7704

Gaus Width: 3.6231

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch50, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

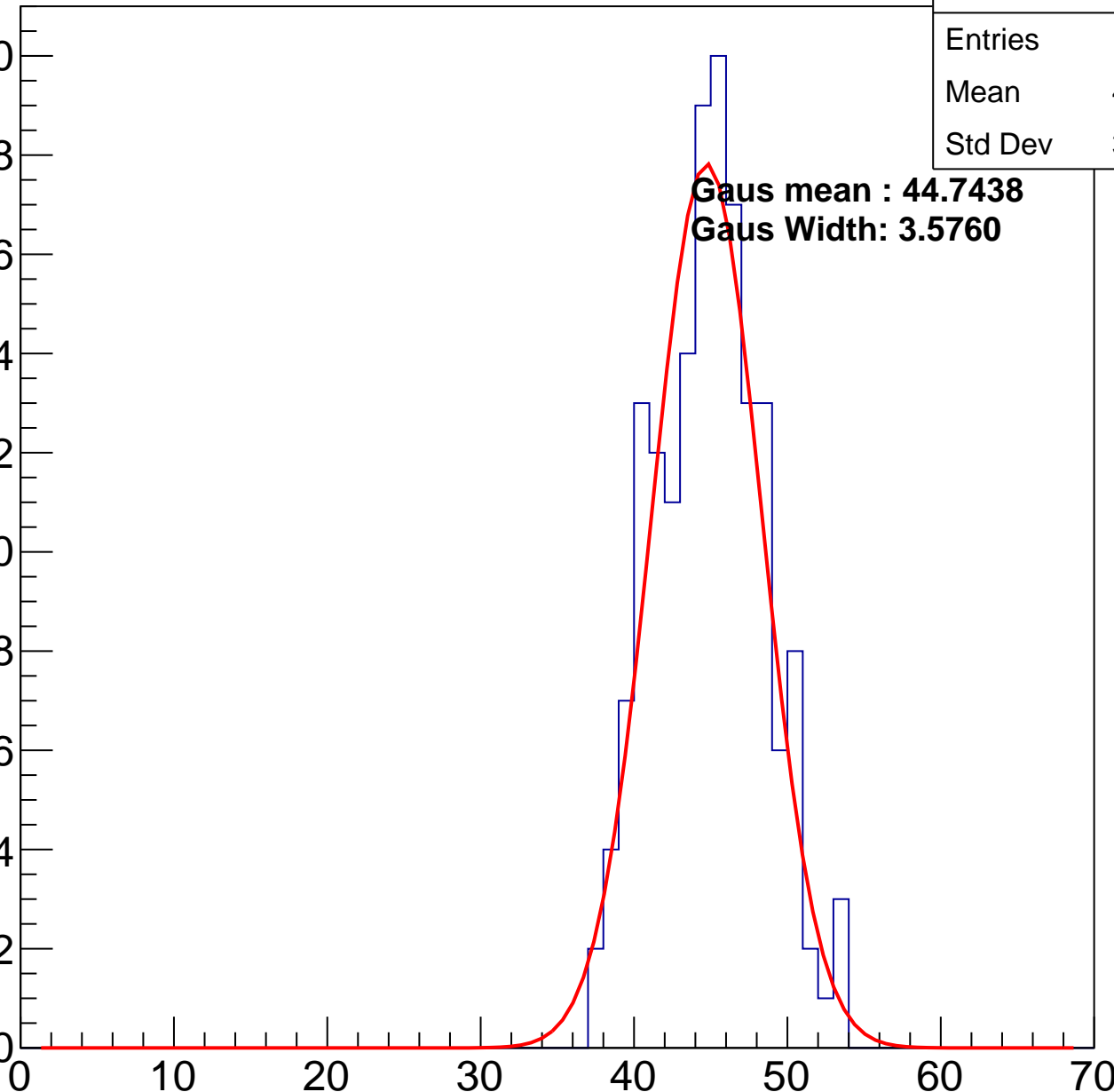
20
18
16
14
12
10
8
6
4
2
0

Entries	165
Mean	44.45
Std Dev	3.493

Gaus mean : 44.7438

Gaus Width: 3.5760

ampl



B1L001S, U25-ch50, adc3

calib_packv5_042523_0143.root, FC#2, port C2

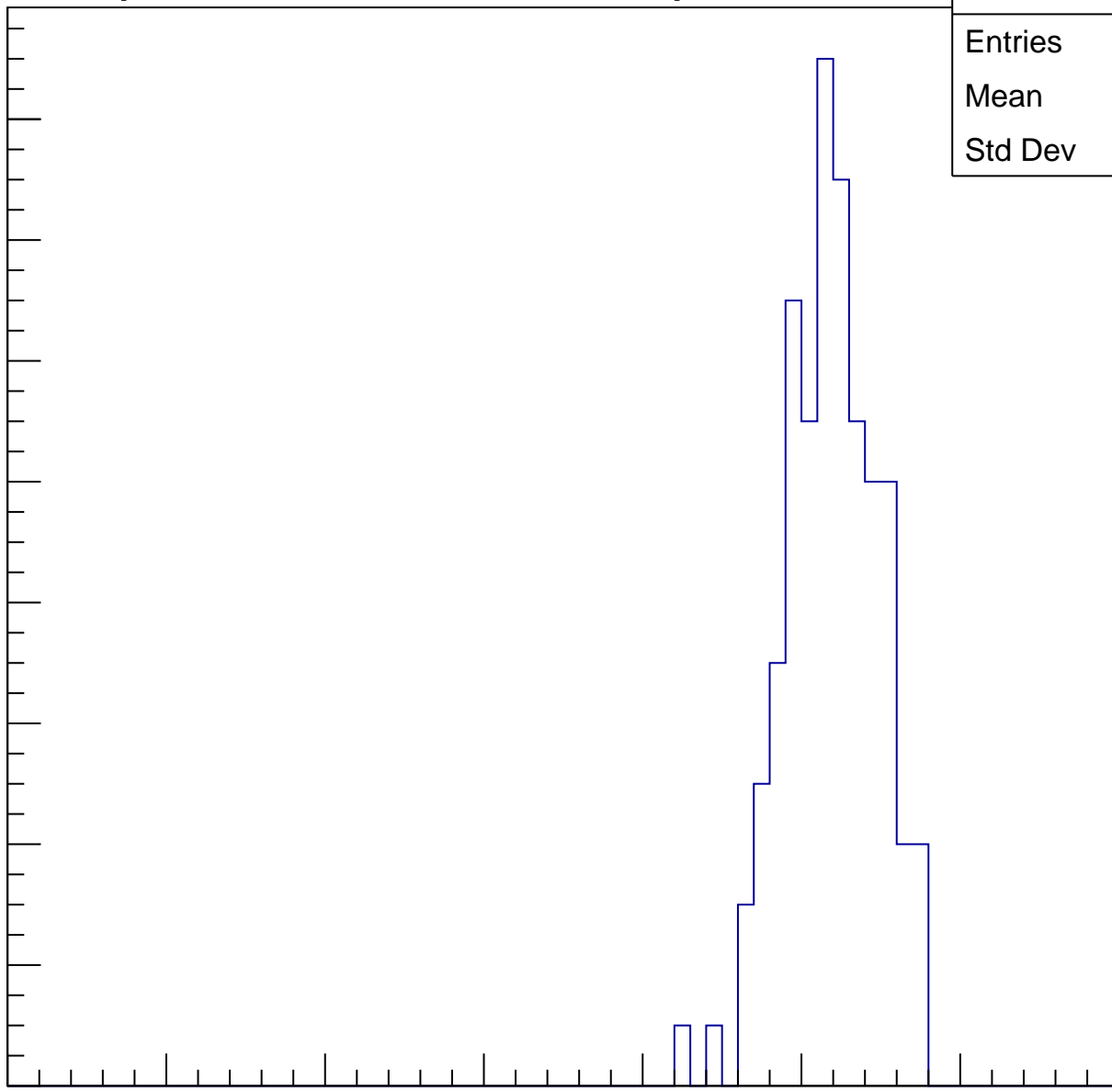
Entries	112
Mean	51.38
Std Dev	2.91

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch50, adc4

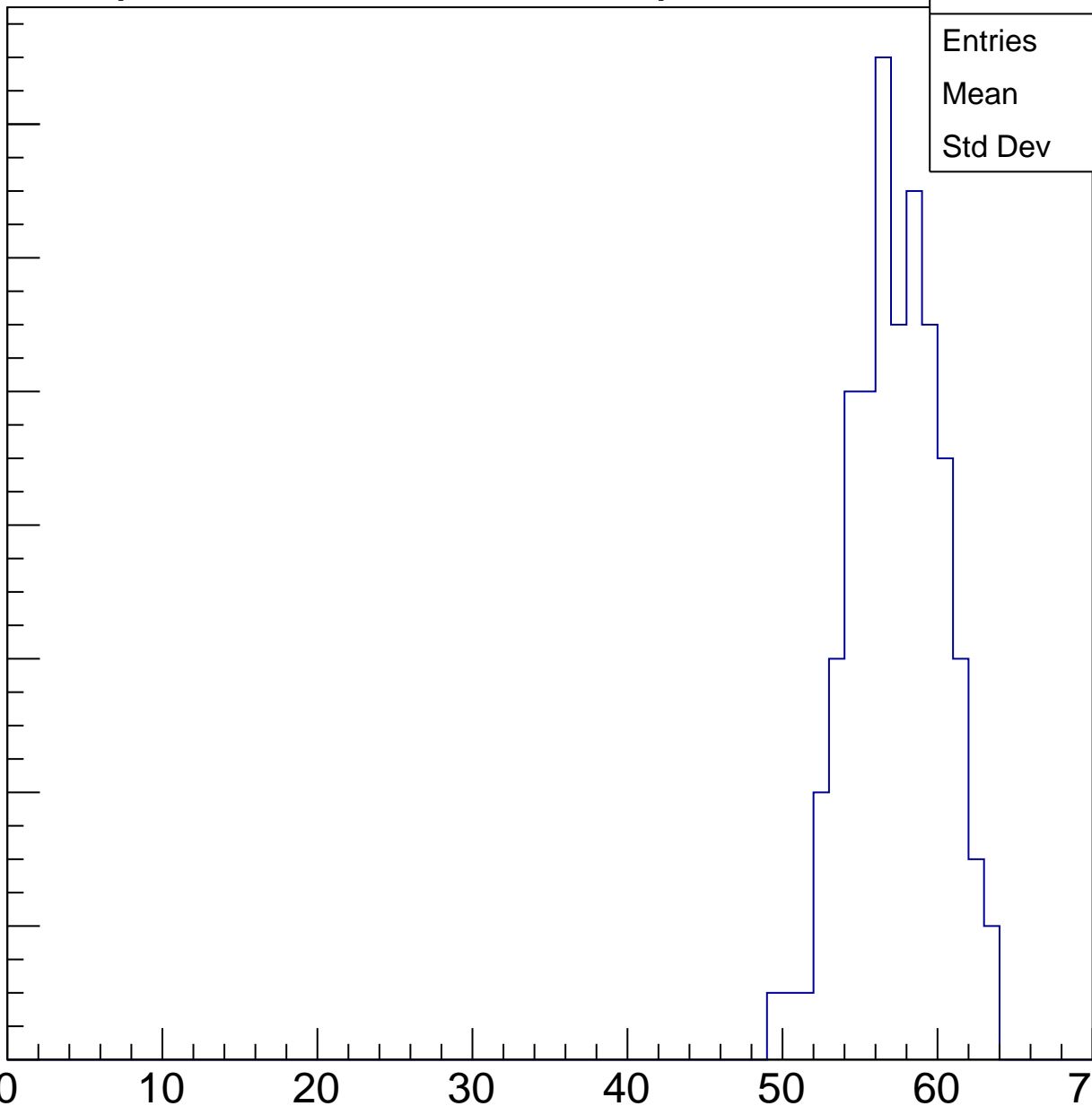
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	103
Mean	56.83
Std Dev	2.903

ampl



B1L001S, U25-ch50, adc5

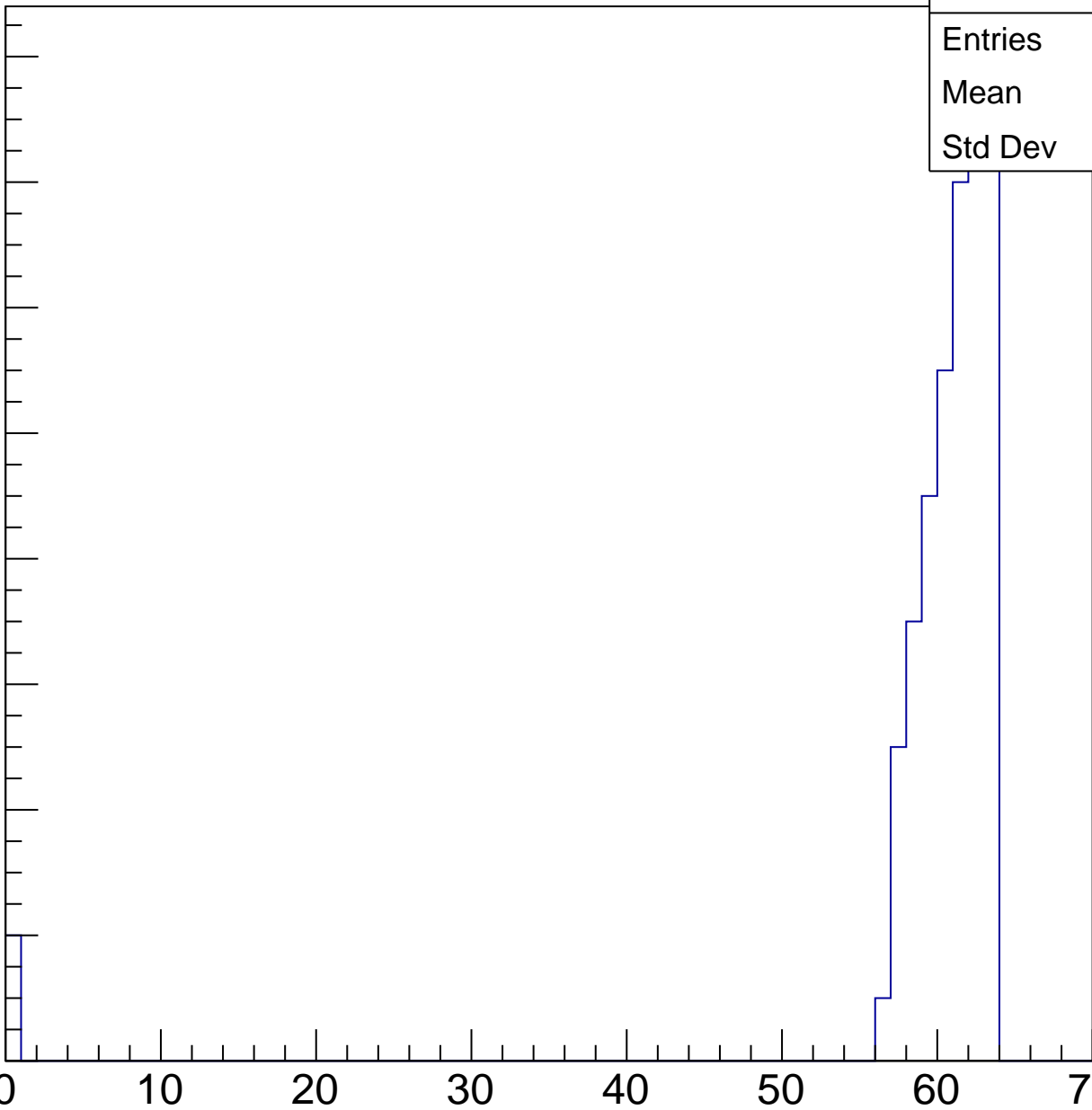
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.11
Std Dev	9.649

ampl



B1L001S, U25-ch50, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch50, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch51, adc0

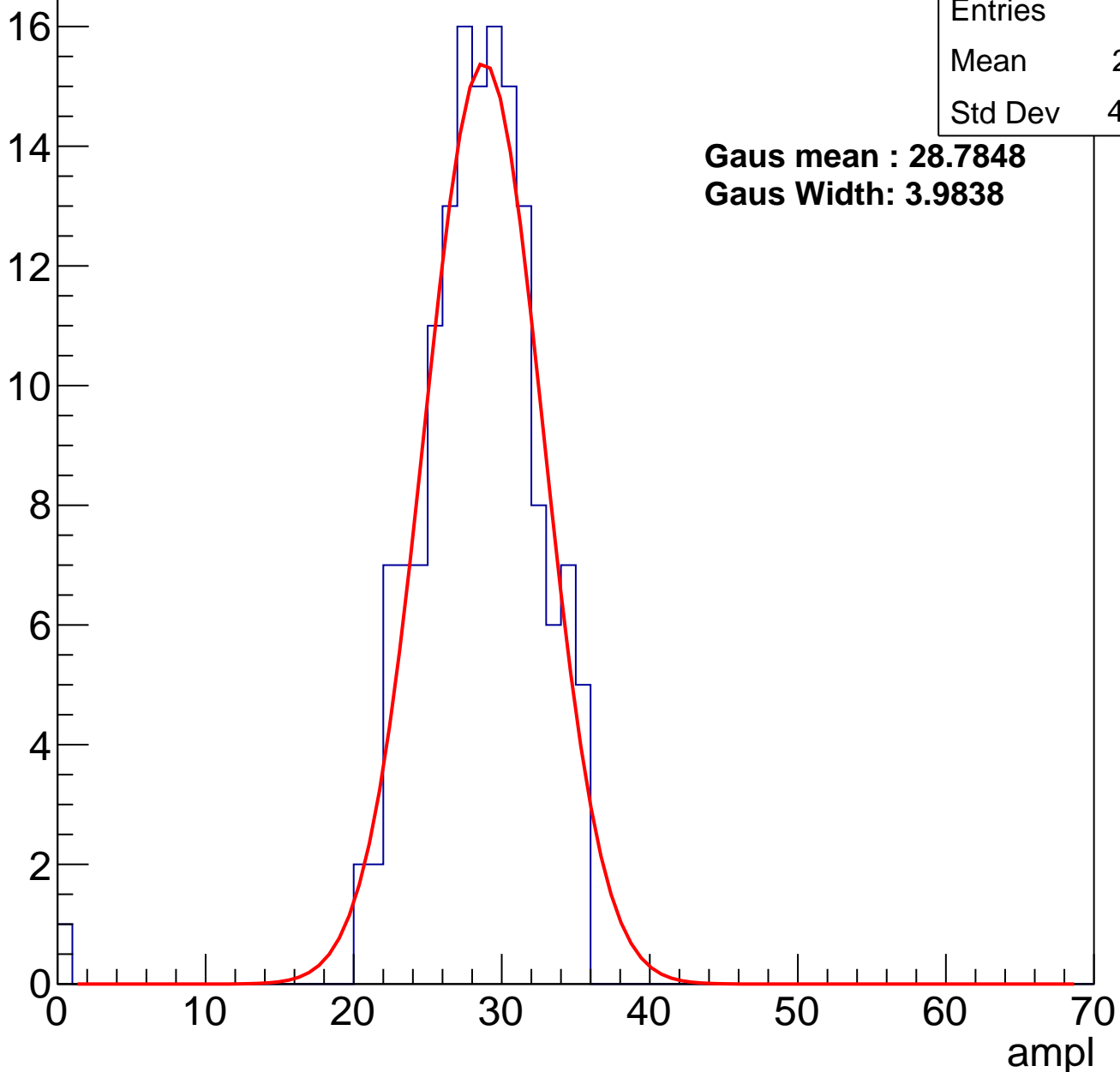
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	27.91
Std Dev	4.215

Gaus mean : 28.7848

Gaus Width: 3.9838

Entry



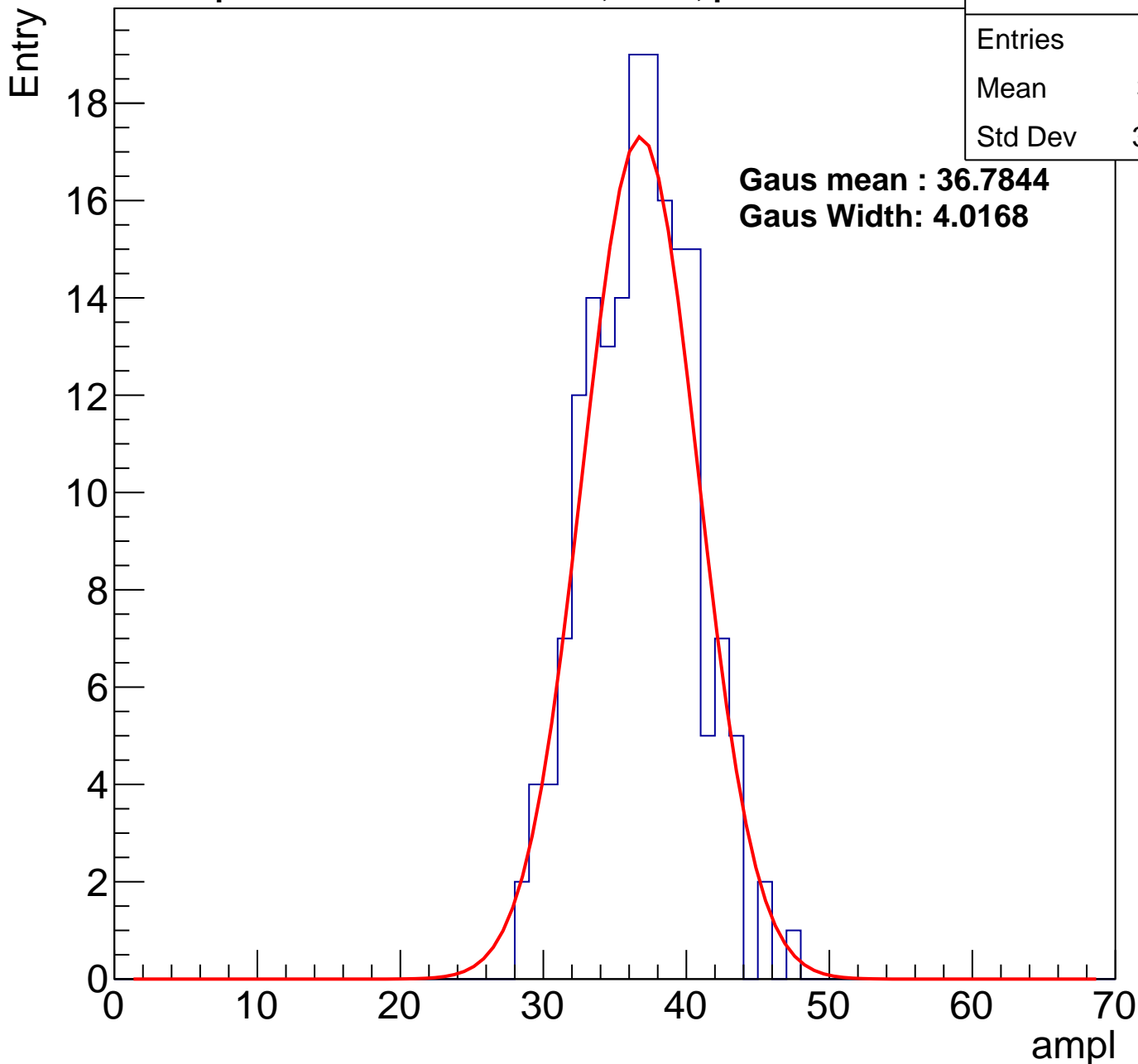
B1L001S, U25-ch51, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	174
Mean	36.31
Std Dev	3.693

Gaus mean : 36.7844

Gaus Width: 4.0168



B1L001S, U25-ch51, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	43.16
Std Dev	3.065

Gaus mean : 43.8084

Gaus Width: 3.1374

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

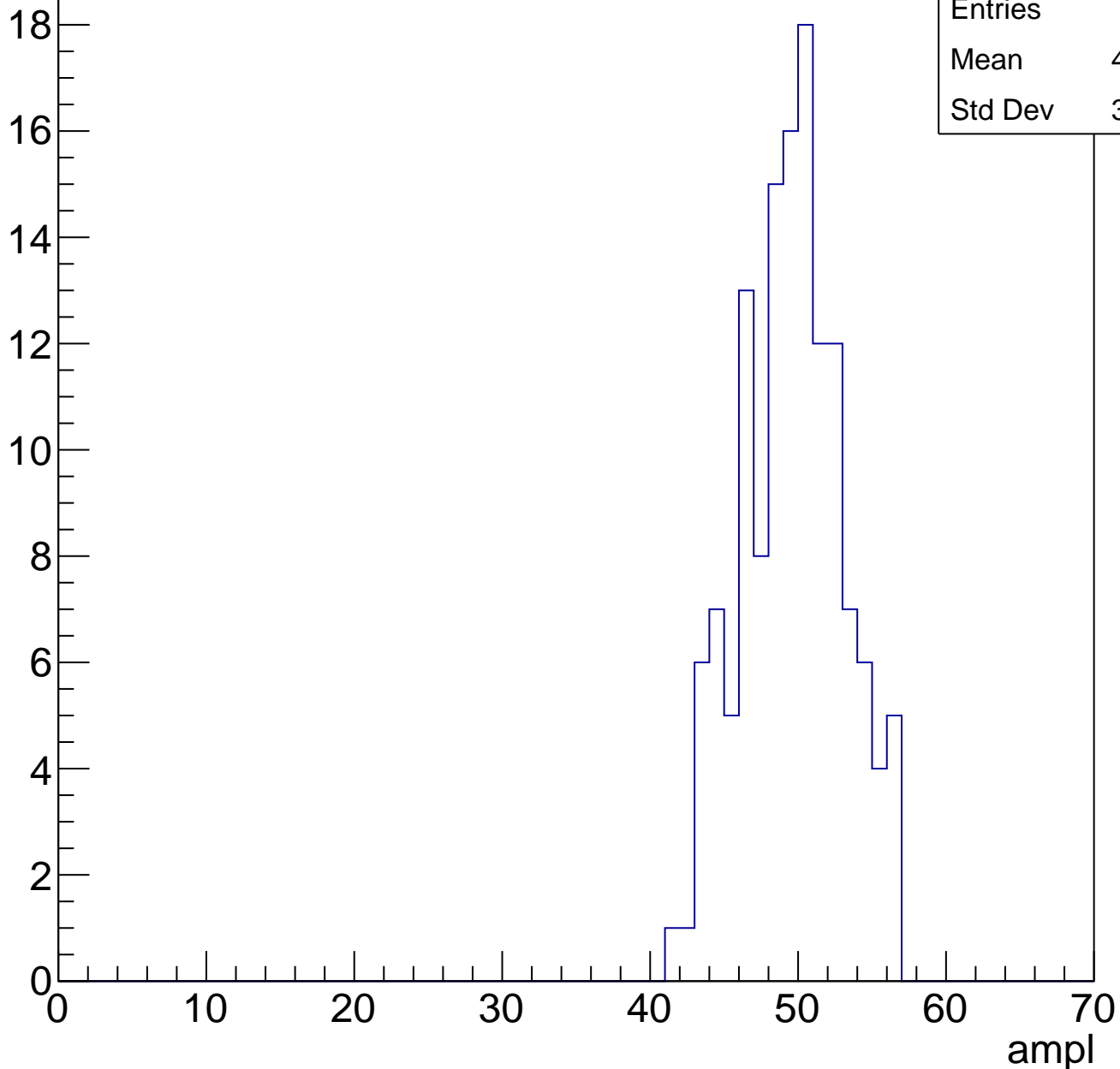
70

B1L001S, U25-ch51, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	49.14
Std Dev	3.398

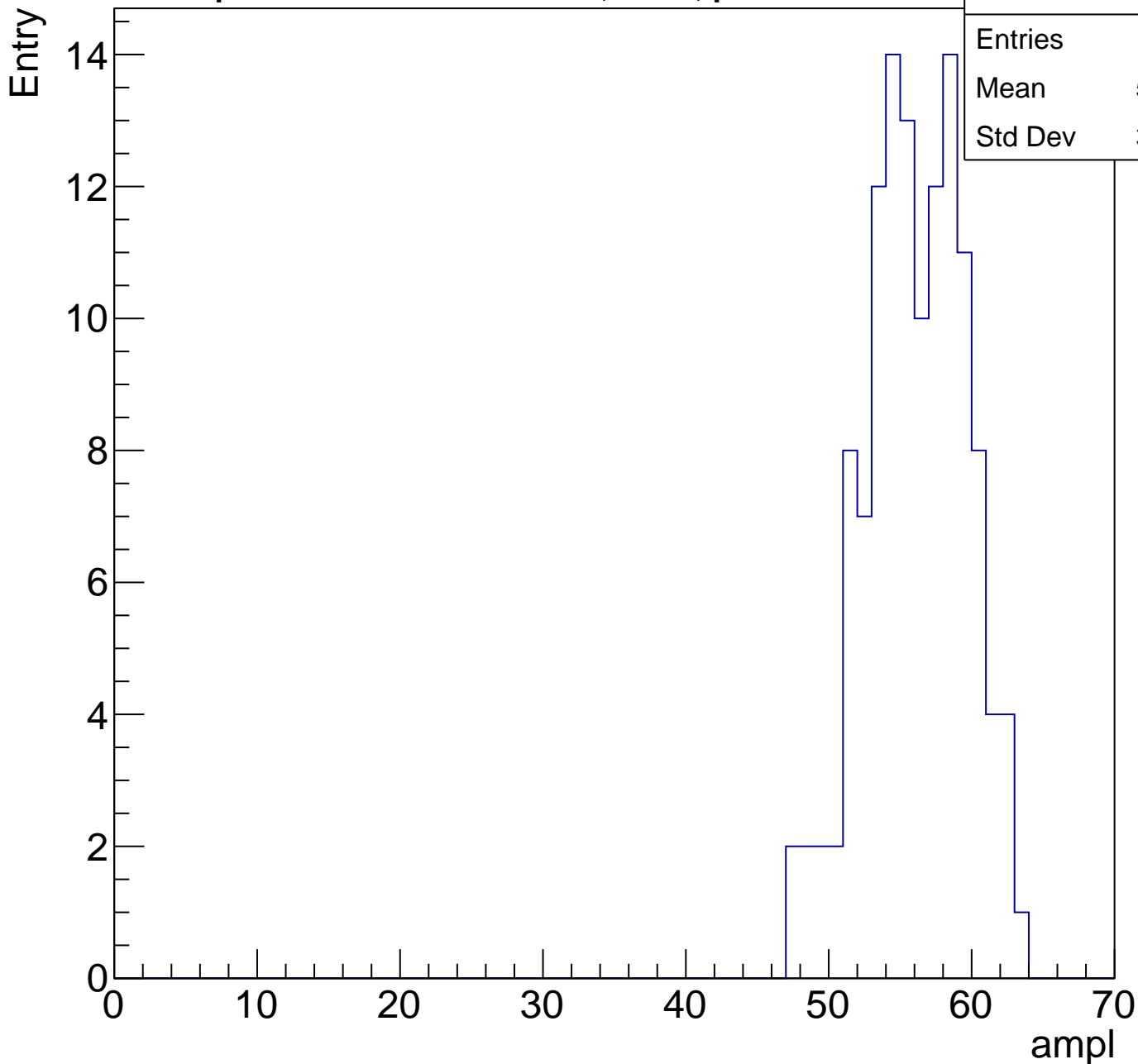
Entry



B1L001S, U25-ch51, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	55.61
Std Dev	3.471



B1L001S, U25-ch51, adc5

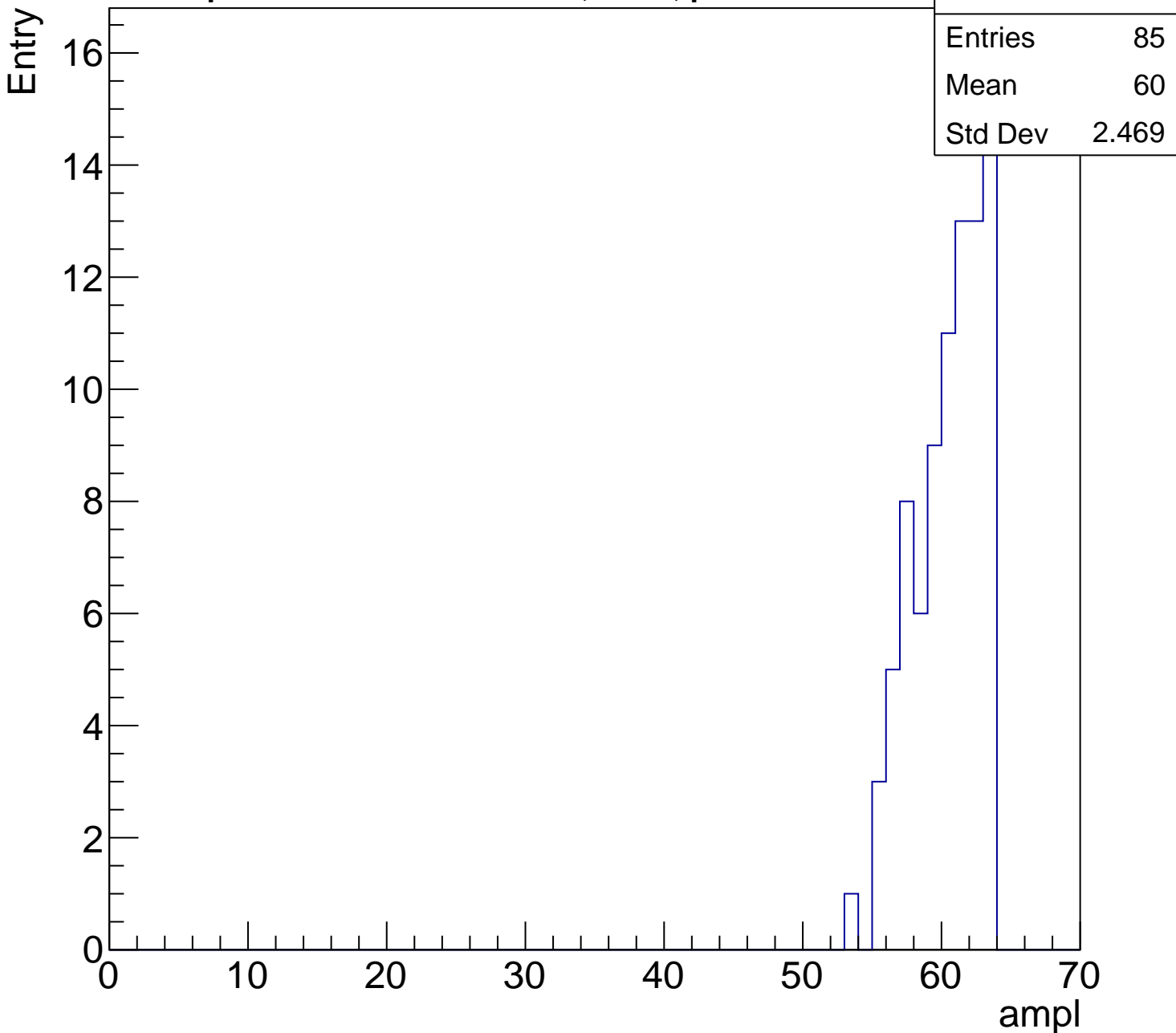
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	60
Std Dev	2.469

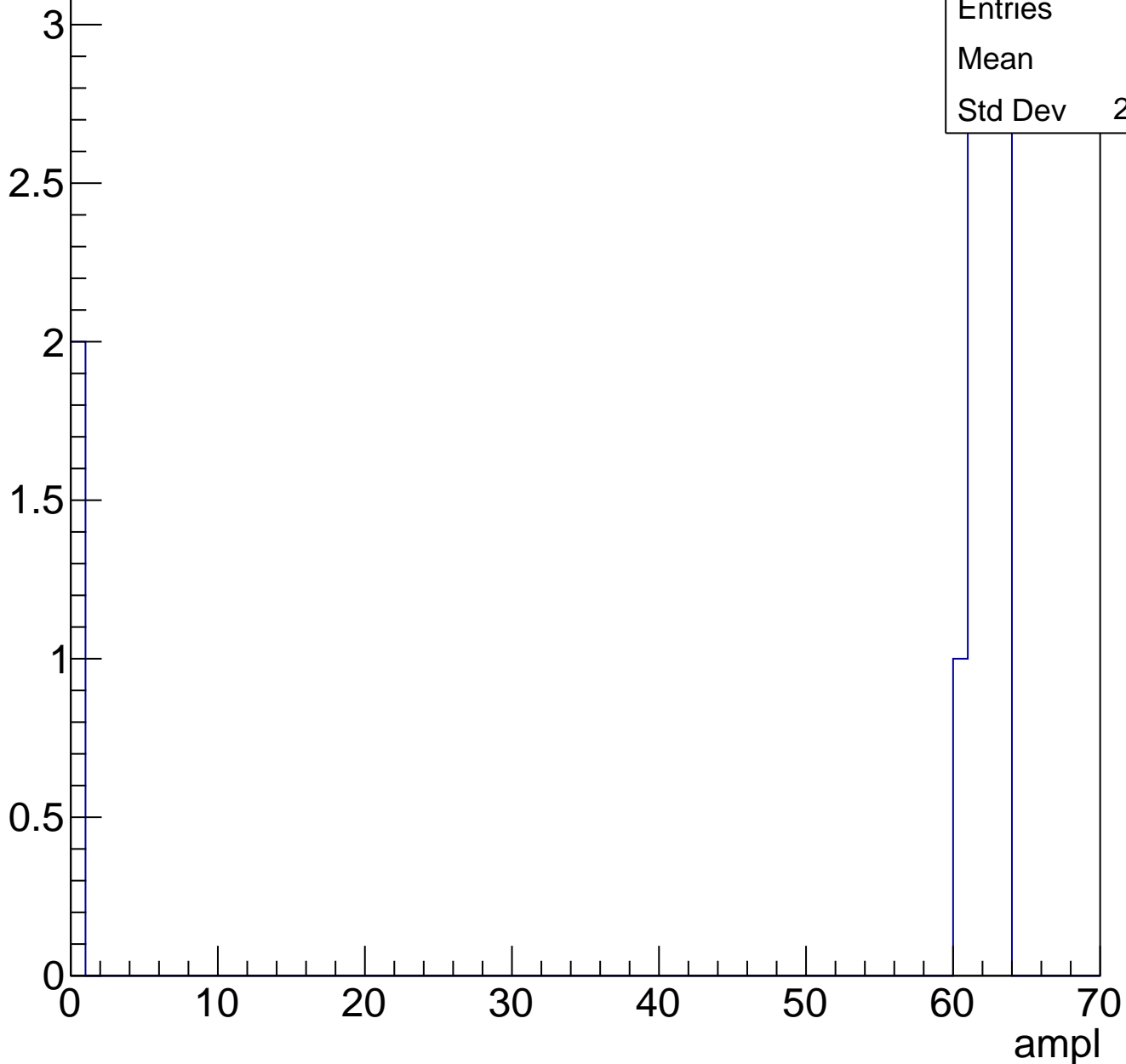
ampl



B1L001S, U25-ch51, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch51, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch52, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

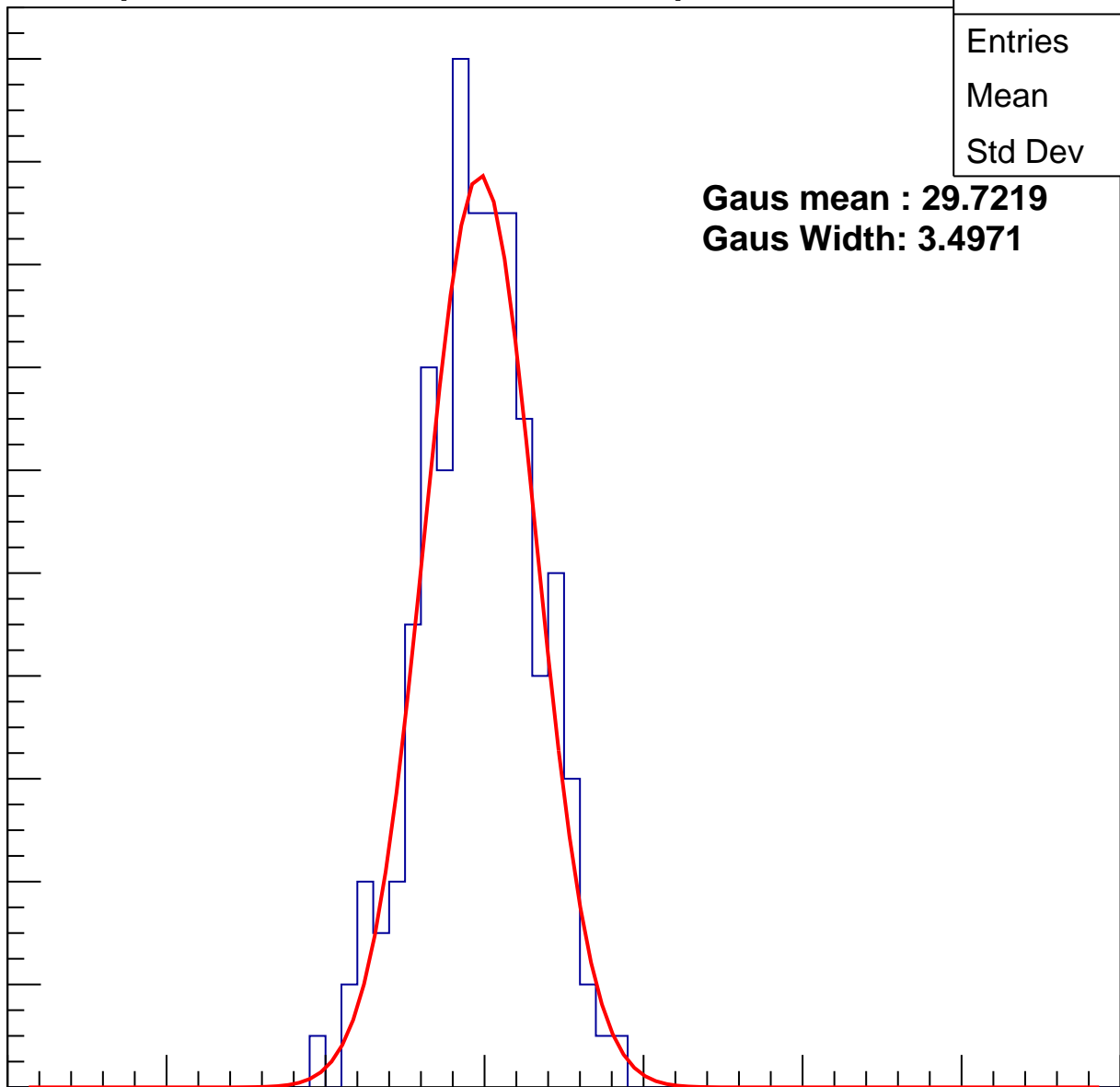
Entries	161
Mean	29.16
Std Dev	3.529

Gaus mean : 29.7219

Gaus Width: 3.4971

0 10 20 30 40 50 60 70

ampl

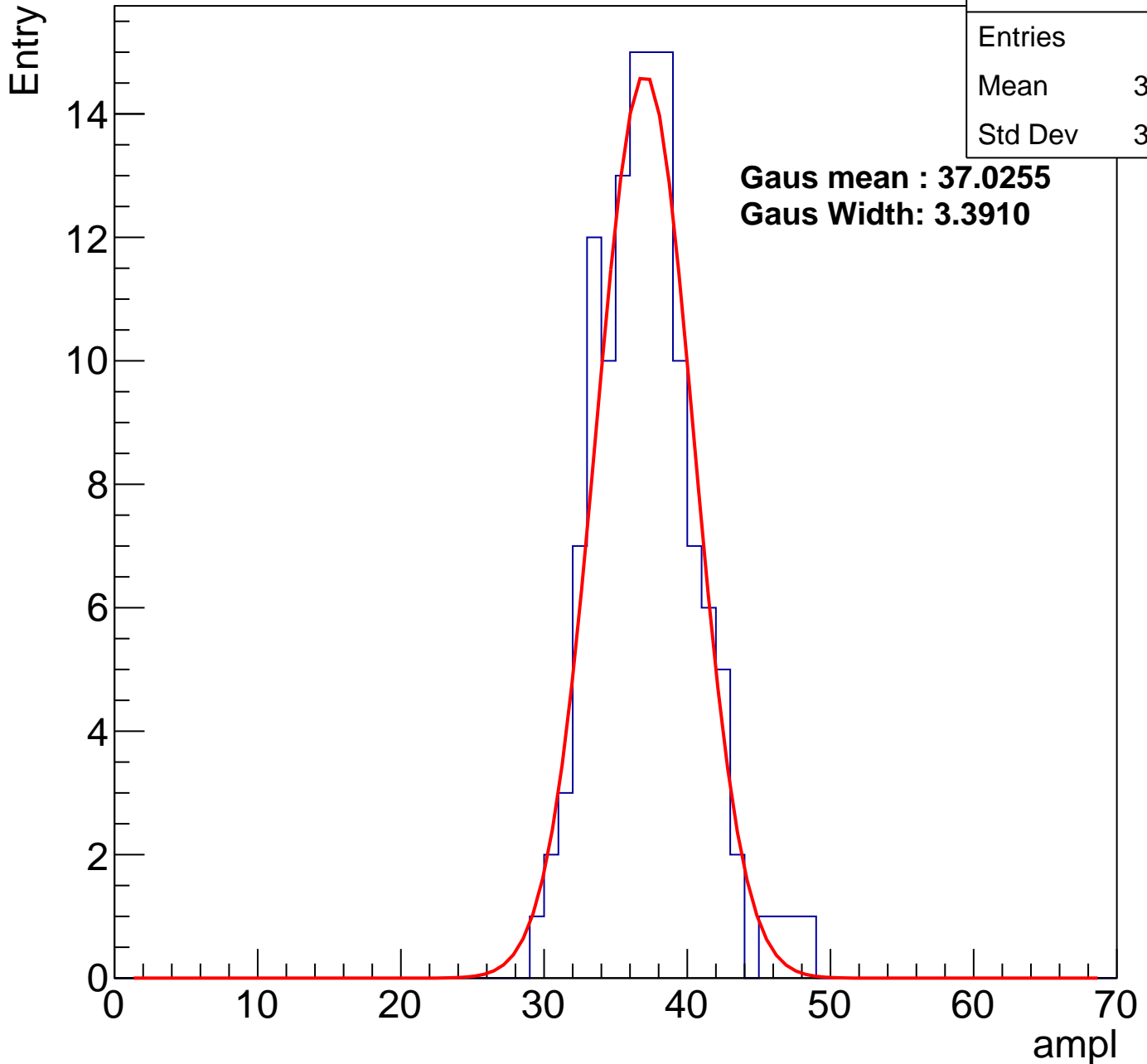


B1L001S, U25-ch52, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.69
Std Dev	3.504

Gaus mean : 37.0255
Gaus Width: 3.3910



B1L001S, U25-ch52, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

139

Mean

42.94

Std Dev

3.456

Gaus mean : 43.2614

Gaus Width: 3.4348

0

2

4

6

8

10

12

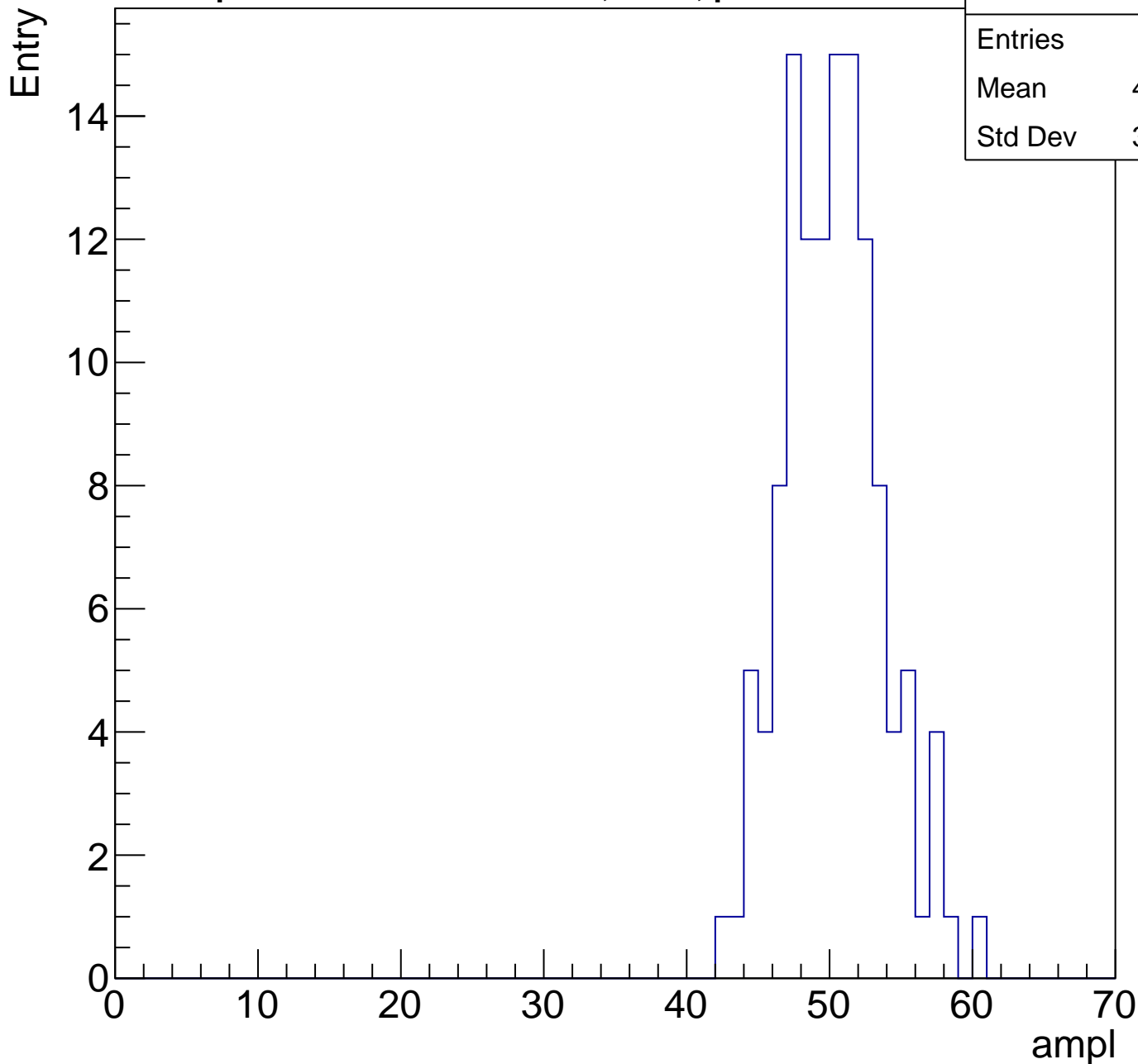
14

16

B1L001S, U25-ch52, adc3

calib_packv5_042523_0143.root, FC#2, port C2

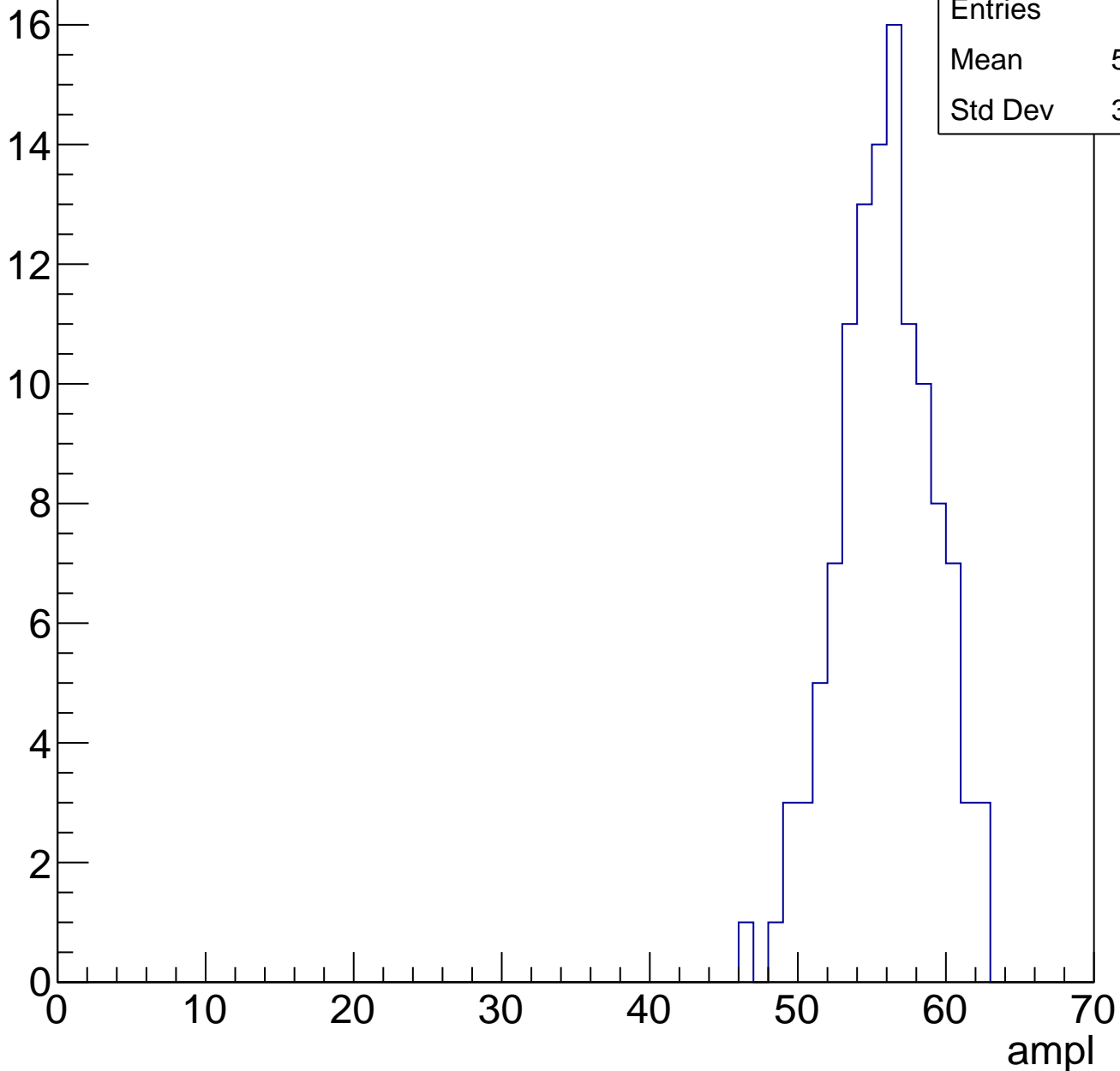
Entries	124
Mean	49.82
Std Dev	3.398



B1L001S, U25-ch52, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch52, adc5

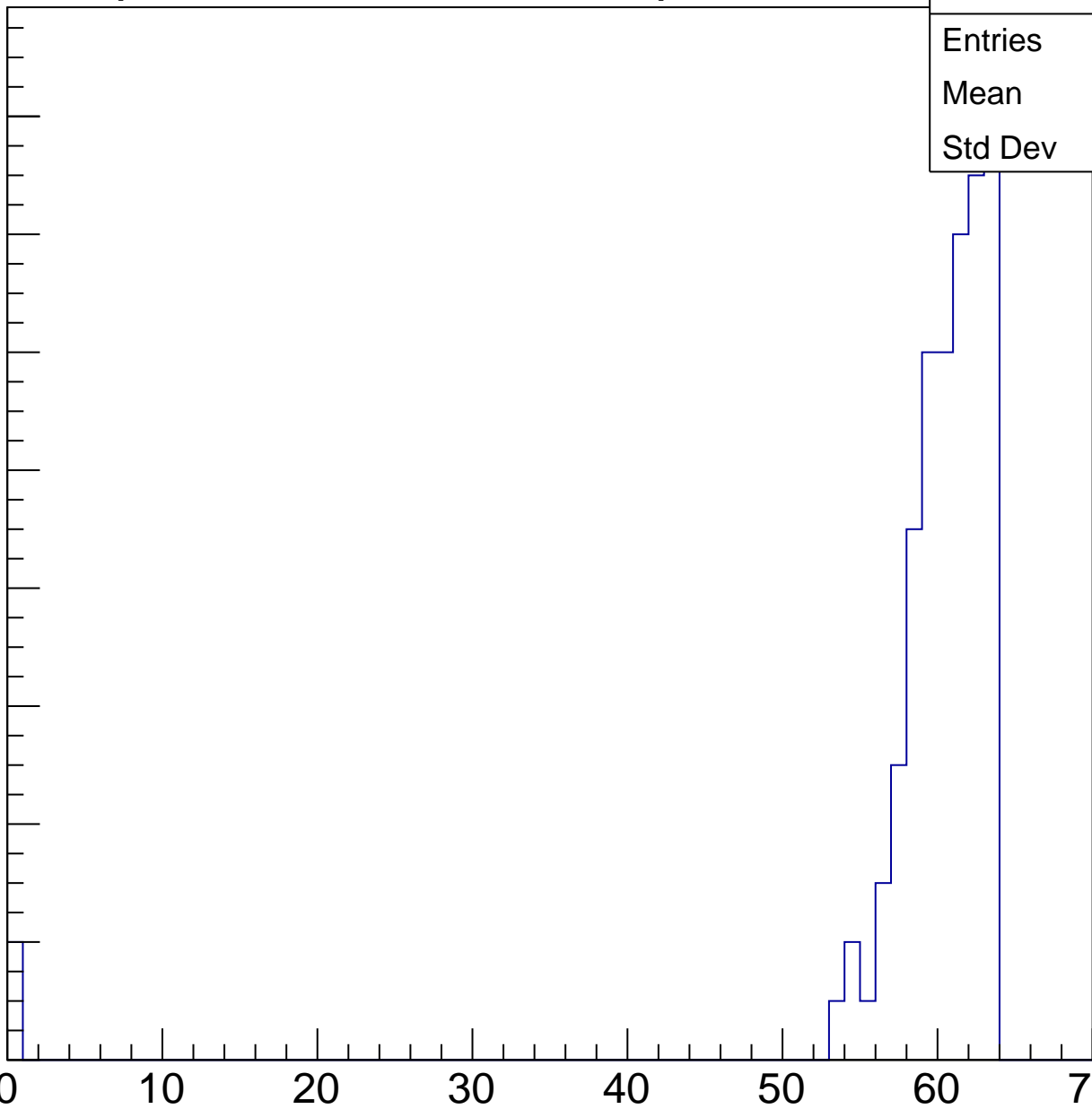
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.86
Std Dev	9.036

ampl



B1L001S, U25-ch52, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch52, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L001S, U25-ch53, adc0

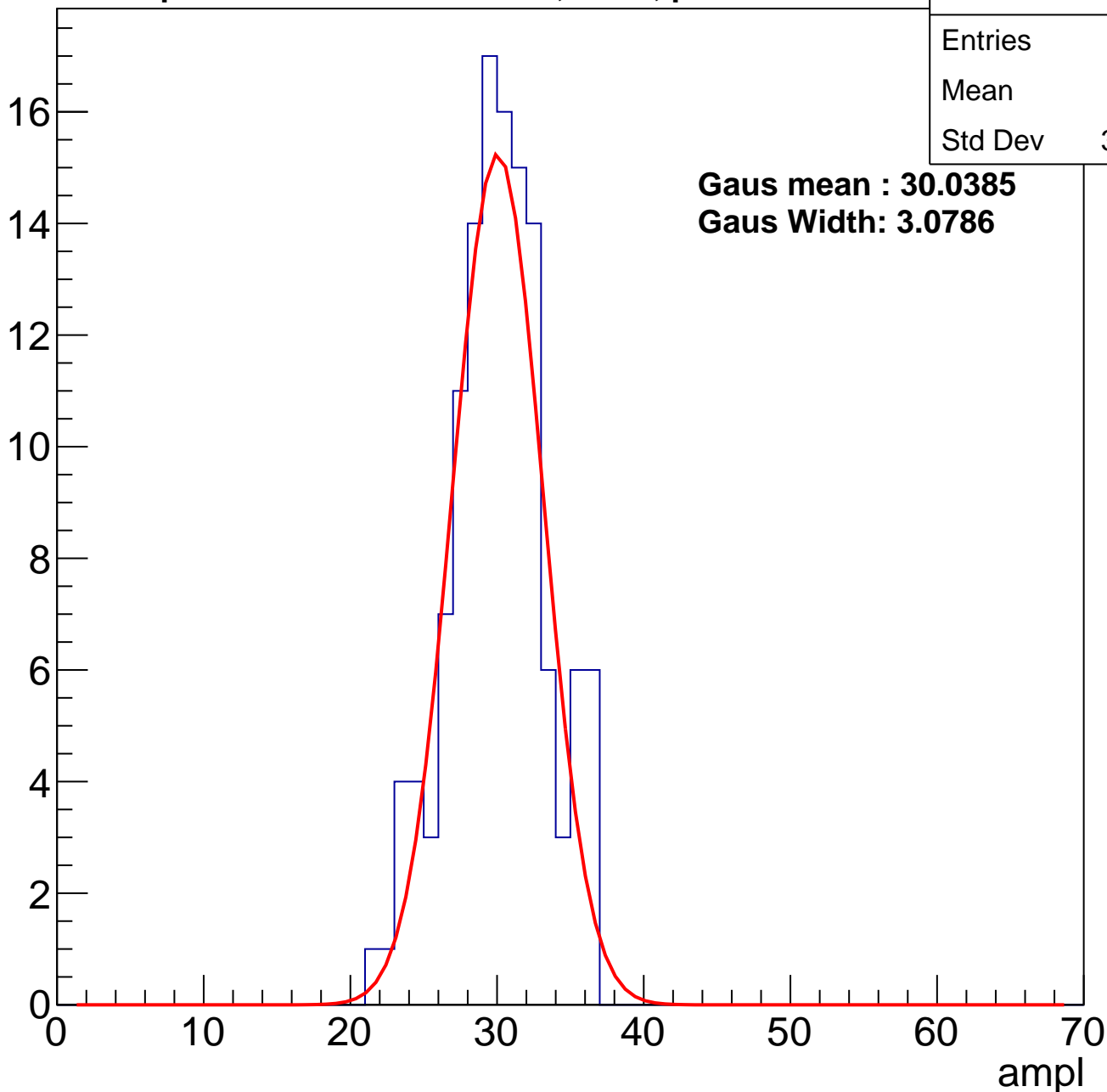
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	29.6
Std Dev	3.263

Gaus mean : 30.0385

Gaus Width: 3.0786

Entry



B1L001S, U25-ch53, adc1

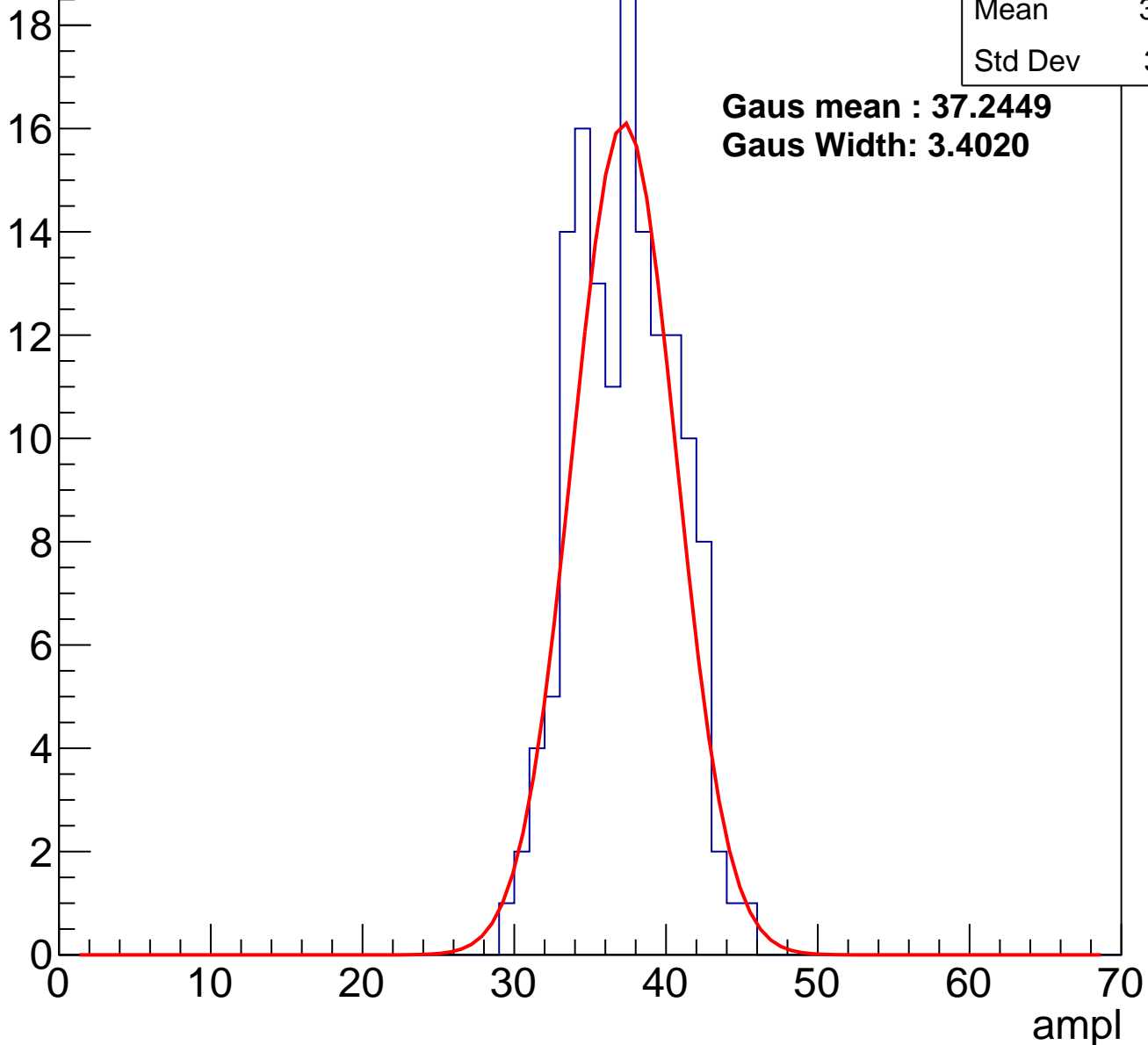
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	36.79
Std Dev	3.291

Gaus mean : 37.2449

Gaus Width: 3.4020

Entry



B1L001S, U25-ch53, adc2

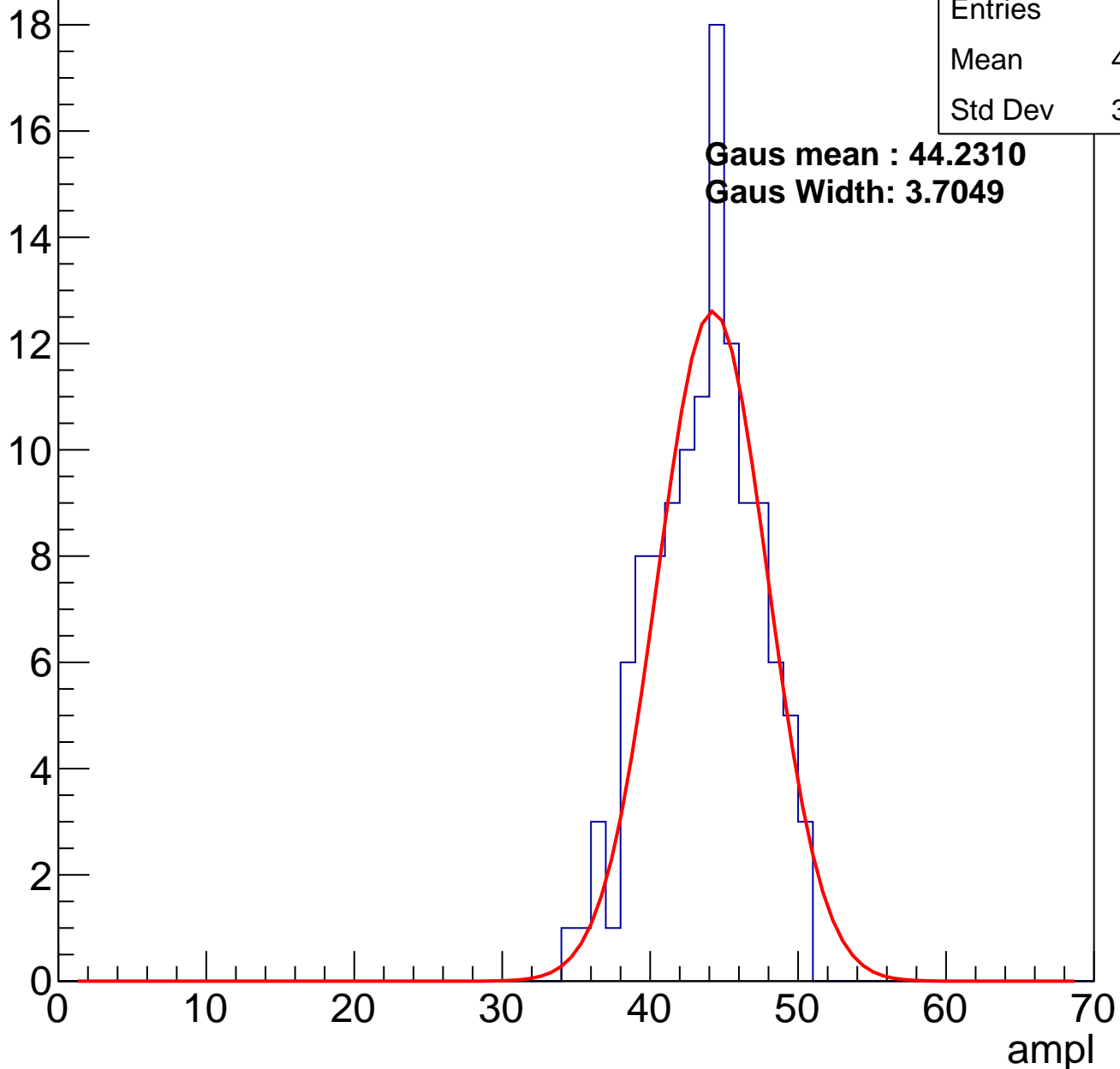
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	43.23
Std Dev	3.518

Gaus mean : 44.2310

Gaus Width: 3.7049

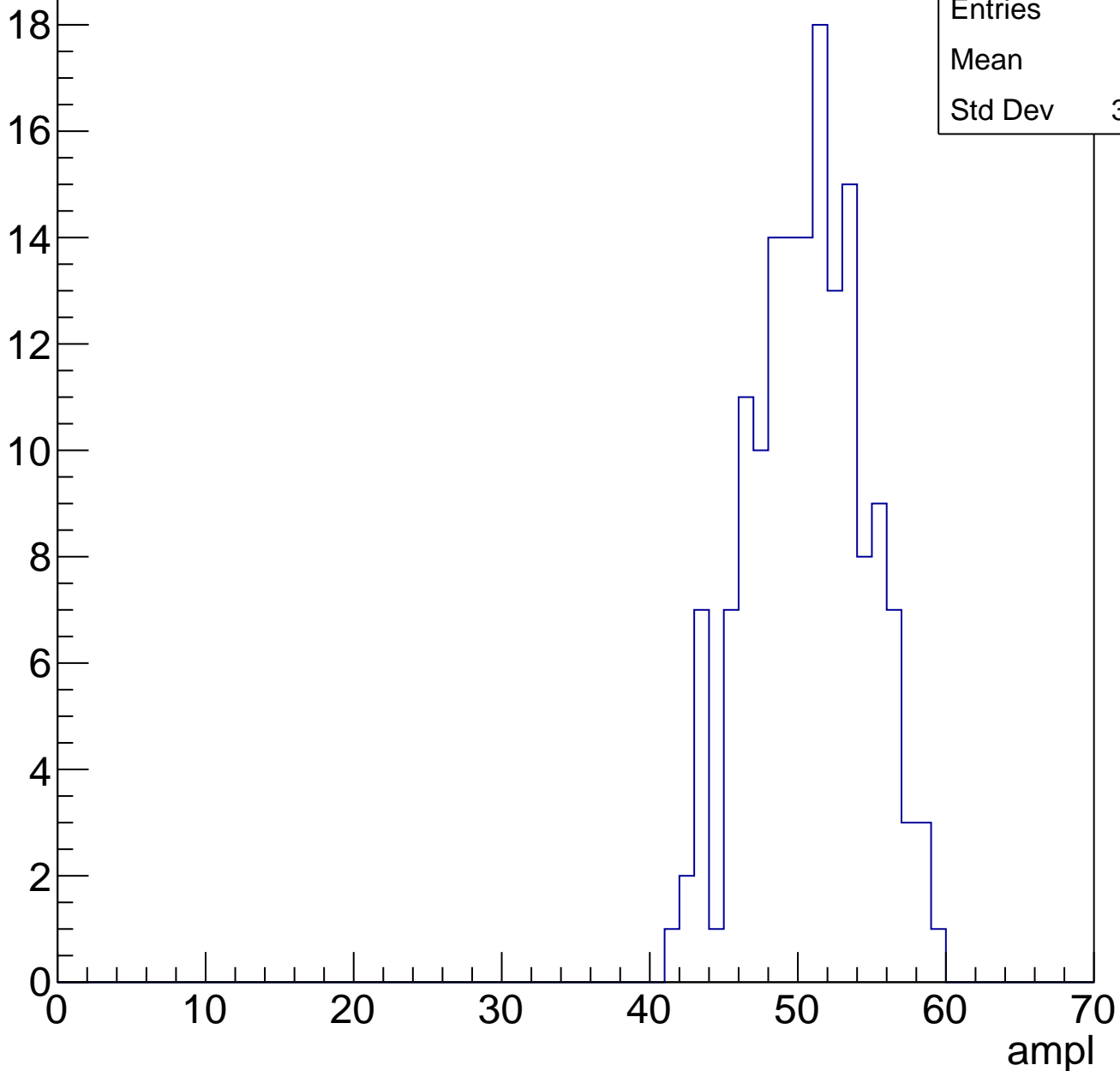
Entry



B1L001S, U25-ch53, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

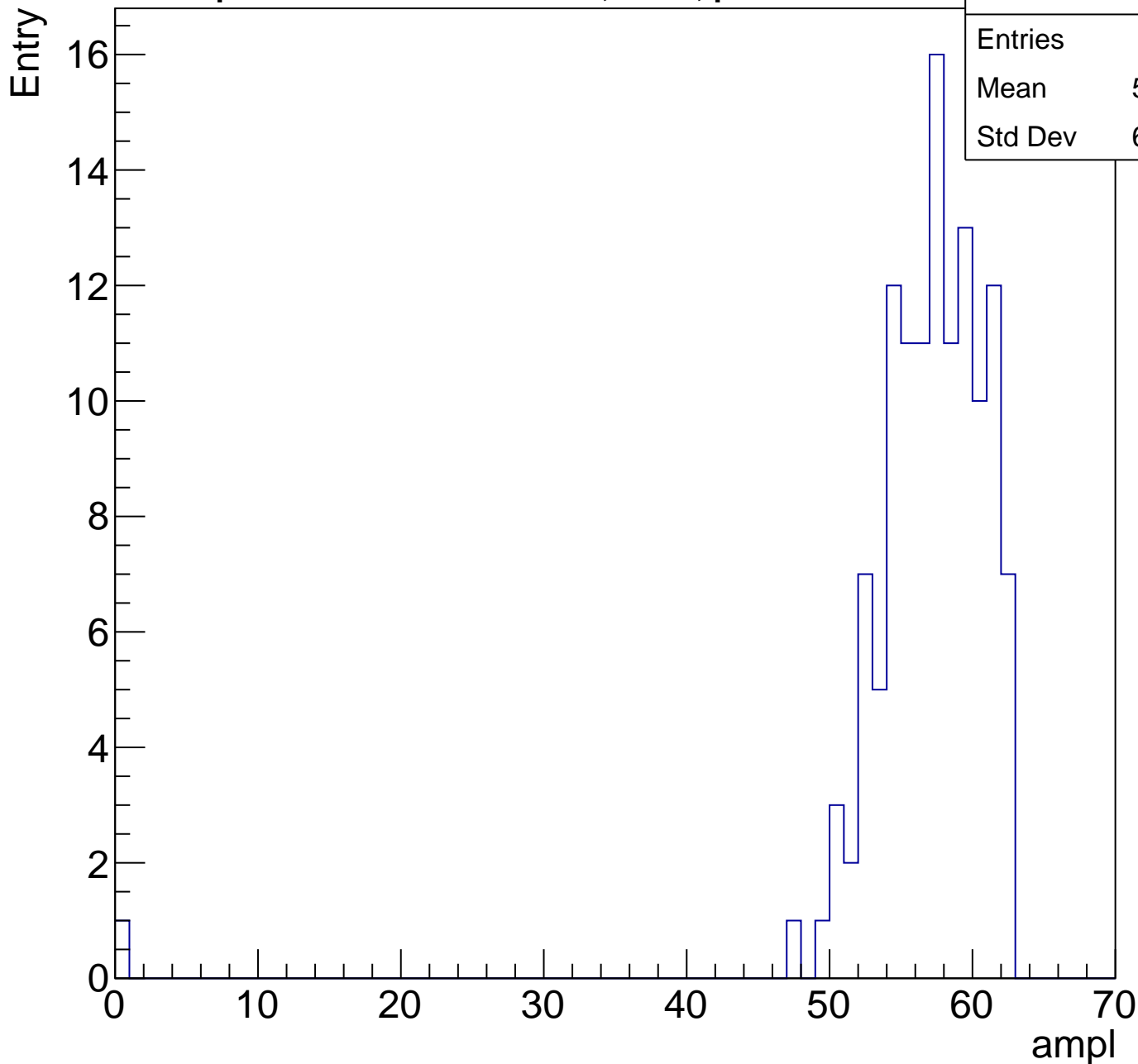


Entries	158
Mean	50.2
Std Dev	3.834

B1L001S, U25-ch53, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	56.33
Std Dev	6.059



B1L001S, U25-ch53, adc5

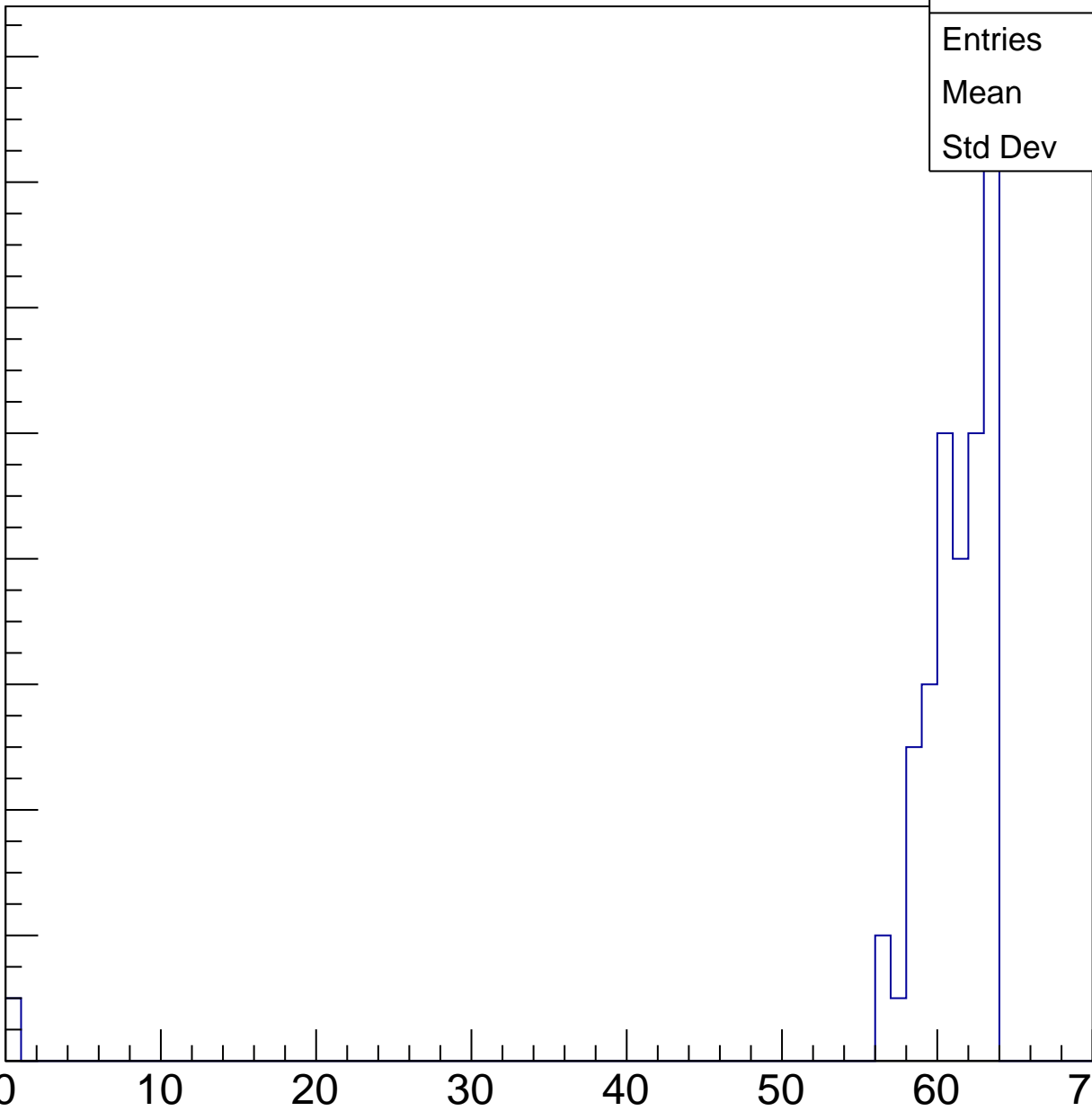
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	59
Mean	59.81
Std Dev	8.085

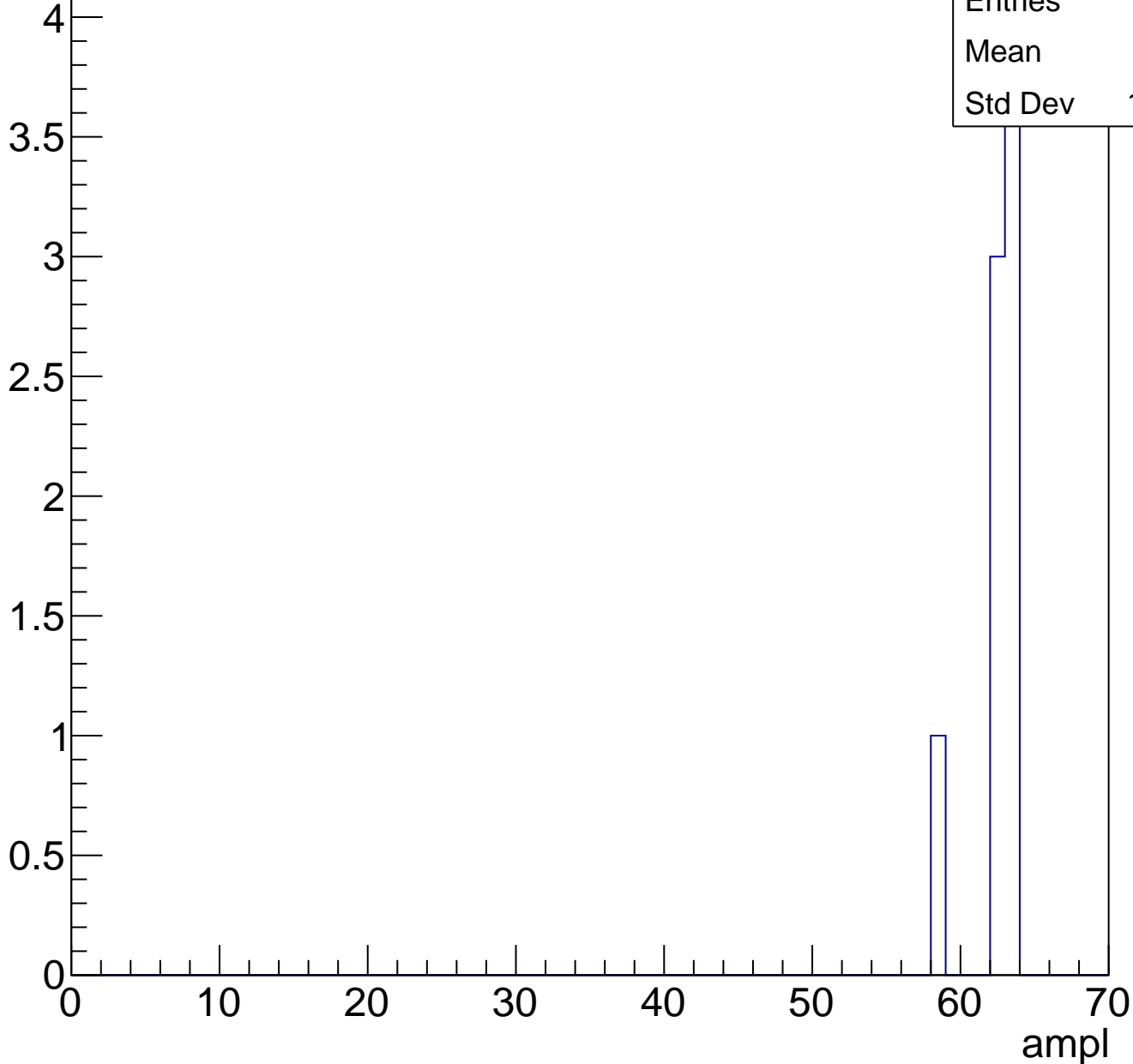
ampl



B1L001S, U25-ch53, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch53, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U25-ch54, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	31.22
Std Dev	3.317

Gaus mean : 31.8169

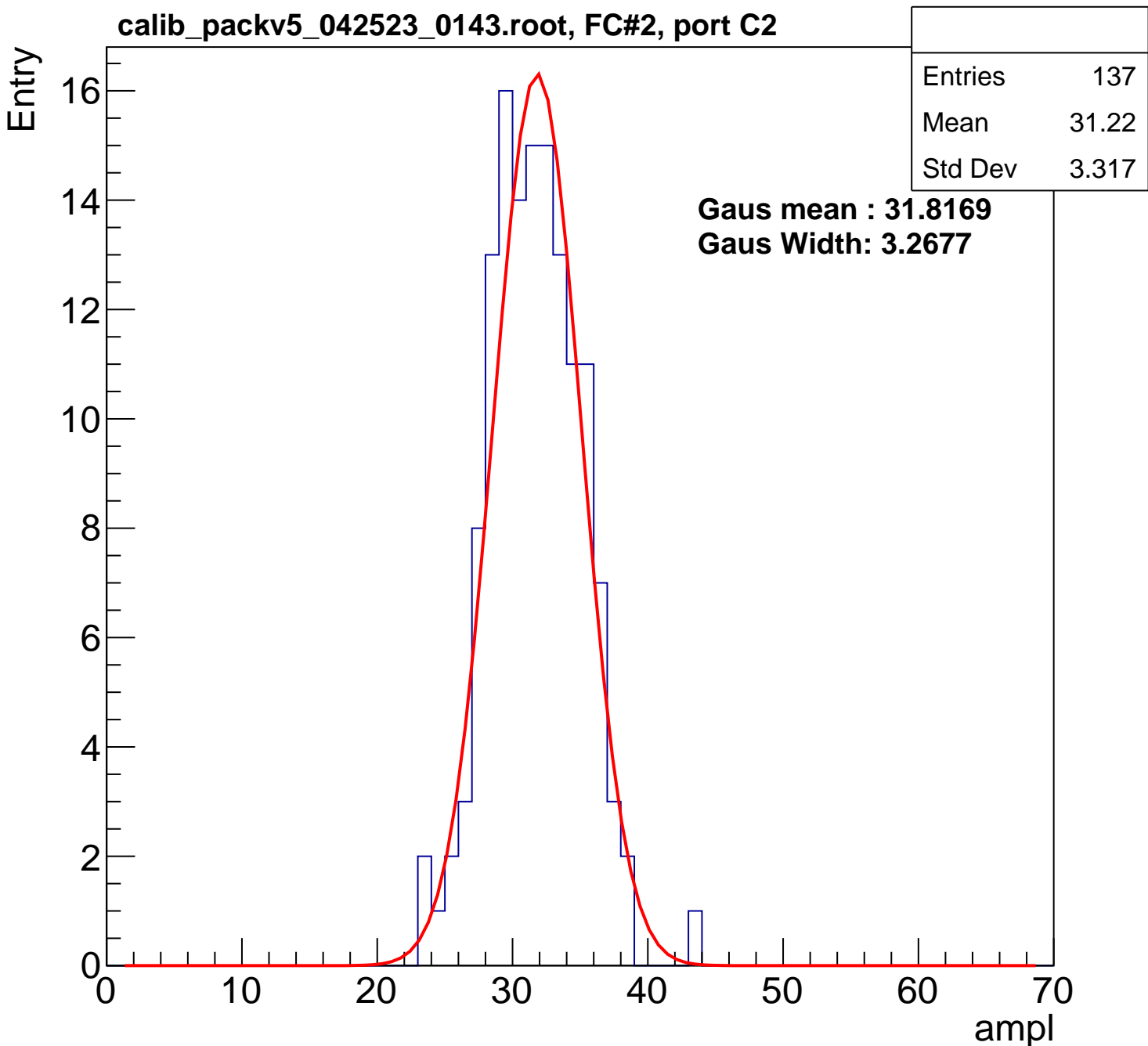
Gaus Width: 3.2677

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



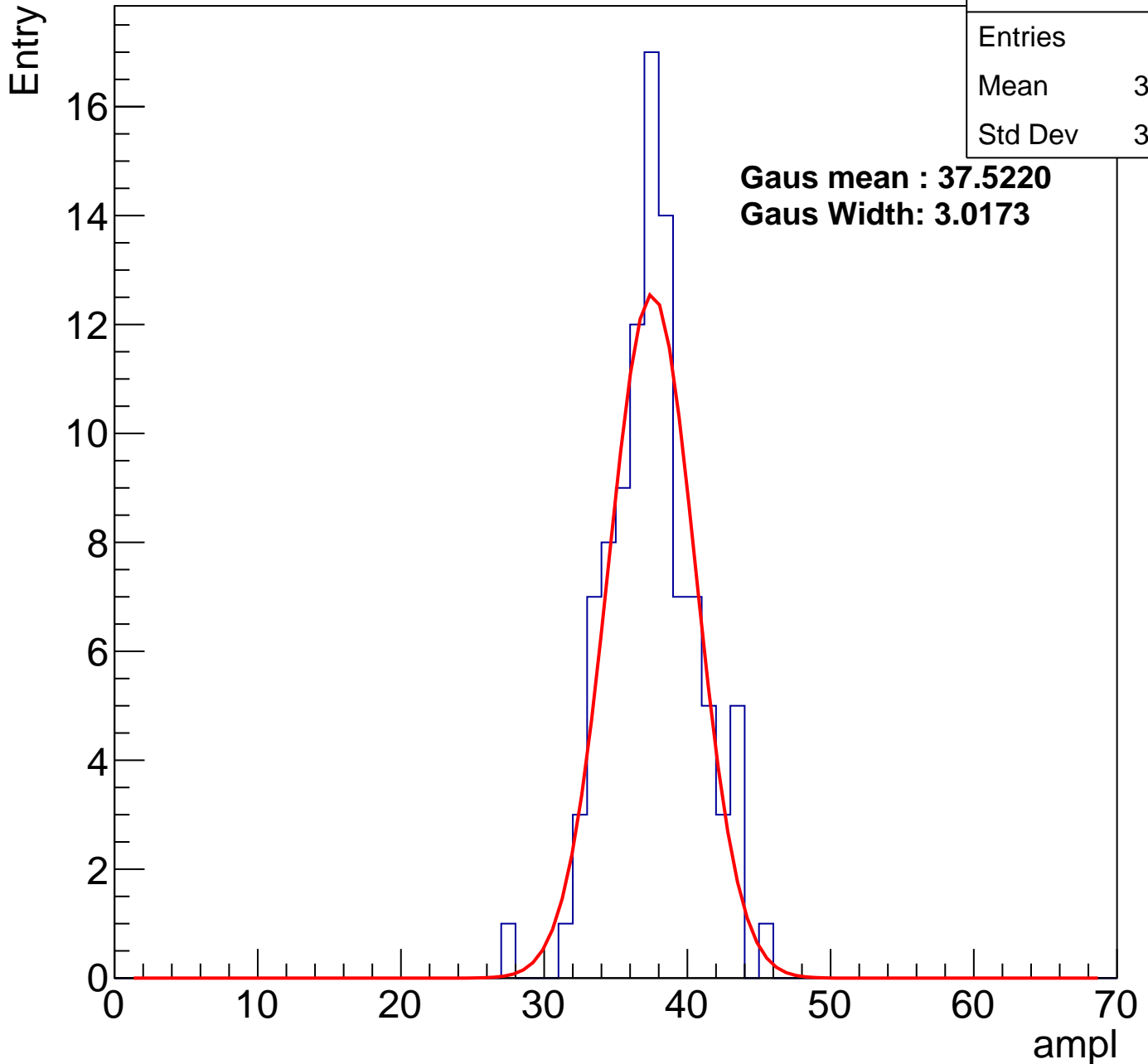
B1L001S, U25-ch54, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	37.09
Std Dev	3.076

Gaus mean : 37.5220

Gaus Width: 3.0173



B1L001S, U25-ch54, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

136

Mean

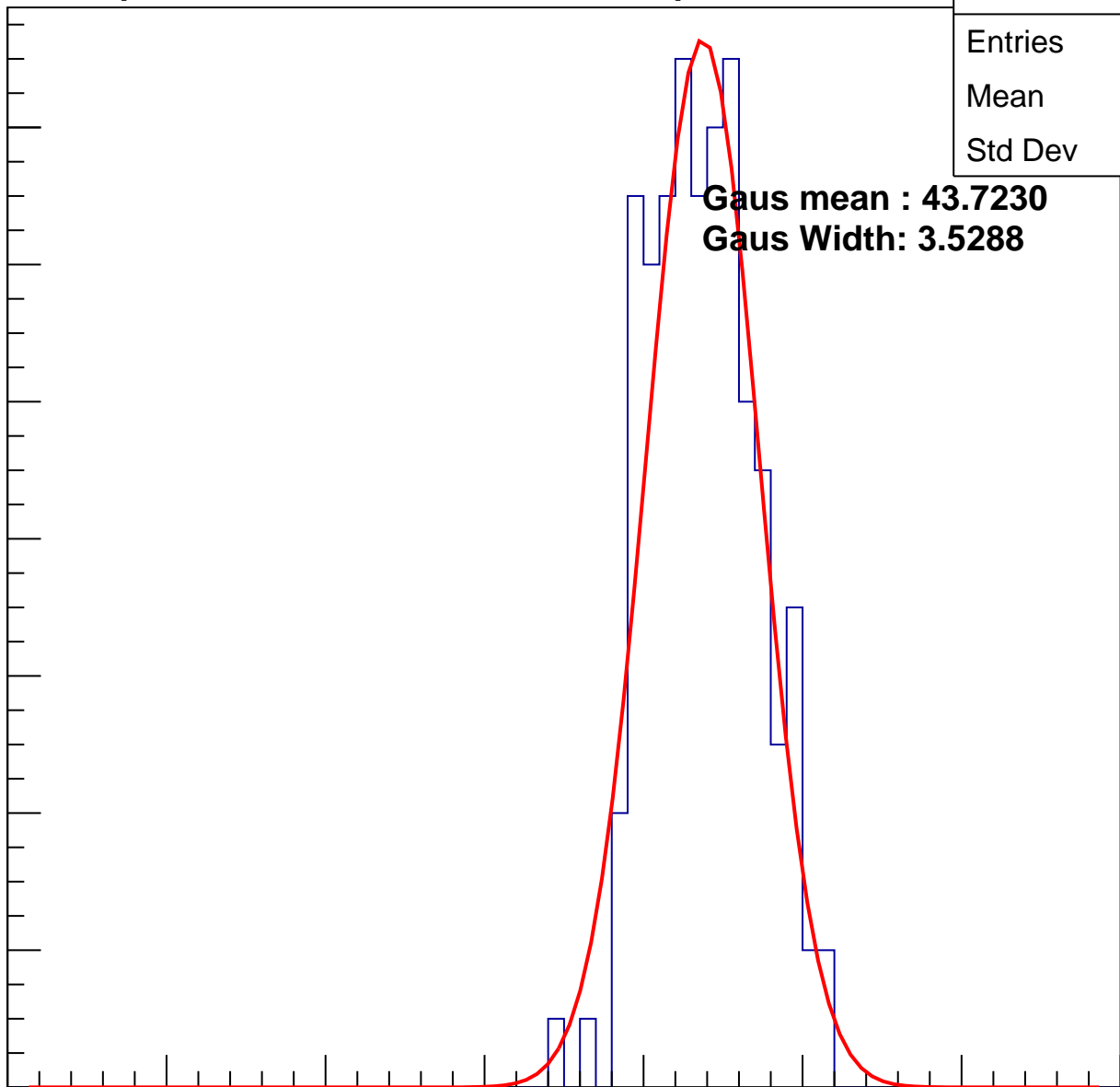
43.31

Std Dev

3.333

Gaus mean : 43.7230

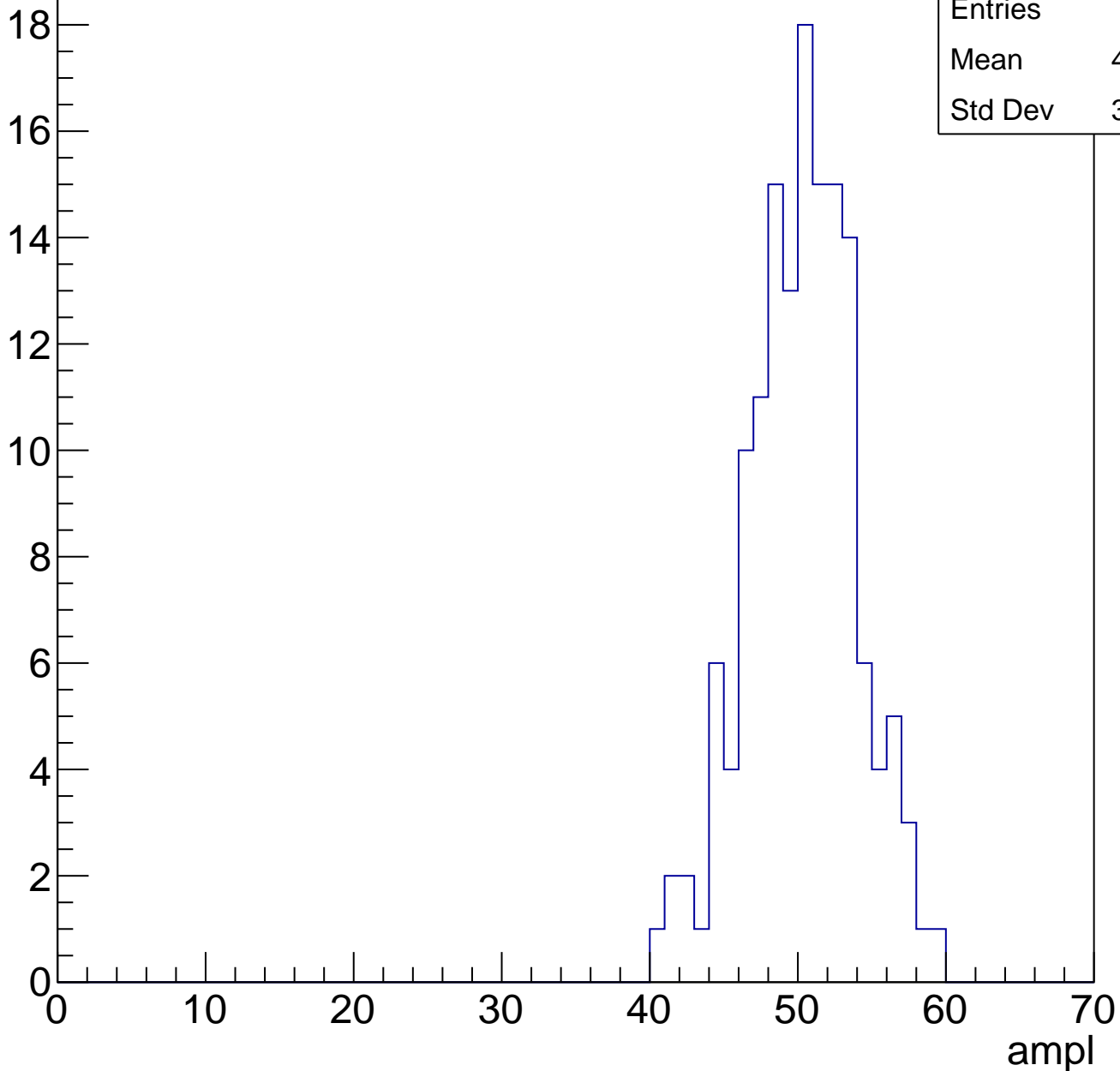
Gaus Width: 3.5288



B1L001S, U25-ch54, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch54, adc4

calib_packv5_042523_0143.root, FC#2, port C2

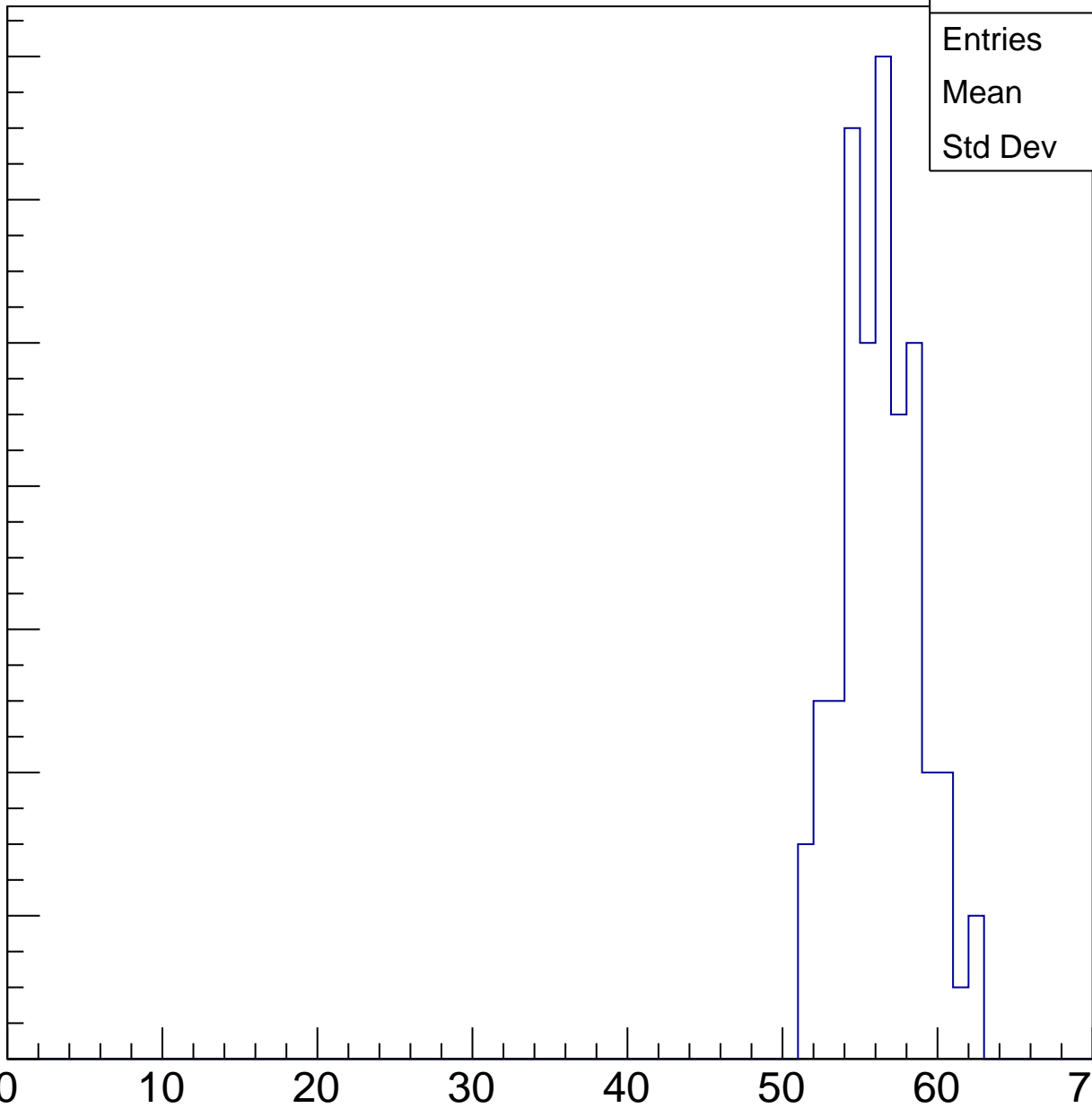
Entries	80
Mean	55.85
Std Dev	2.515

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch54, adc5

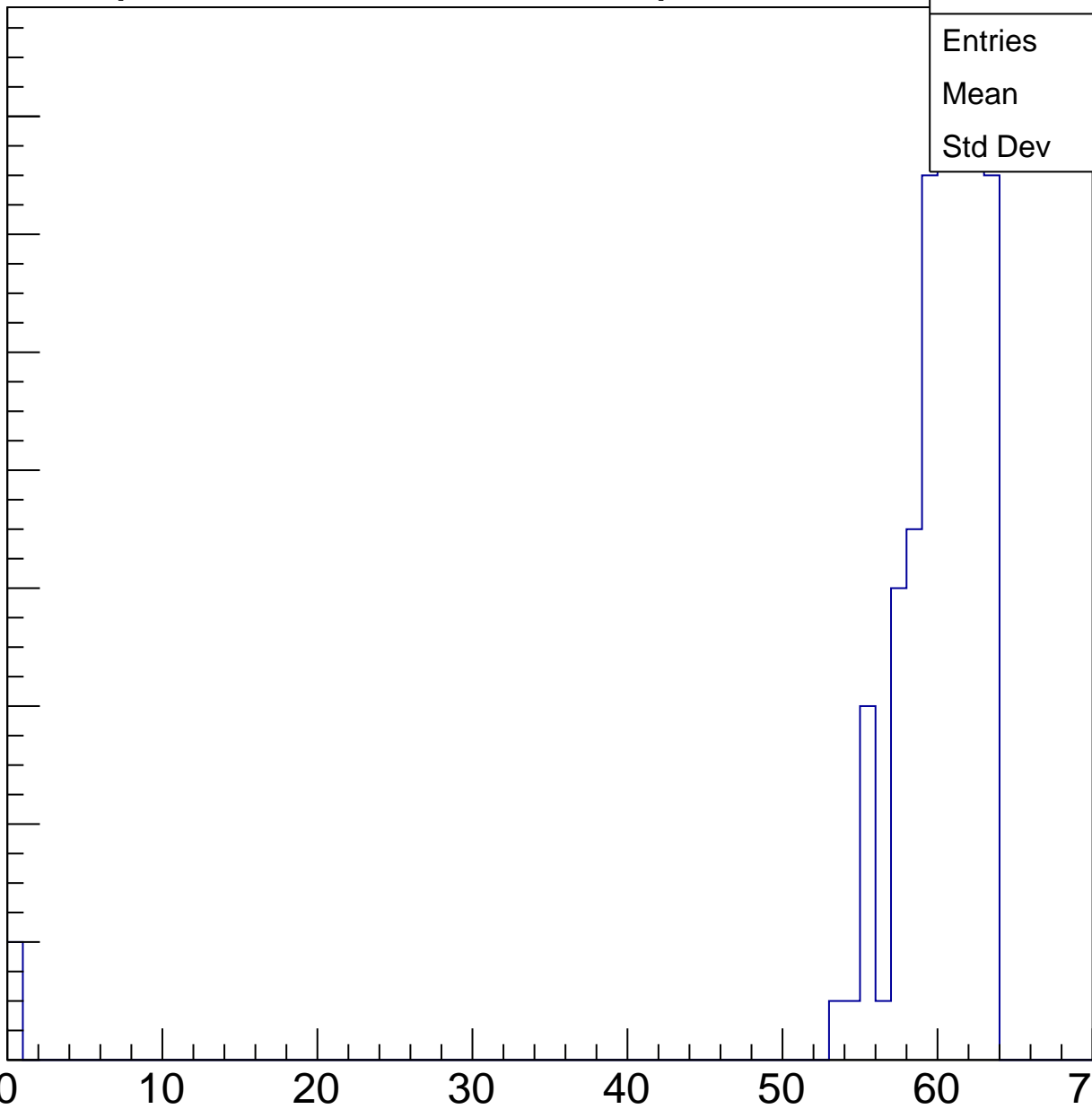
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	108
Mean	58.81
Std Dev	8.412

ampl

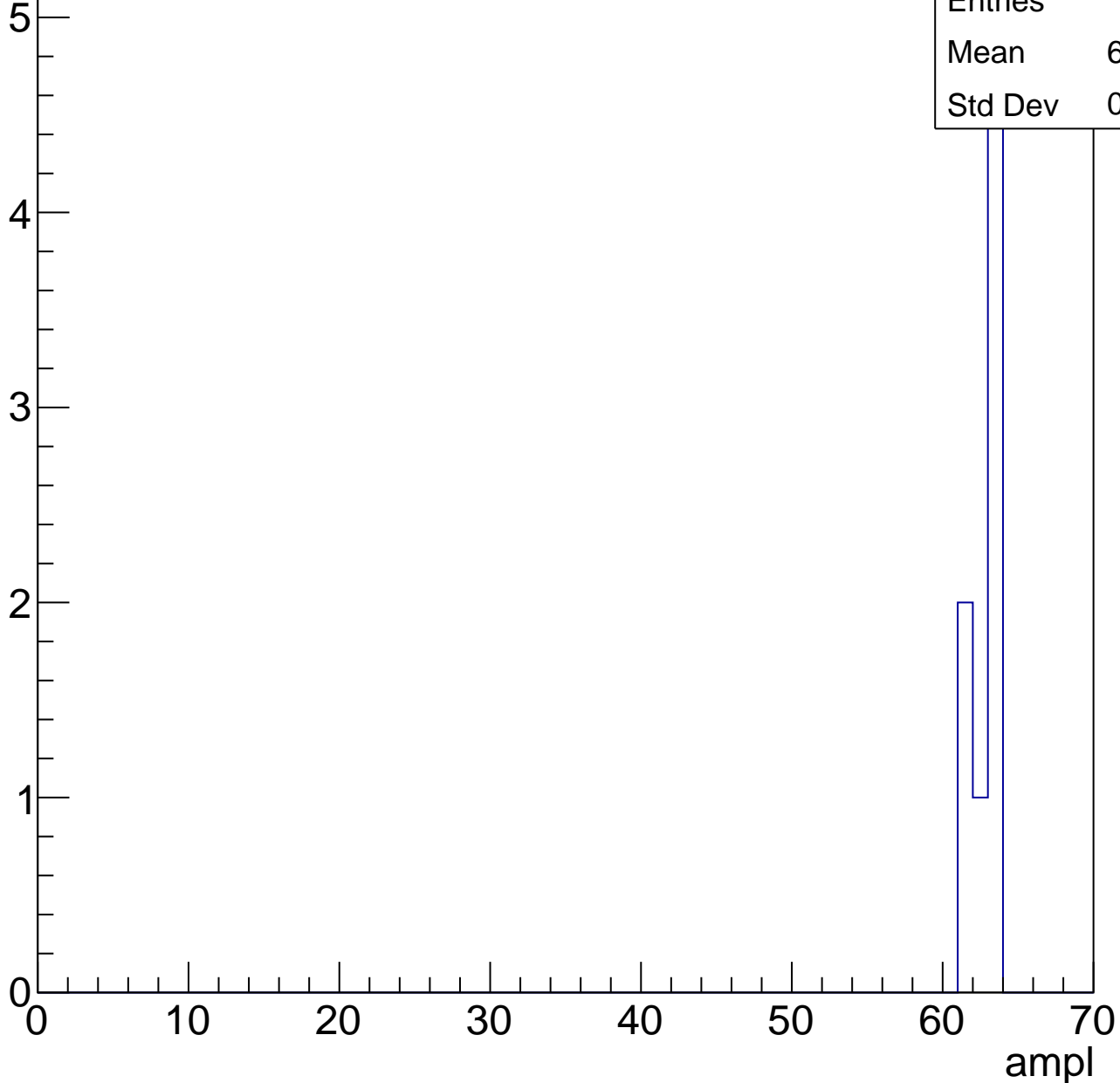


B1L001S, U25-ch54, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	62.38
Std Dev	0.857



B1L001S, U25-ch54, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	12
Std Dev	12

B1L001S, U25-ch55, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	30.52
Std Dev	3.552

Gaus mean : 30.9422

Gaus Width: 3.7292

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

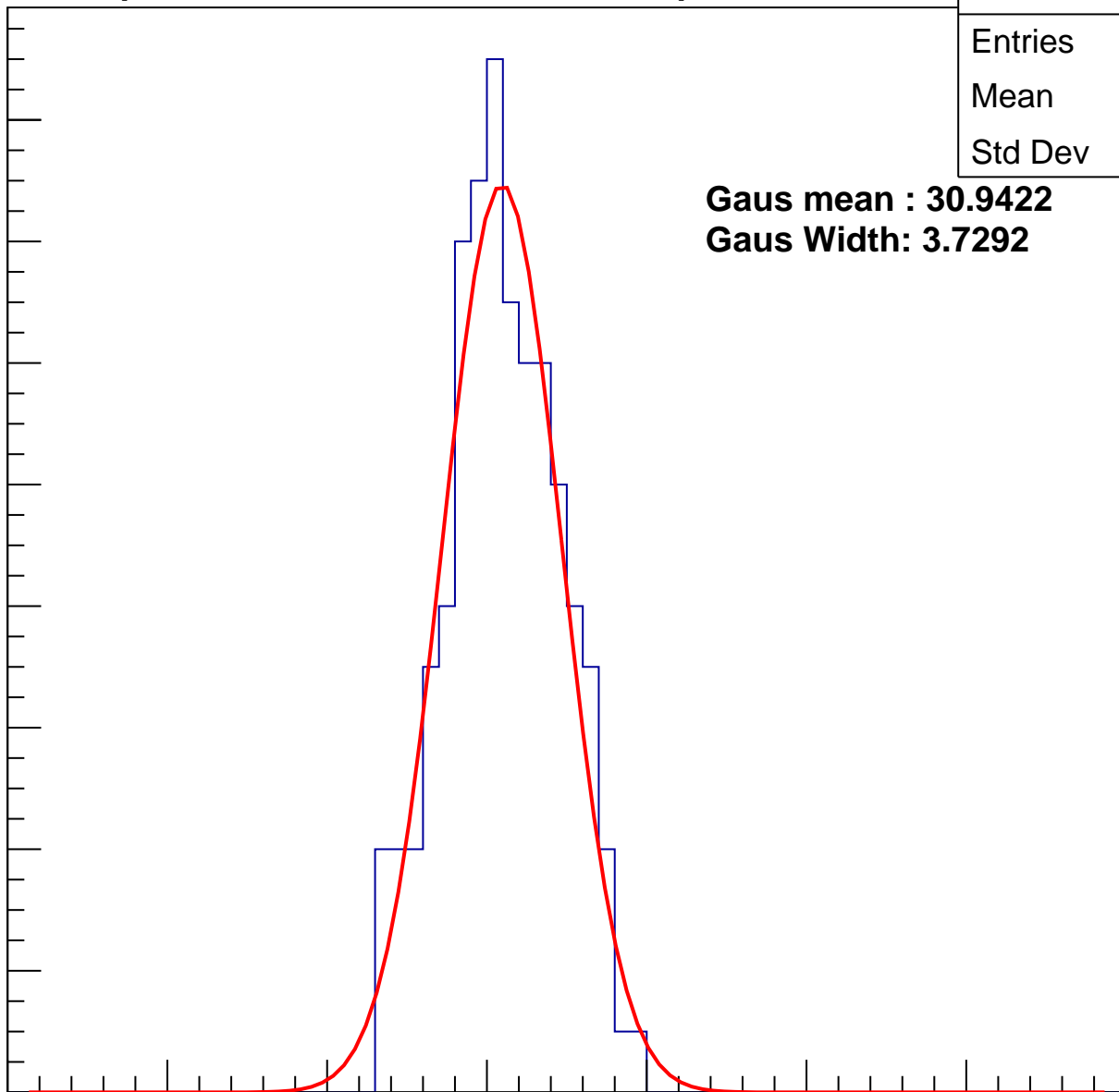
40

50

60

70

ampl

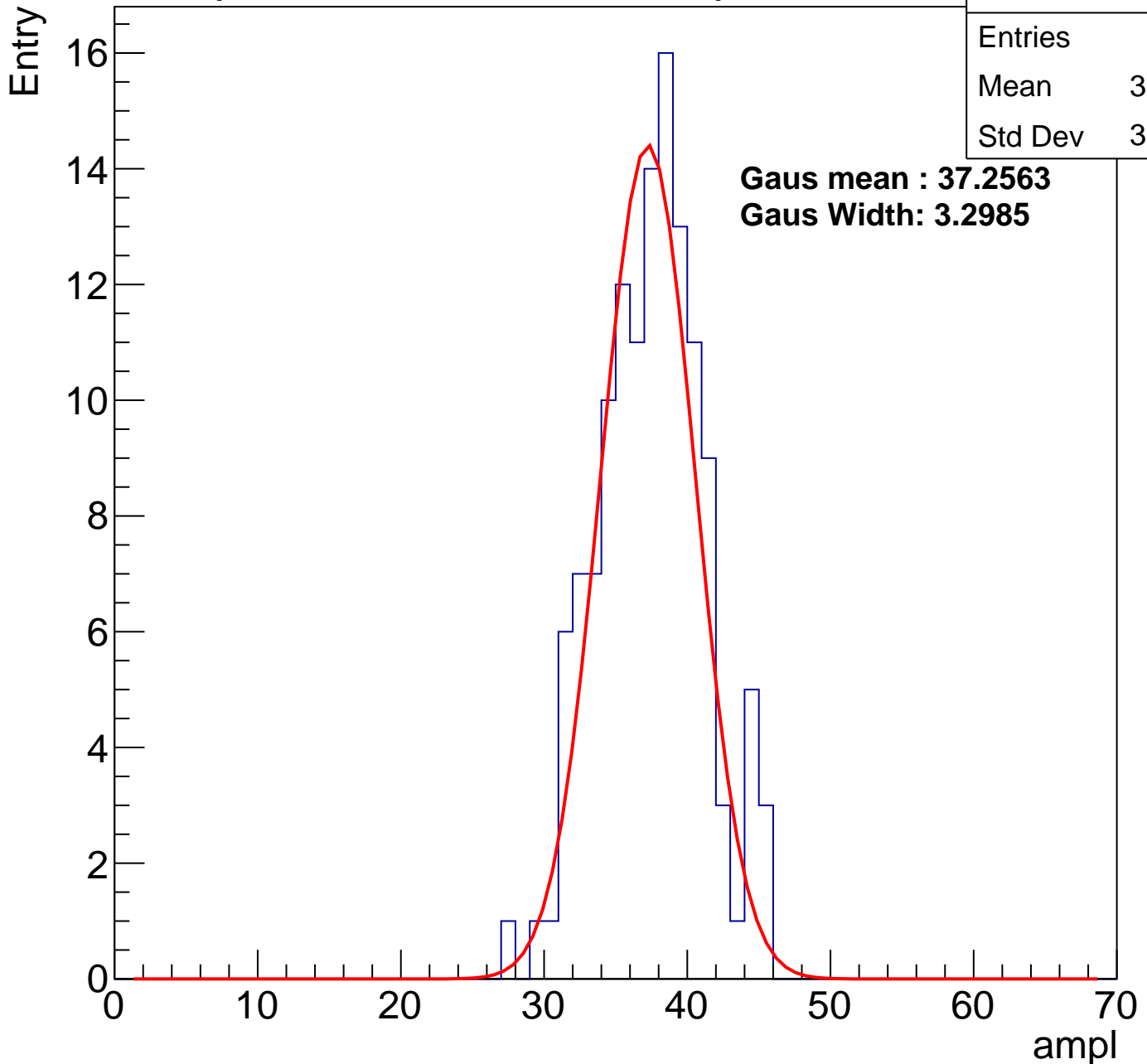


B1L001S, U25-ch55, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	37.02
Std Dev	3.628

Gaus mean : 37.2563
Gaus Width: 3.2985



B1L001S, U25-ch55, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

115

Mean

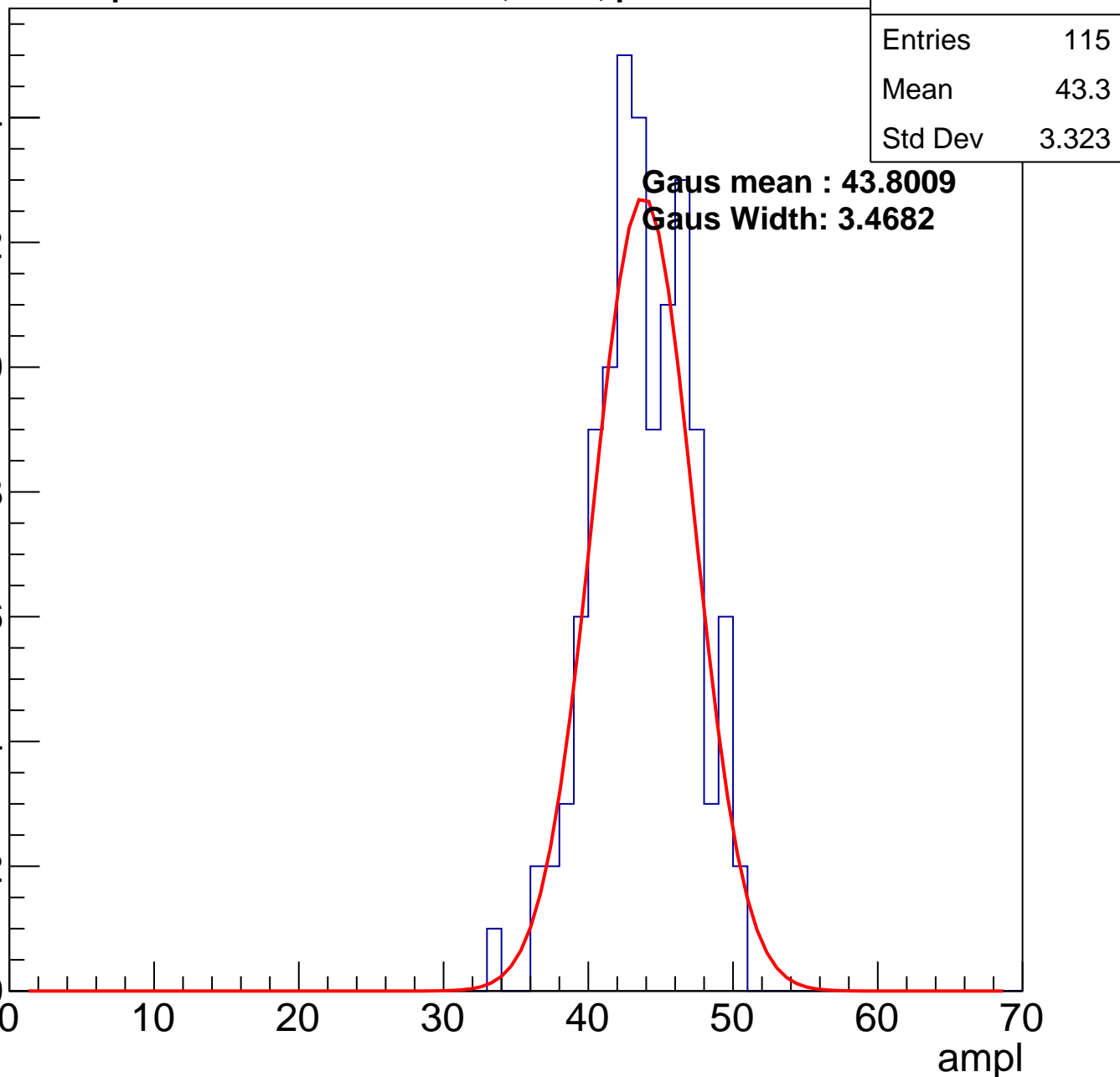
43.3

Std Dev

3.323

Gaus mean : 43.8009

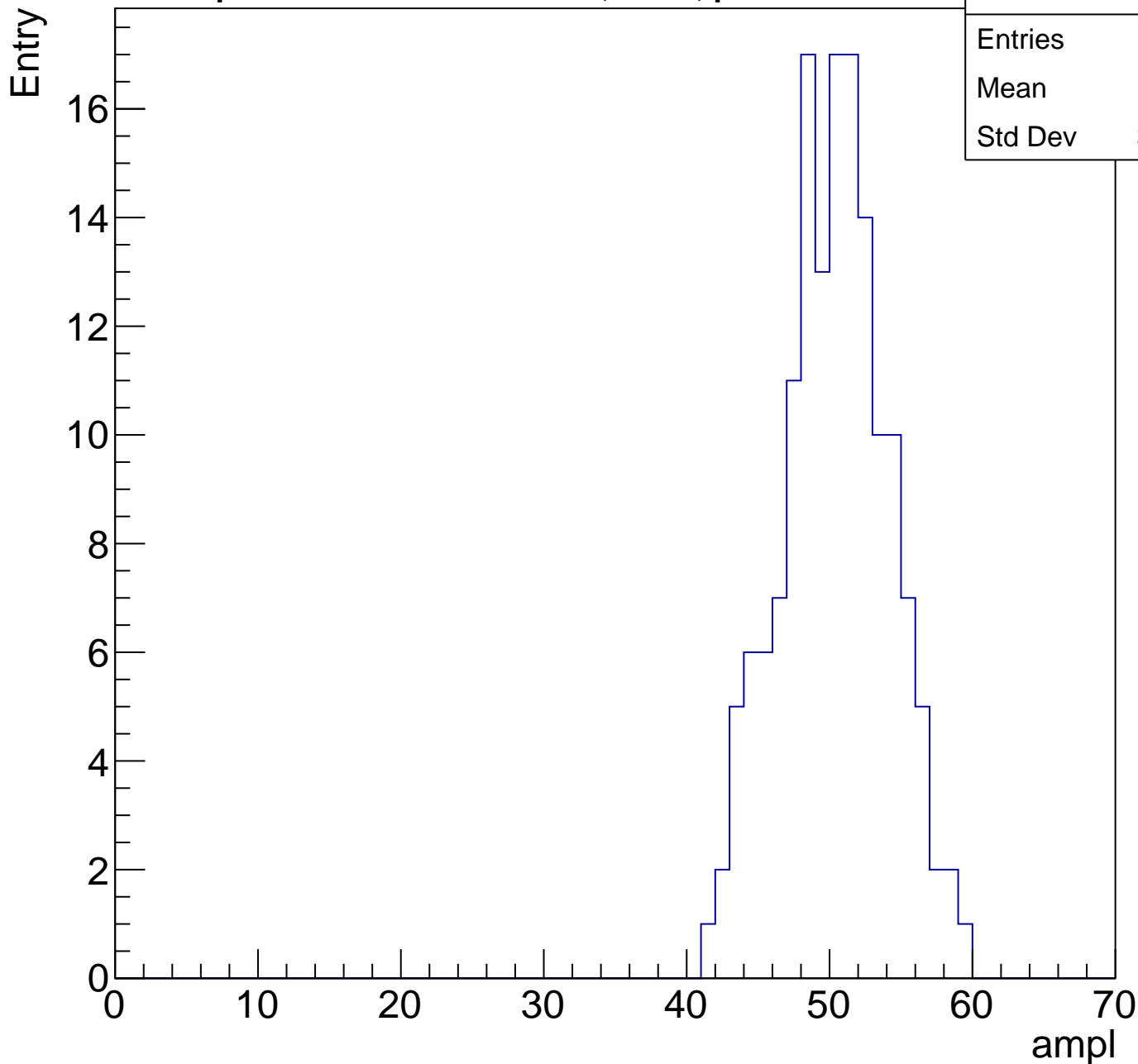
Gaus Width: 3.4682



B1L001S, U25-ch55, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	49.9
Std Dev	3.701

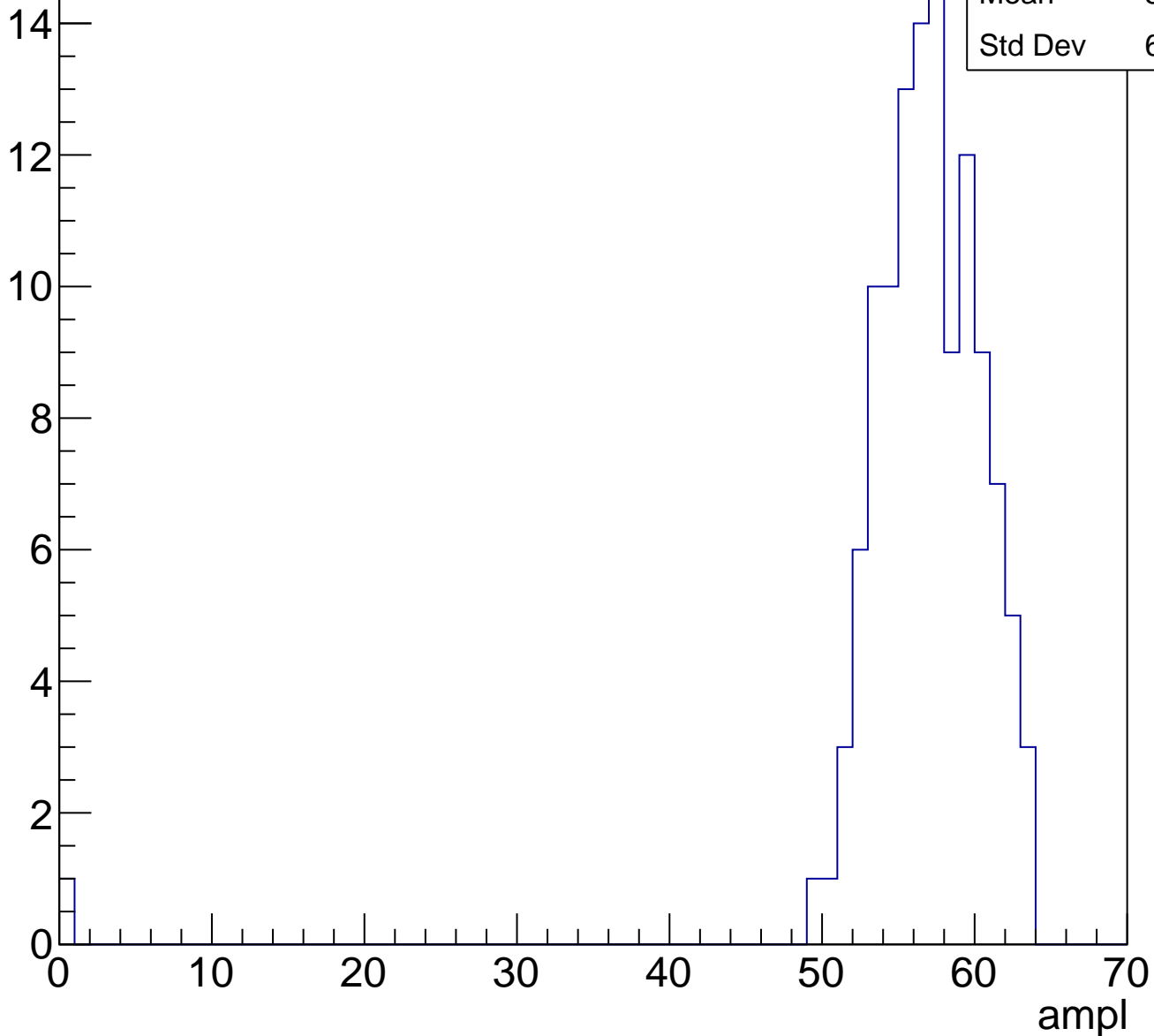


B1L001S, U25-ch55, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	56.17
Std Dev	6.037

Entry



B1L001S, U25-ch55, adc5

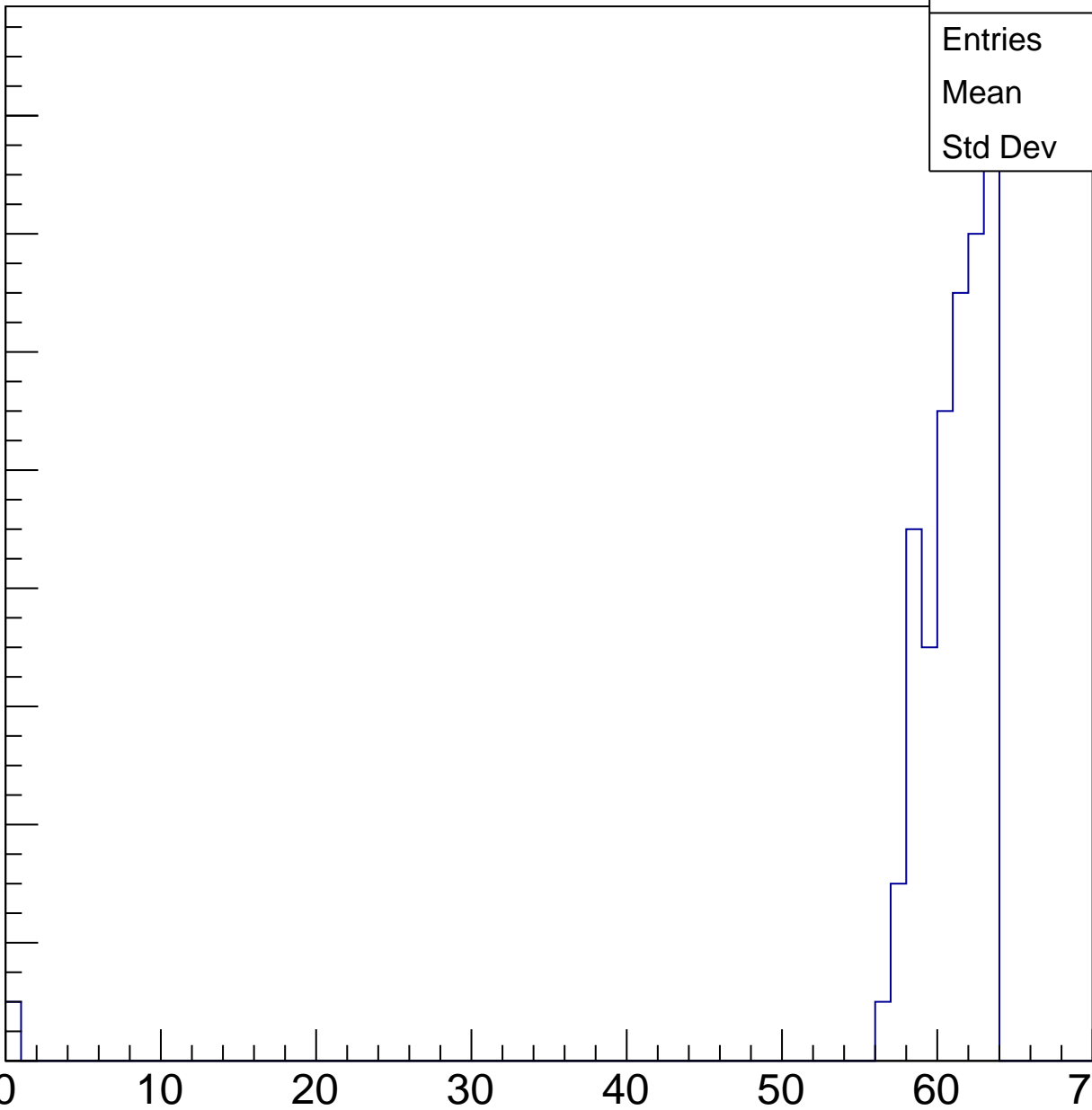
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	76
Mean	59.92
Std Dev	7.17

ampl



B1L001S, U25-ch55, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch55, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



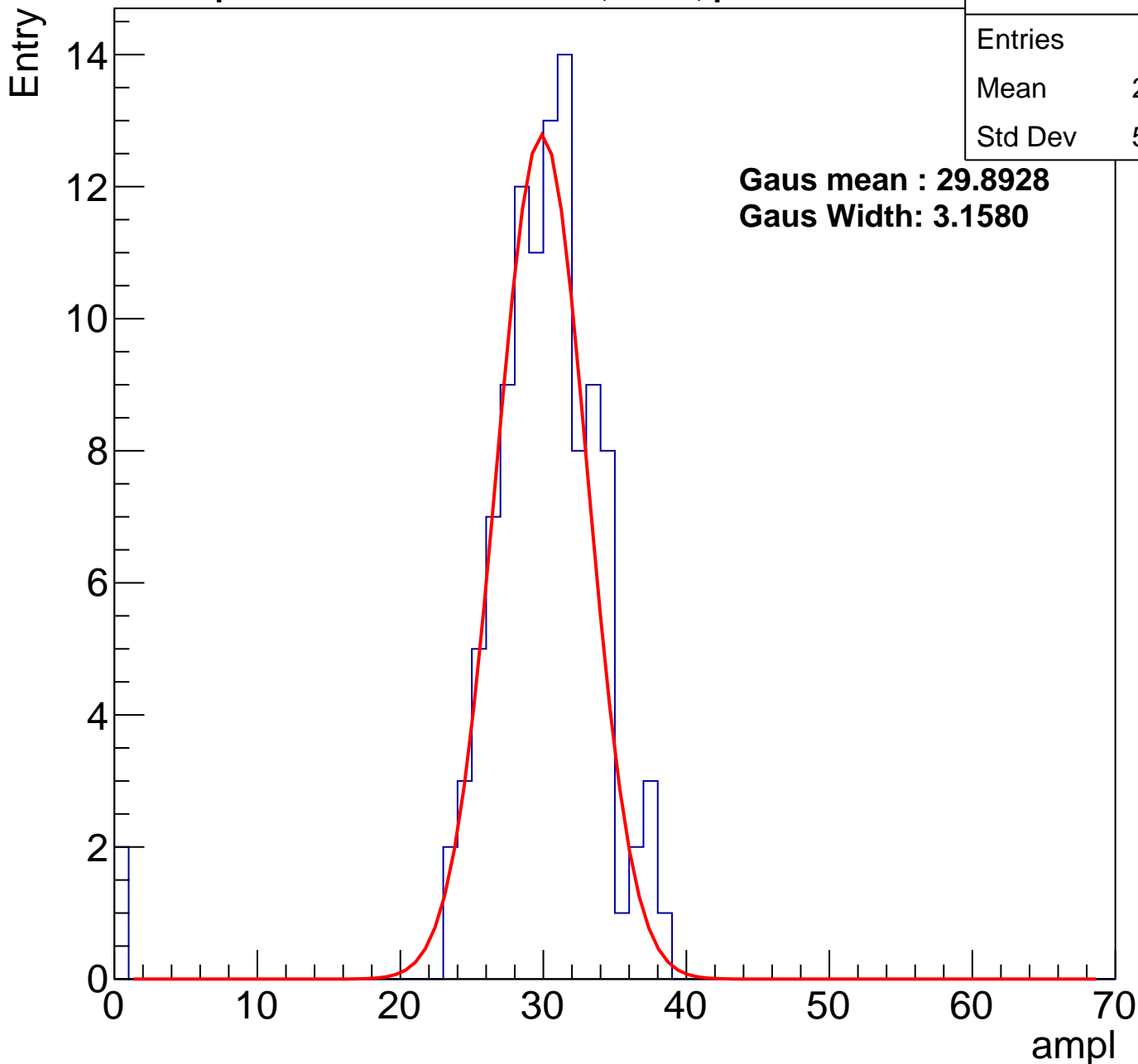
B1L001S, U25-ch56, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	29.35
Std Dev	5.134

Gaus mean : 29.8928

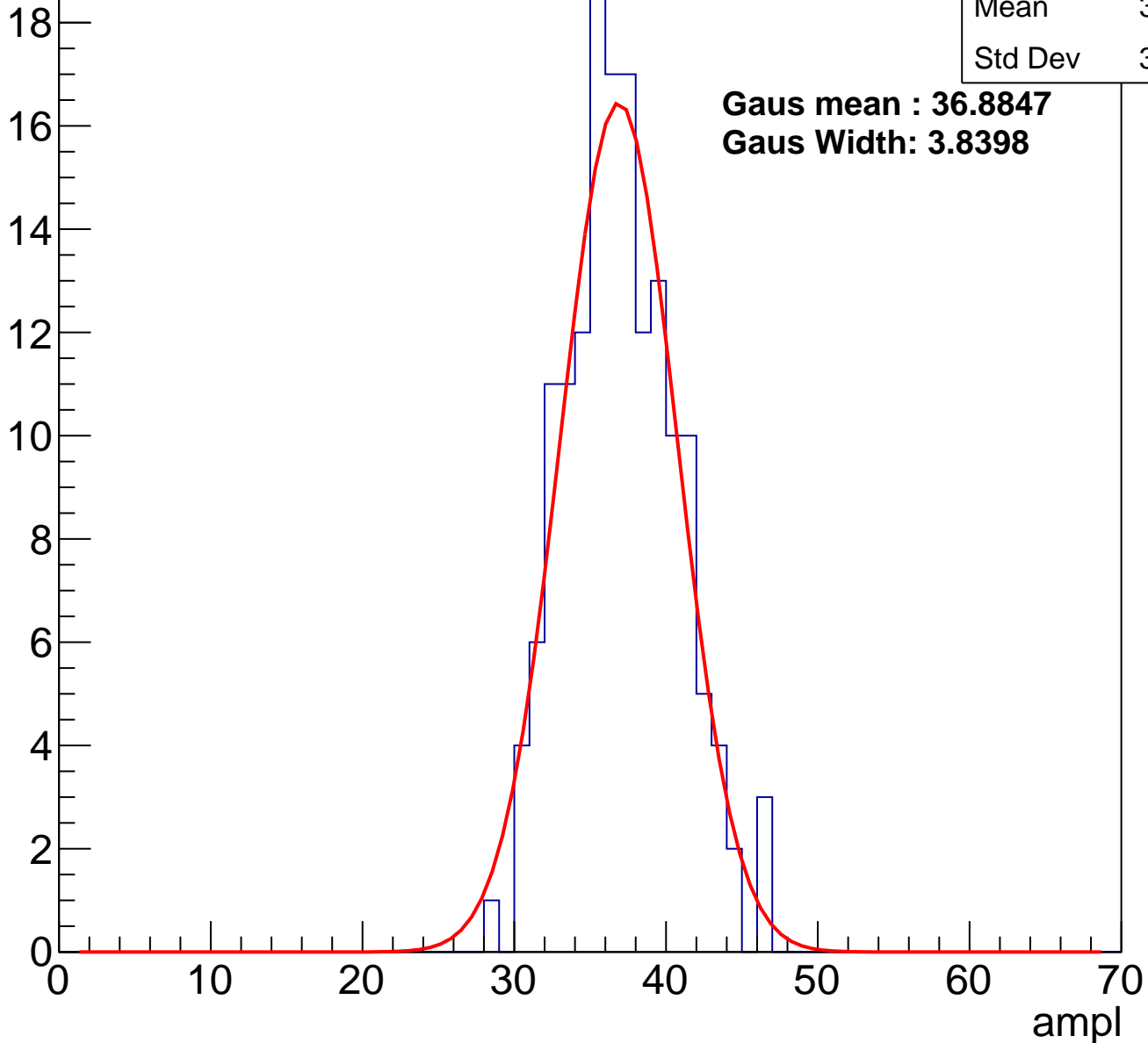
Gaus Width: 3.1580



B1L001S, U25-ch56, adc1

calib_packv5_042523_0143.root, FC#2, port C2

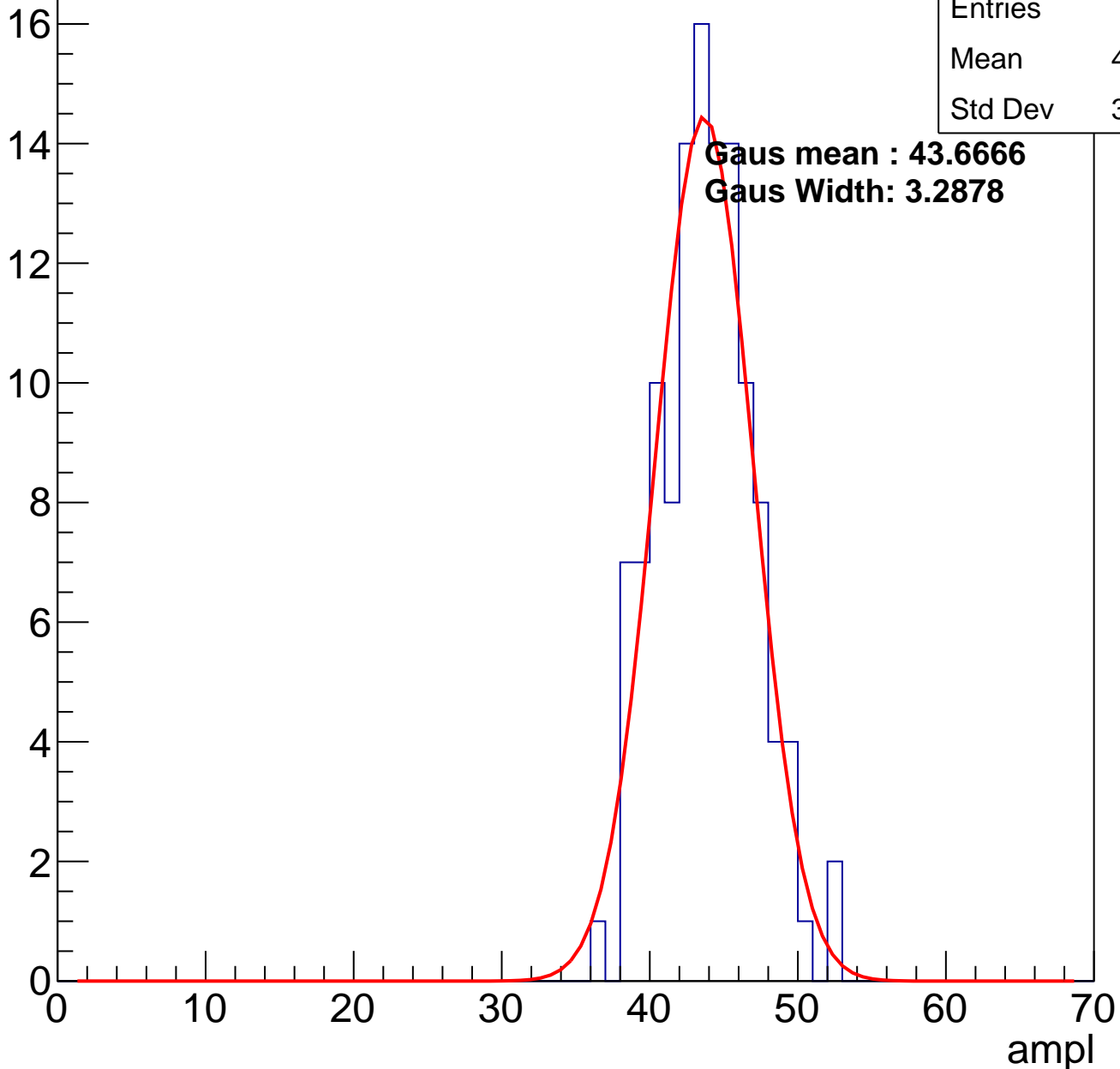
Entry



B1L001S, U25-ch56, adc2

calib_packv5_042523_0143.root, FC#2, port C2

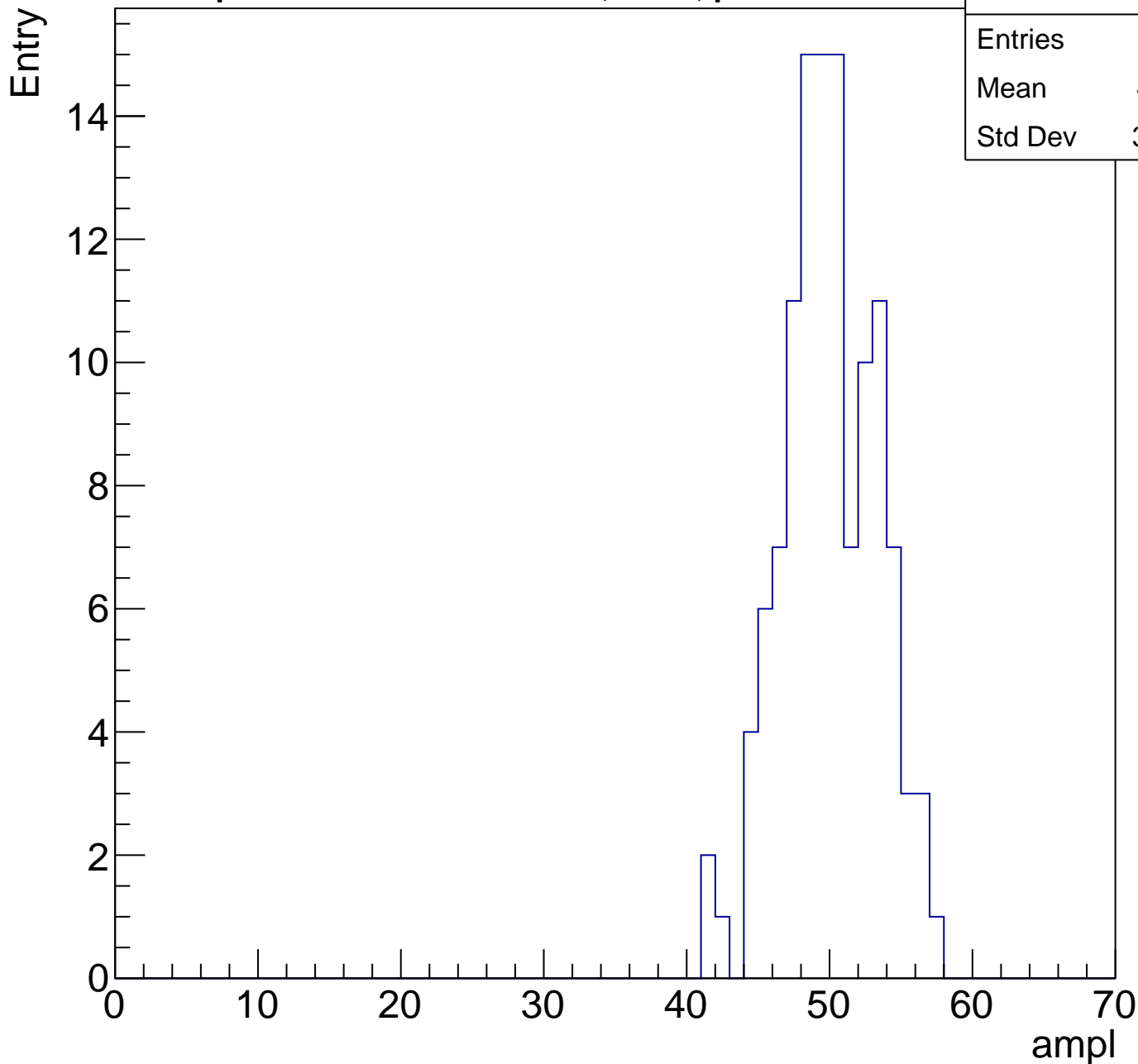
Entry



B1L001S, U25-ch56, adc3

calib_packv5_042523_0143.root, FC#2, port C2

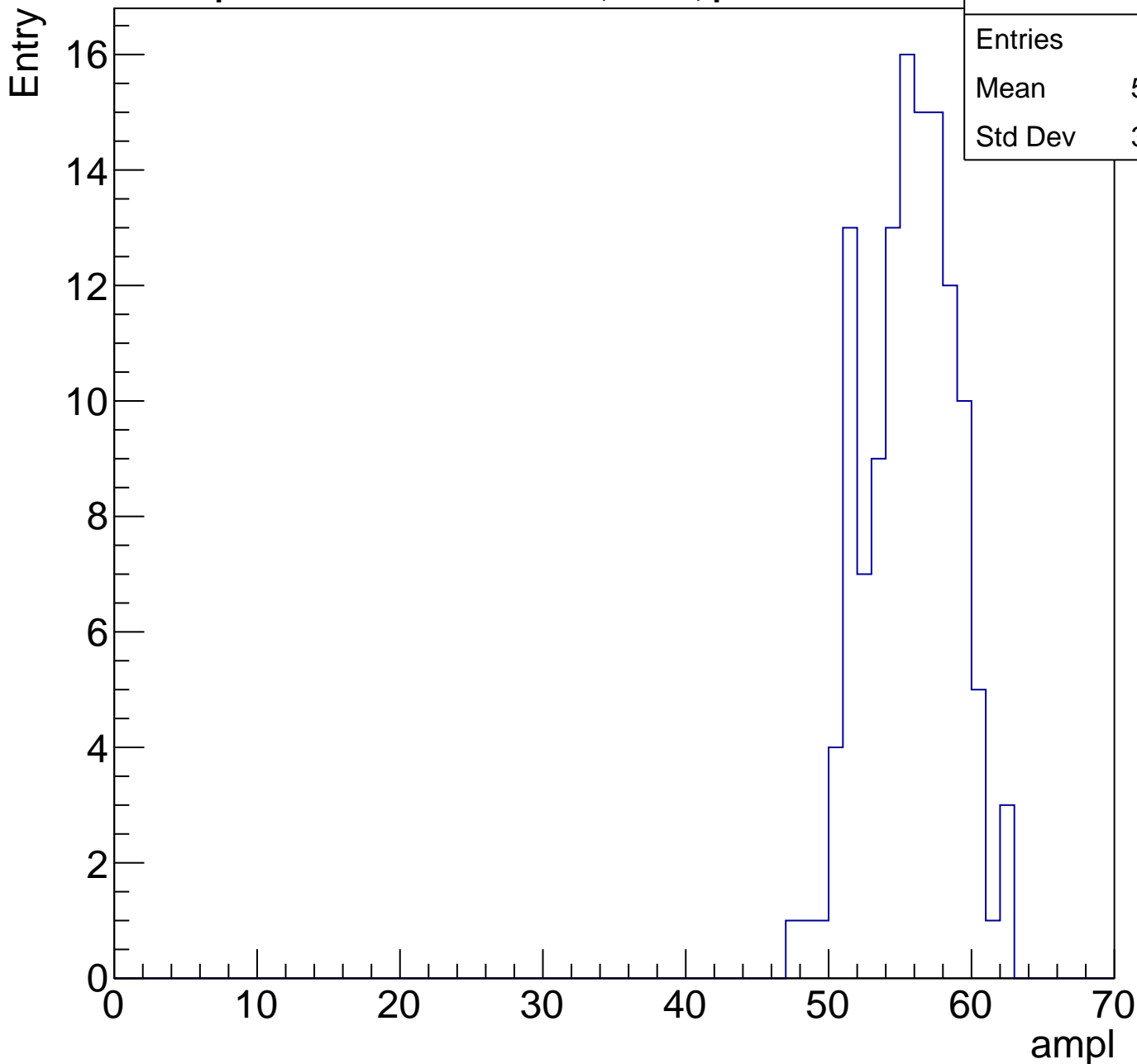
Entries	118
Mean	49.51
Std Dev	3.277



B1L001S, U25-ch56, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	55.22
Std Dev	3.104



B1L001S, U25-ch56, adc5

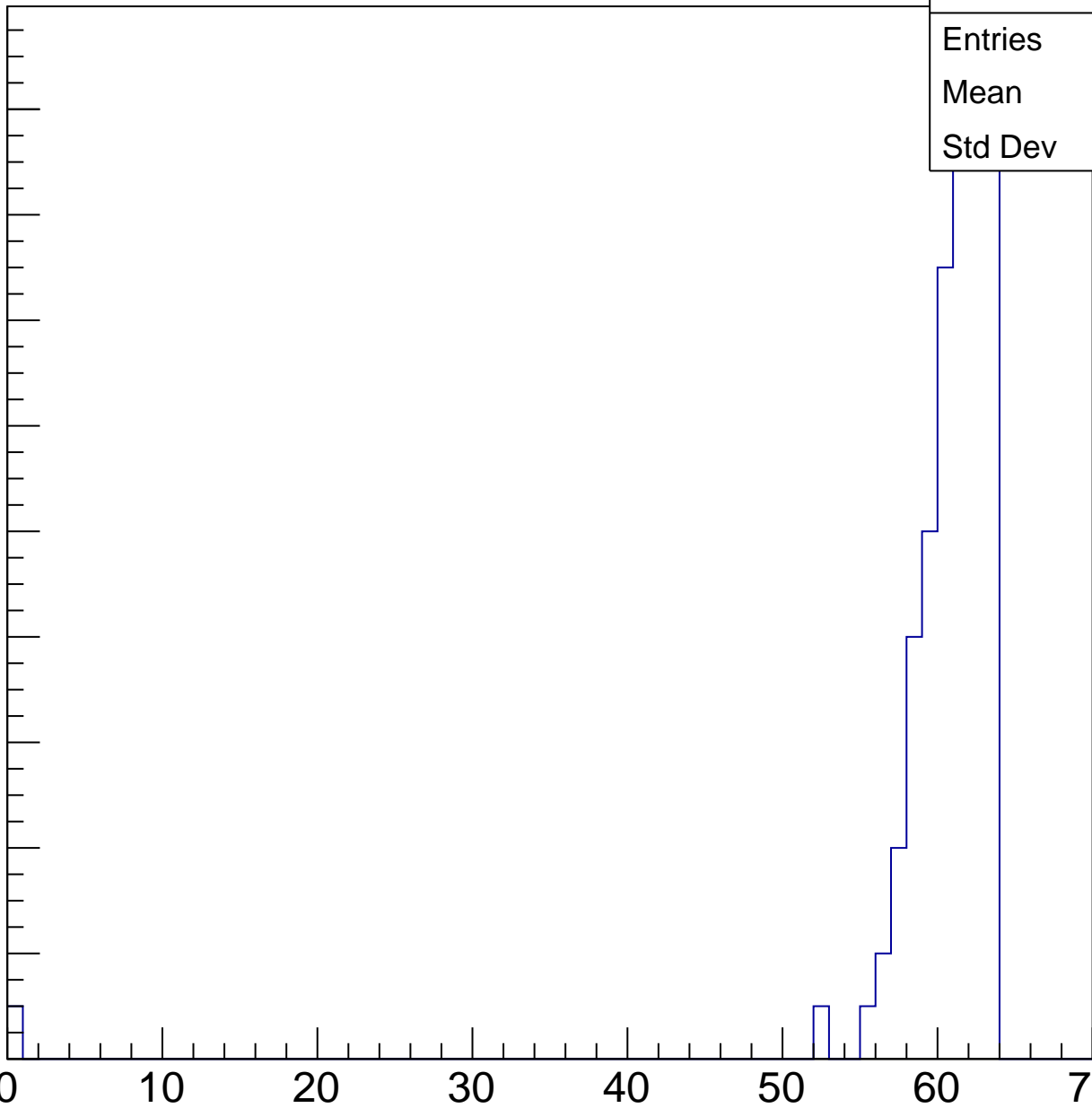
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.88
Std Dev	6.492

ampl



B1L001S, U25-ch56, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch56, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch57, adc0

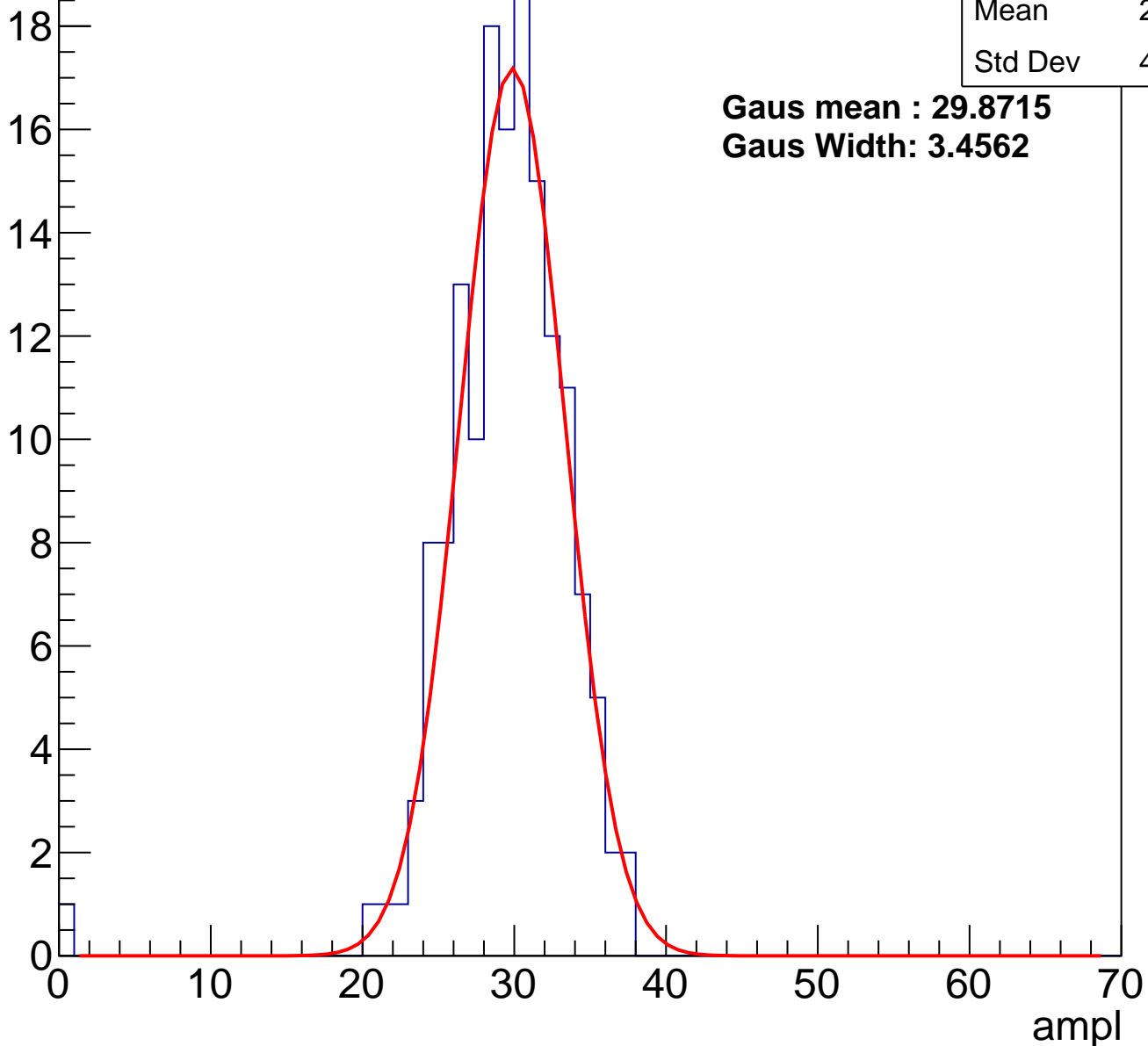
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	29.03
Std Dev	4.114

Gaus mean : 29.8715

Gaus Width: 3.4562

Entry

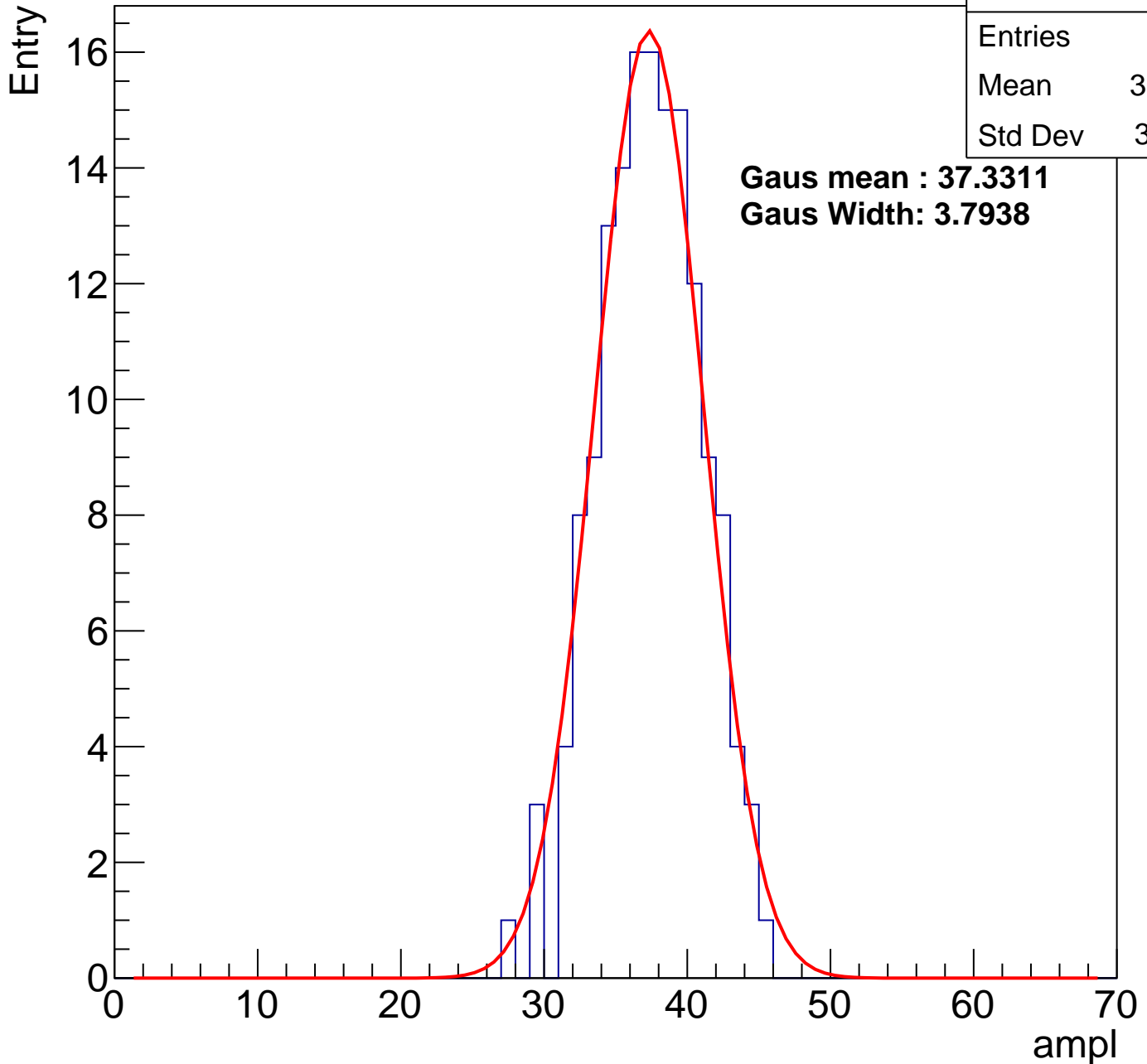


B1L001S, U25-ch57, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	36.95
Std Dev	3.501

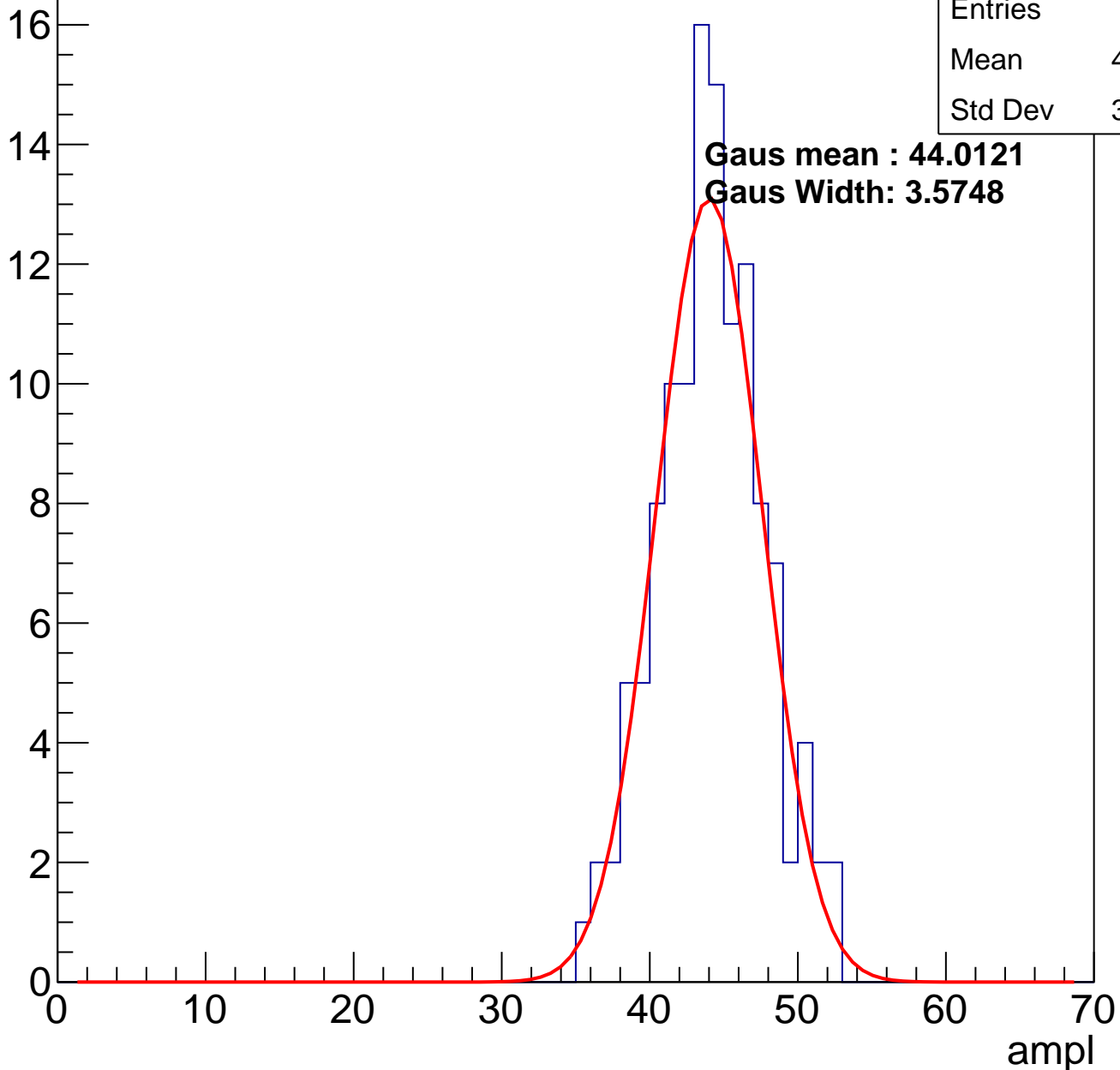
Gaus mean : 37.3311
Gaus Width: 3.7938



B1L001S, U25-ch57, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch57, adc3

calib_packv5_042523_0143.root, FC#2, port C2

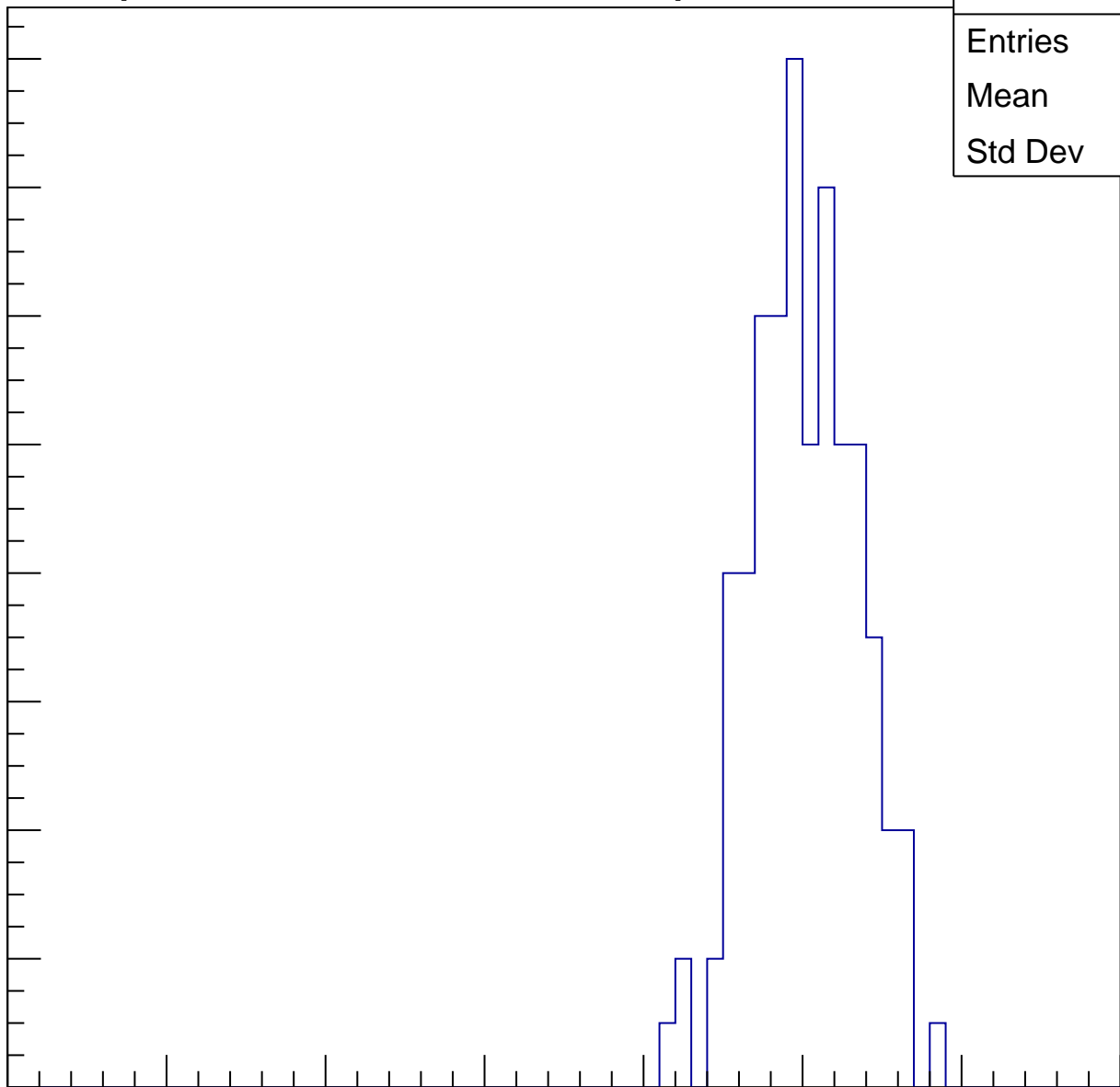
Entries	121
Mean	49.66
Std Dev	3.317

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch57, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	55.9
Std Dev	3.219

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

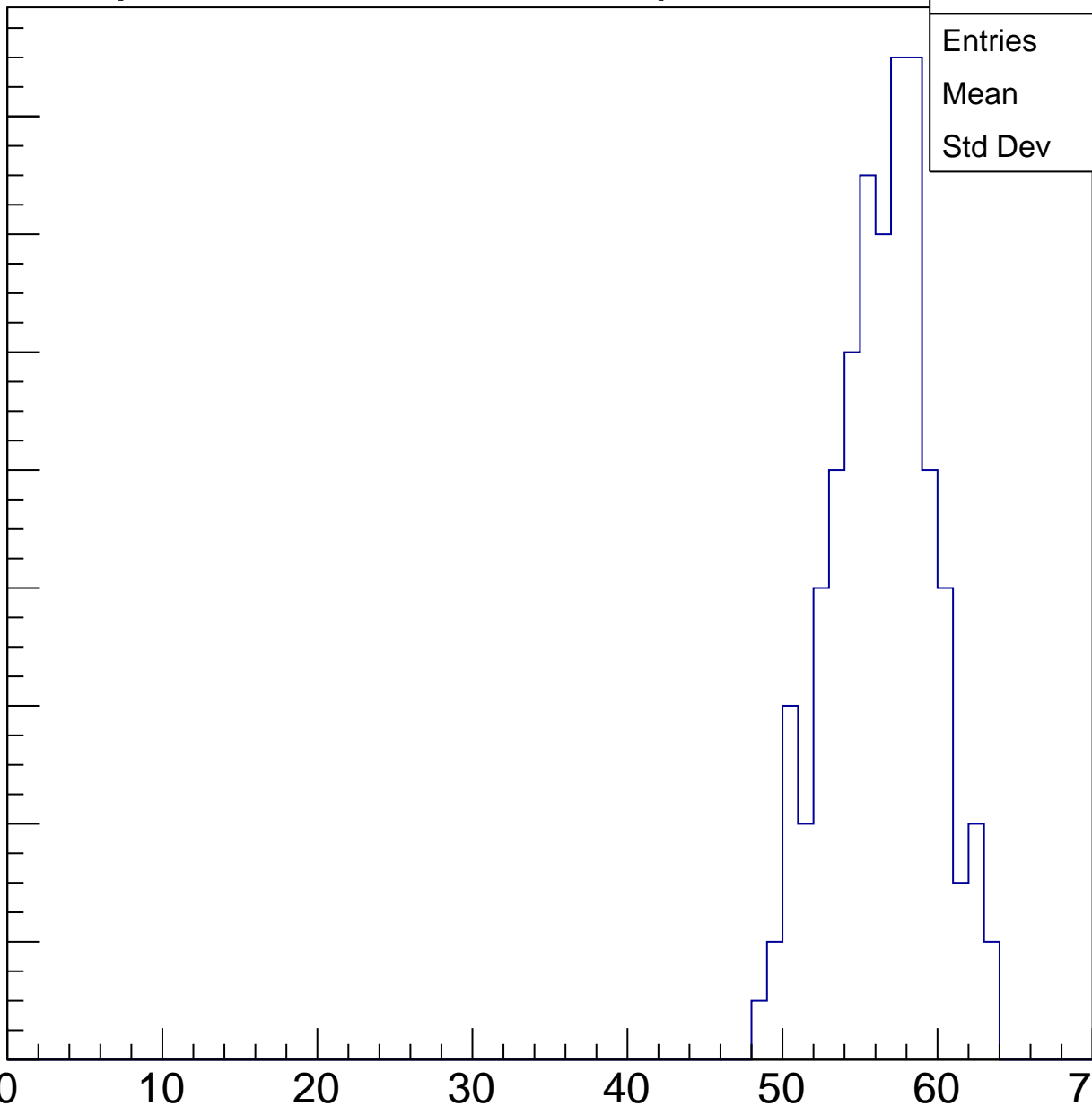
40

50

60

70

ampl



B1L001S, U25-ch57, adc5

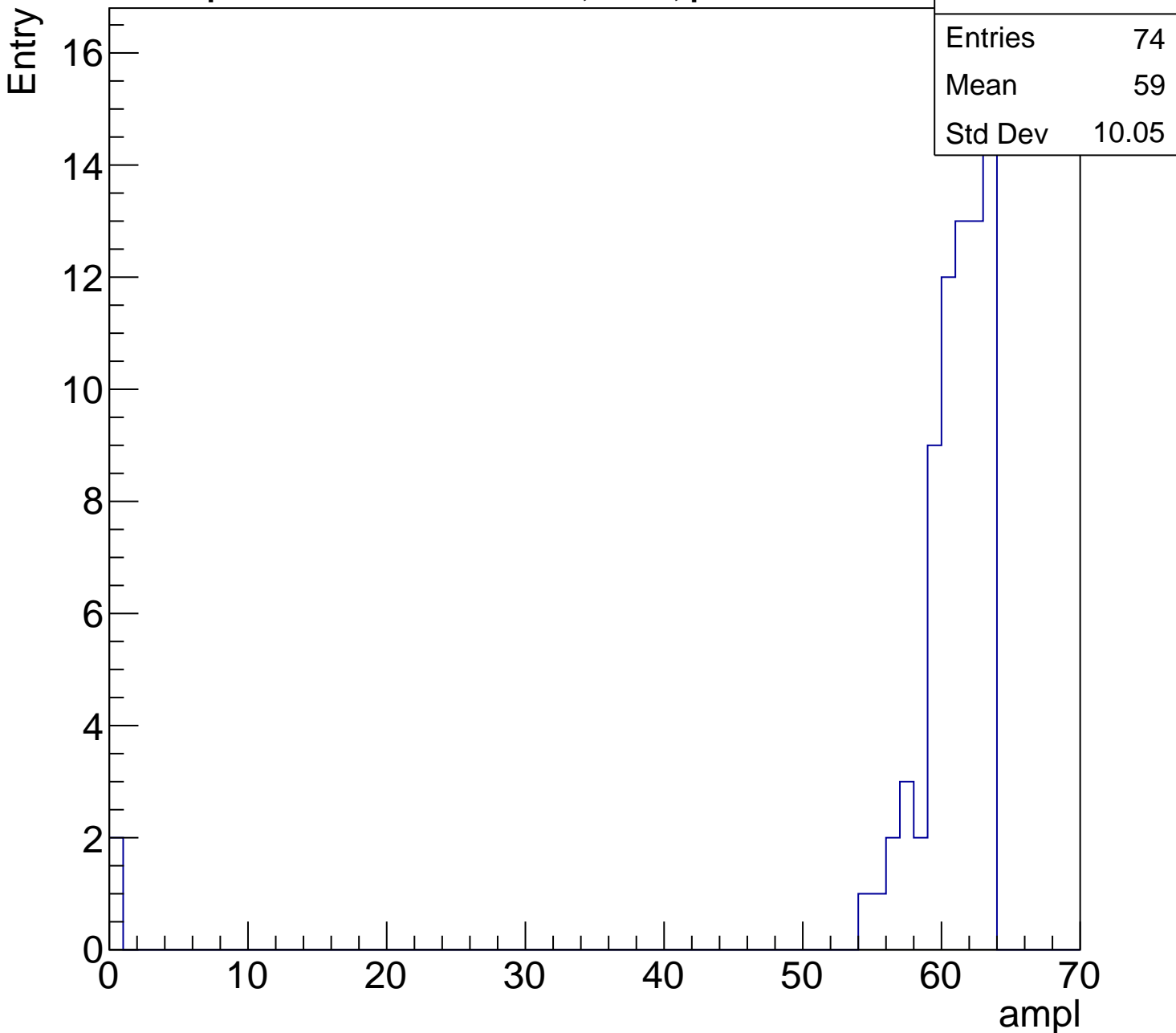
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	74
Mean	59
Std Dev	10.05

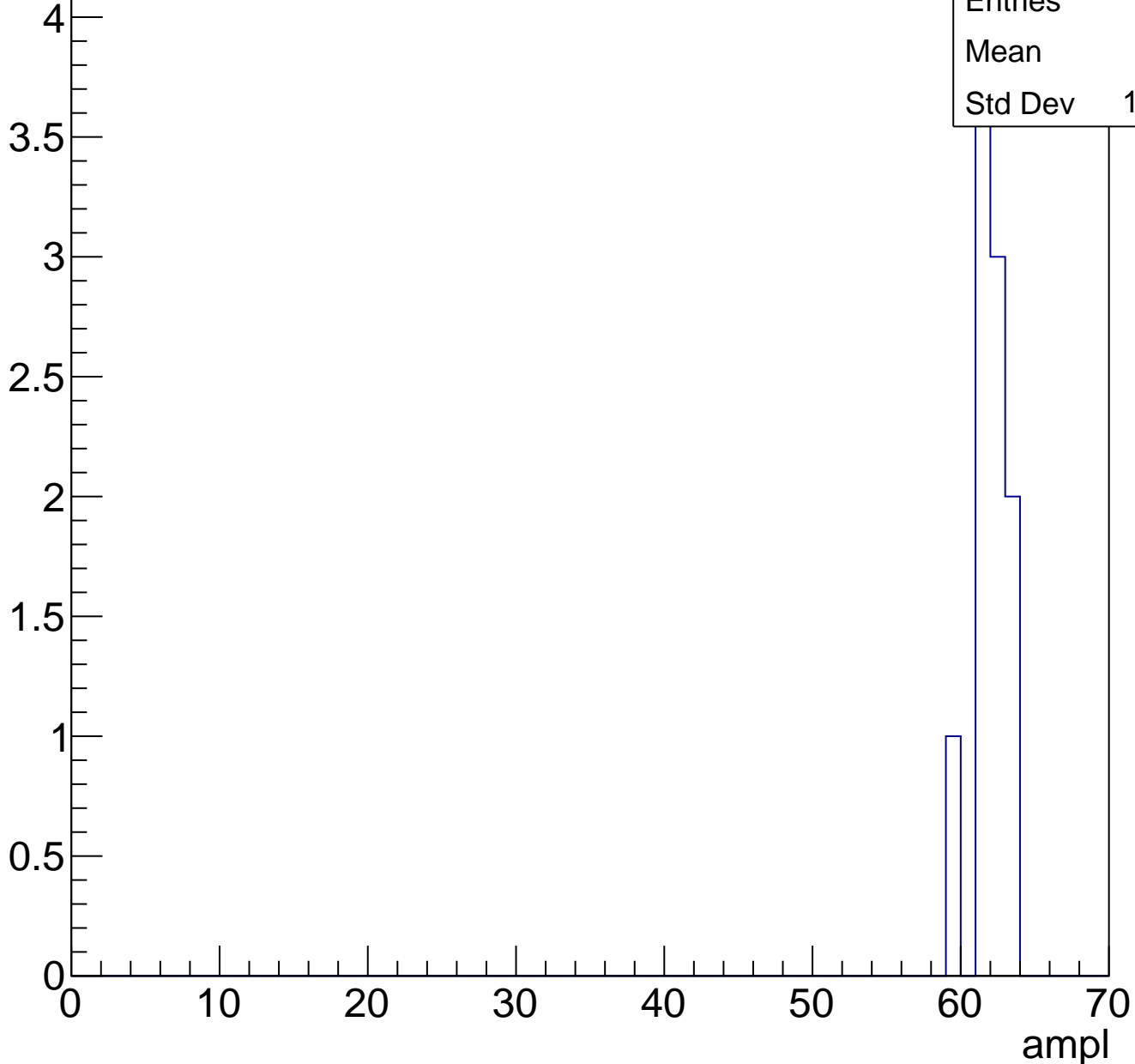
ampl



B1L001S, U25-ch57, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch57, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

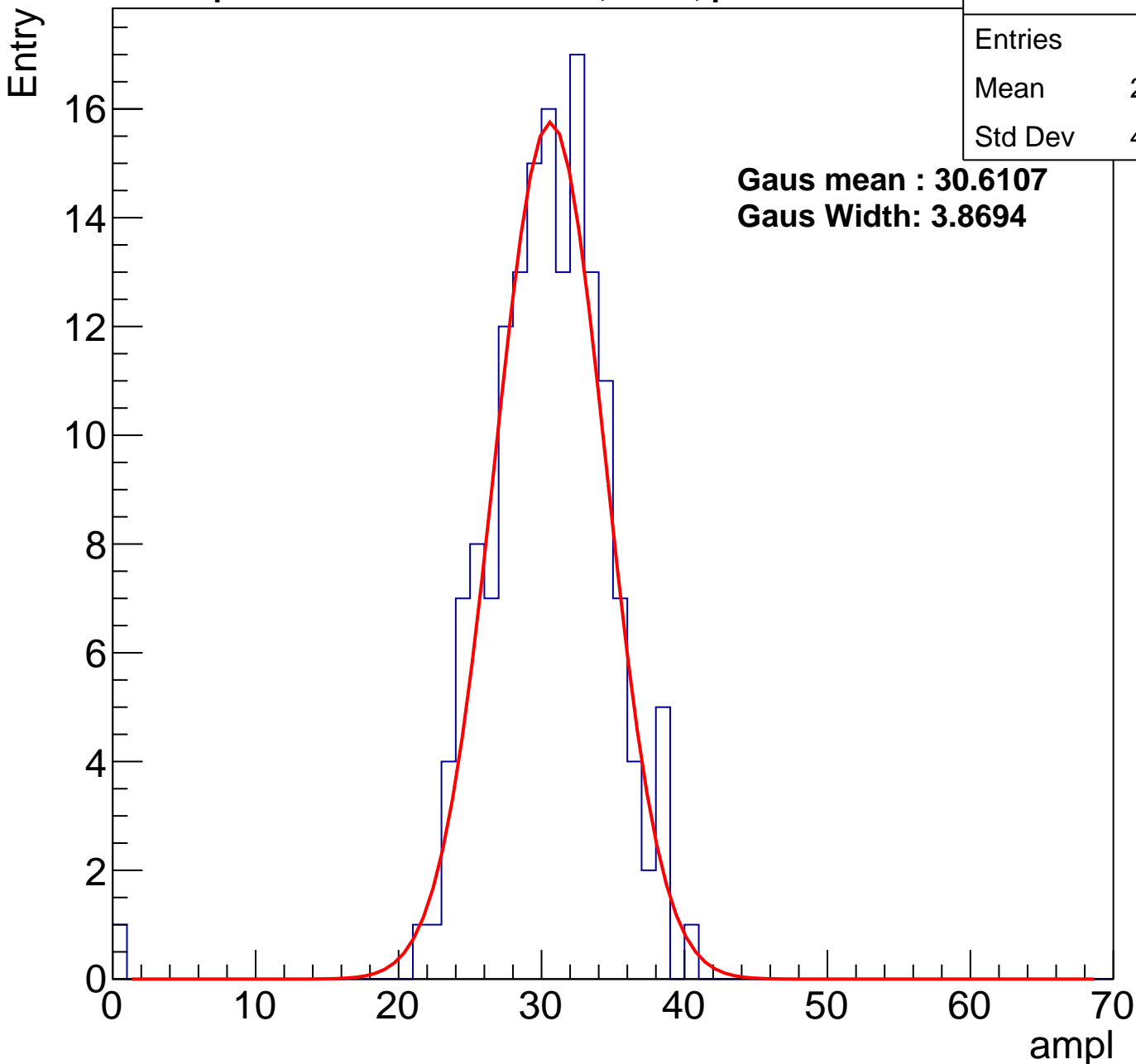
B1L001S, U25-ch58, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	29.94
Std Dev	4.494

Gaus mean : 30.6107

Gaus Width: 3.8694



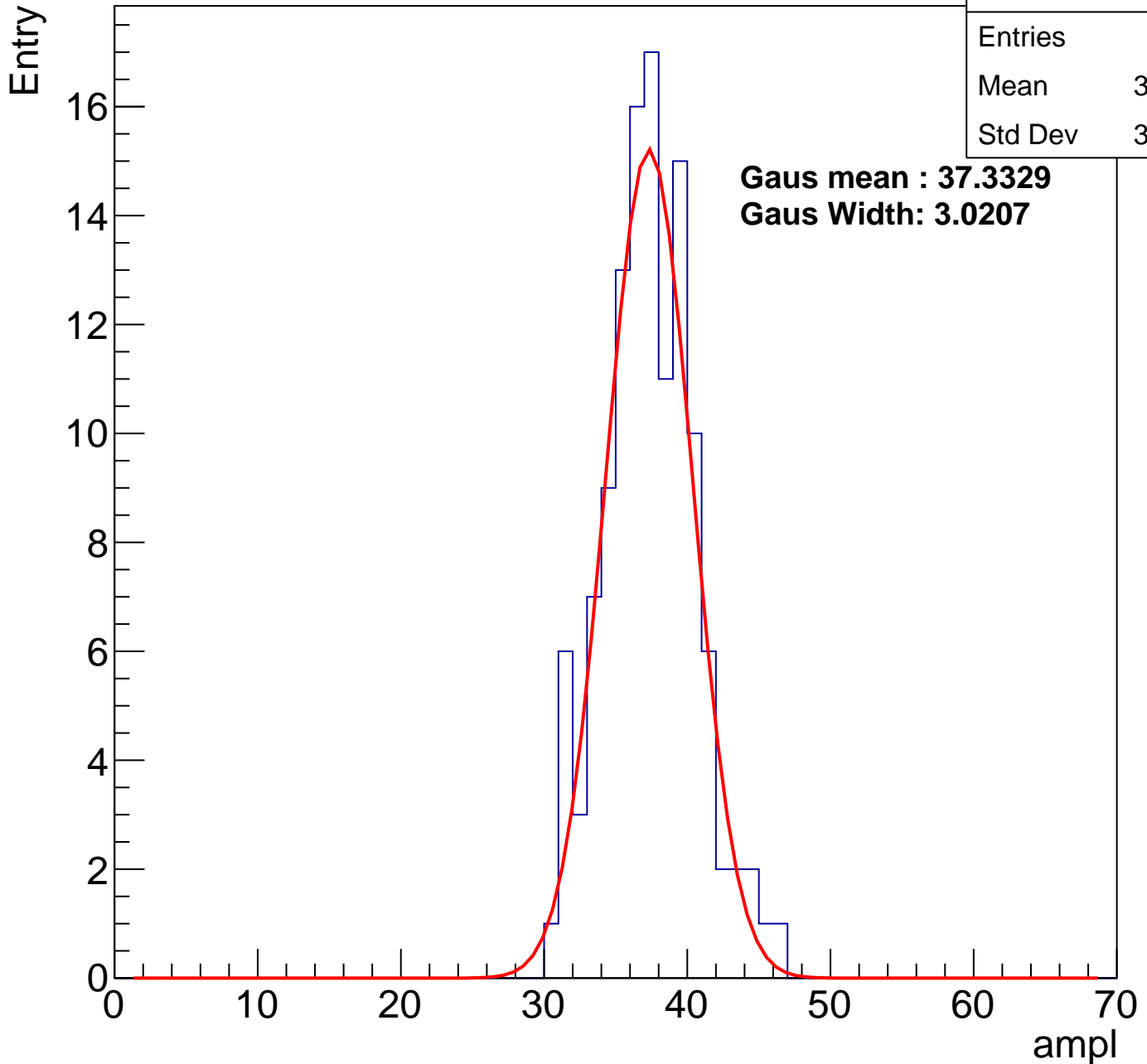
B1L001S, U25-ch58, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	36.94
Std Dev	3.166

Gaus mean : 37.3329

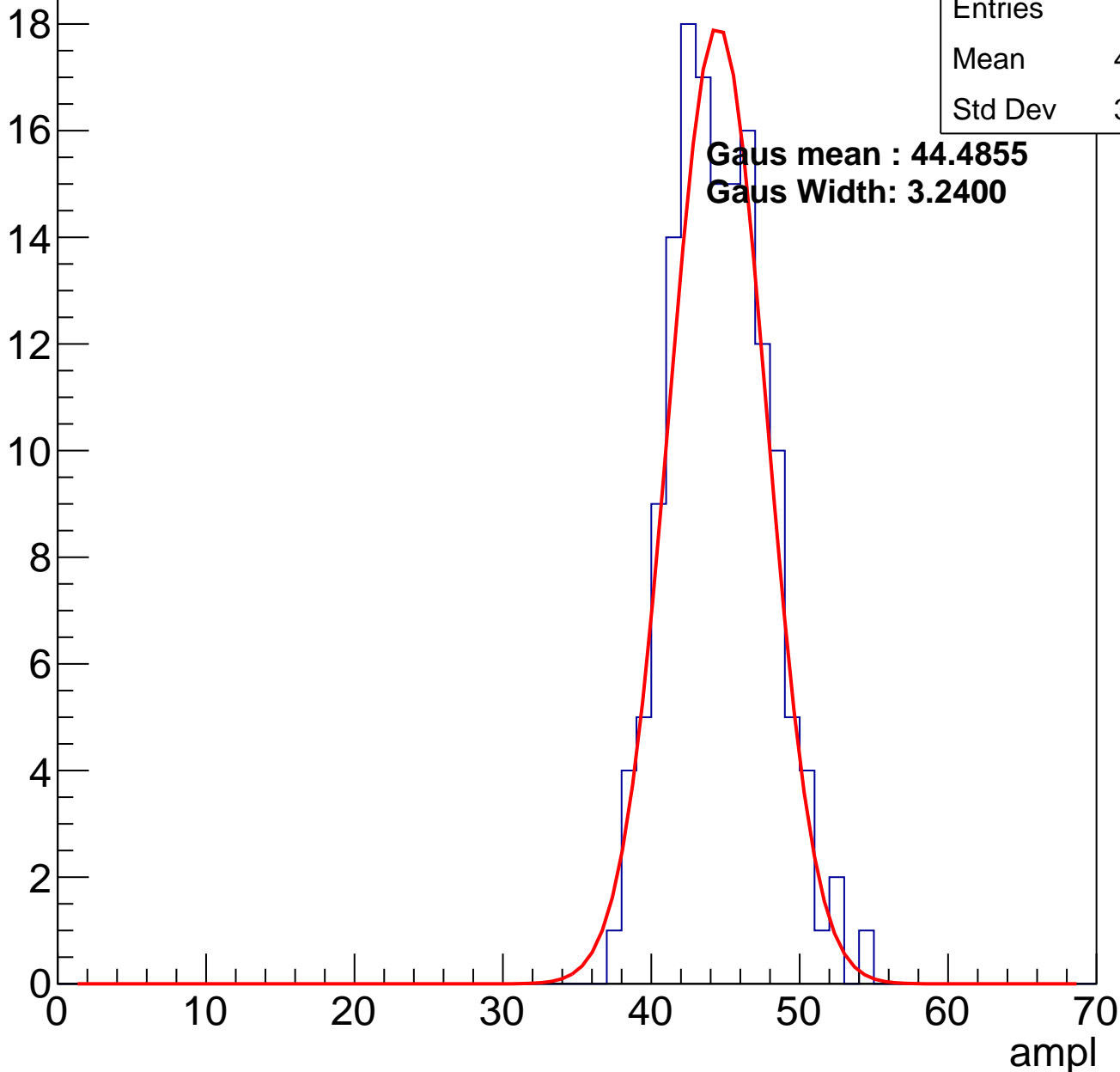
Gaus Width: 3.0207



B1L001S, U25-ch58, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch58, adc3

calib_packv5_042523_0143.root, FC#2, port C2

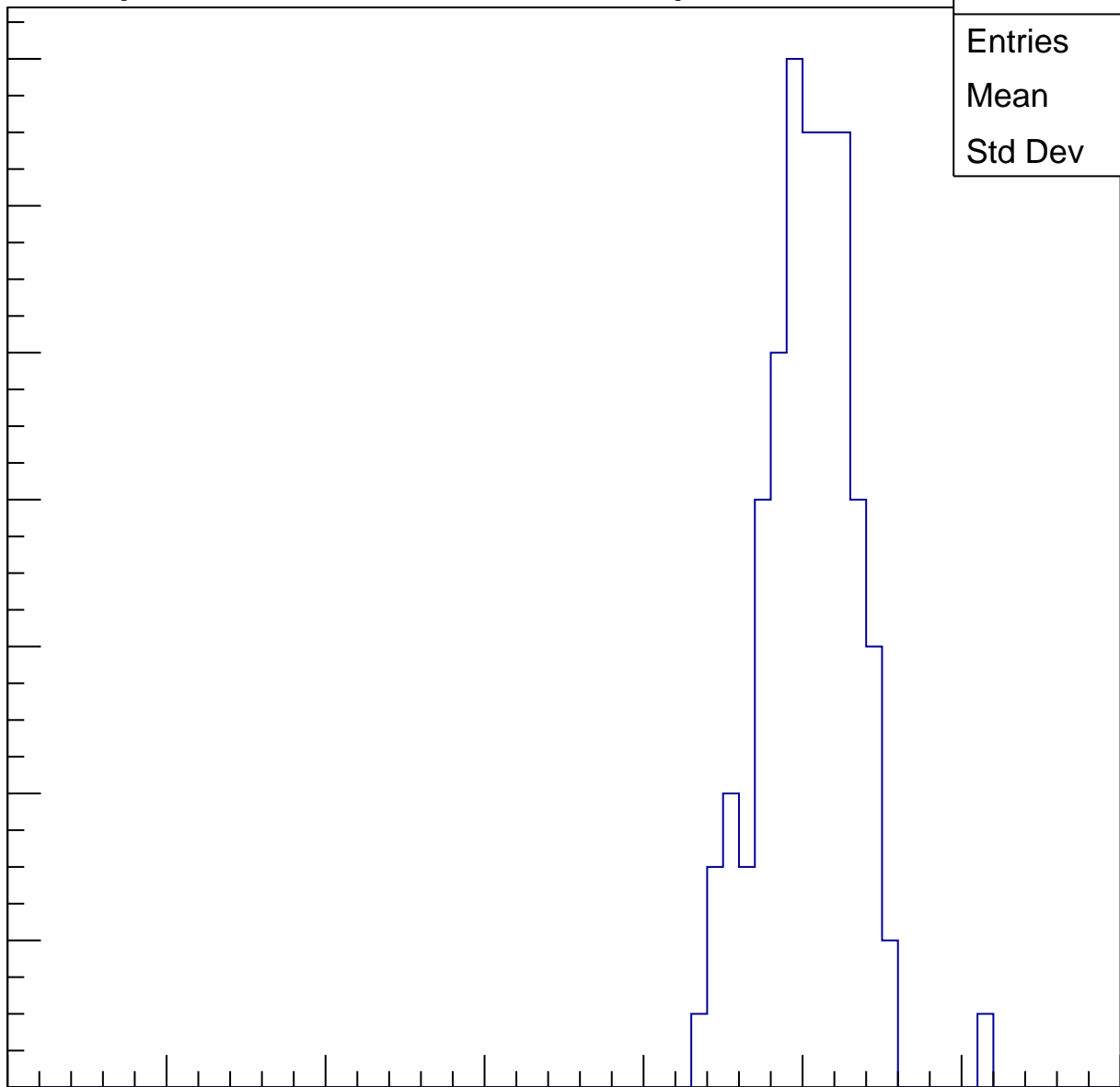
Entries	99
Mean	49.93
Std Dev	2.903

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

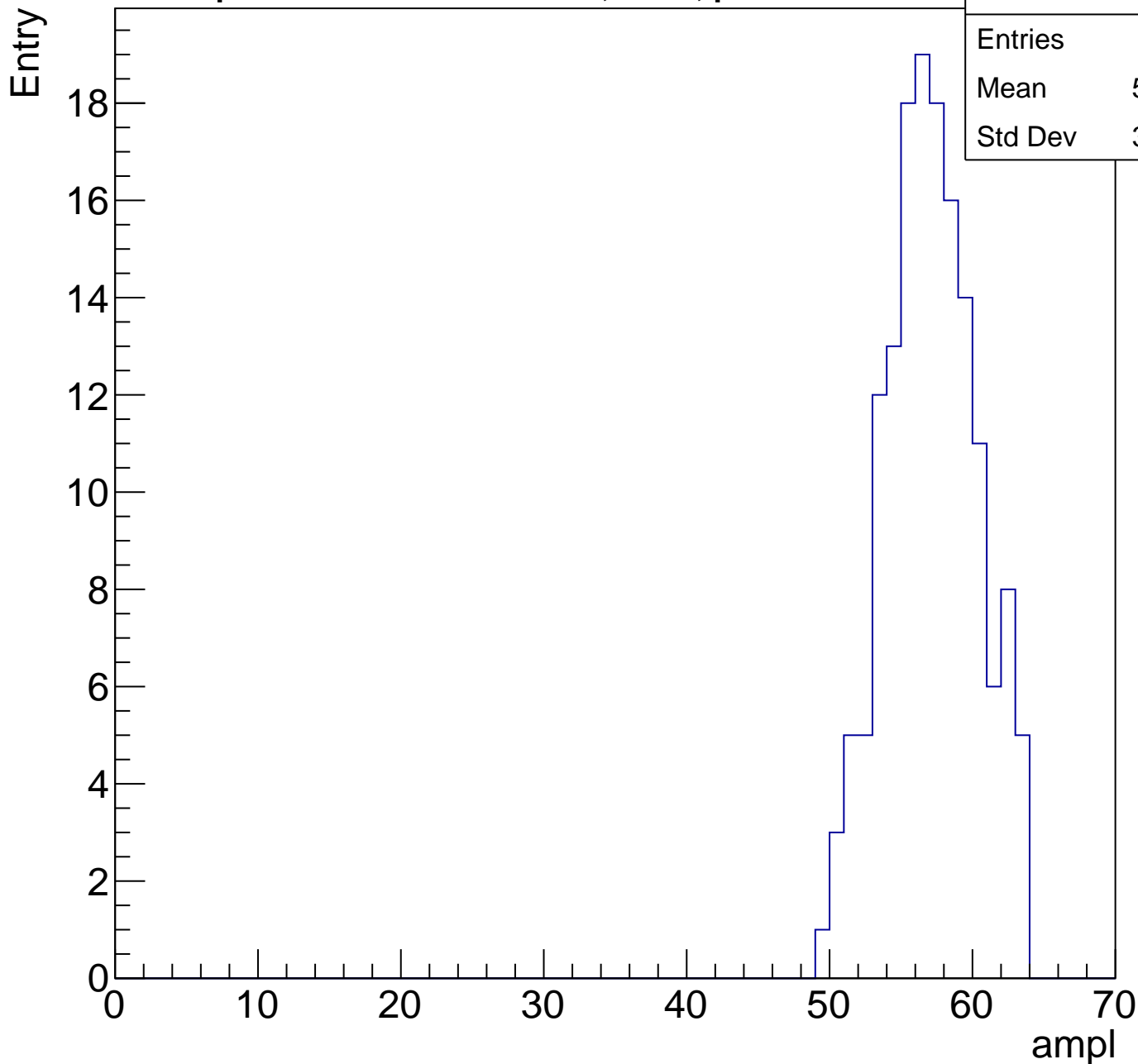
ampl



B1L001S, U25-ch58, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	56.64
Std Dev	3.166



B1L001S, U25-ch58, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

57

Mean

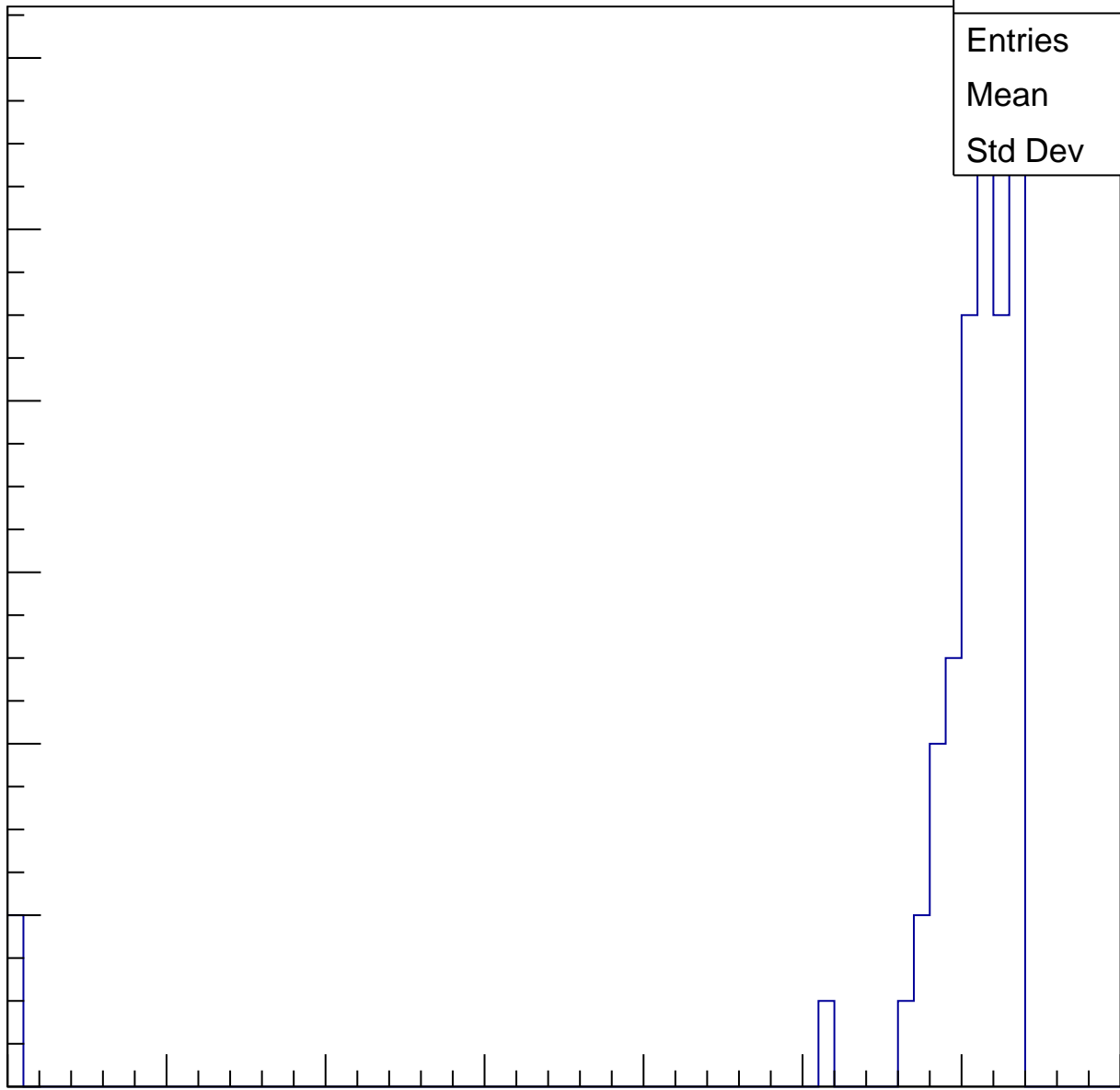
58.49

Std Dev

11.36

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch58, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.88

Std Dev

1.269

B1L001S, U25-ch58, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch59, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	30.89
Std Dev	2.892

Gaus mean : 31.2076

Gaus Width: 2.6414

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

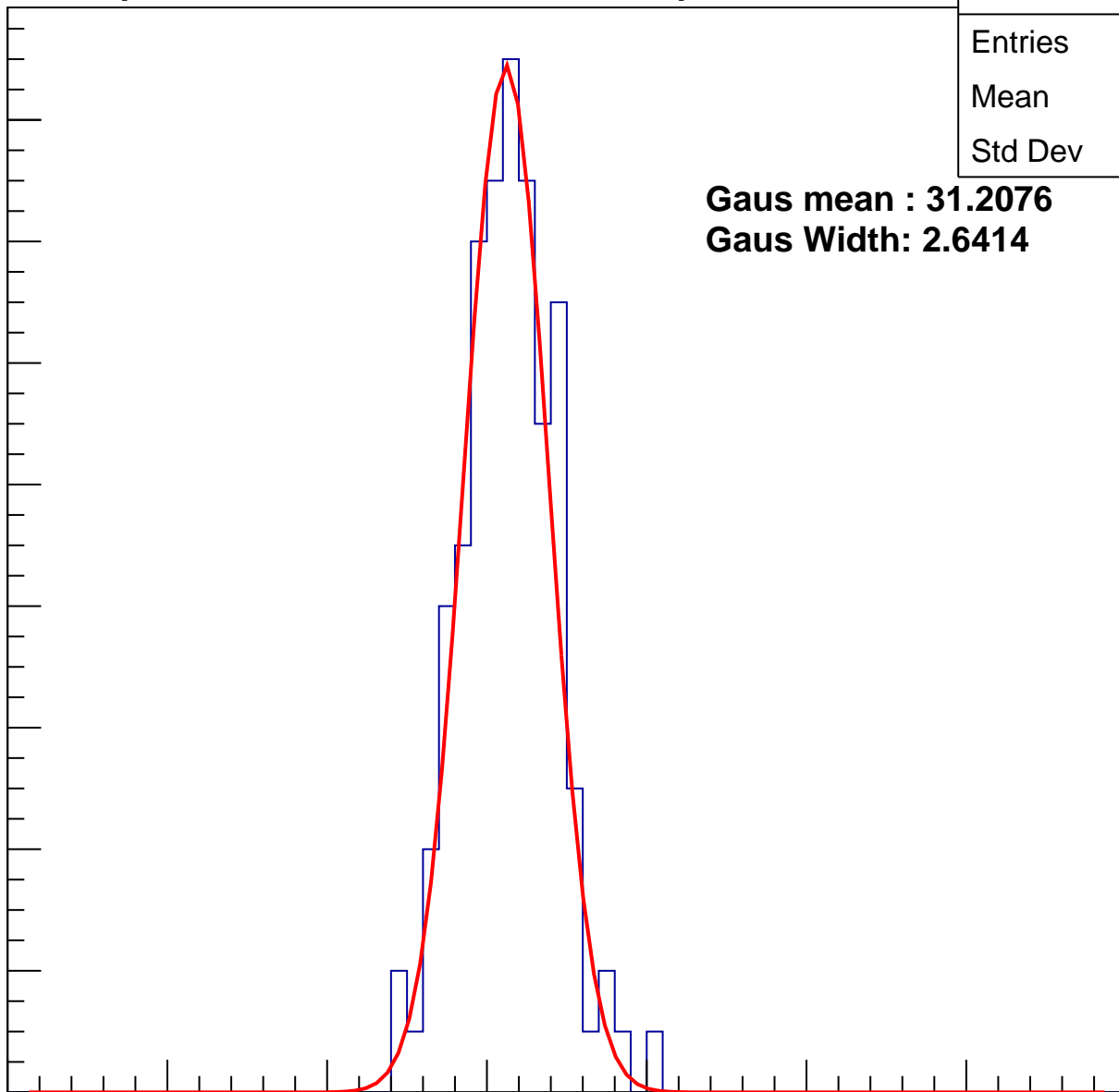
40

50

60

70

ampl



B1L001S, U25-ch59, adc1

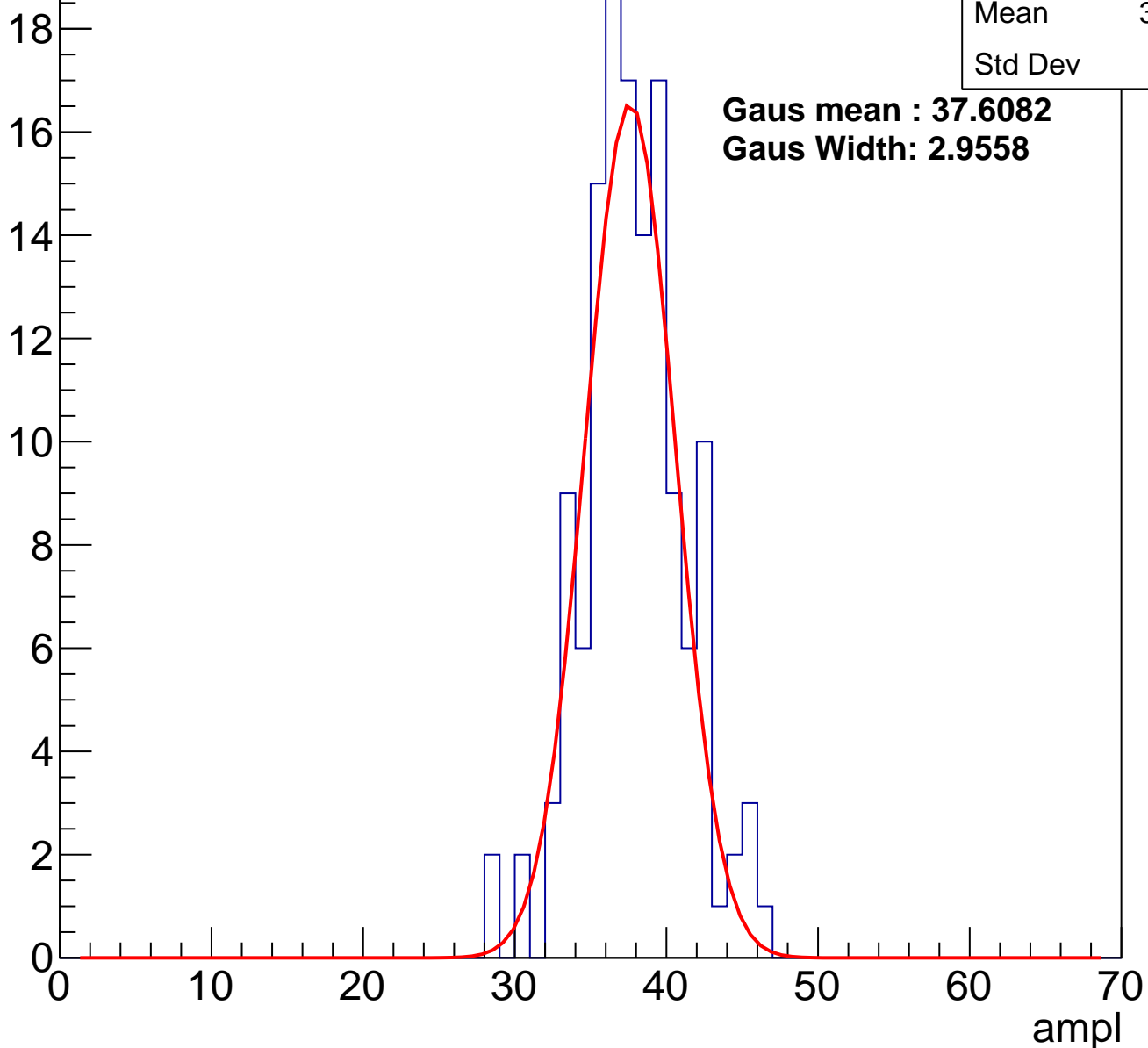
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	37.38
Std Dev	3.33

Gaus mean : 37.6082

Gaus Width: 2.9558

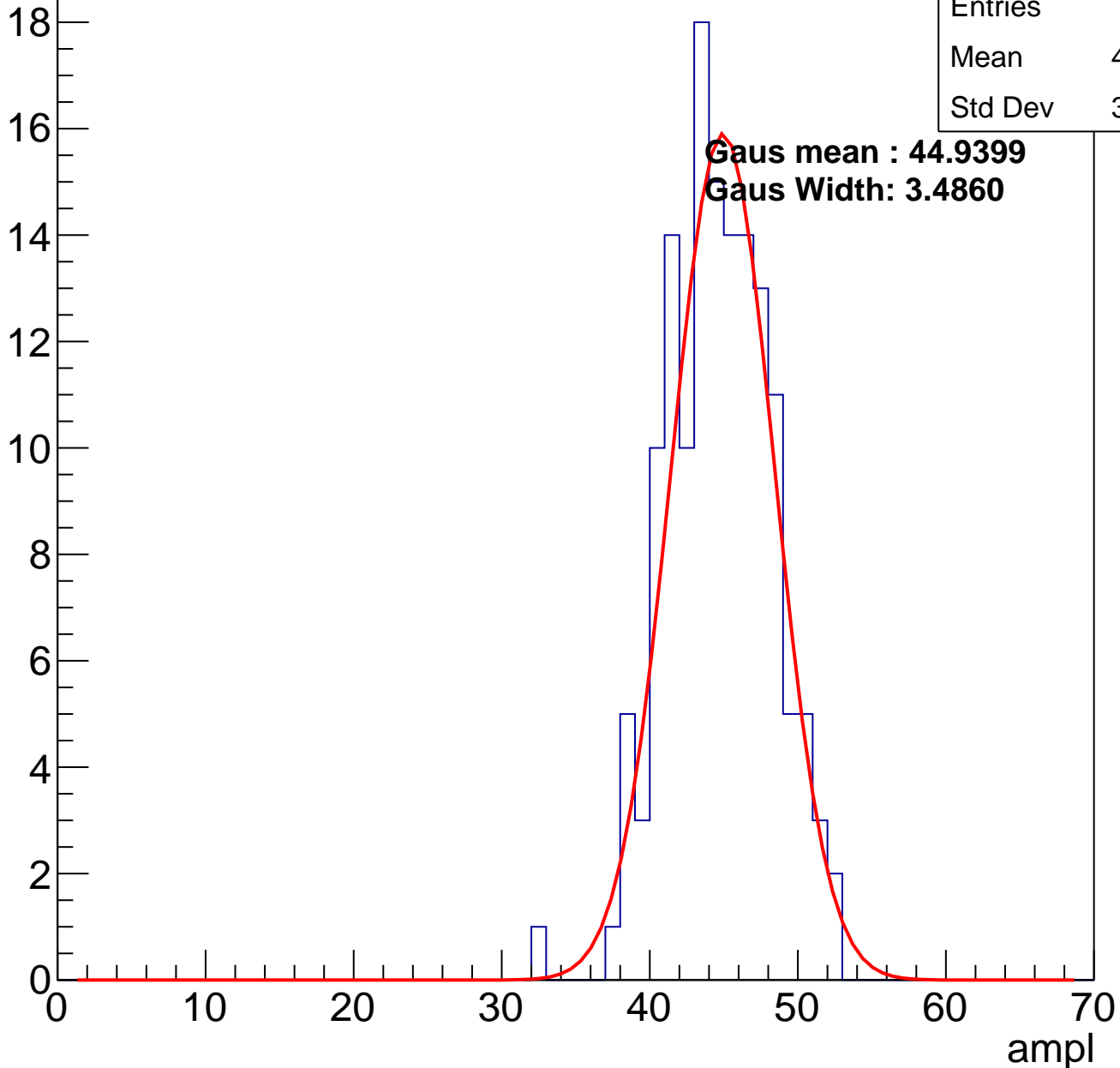
Entry



B1L001S, U25-ch59, adc2

calib_packv5_042523_0143.root, FC#2, port C2

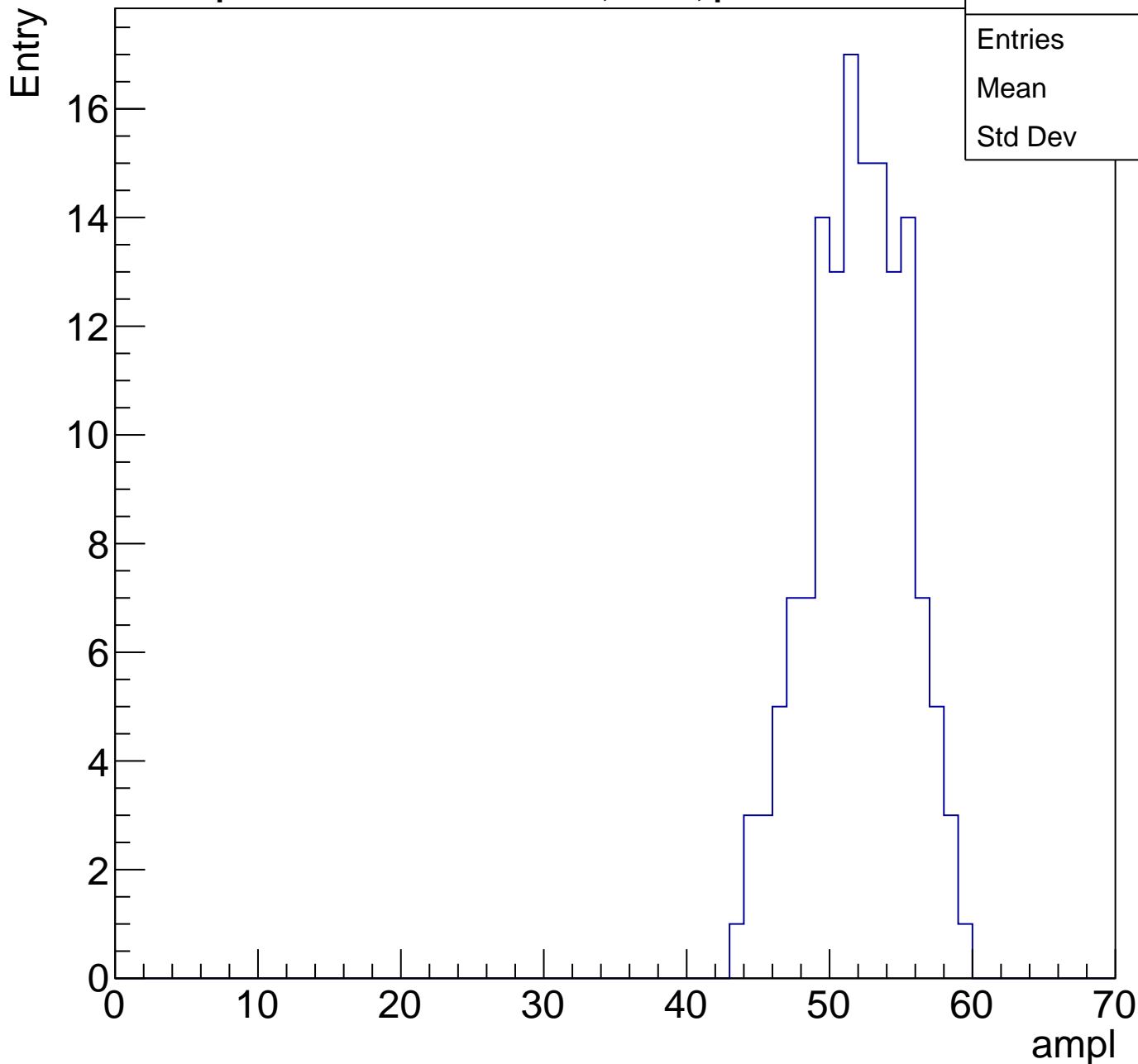
Entry



B1L001S, U25-ch59, adc3

calib_packv5_042523_0143.root, FC#2, port C2

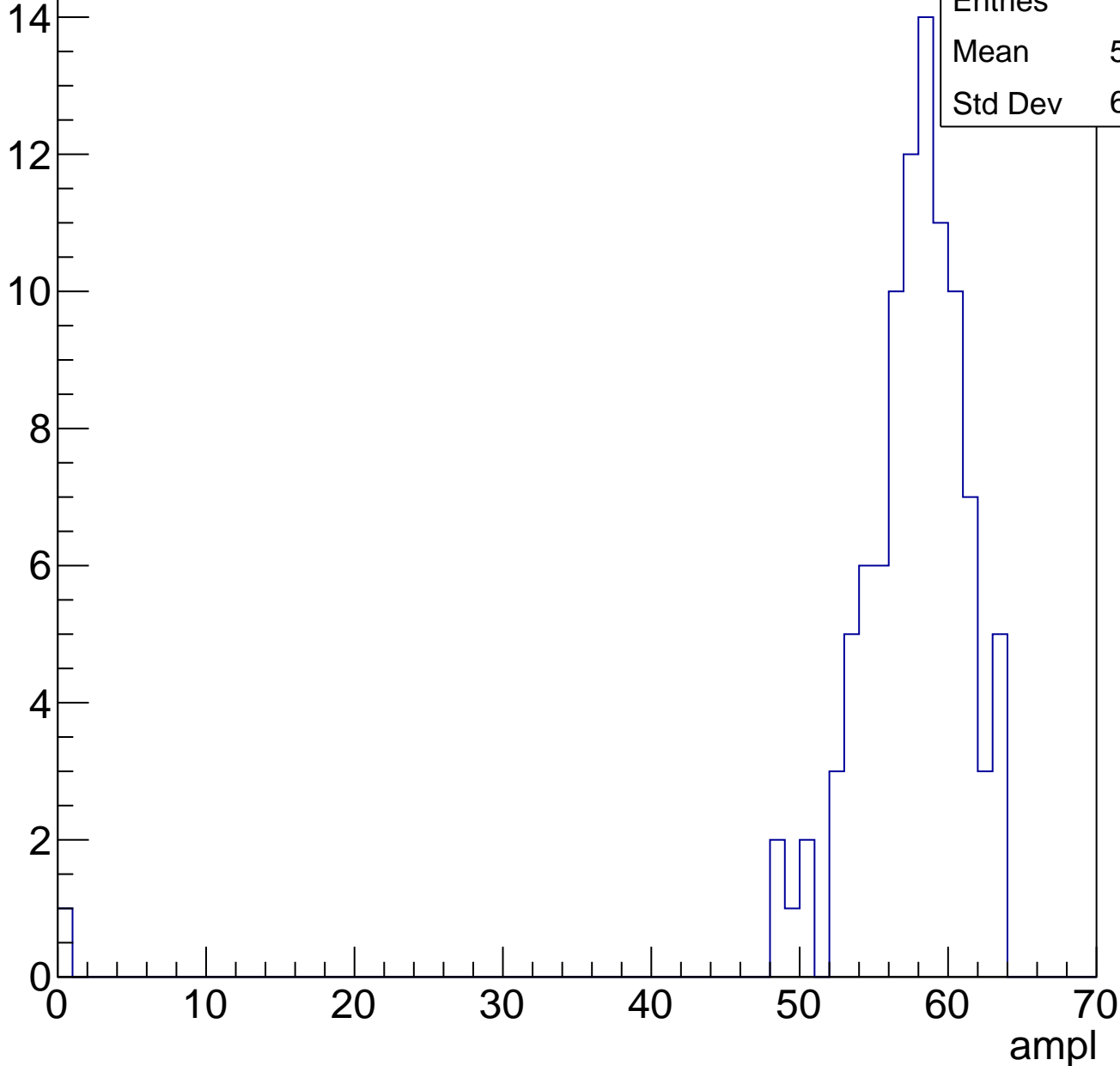
Entries	143
Mean	51.5
Std Dev	3.38



B1L001S, U25-ch59, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch59, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

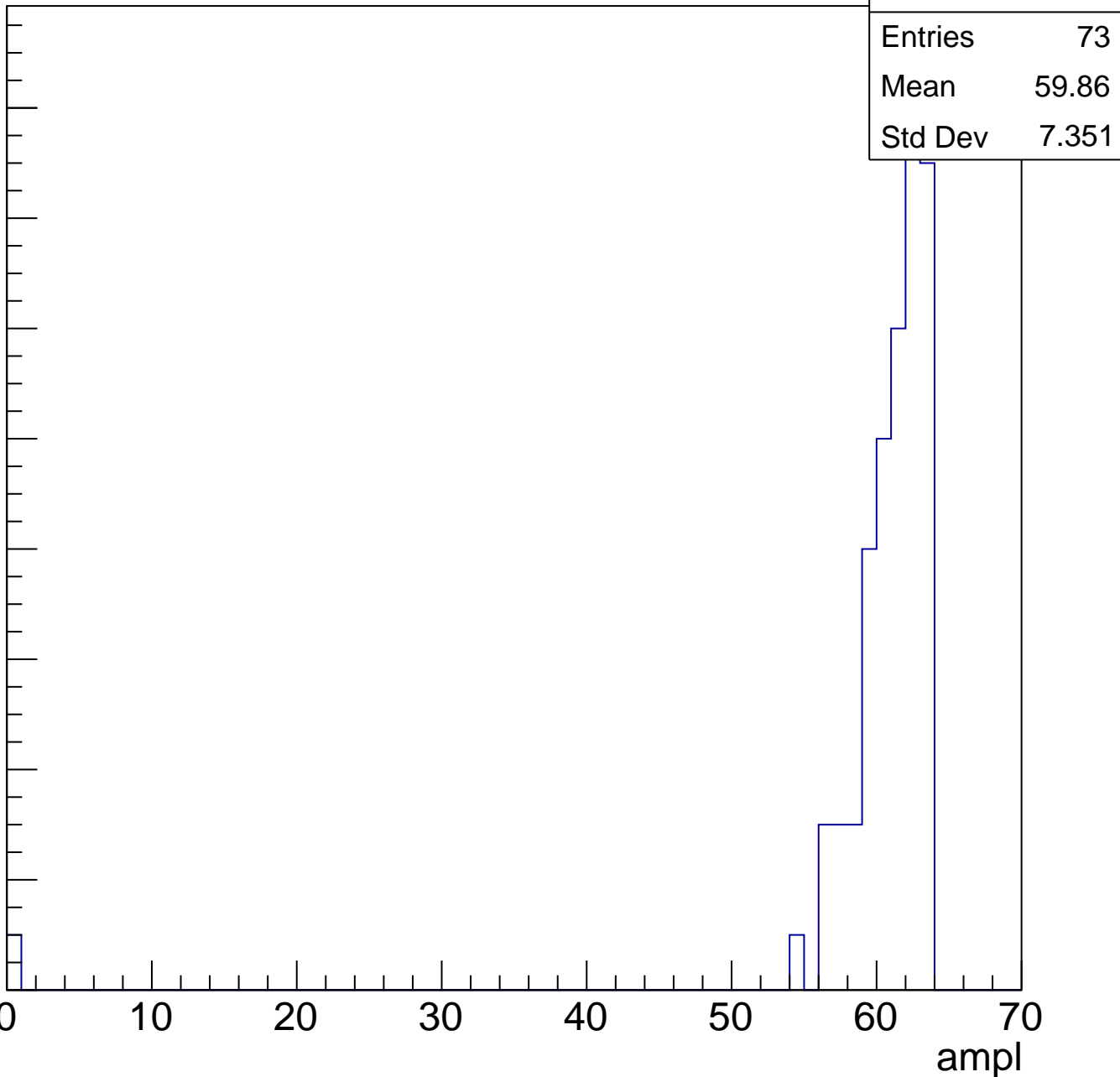
40

50

60

ampl

Entries	73
Mean	59.86
Std Dev	7.351



B1L001S, U25-ch59, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch59, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

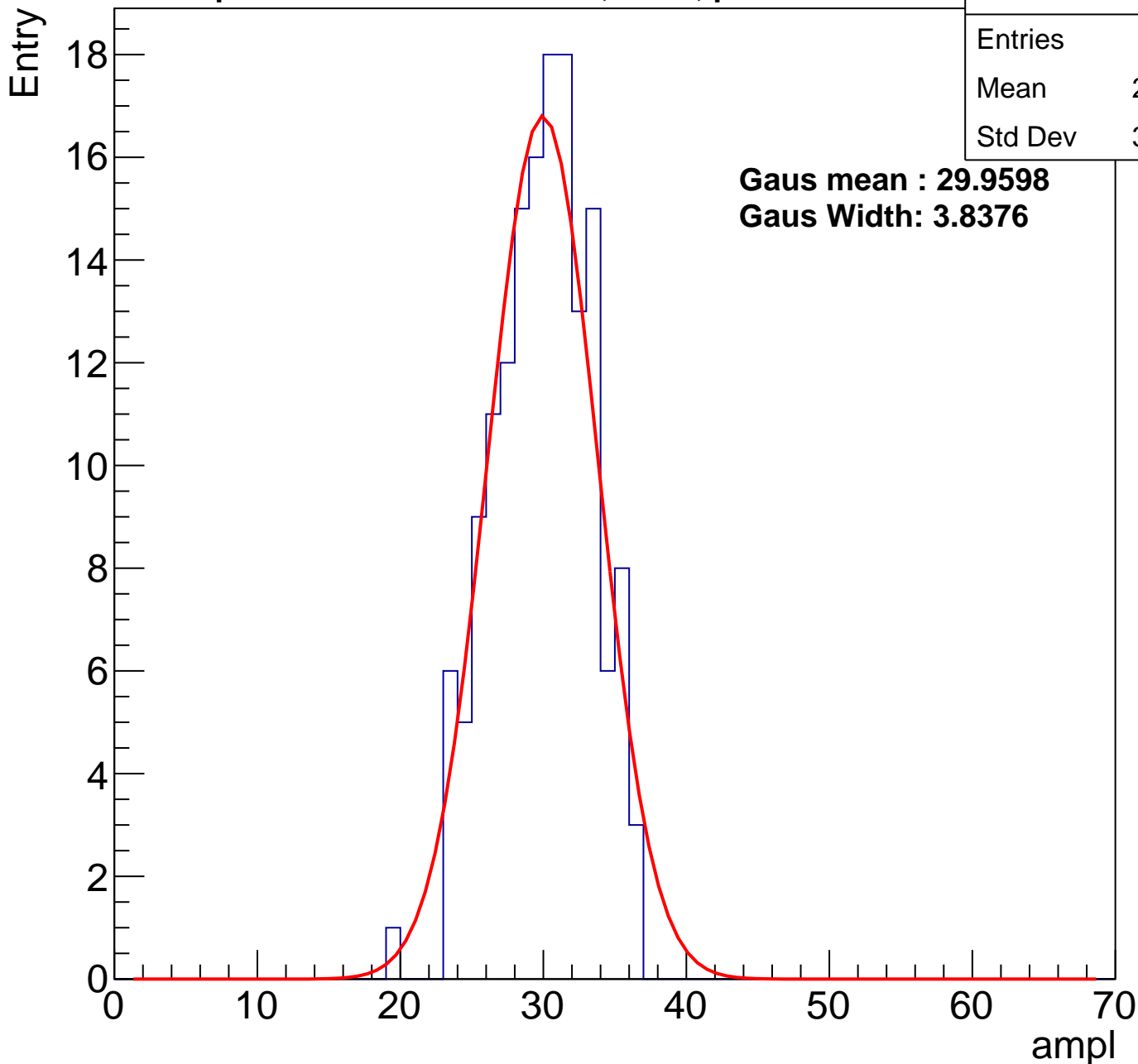
B1L001S, U25-ch60, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	29.47
Std Dev	3.344

Gaus mean : 29.9598

Gaus Width: 3.8376



B1L001S, U25-ch60, adc1

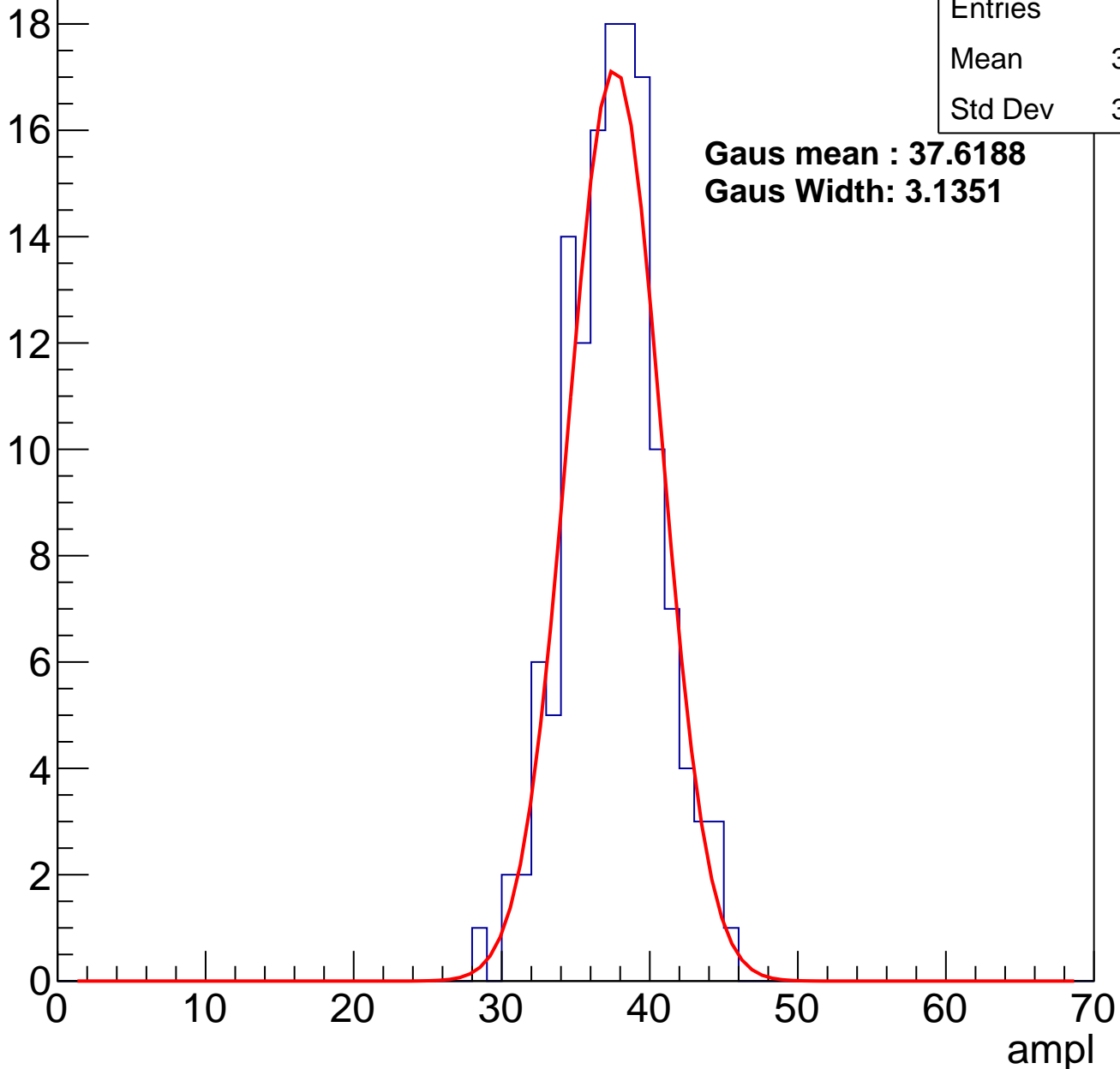
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	37.07
Std Dev	3.148

Gaus mean : 37.6188

Gaus Width: 3.1351

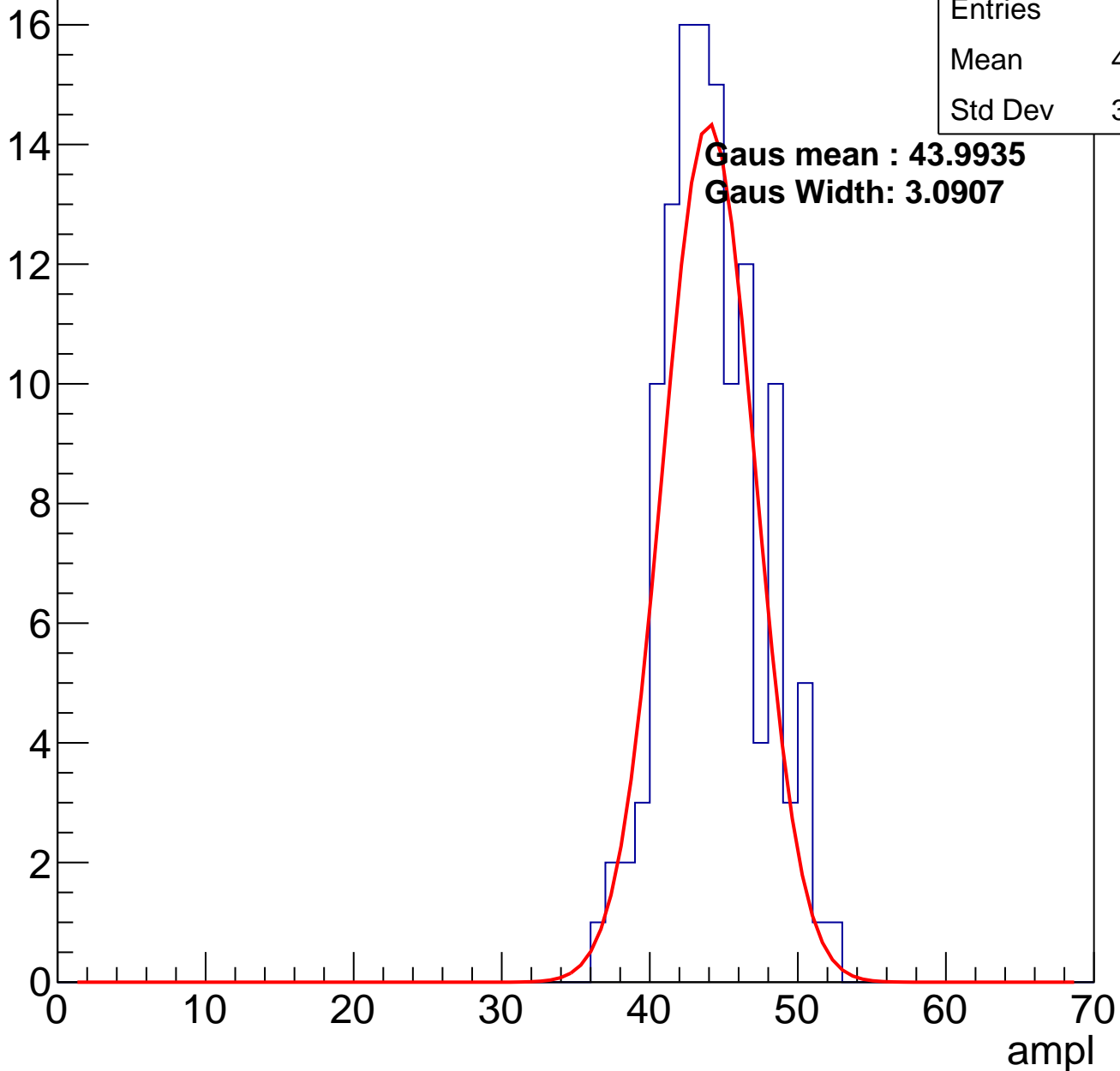
Entry



B1L001S, U25-ch60, adc2

calib_packv5_042523_0143.root, FC#2, port C2

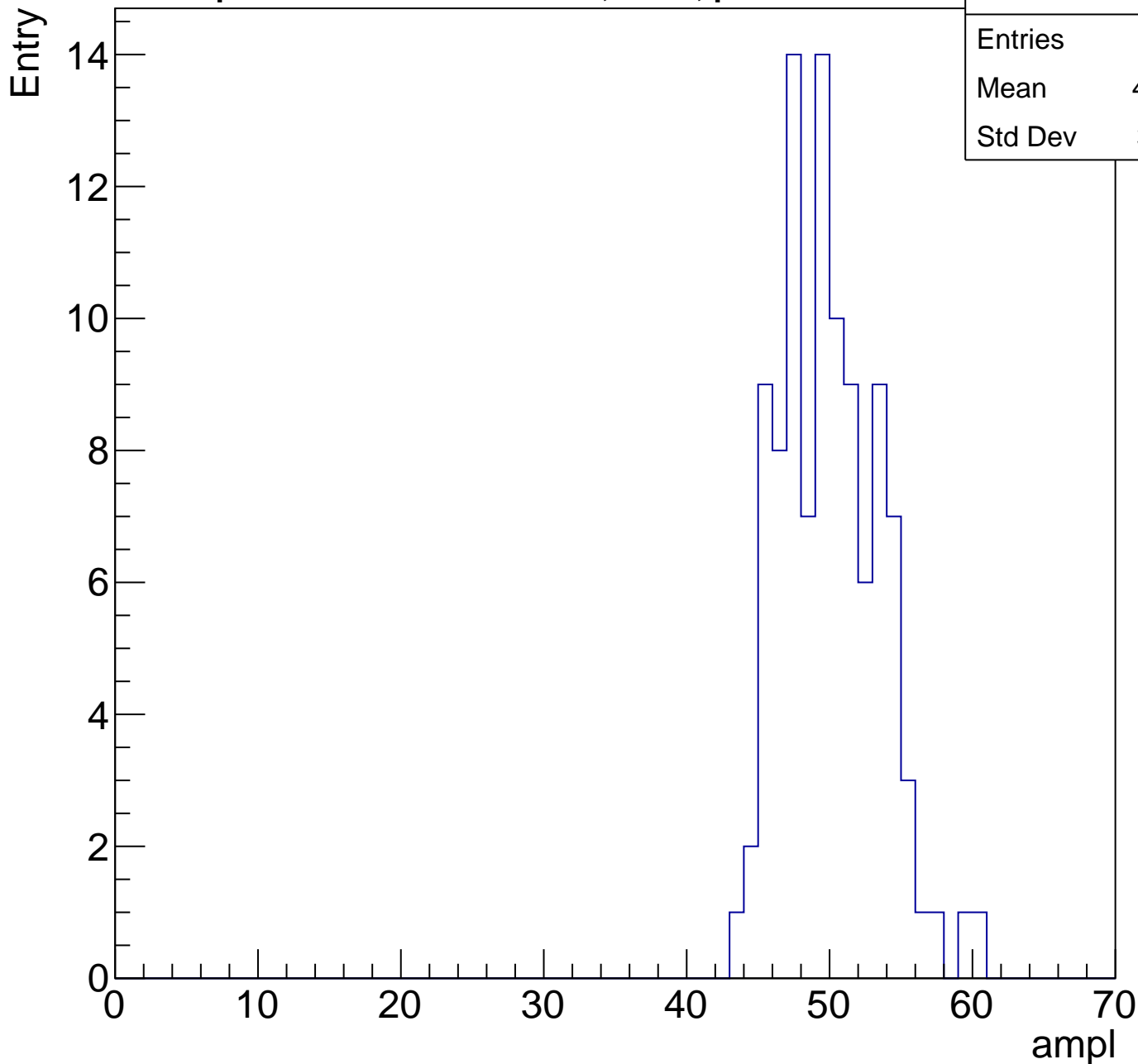
Entry



B1L001S, U25-ch60, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	49.58
Std Dev	3.401



B1L001S, U25-ch60, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	54.95
Std Dev	3.601

Entry

12

10

8

6

4

2

0

0

10

20

30

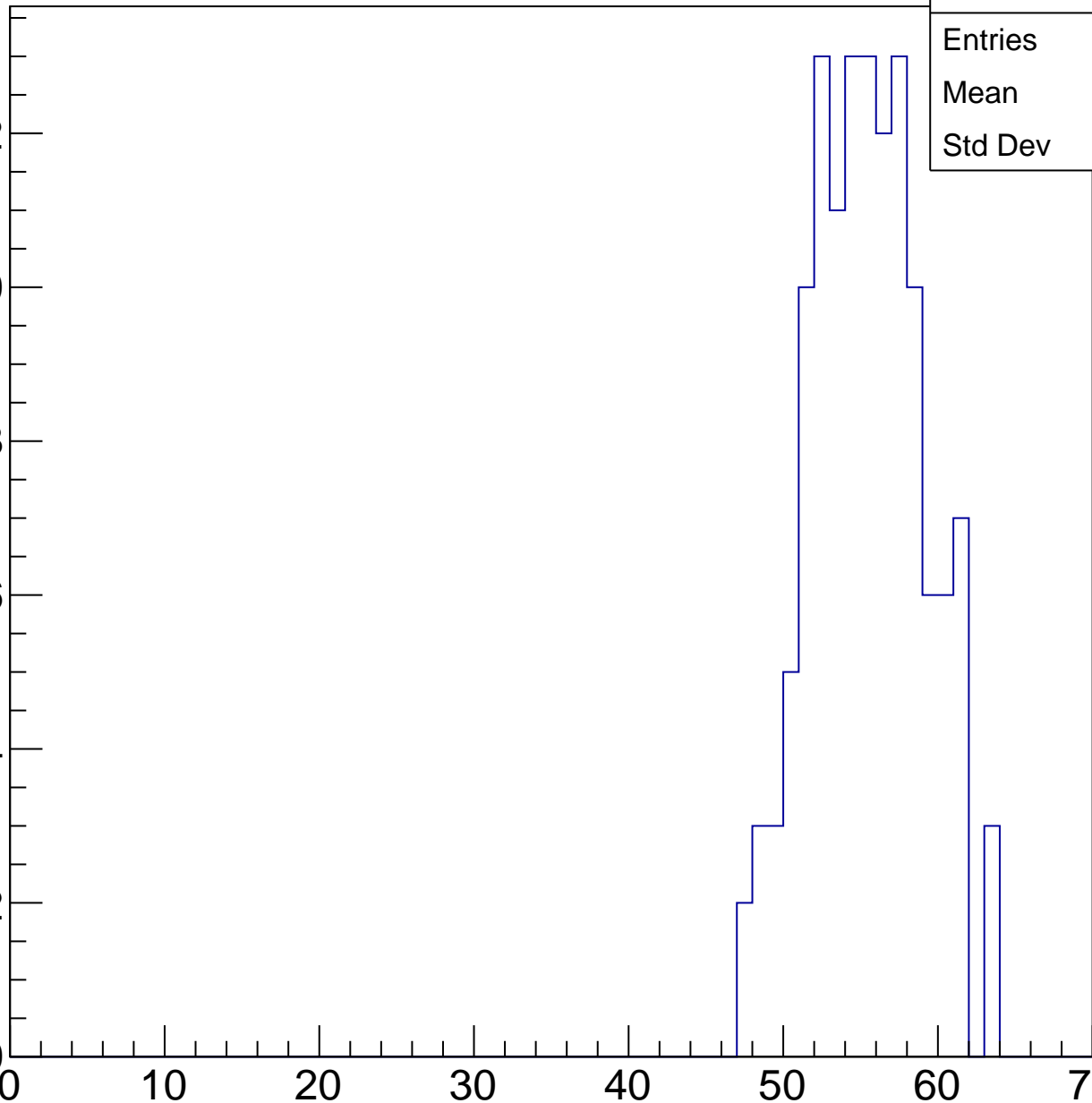
40

50

60

70

ampl



B1L001S, U25-ch60, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	89
Mean	59.02
Std Dev	6.706

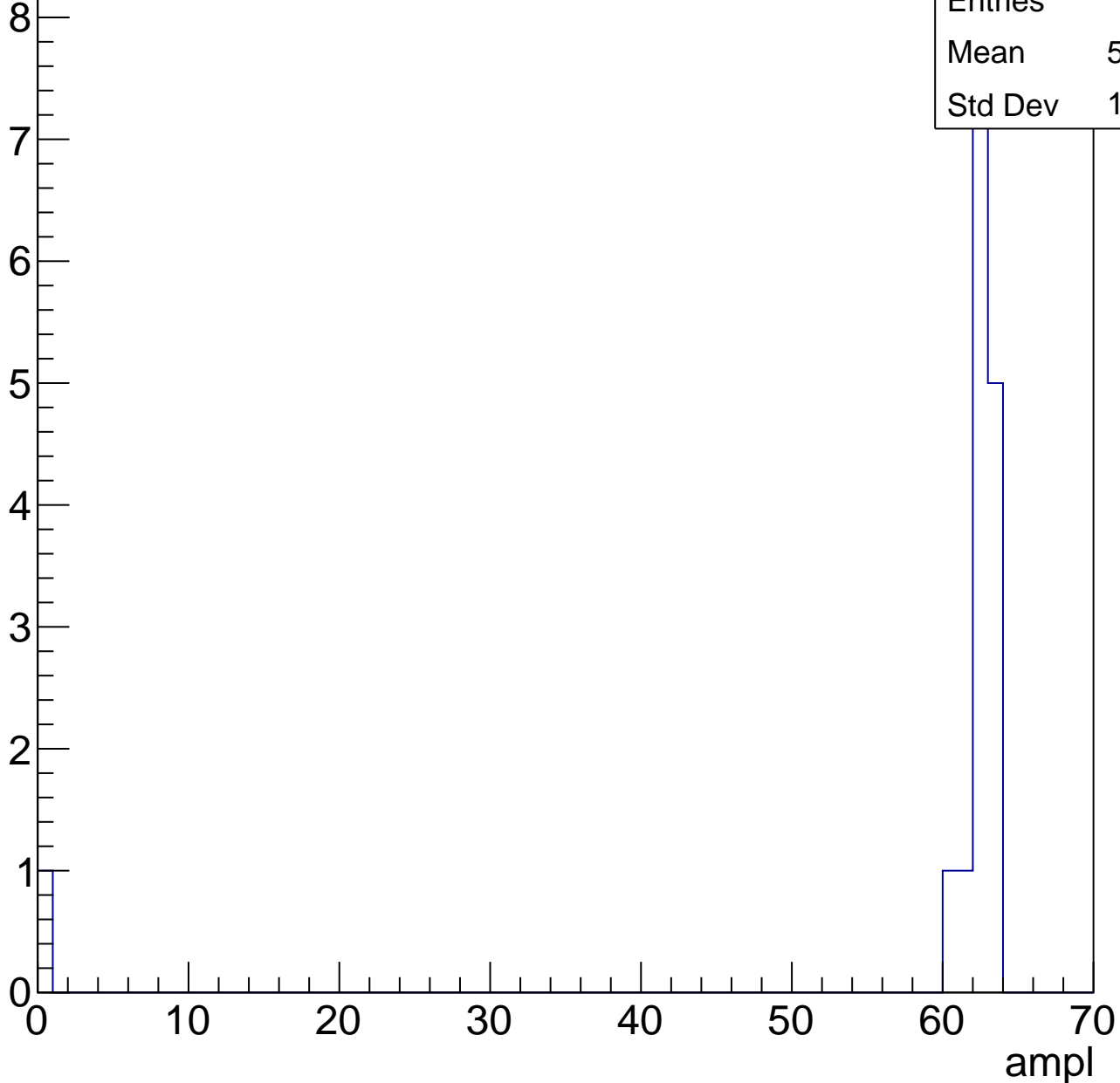
0

B1L001S, U25-ch60, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	58.25
Std Dev	15.06



B1L001S, U25-ch60, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch61, adc0

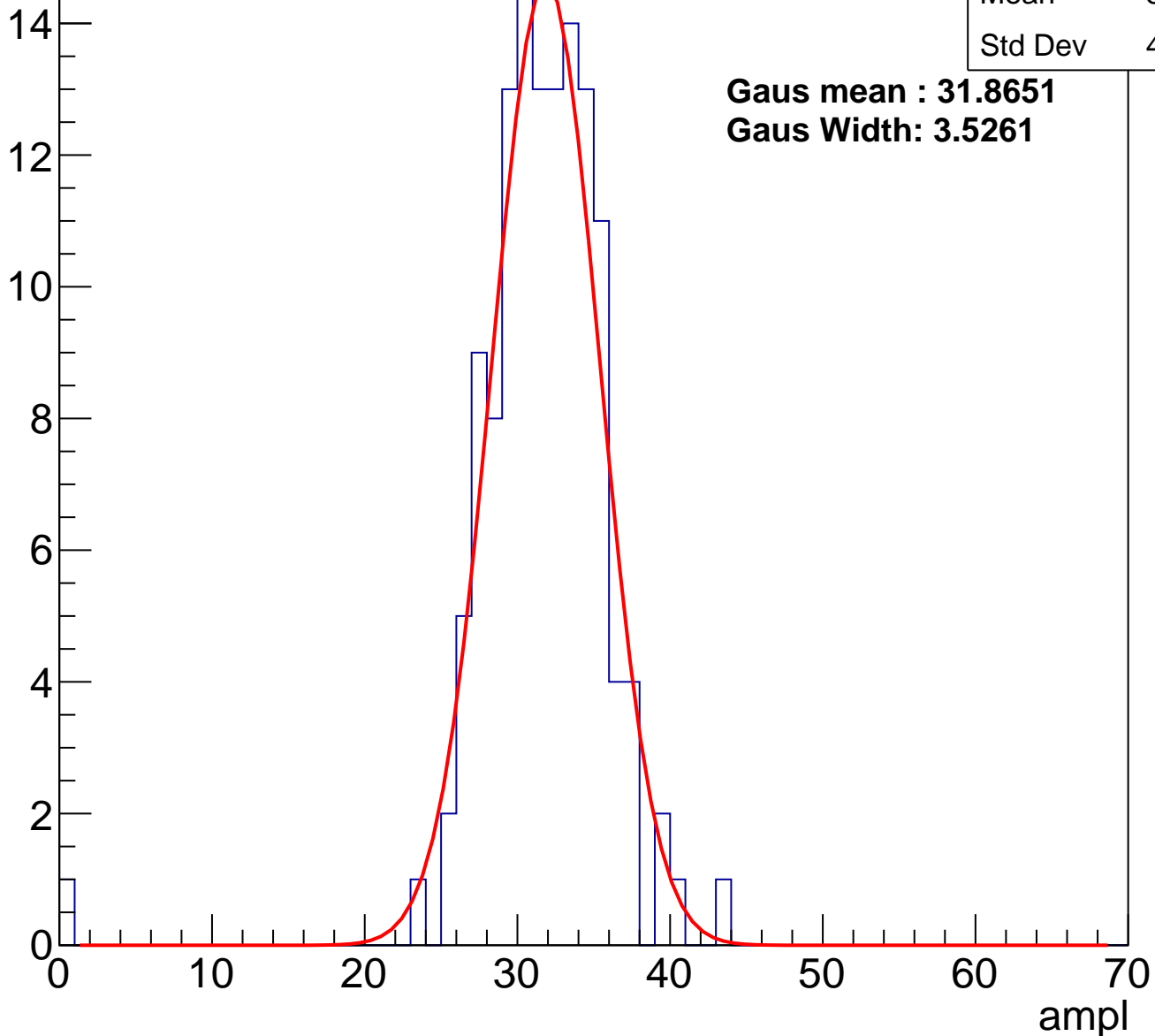
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	31.22
Std Dev	4.348

Gaus mean : 31.8651

Gaus Width: 3.5261

Entry

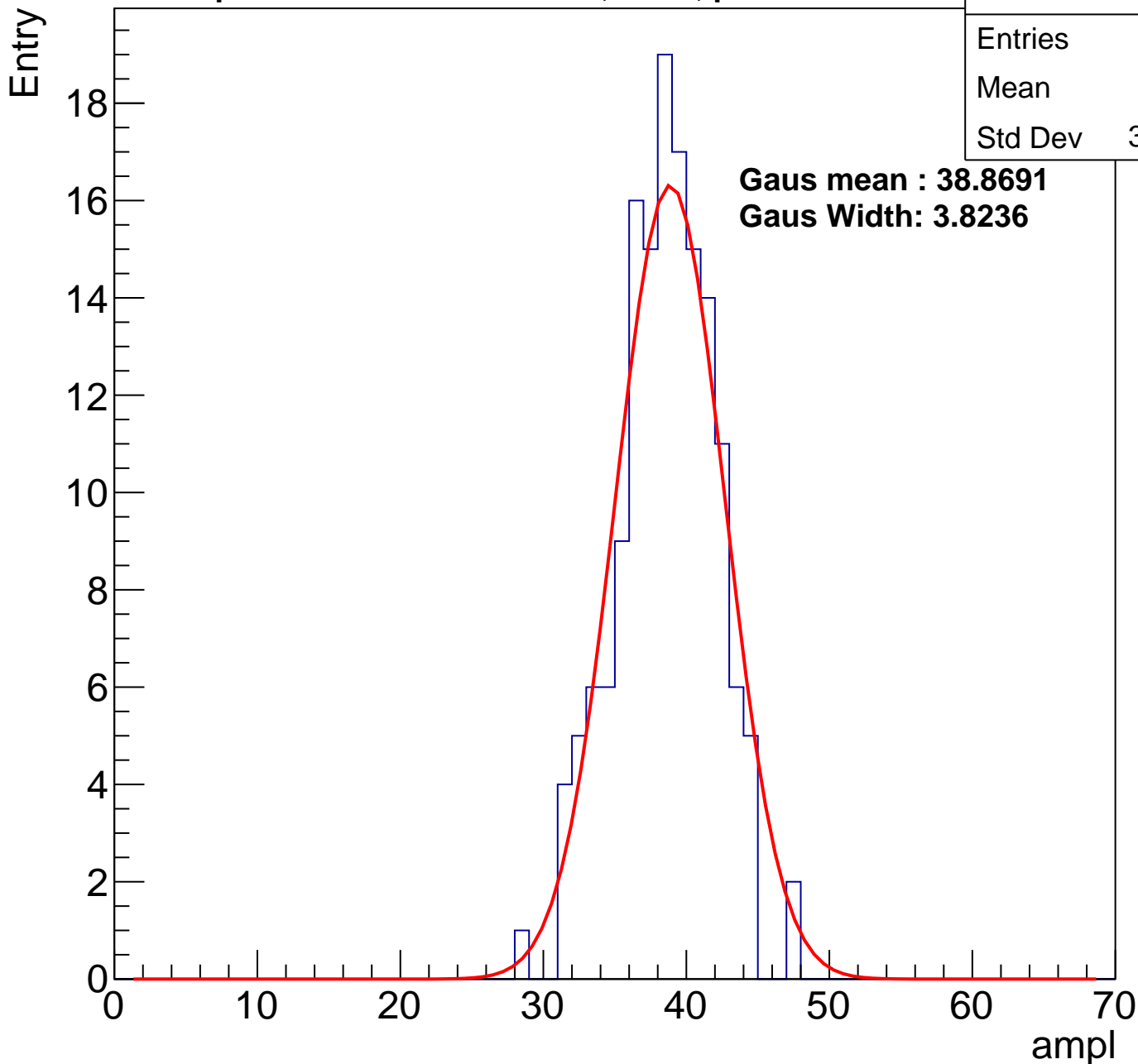


B1L001S, U25-ch61, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	38.1
Std Dev	3.406

Gaus mean : 38.8691
Gaus Width: 3.8236



B1L001S, U25-ch61, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	45.47
Std Dev	3.135

Gaus mean : 45.9591

Gaus Width: 3.0908

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

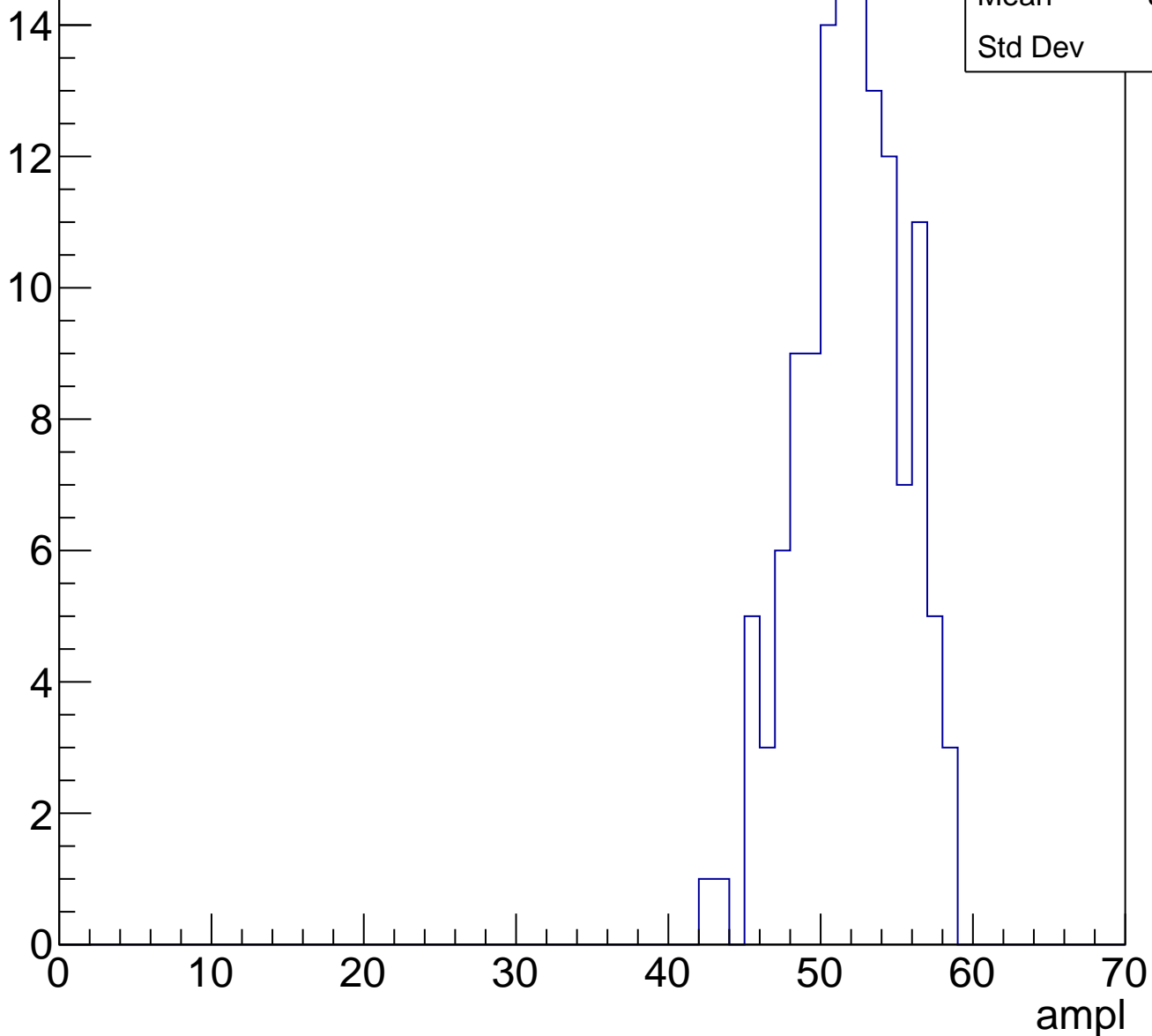
70

B1L001S, U25-ch61, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	51.51
Std Dev	3.39

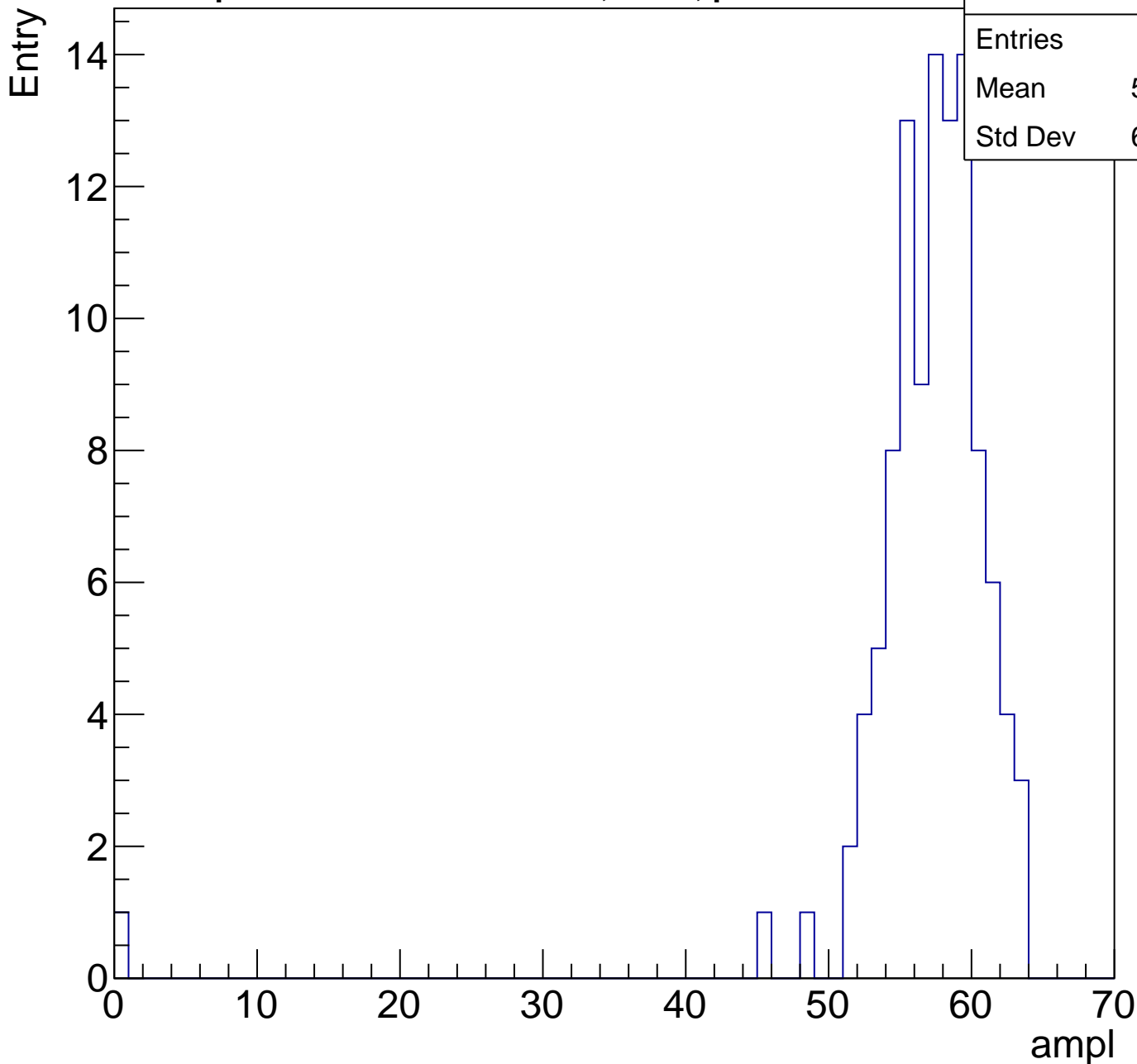
Entry



B1L001S, U25-ch61, adc4

calib_packv5_042523_0143.root, FC#2, port C2

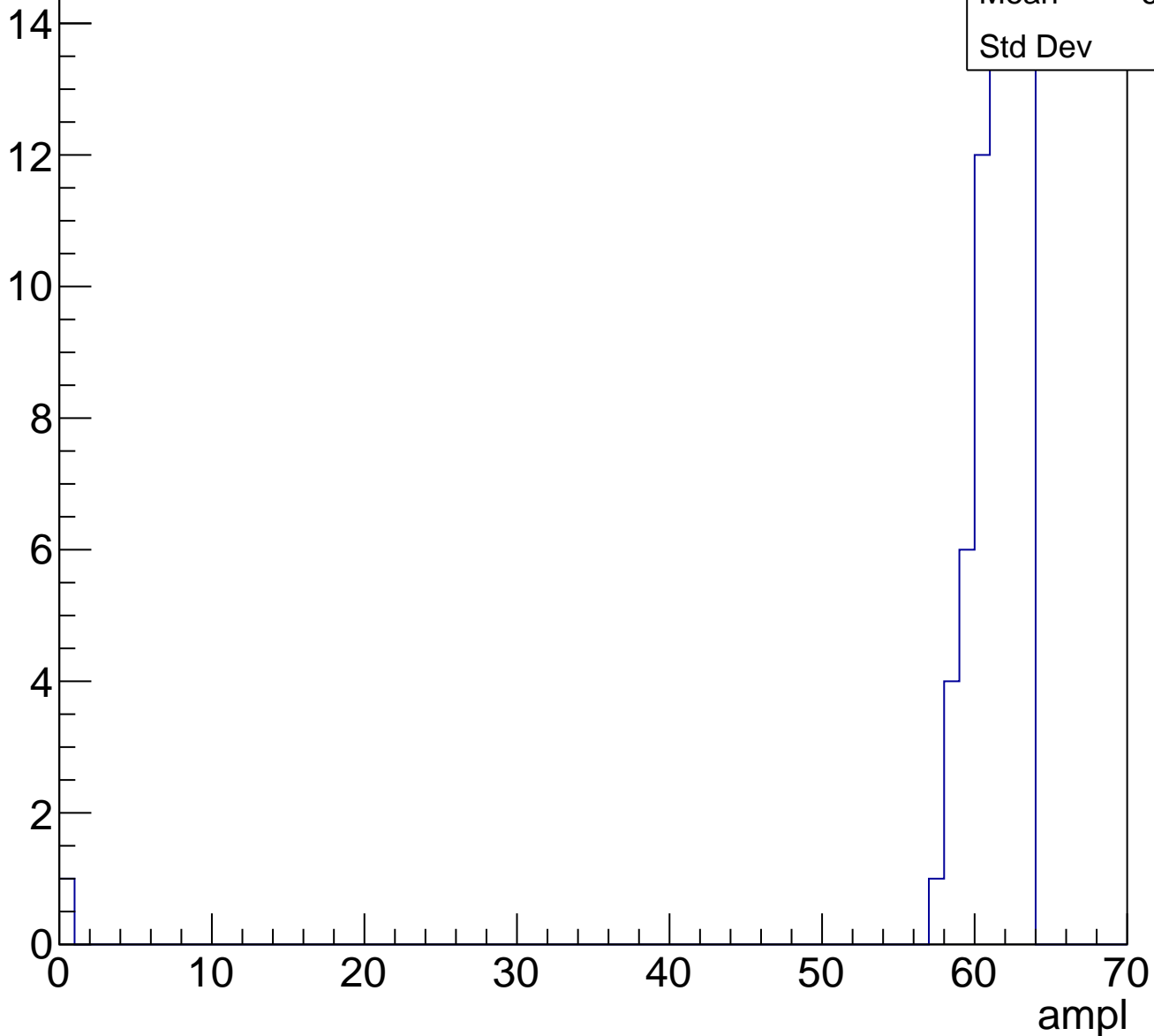
Entries	106
Mean	56.42
Std Dev	6.345



B1L001S, U25-ch61, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	67
Mean	60.13
Std Dev	7.56

B1L001S, U25-ch61, adc6

calib_packv5_042523_0143.root, FC#2, port C2

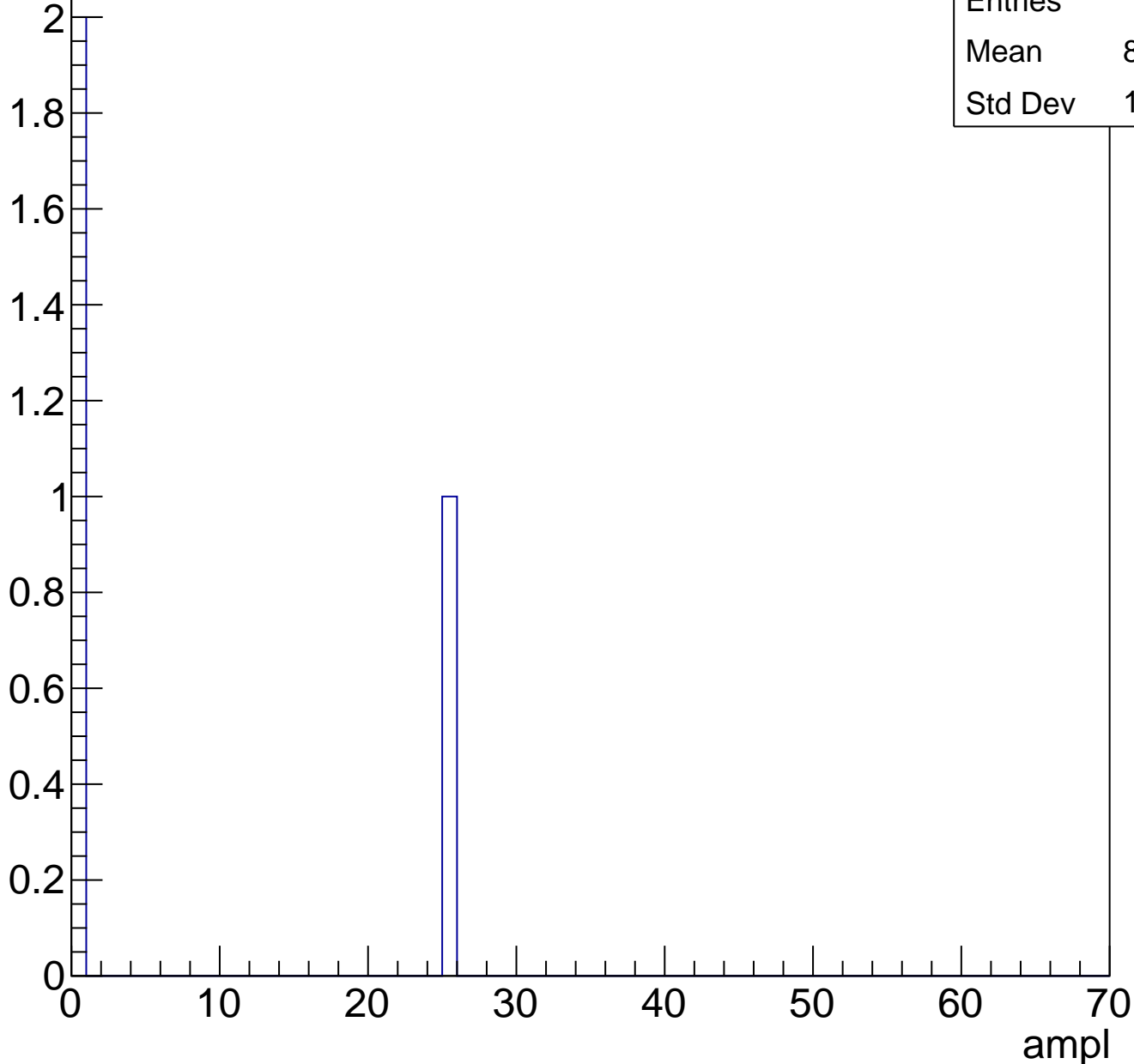
Entry



B1L001S, U25-ch61, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

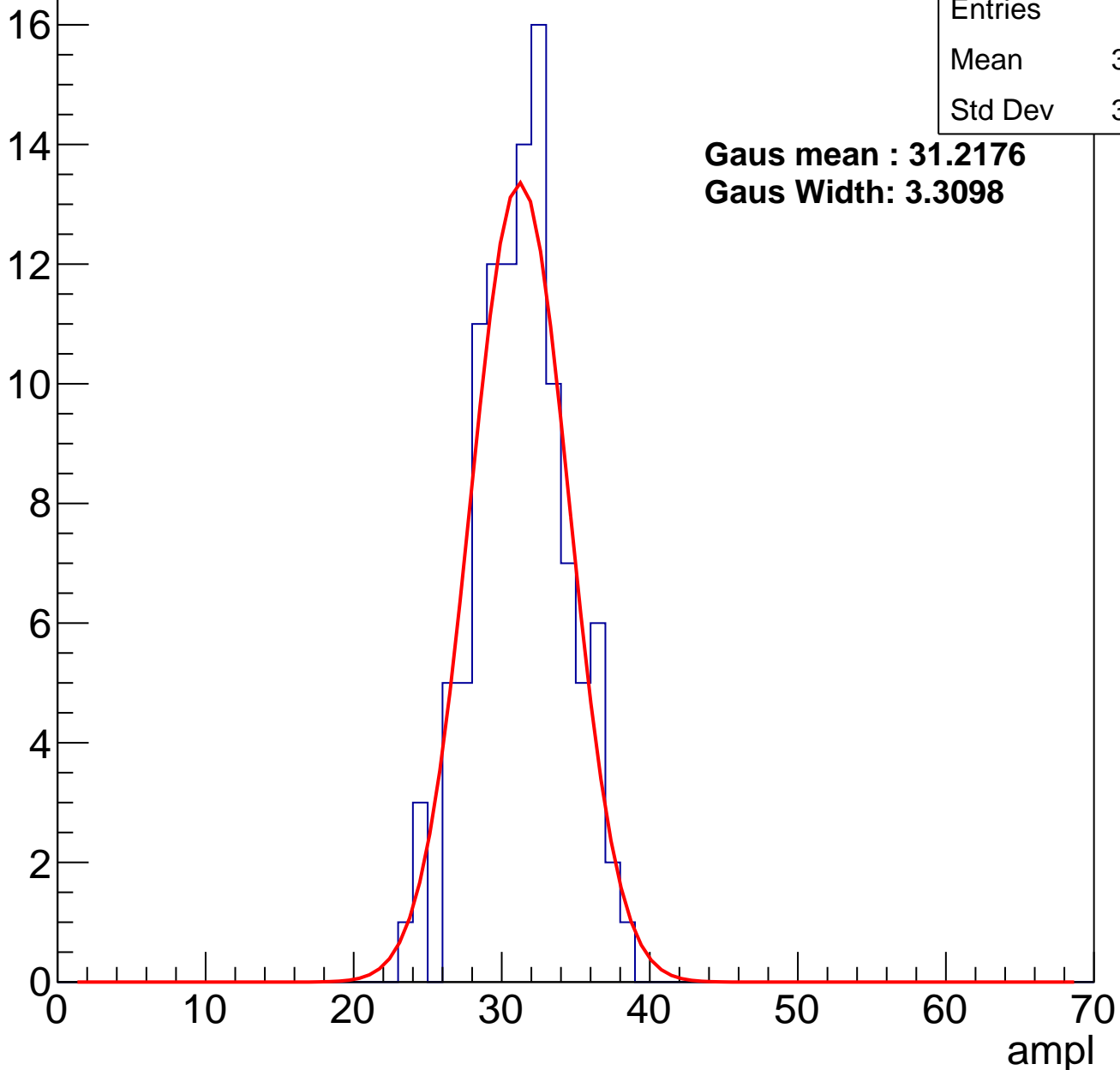


Entries	3
Mean	8.333
Std Dev	11.79

B1L001S, U25-ch62, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



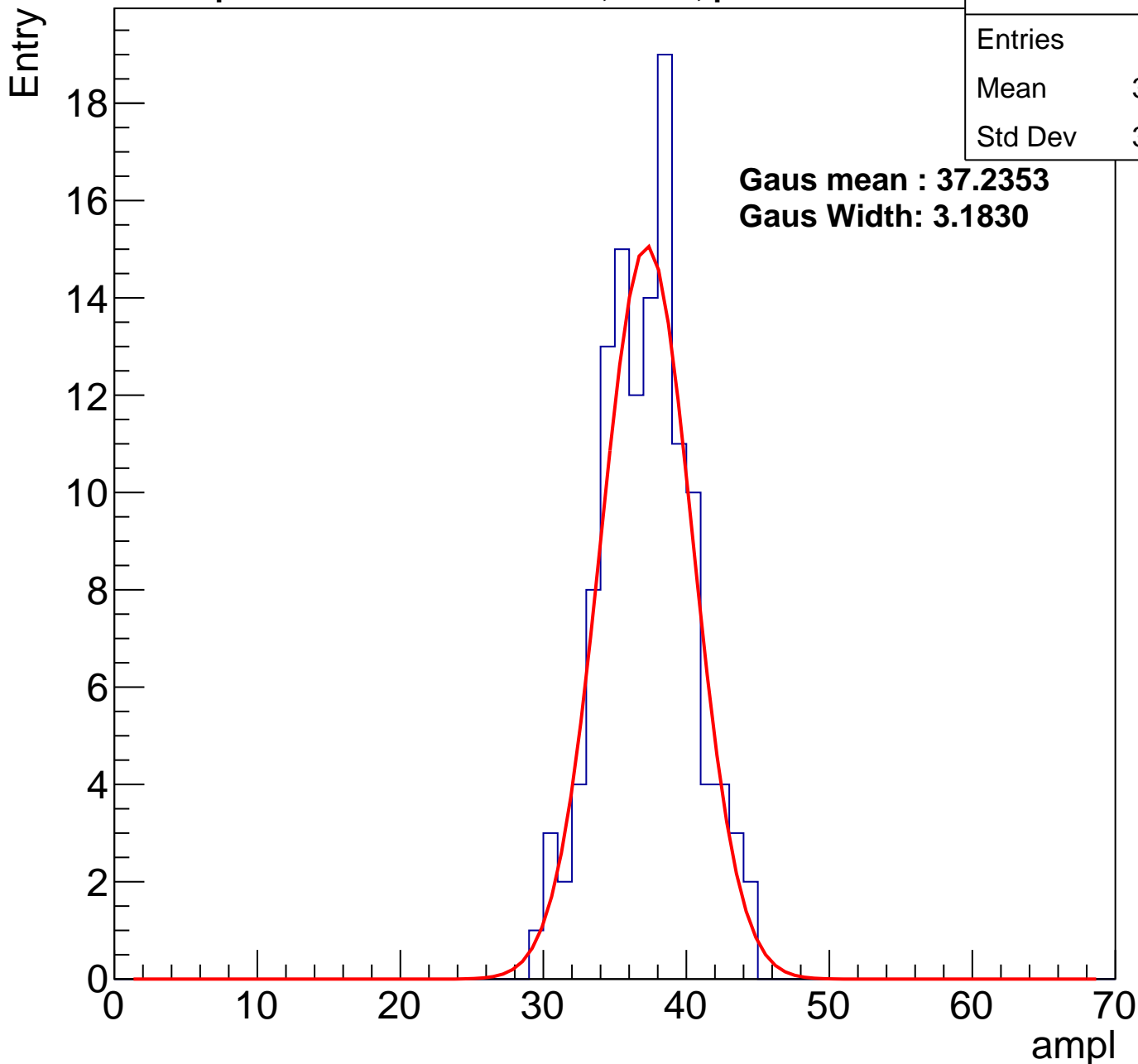
B1L001S, U25-ch62, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.72
Std Dev	3.115

Gaus mean : 37.2353

Gaus Width: 3.1830



B1L001S, U25-ch62, adc2

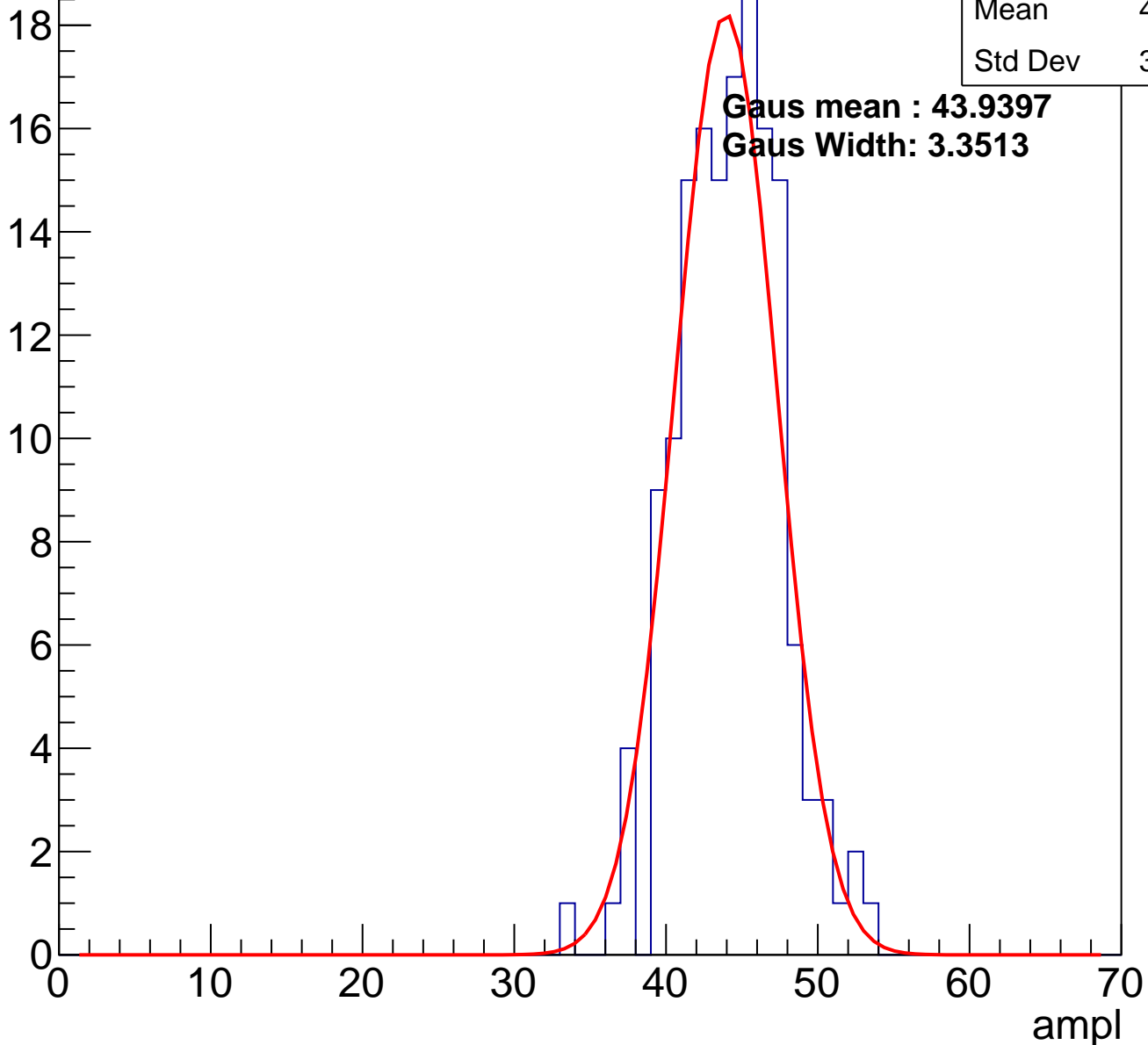
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	43.75
Std Dev	3.347

Gaus mean : 43.9397

Gaus Width: 3.3513

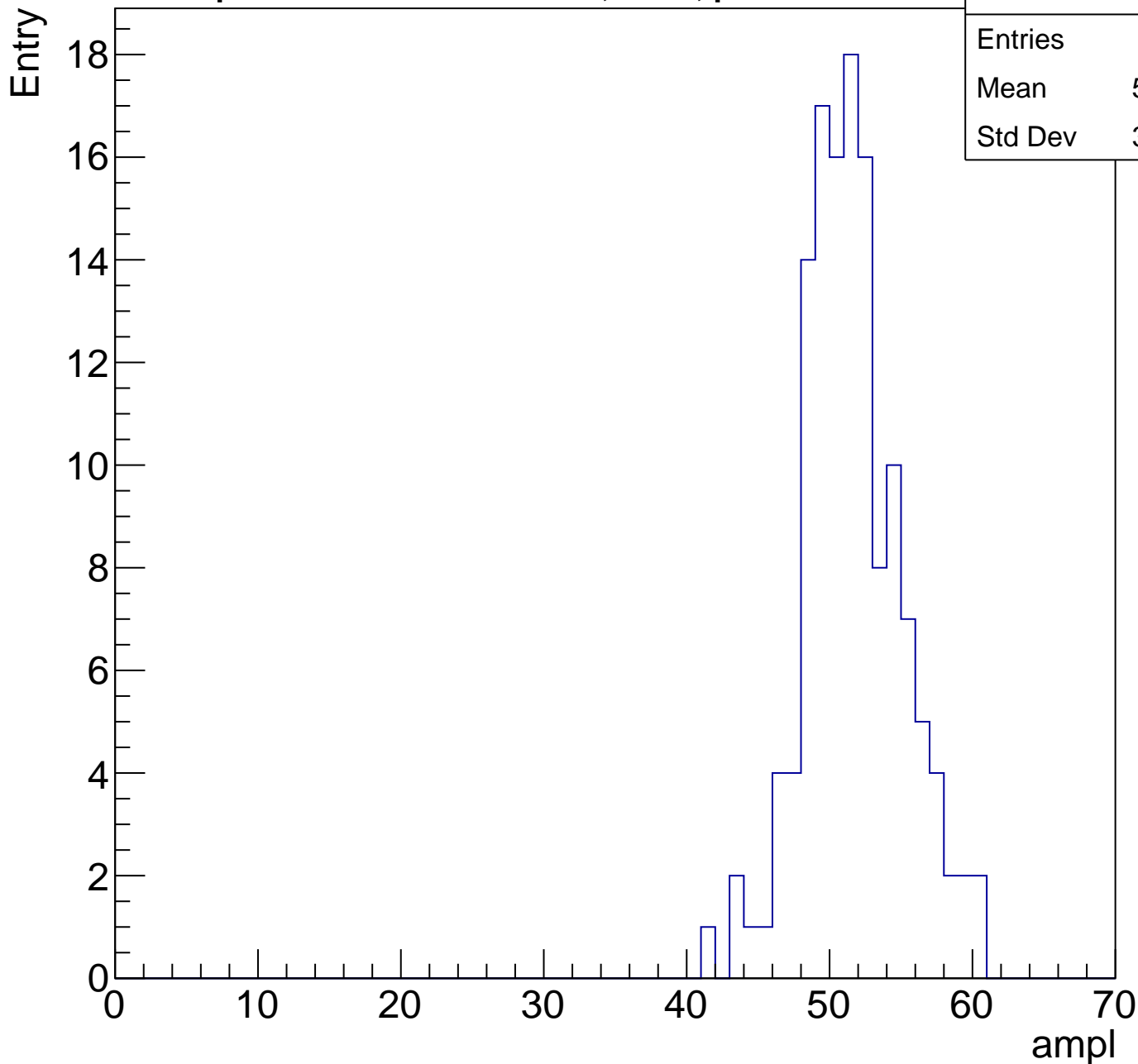
Entry



B1L001S, U25-ch62, adc3

calib_packv5_042523_0143.root, FC#2, port C2

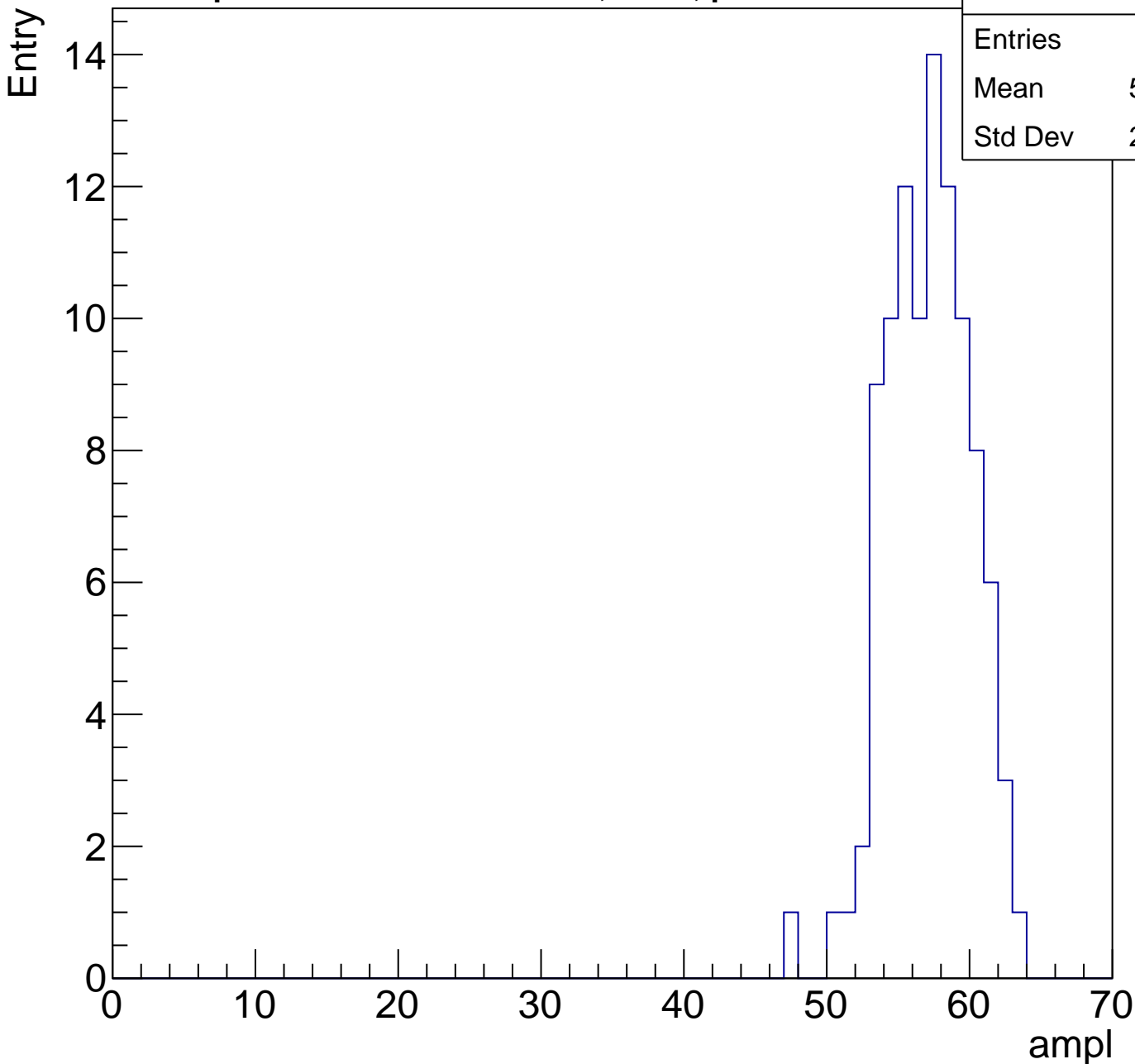
Entries	134
Mean	51.15
Std Dev	3.446



B1L001S, U25-ch62, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	56.68
Std Dev	2.912



B1L001S, U25-ch62, adc5

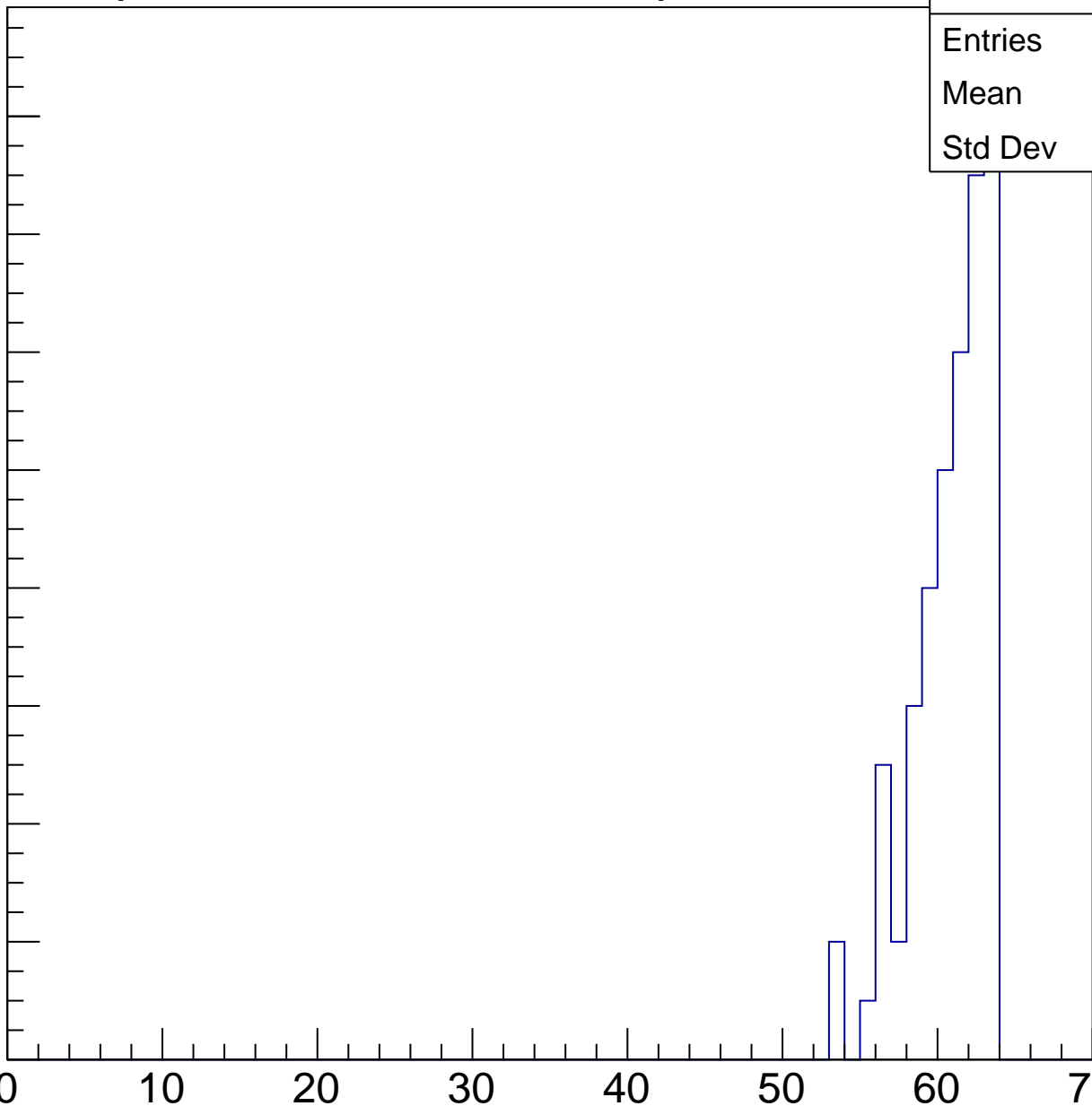
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	60.36
Std Dev	2.449

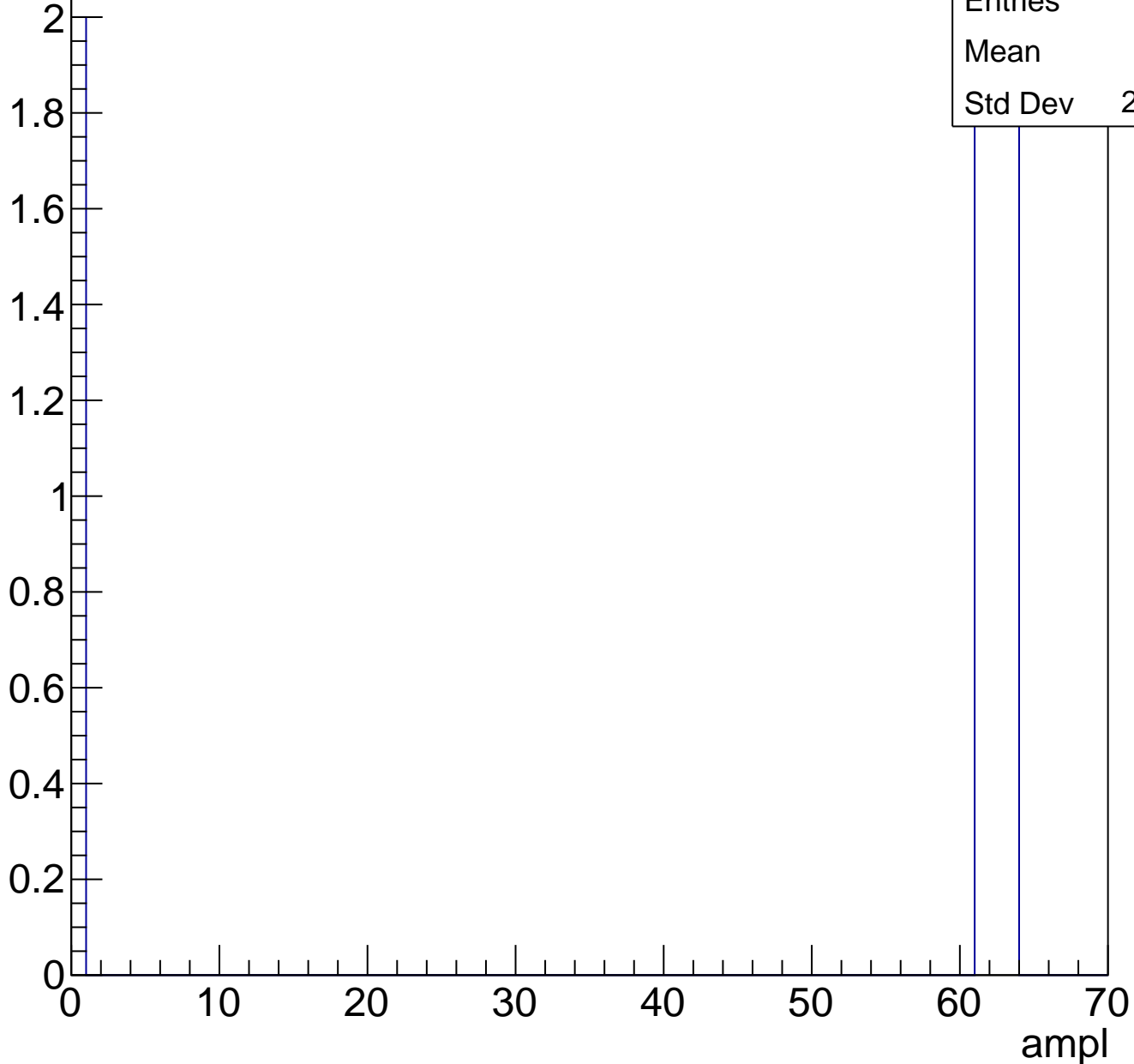
ampl



B1L001S, U25-ch62, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch62, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U25-ch63, adc0

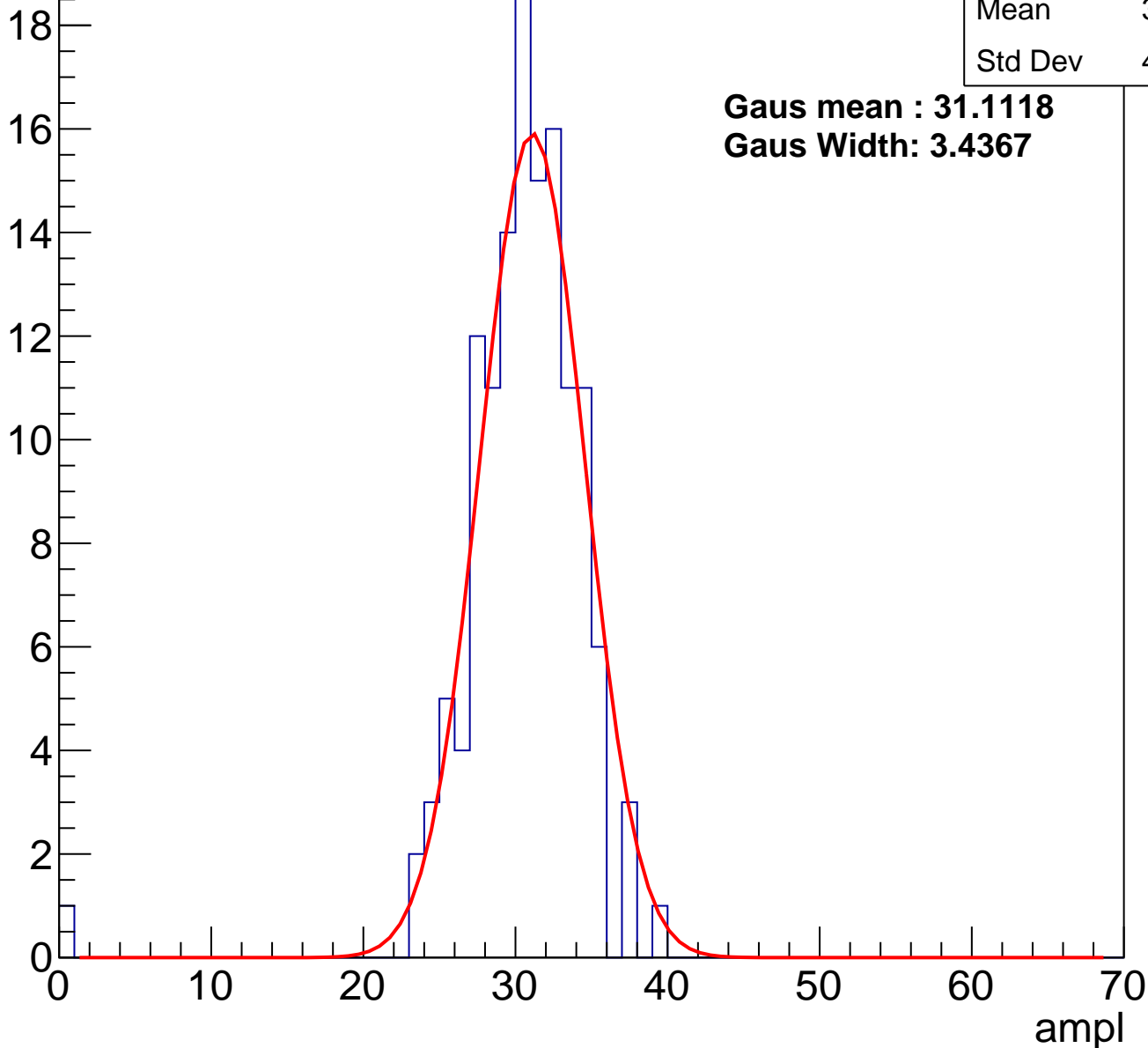
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	30.07
Std Dev	4.045

Gaus mean : 31.1118

Gaus Width: 3.4367

Entry



B1L001S, U25-ch63, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	37.08
Std Dev	2.889

Gaus mean : 37.6040

Gaus Width: 3.0011

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

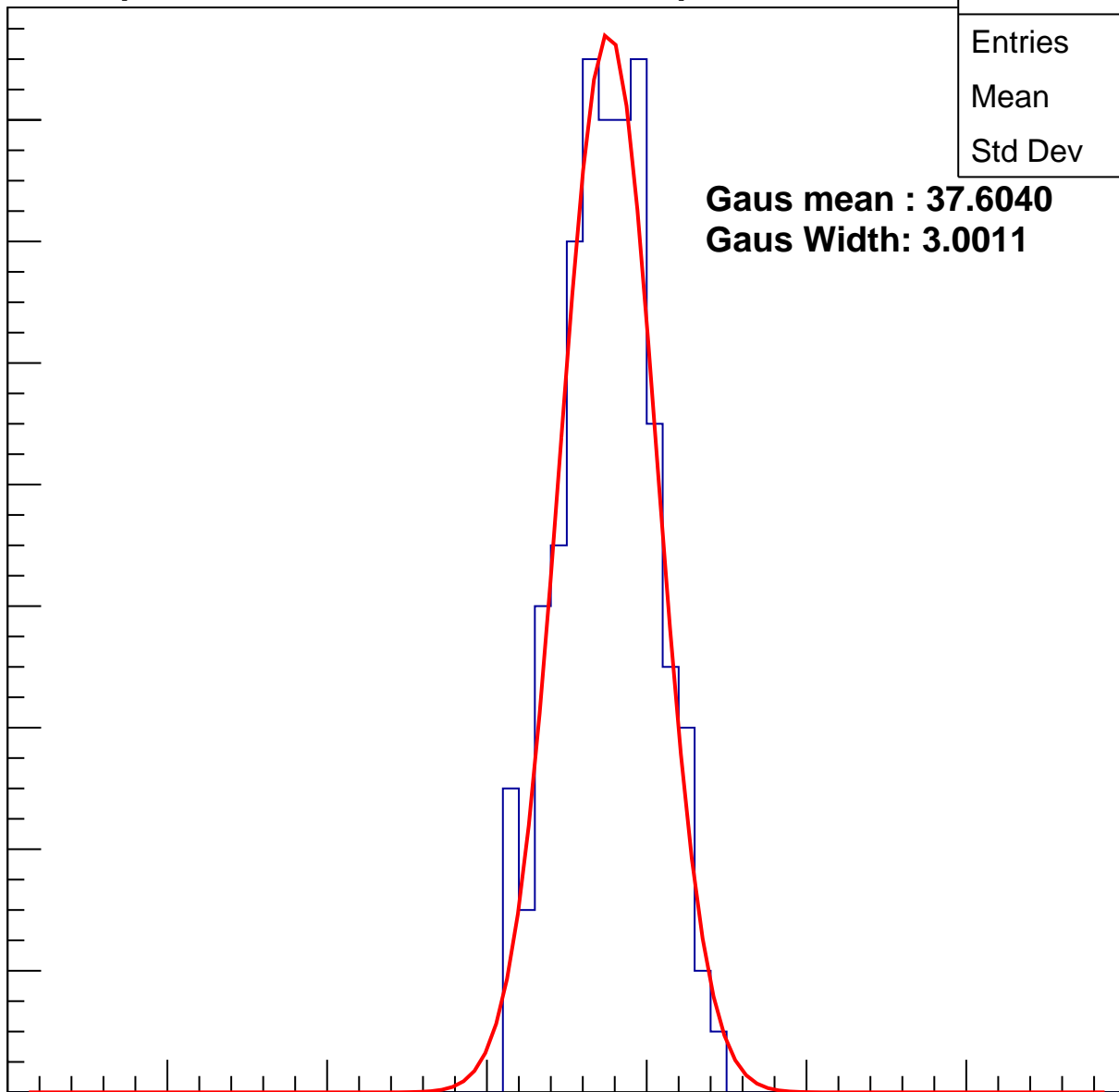
40

50

60

70

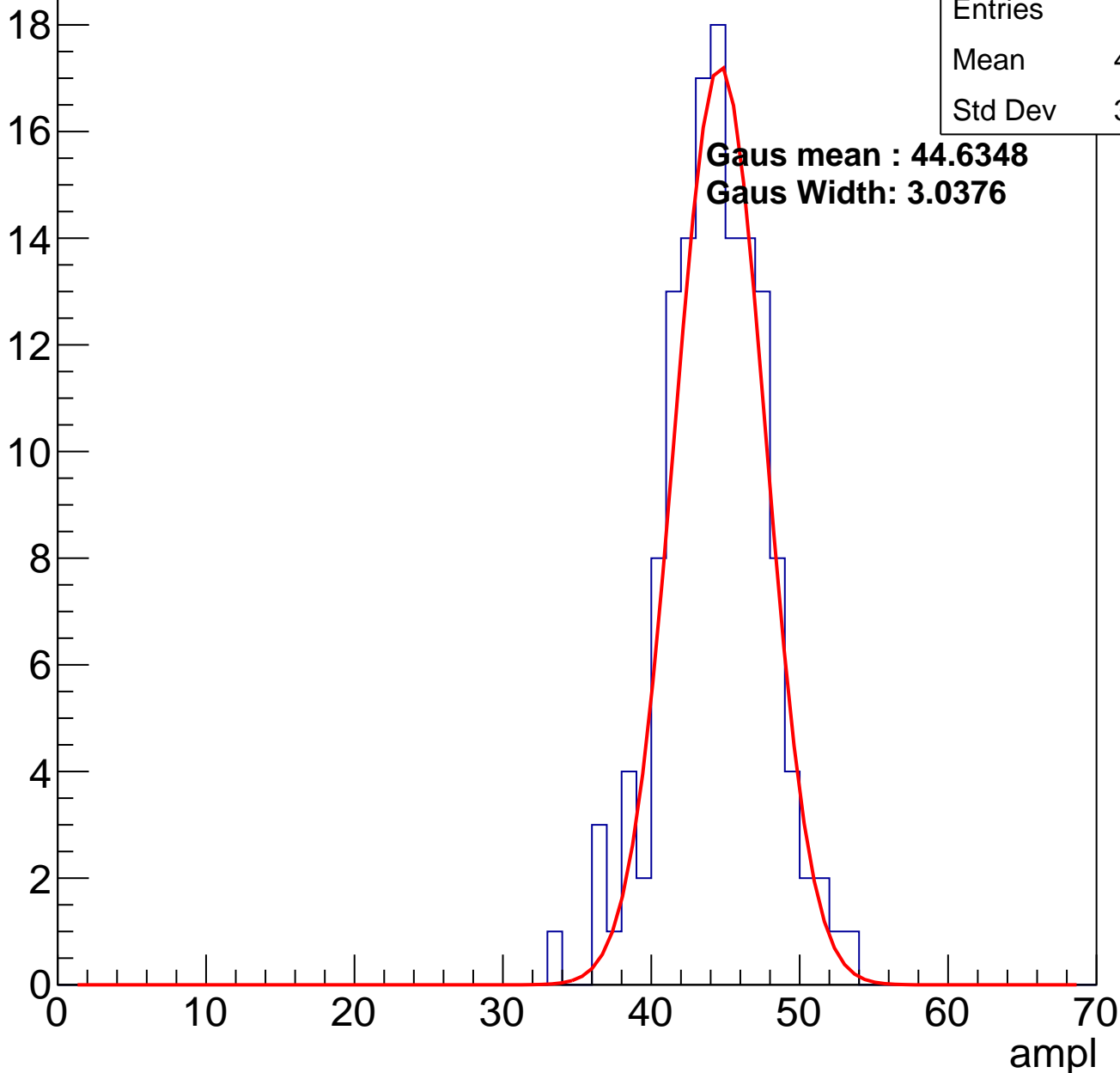
ampl



B1L001S, U25-ch63, adc2

calib_packv5_042523_0143.root, FC#2, port C2

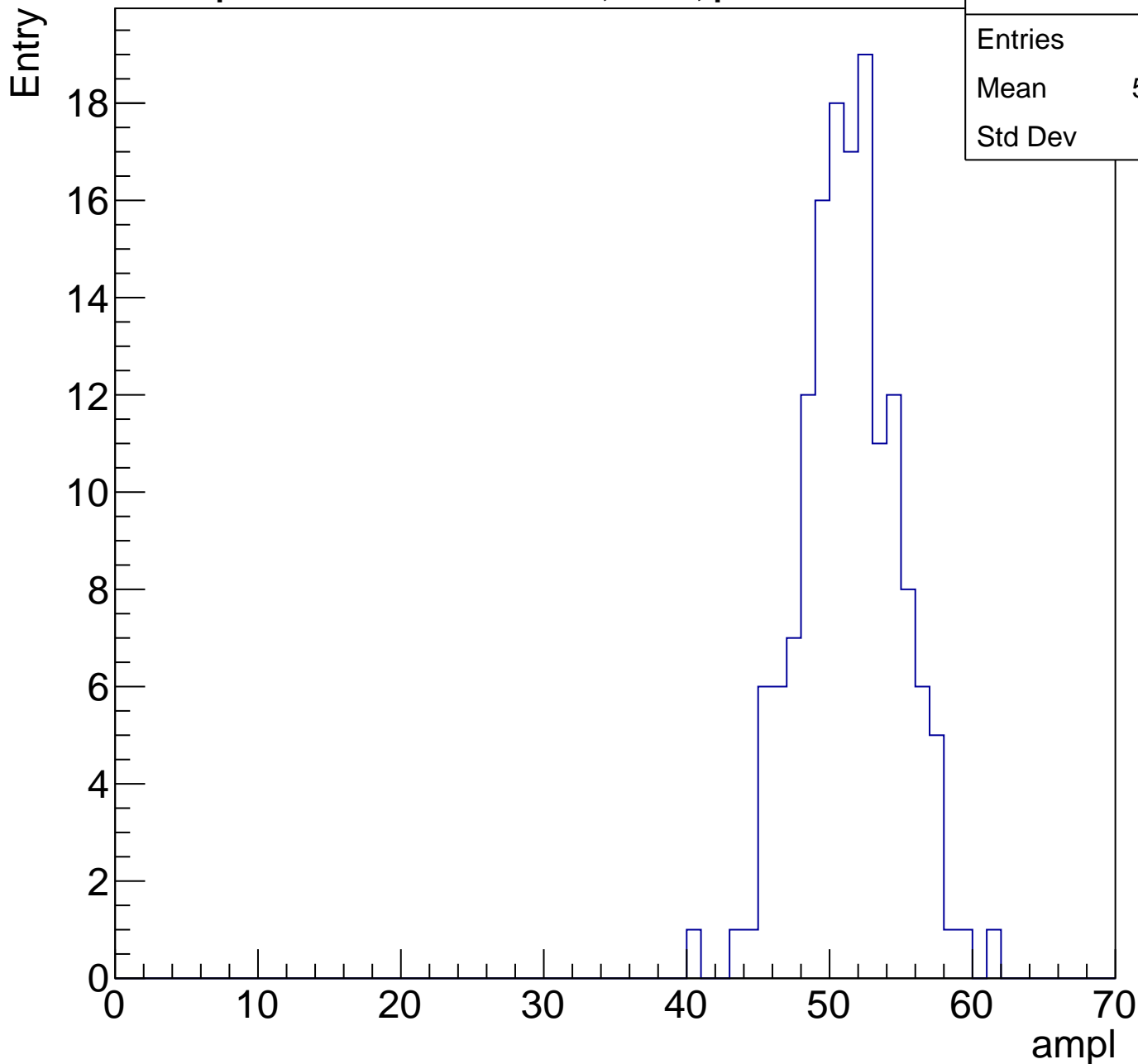
Entry



B1L001S, U25-ch63, adc3

calib_packv5_042523_0143.root, FC#2, port C2

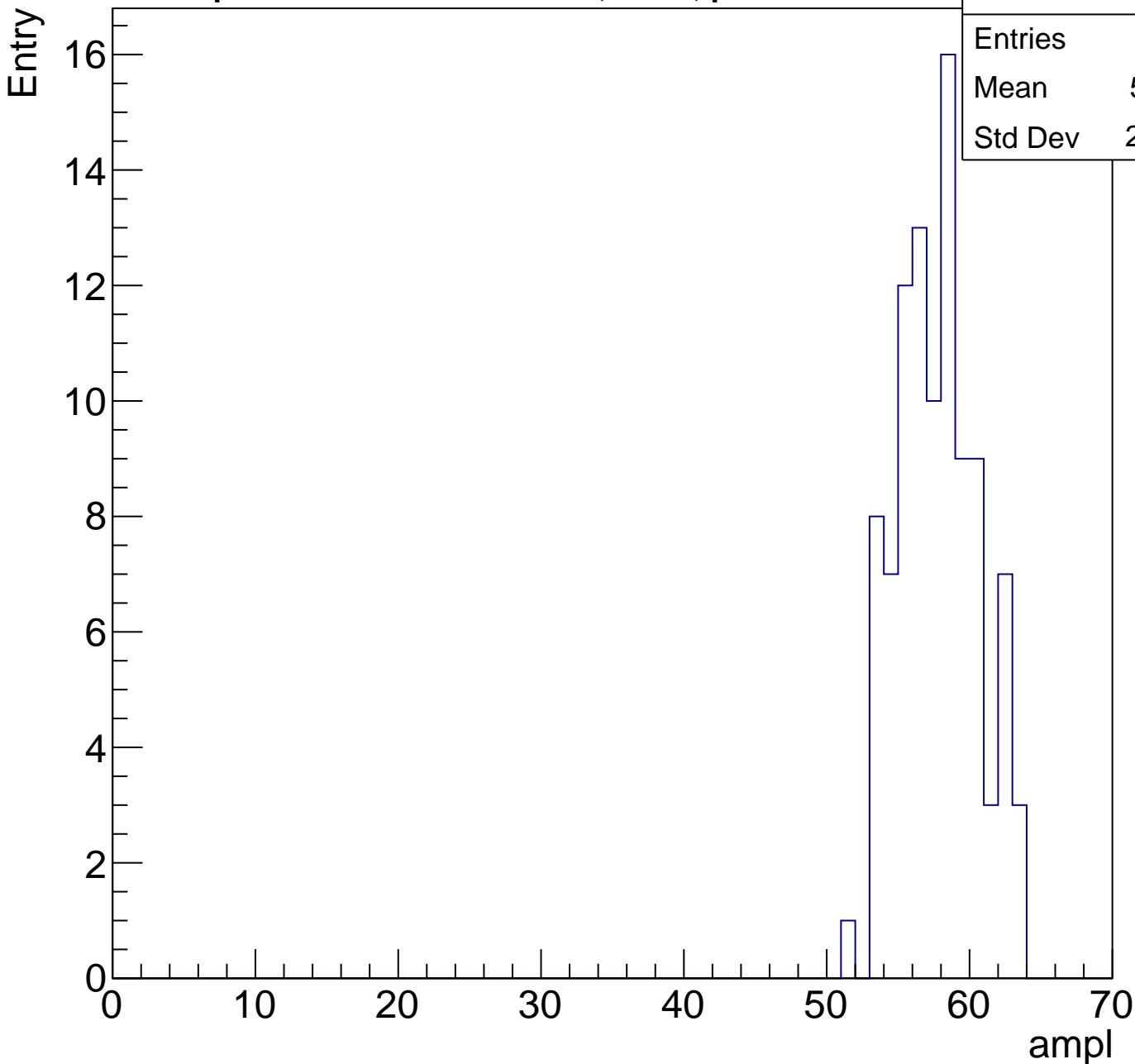
Entries	149
Mean	50.92
Std Dev	3.42



B1L001S, U25-ch63, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	57.31
Std Dev	2.753



B1L001S, U25-ch63, adc5

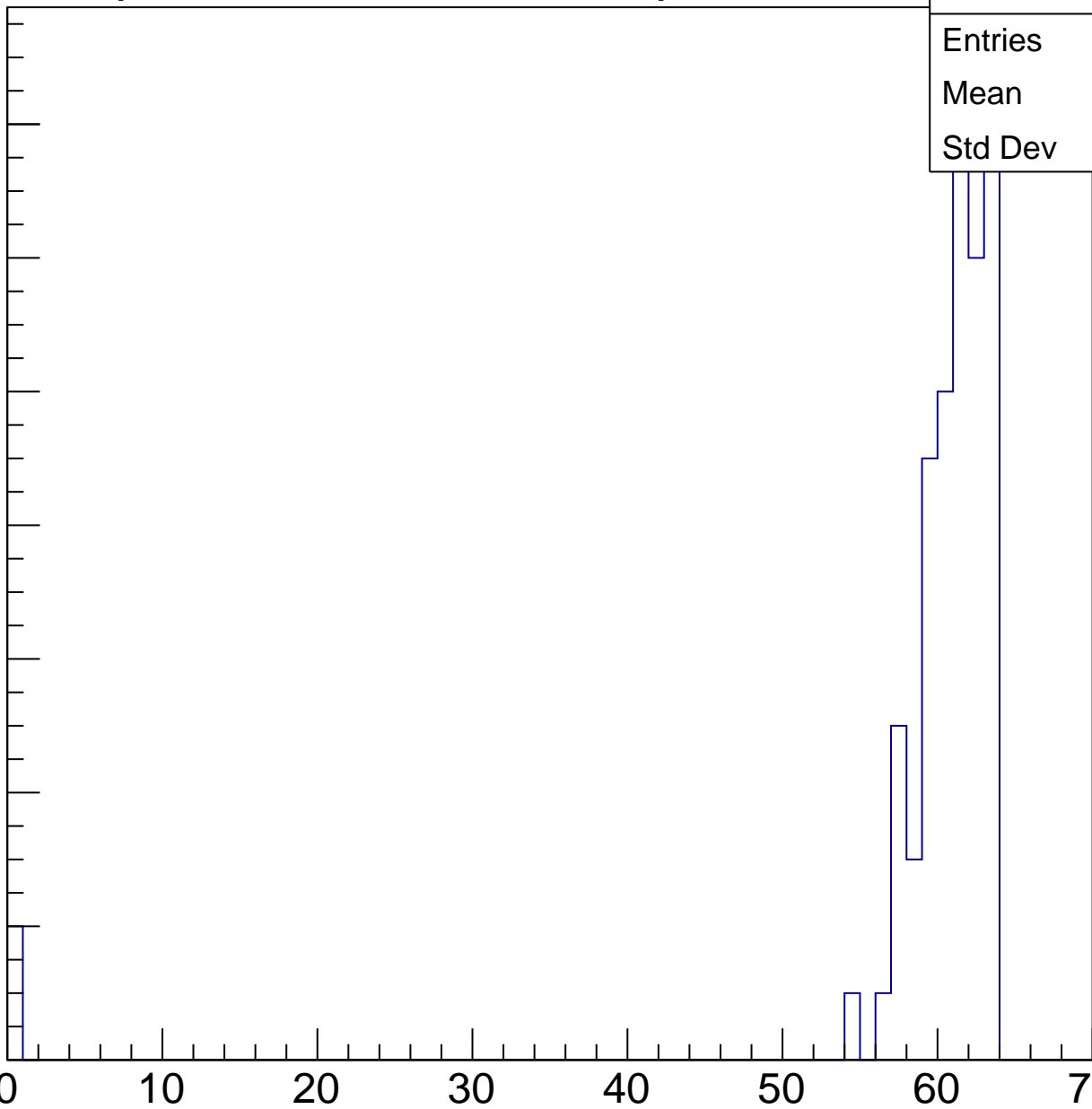
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.93
Std Dev	10.16

ampl



B1L001S, U25-ch63, adc6

calib_packv5_042523_0143.root, FC#2, port C2

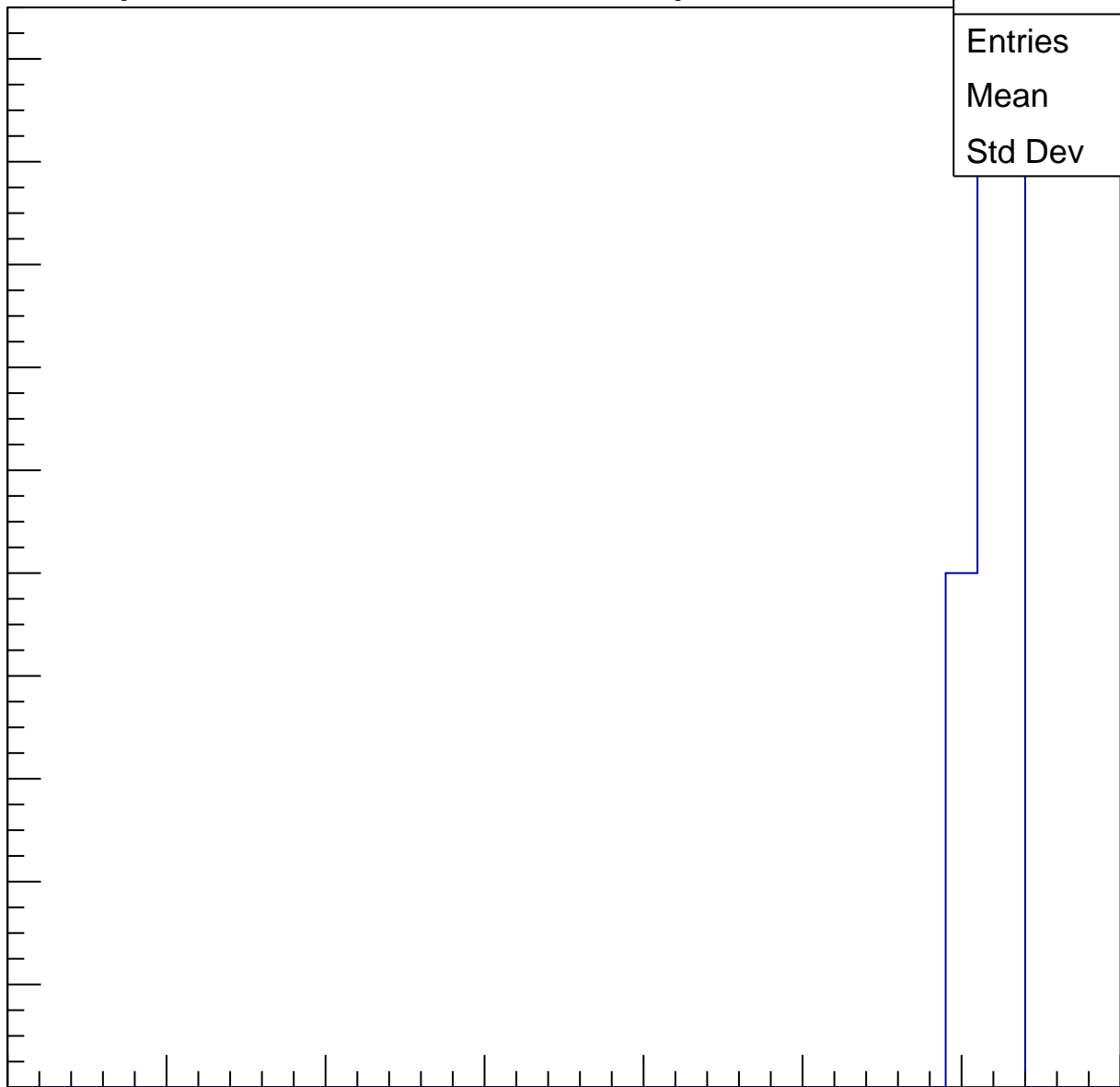
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.38
Std Dev	1.317

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch63, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

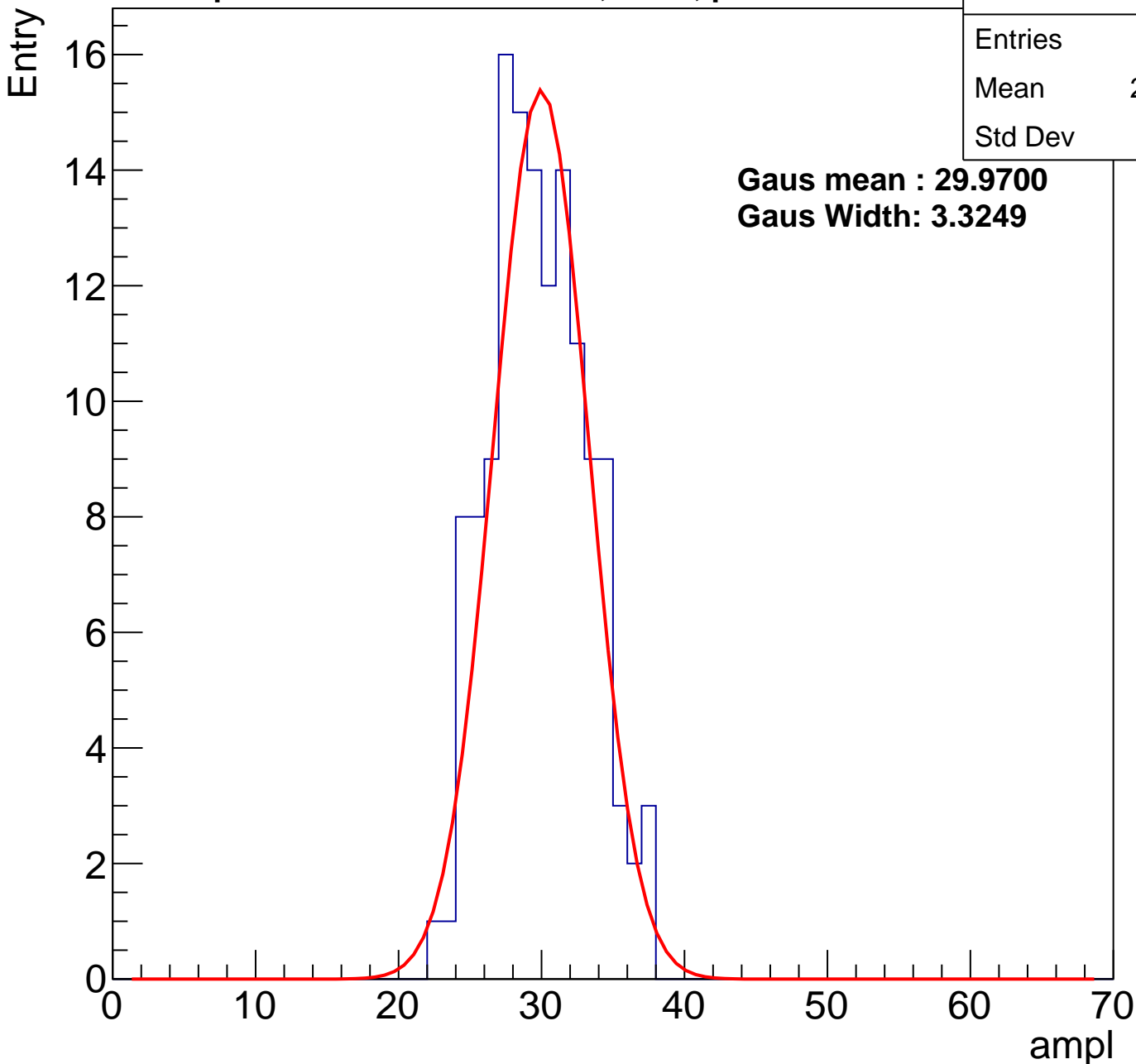
B1L001S, U25-ch64, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	29.38
Std Dev	3.32

Gaus mean : 29.9700

Gaus Width: 3.3249



B1L001S, U25-ch64, adc1

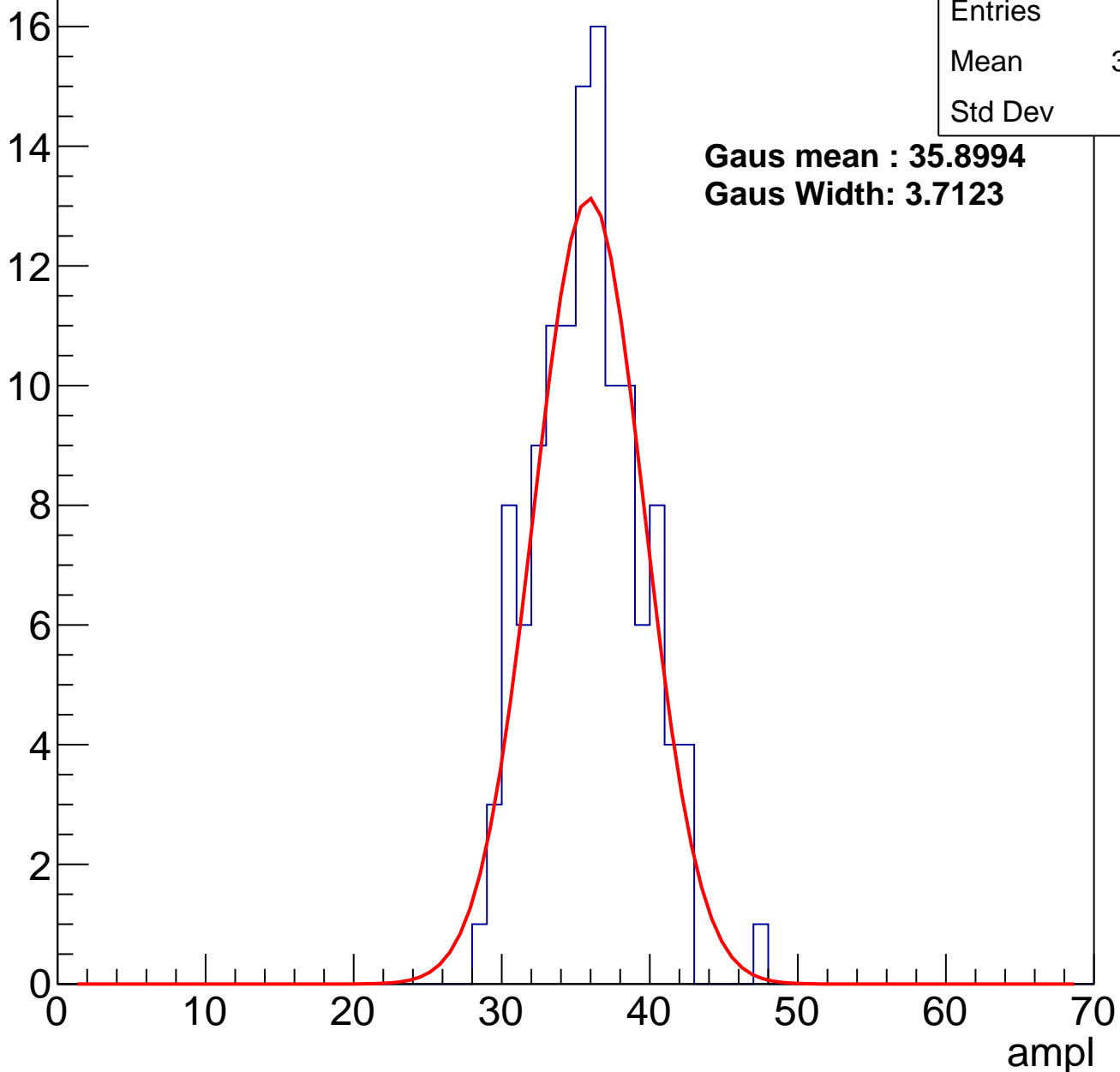
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	35.37
Std Dev	3.49

Gaus mean : 35.8994

Gaus Width: 3.7123

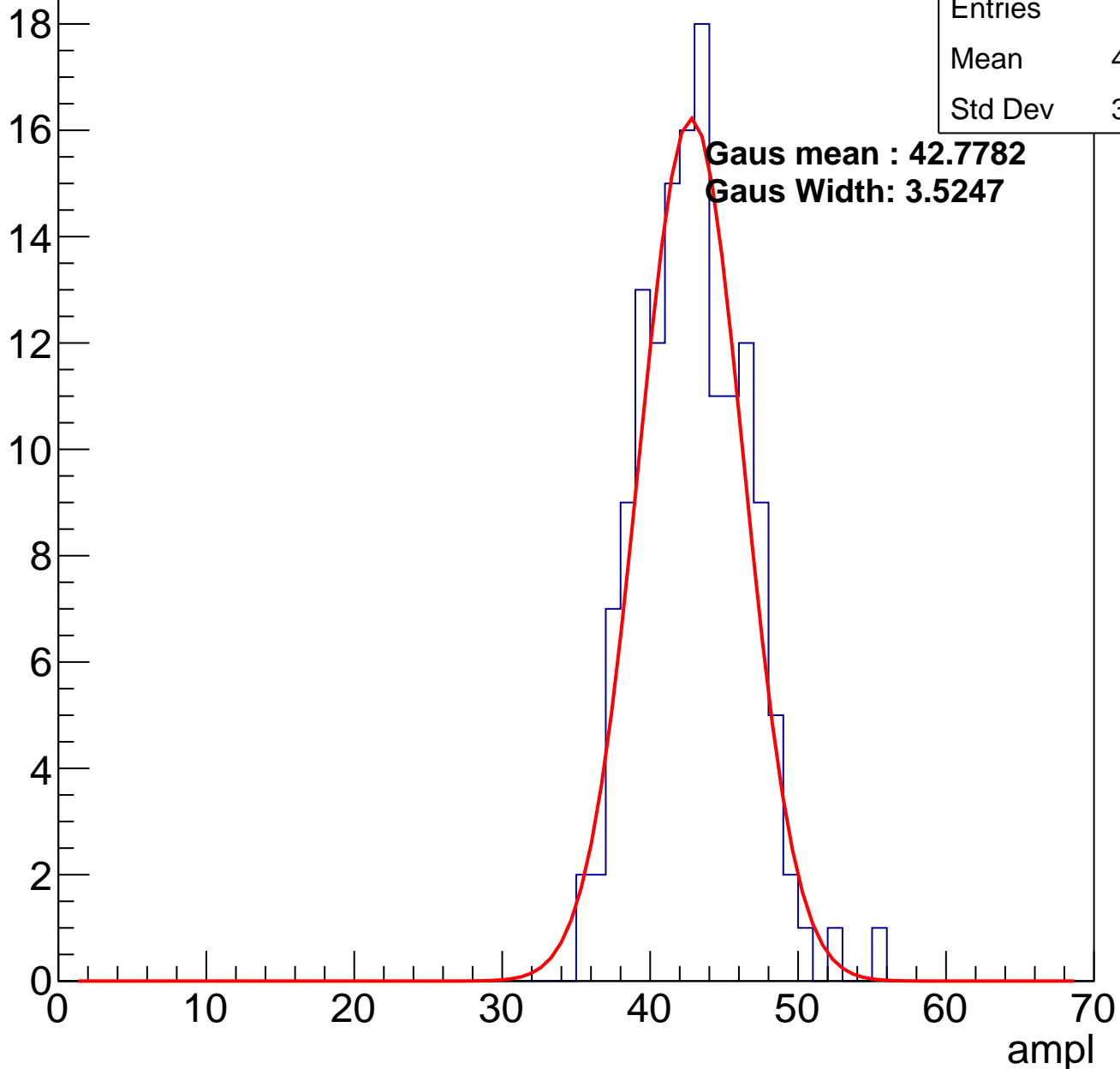
Entry



B1L001S, U25-ch64, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	147
Mean	42.45
Std Dev	3.527

Gaus mean : 42.7782

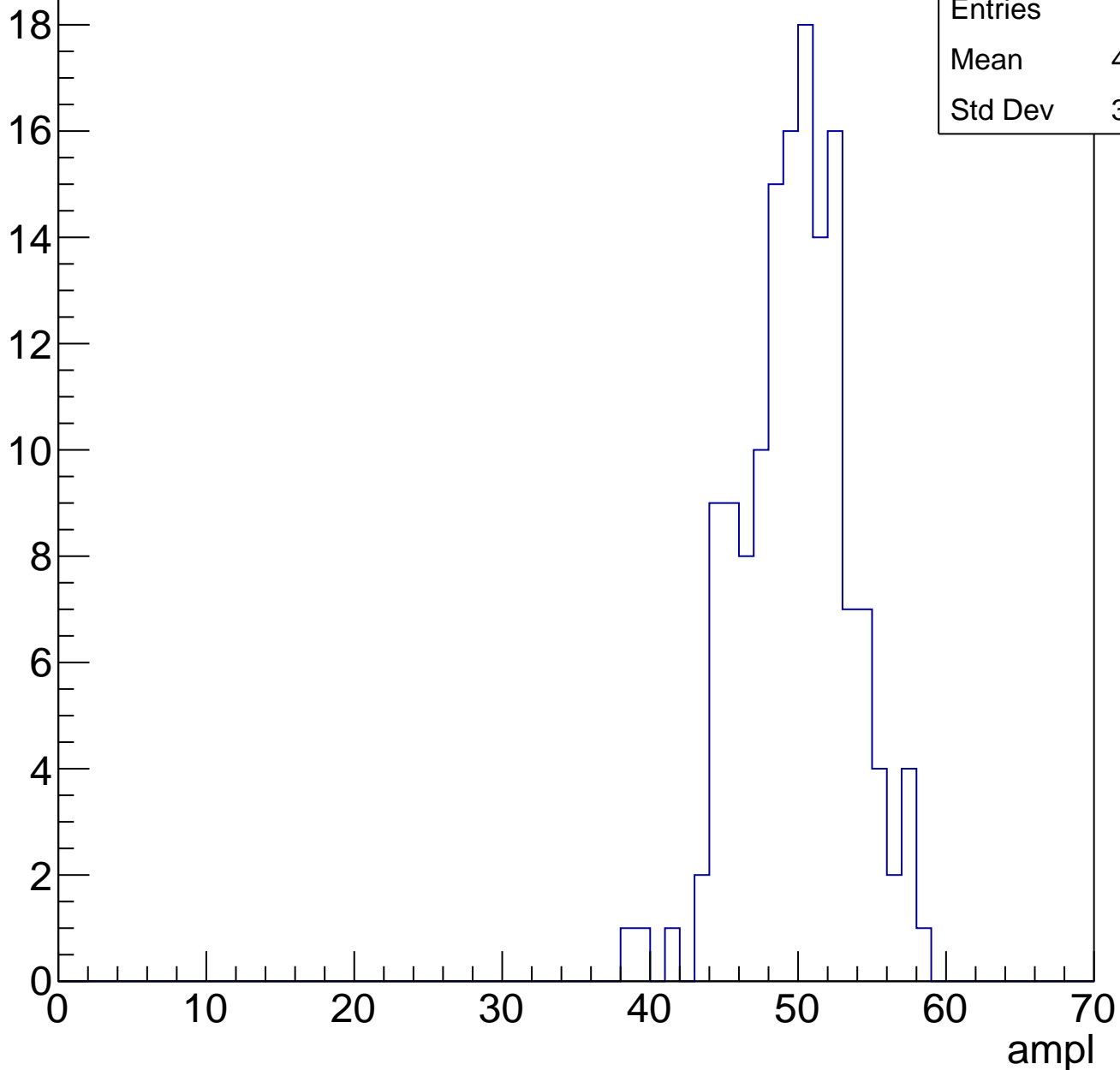
Gaus Width: 3.5247

B1L001S, U25-ch64, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	49.38
Std Dev	3.635

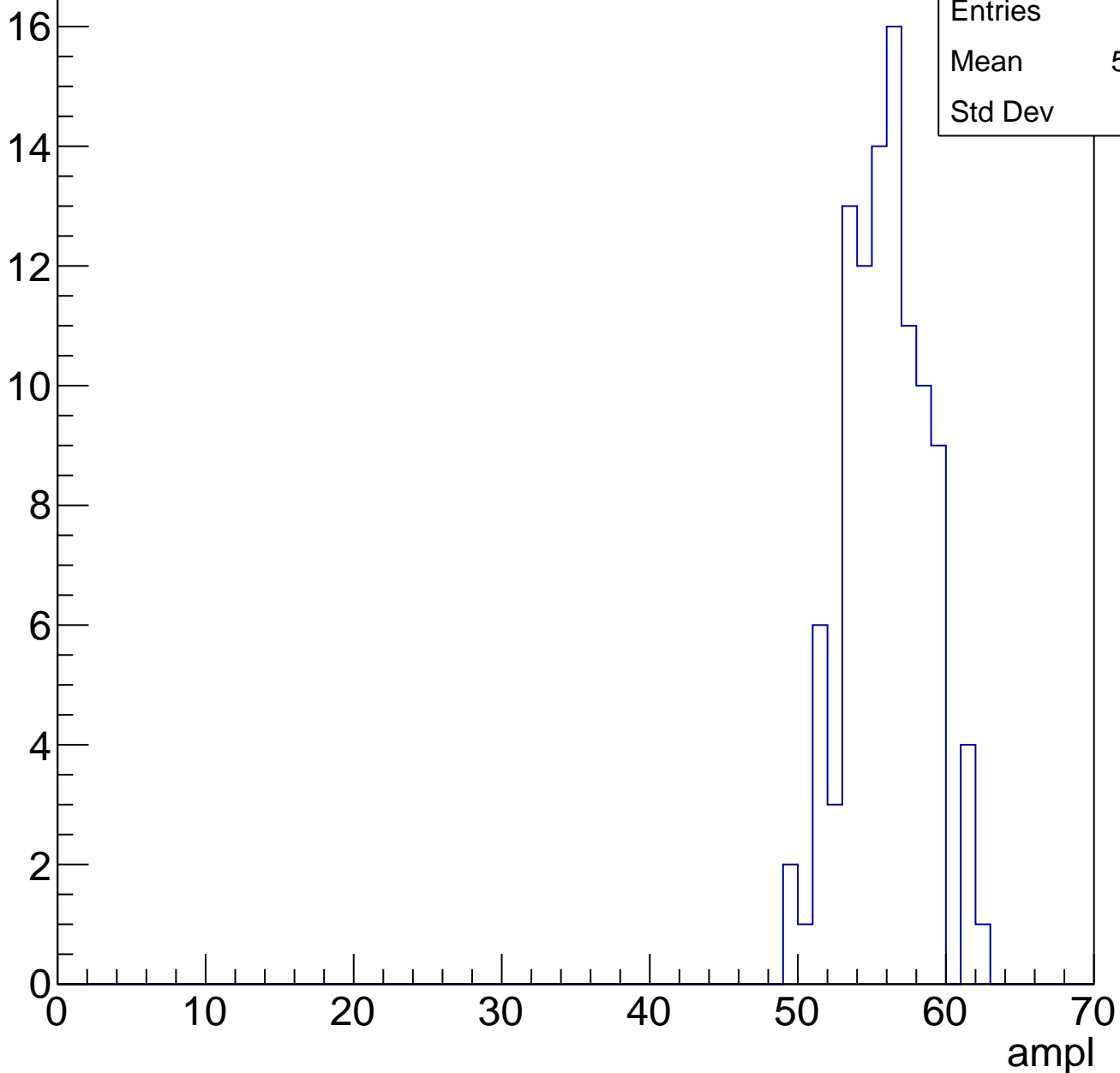
Entry



B1L001S, U25-ch64, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch64, adc5

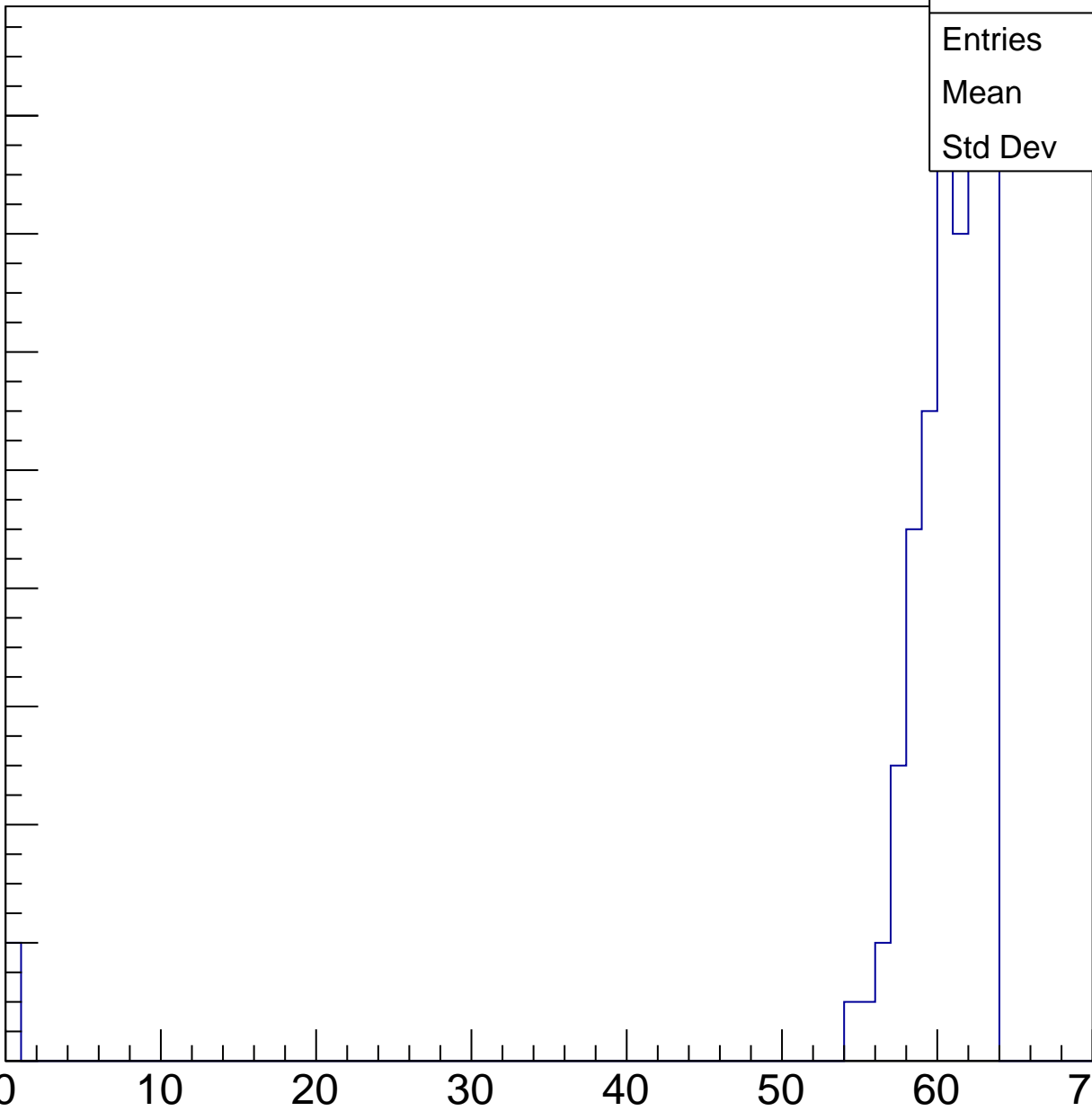
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	59.05
Std Dev	8.946

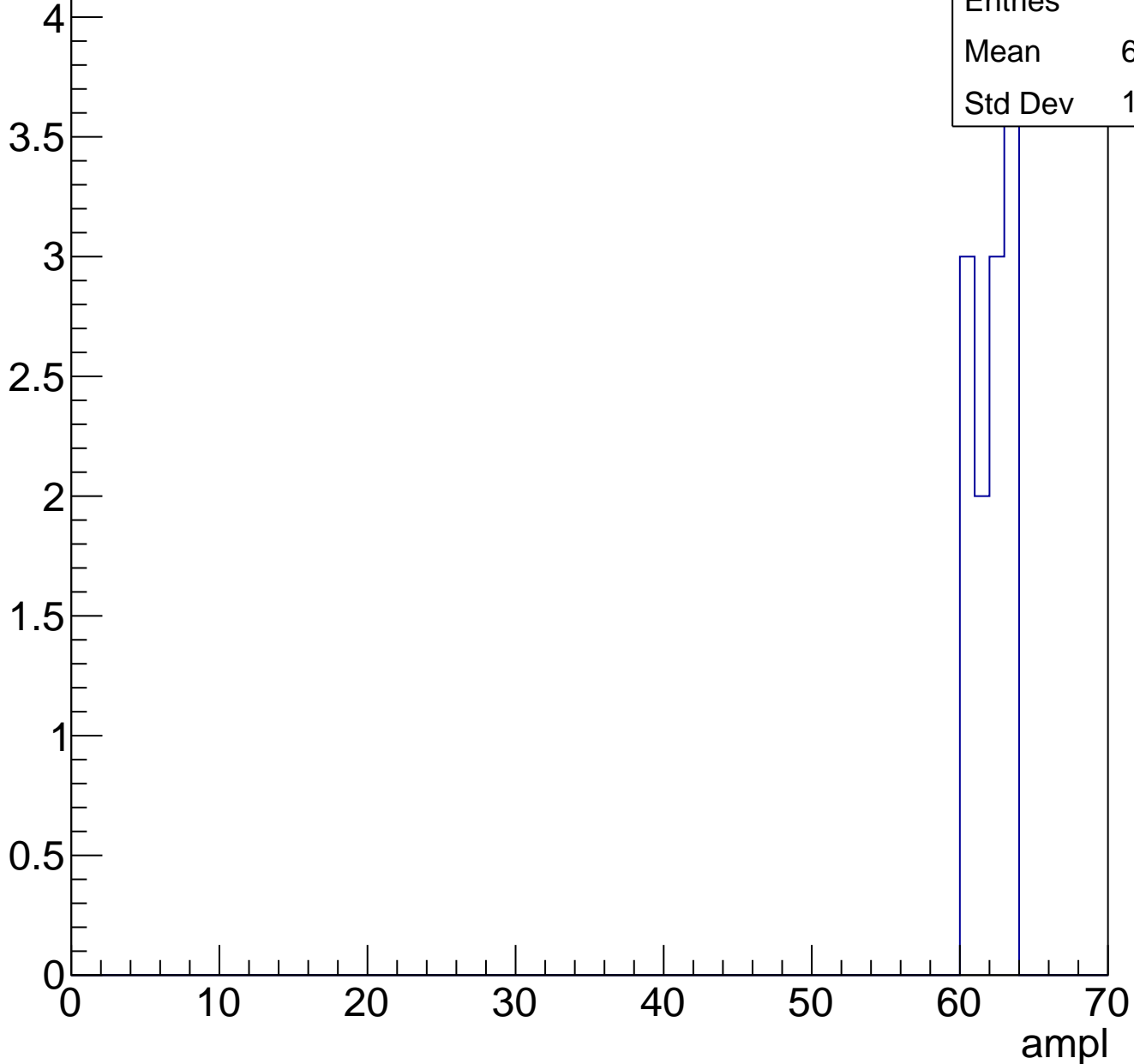
ampl



B1L001S, U25-ch64, adc6

calib_packv5_042523_0143.root, FC#2, port C2

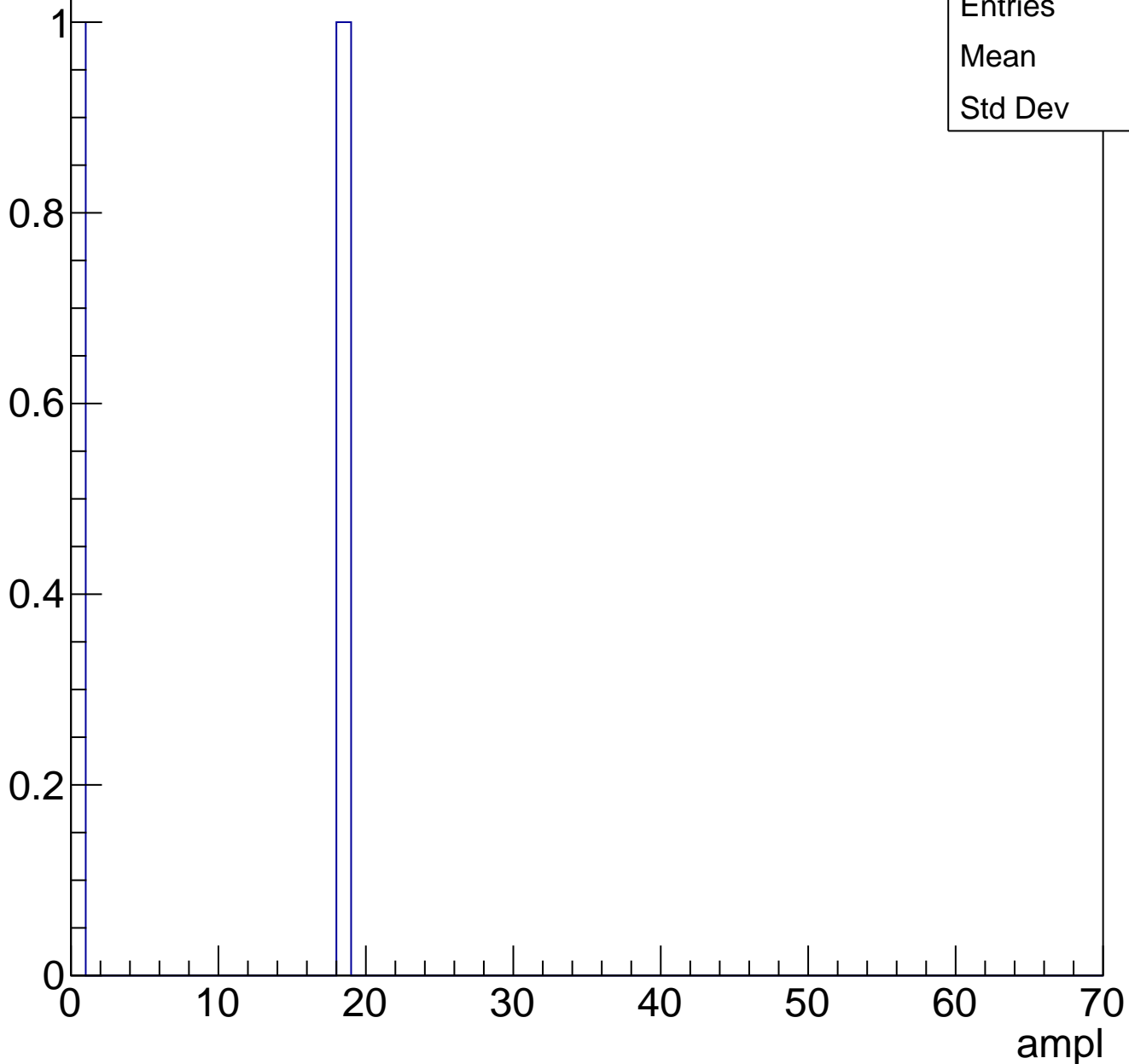
Entry



B1L001S, U25-ch64, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	9
Std Dev	9

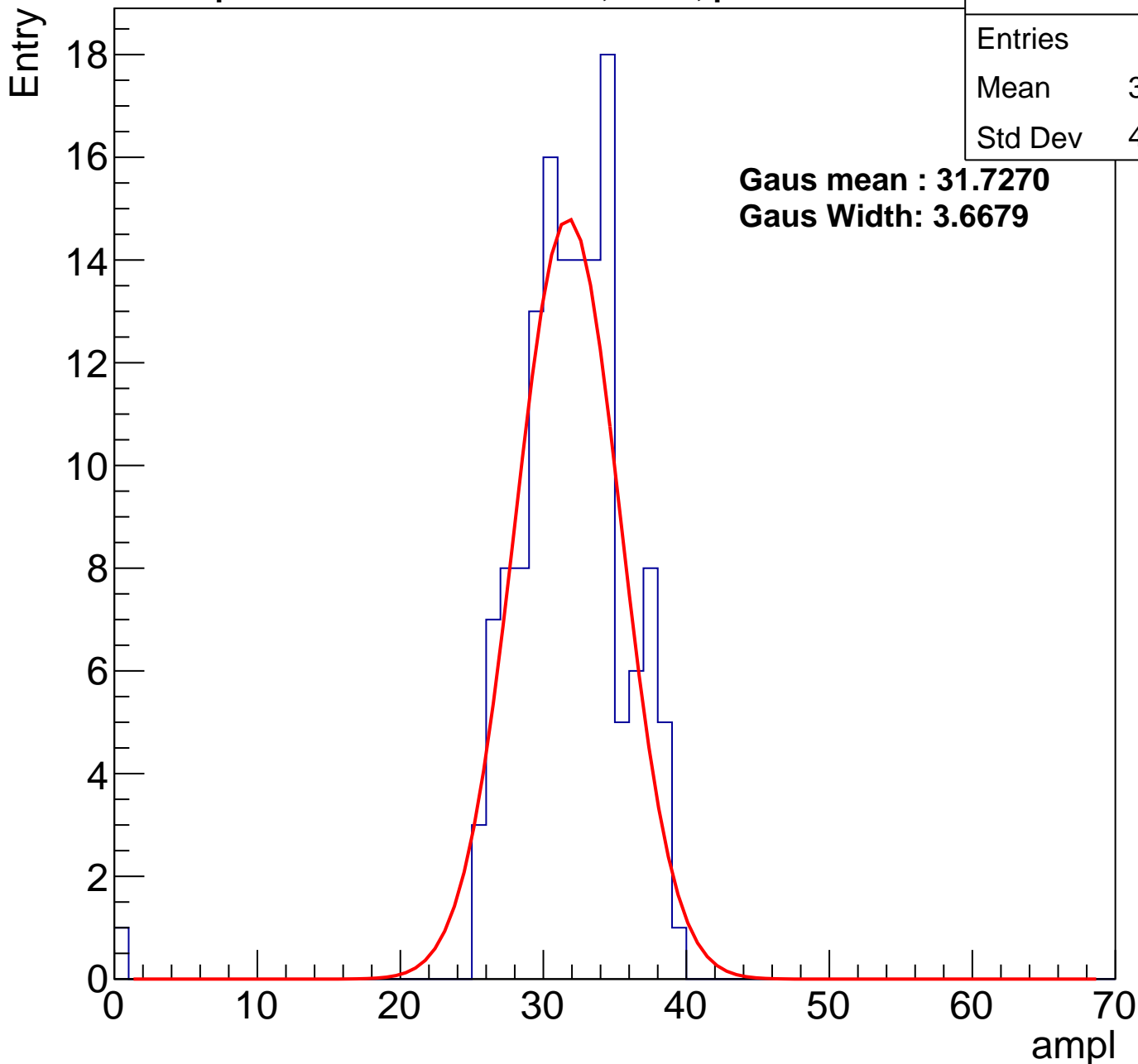
B1L001S, U25-ch65, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	31.39
Std Dev	4.252

Gaus mean : 31.7270

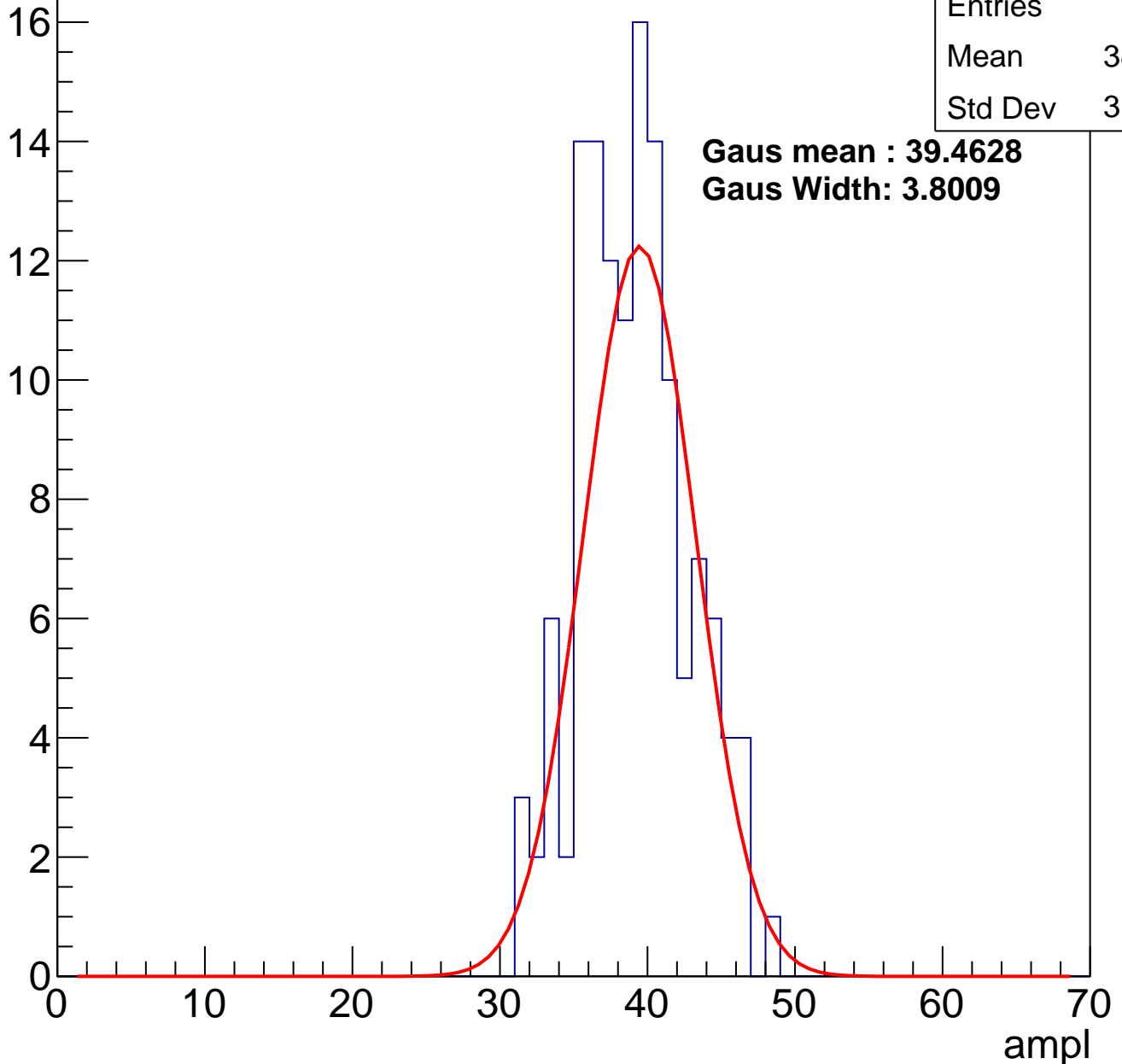
Gaus Width: 3.6679



B1L001S, U25-ch65, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch65, adc2

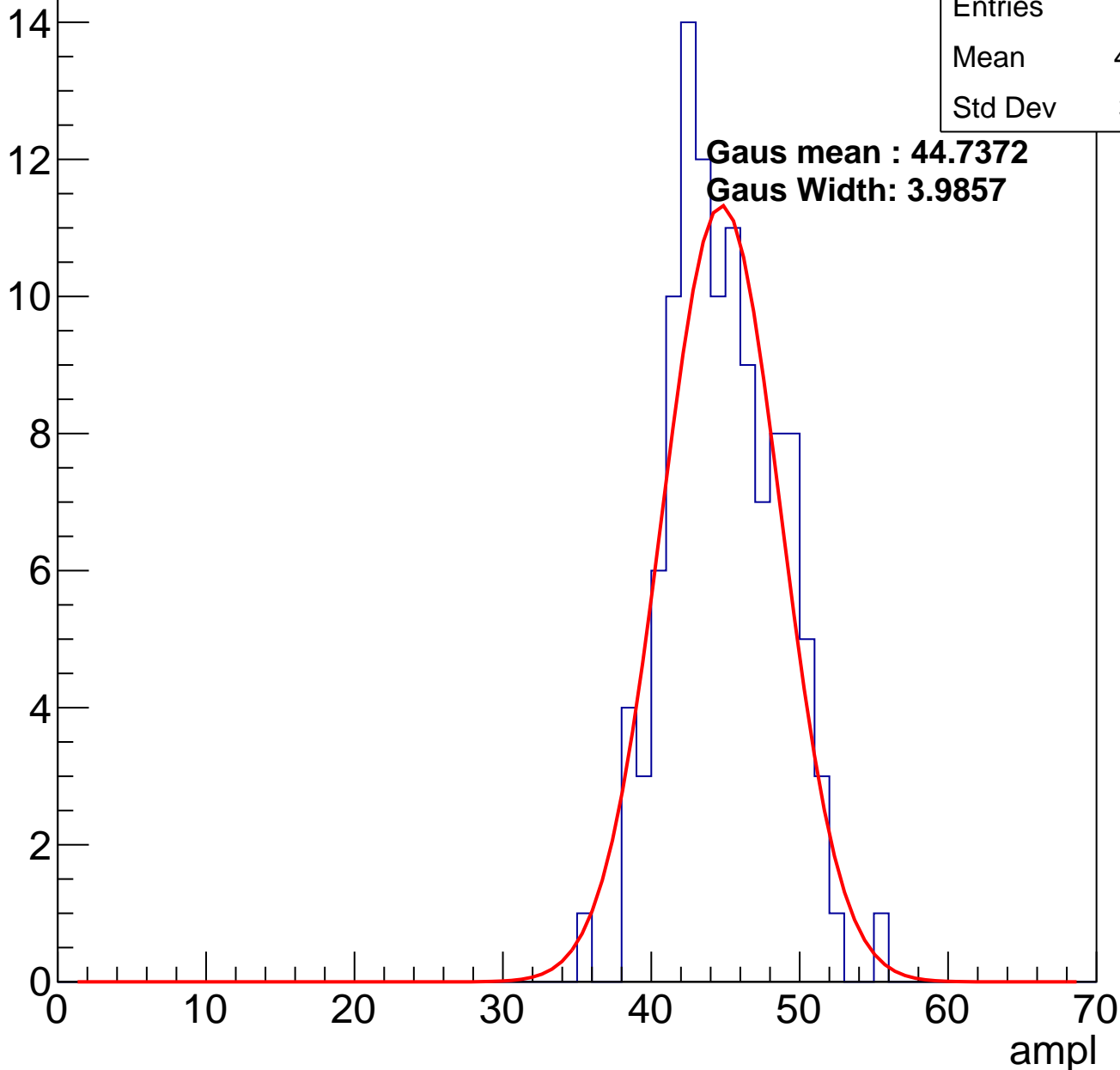
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	113
Mean	44.44
Std Dev	3.621

Gaus mean : 44.7372

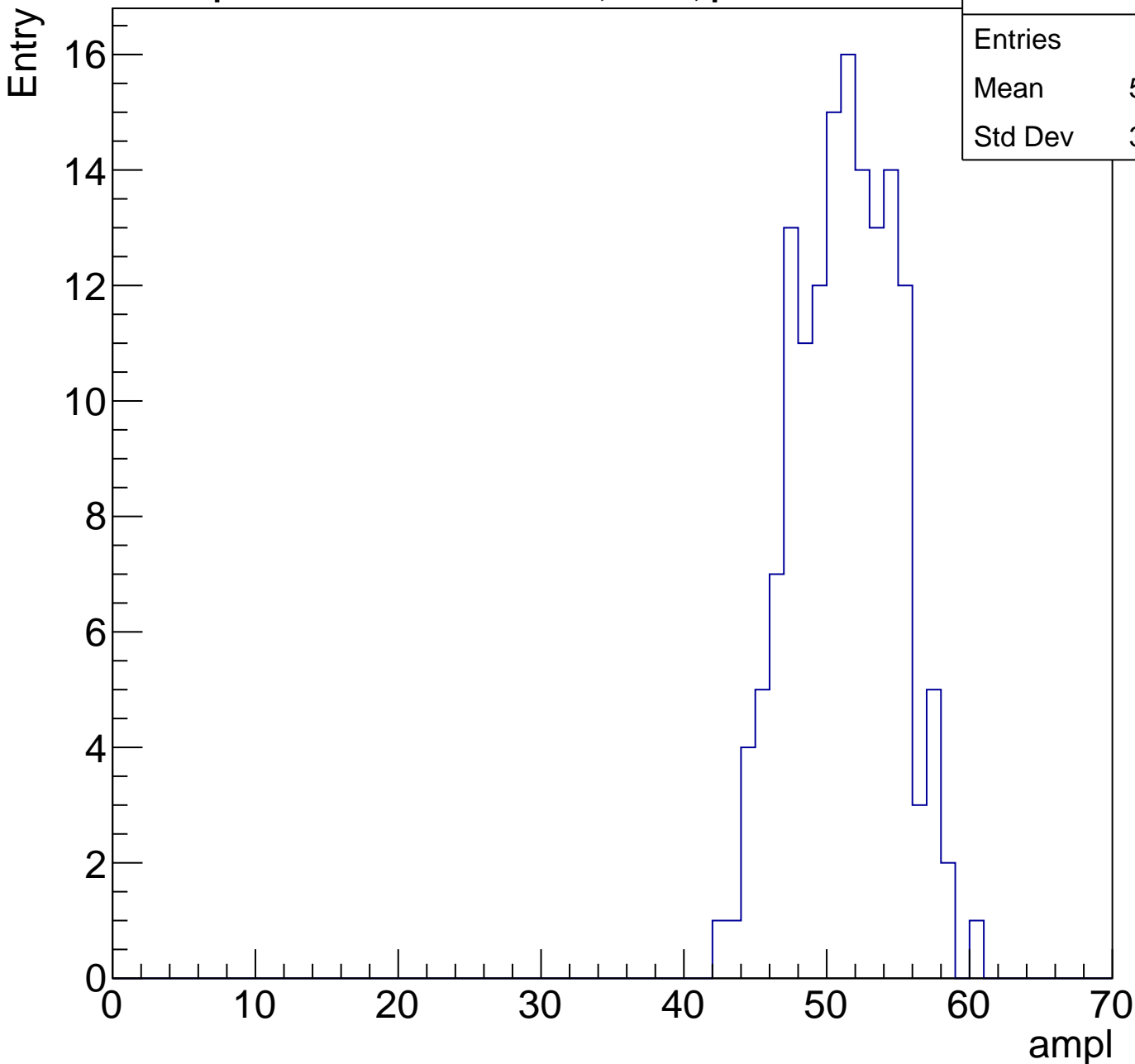
Gaus Width: 3.9857



B1L001S, U25-ch65, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	50.76
Std Dev	3.548



B1L001S, U25-ch65, adc4

calib_packv5_042523_0143.root, FC#2, port C2

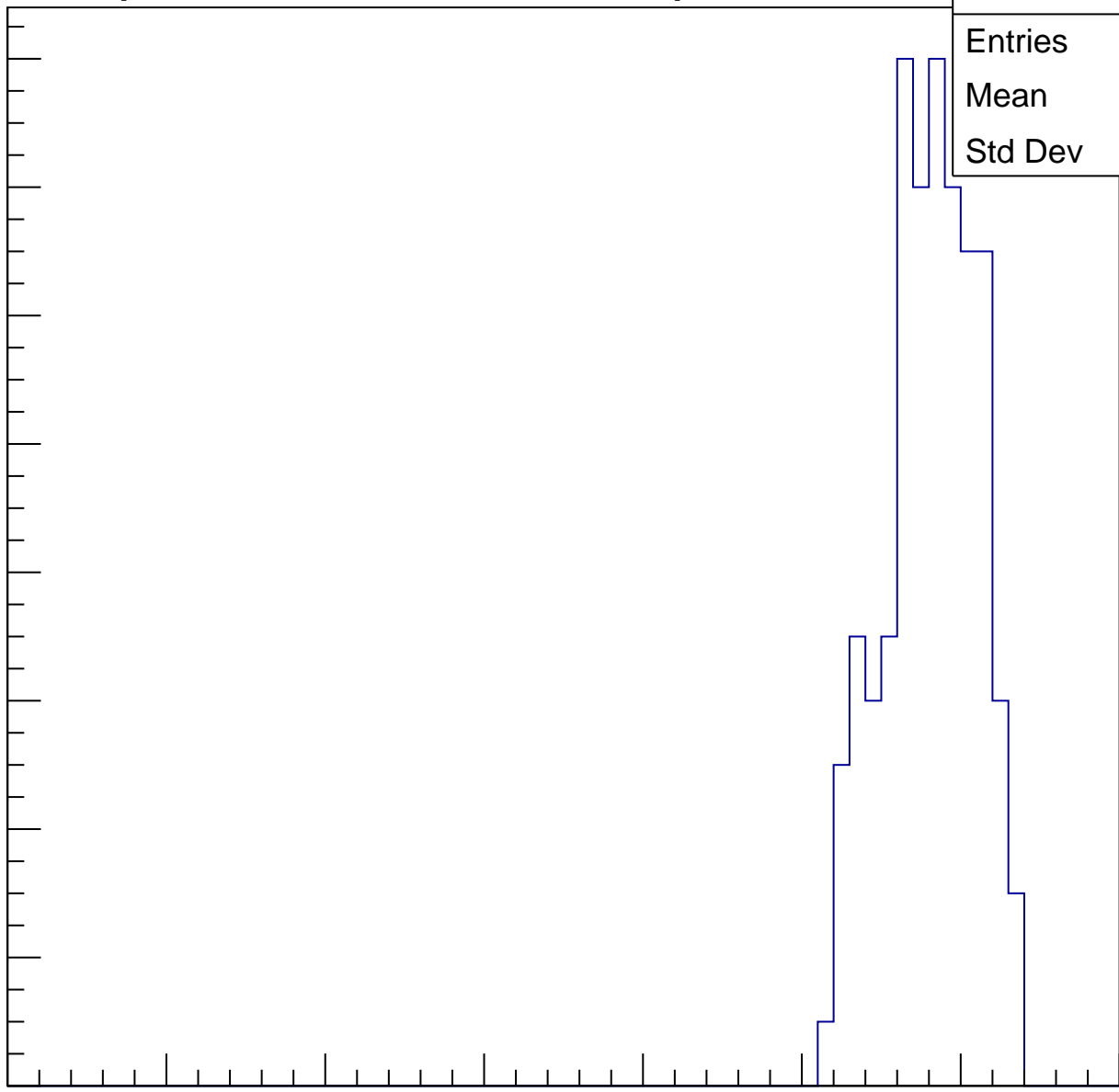
Entries	121
Mean	57.63
Std Dev	2.843

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch65, adc5

calib_packv5_042523_0143.root, FC#2, port C2

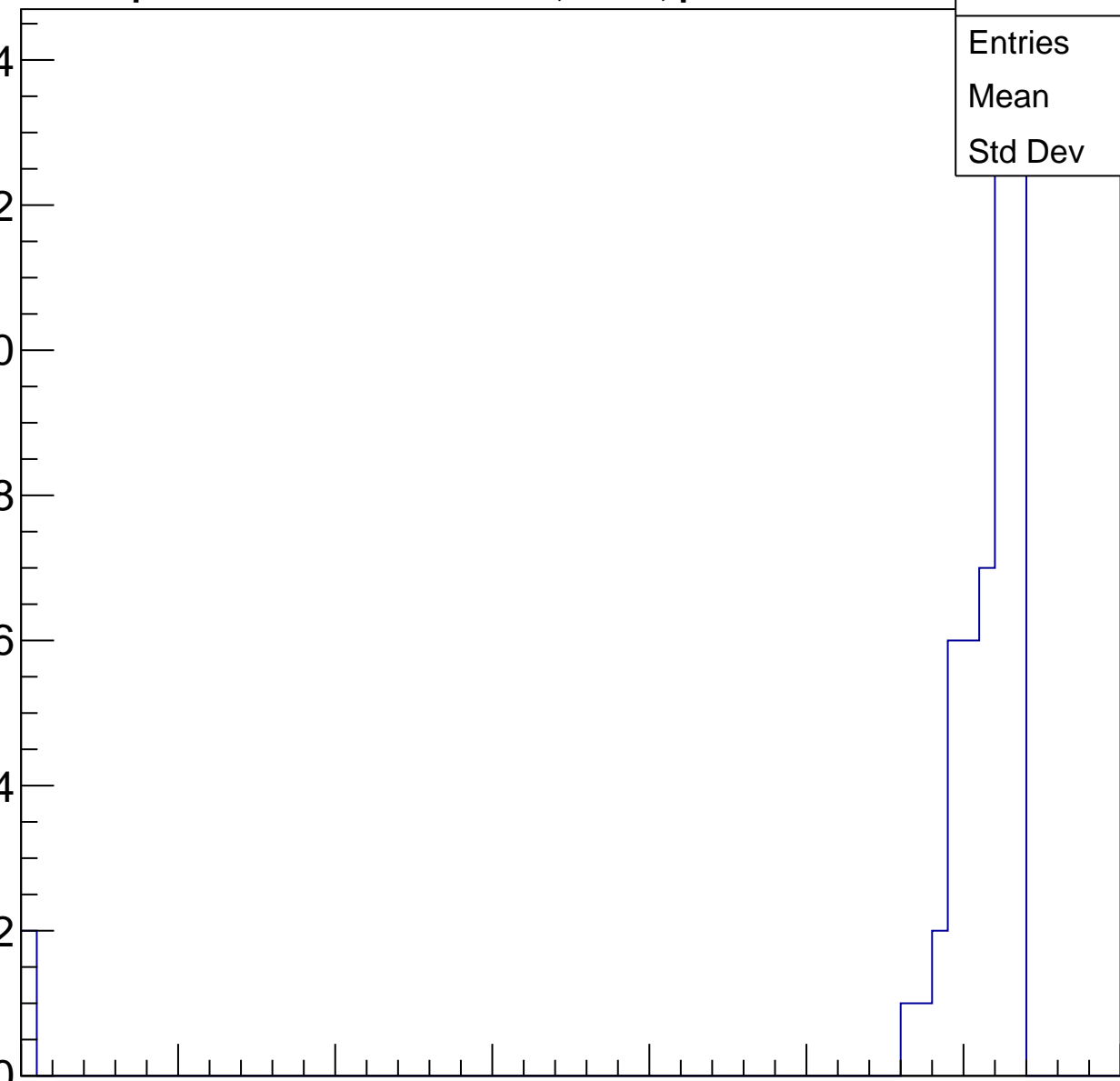
Entries	53
Mean	58.87
Std Dev	11.78

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch65, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch65, adc7

calib_packv5_042523_0143.root, FC#2, port C2

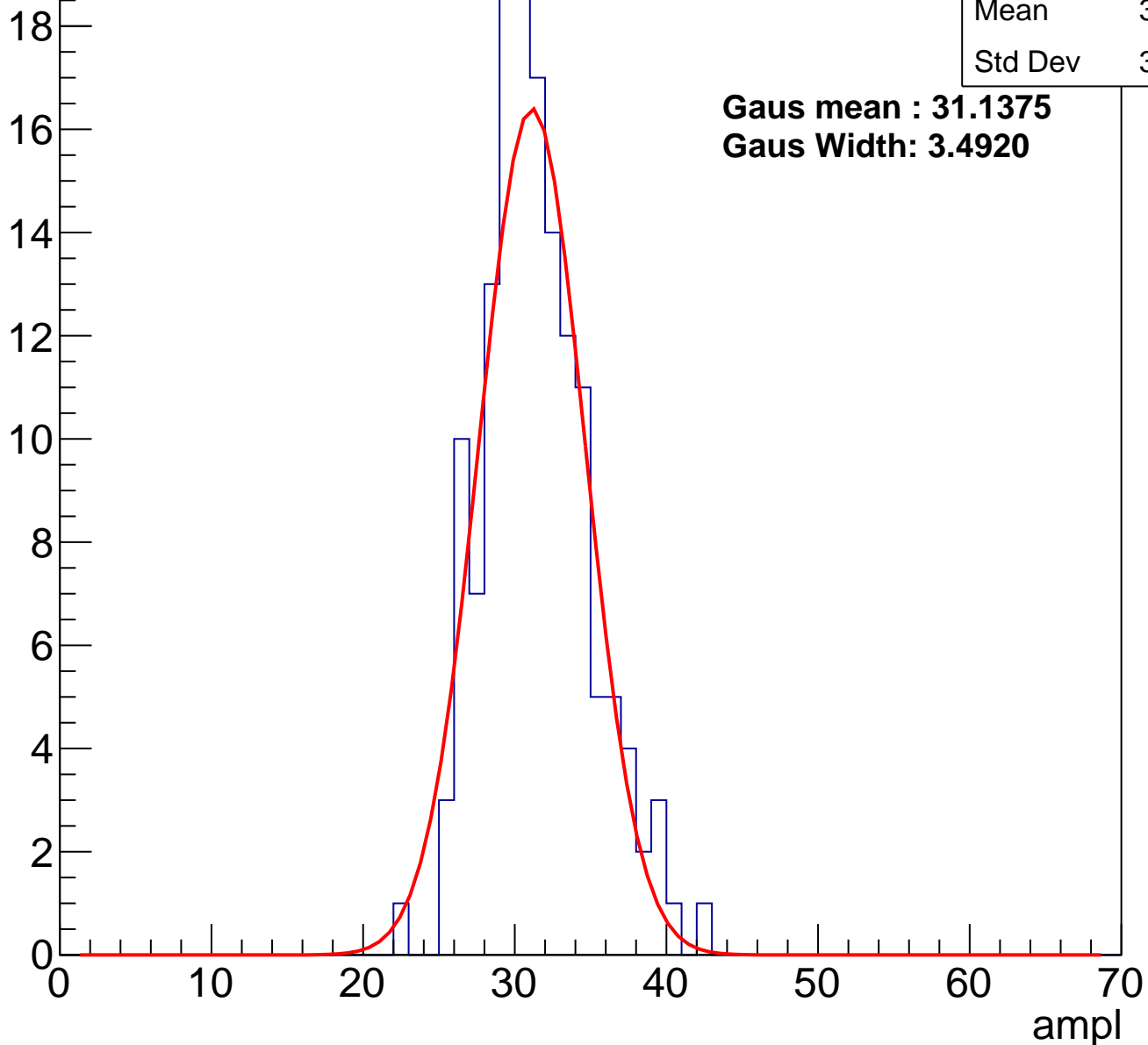
Entry



B1L001S, U25-ch66, adc0

calib_packv5_042523_0143.root, FC#2, port C2

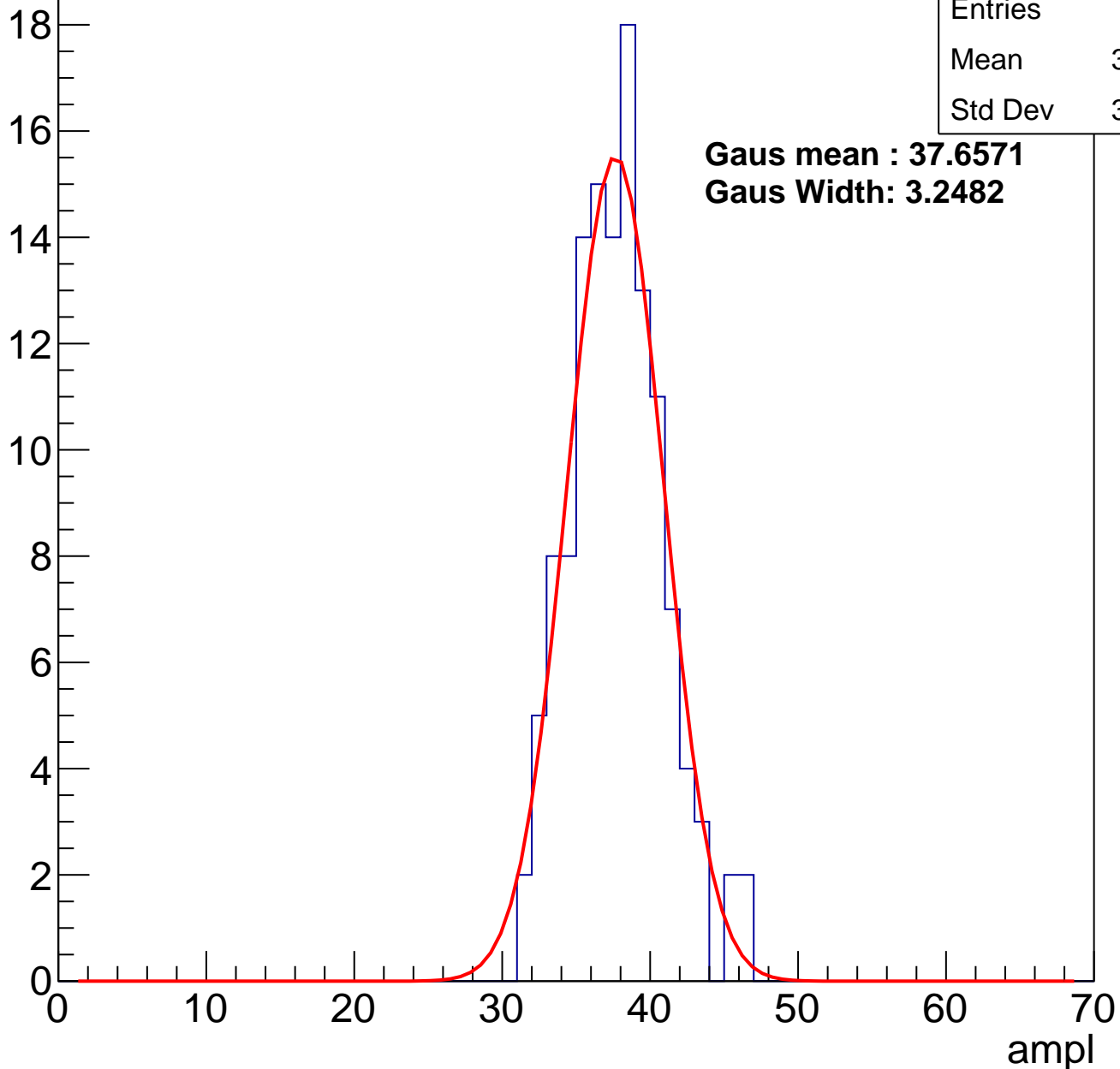
Entry



B1L001S, U25-ch66, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

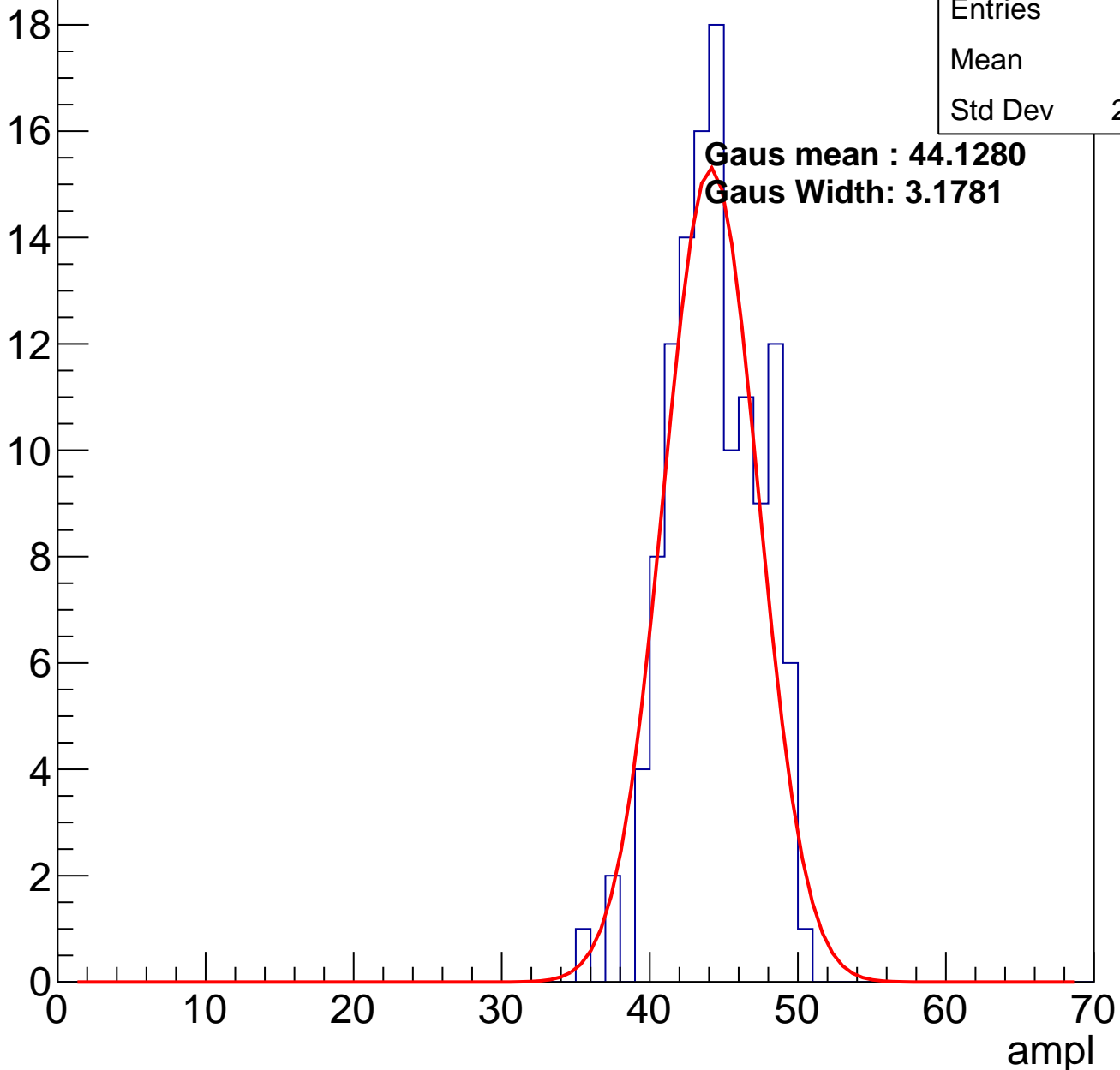


Entries	126
Mean	37.33
Std Dev	3.129

B1L001S, U25-ch66, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

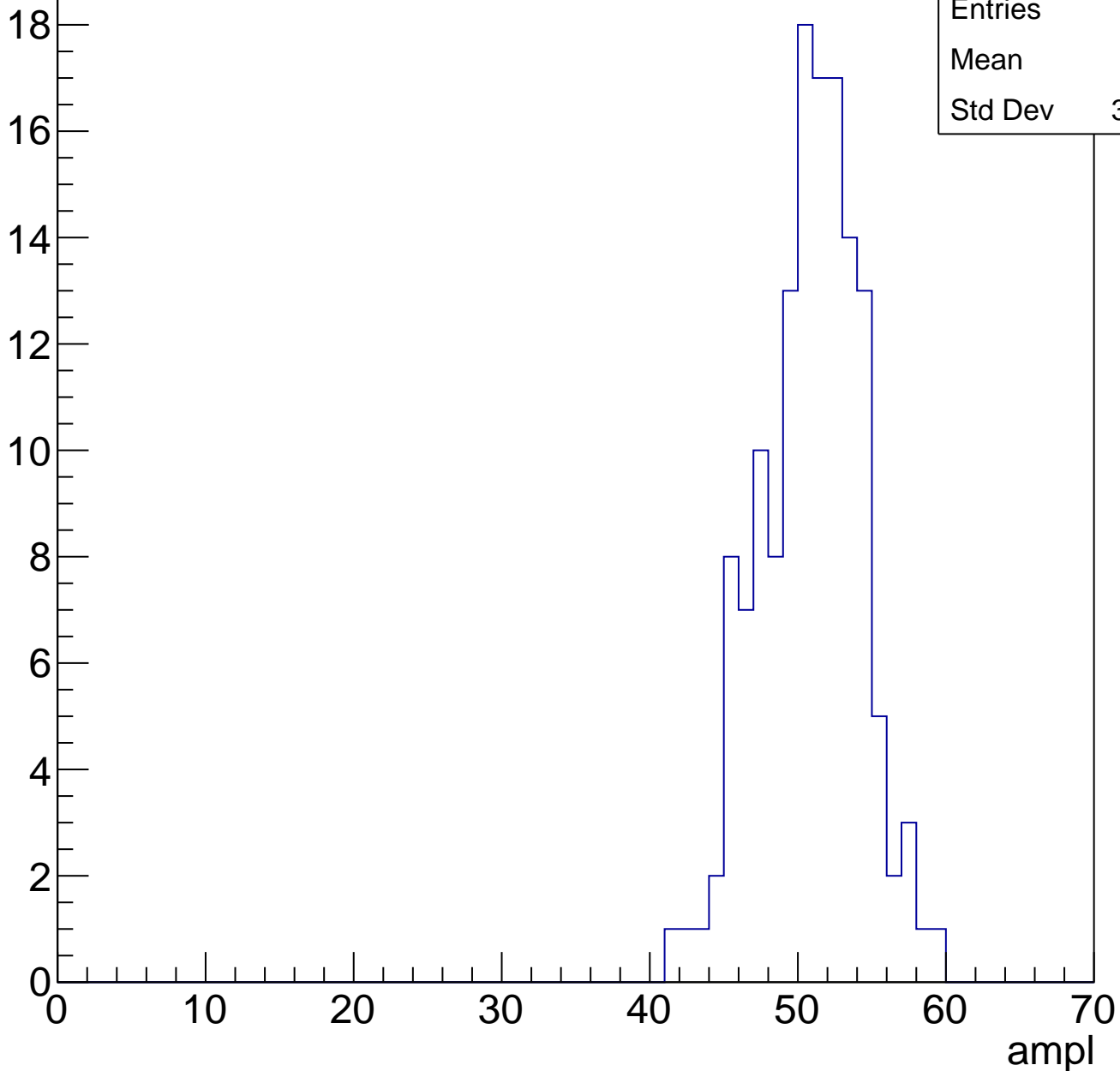


B1L001S, U25-ch66, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	50.4
Std Dev	3.355

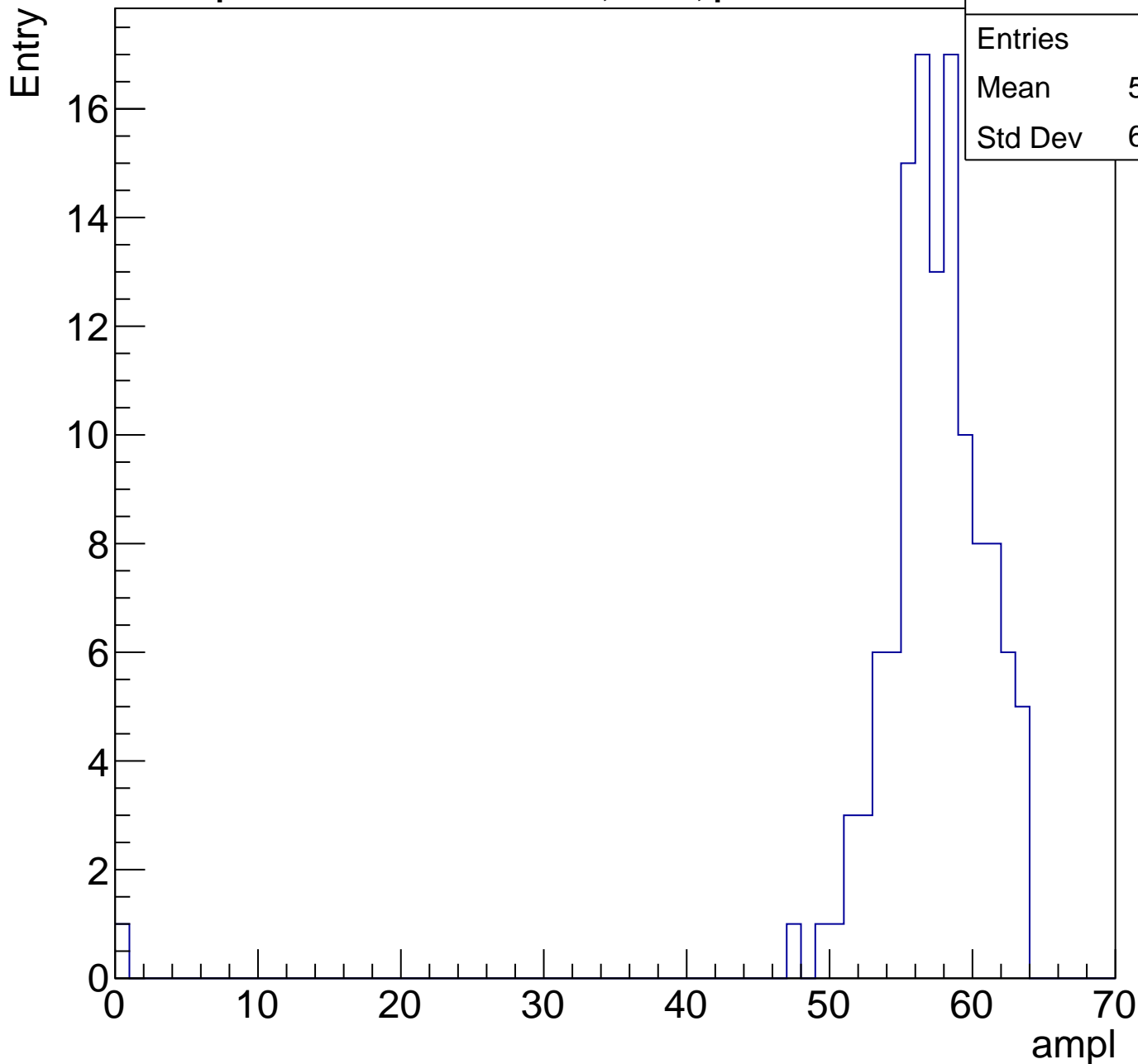
Entry



B1L001S, U25-ch66, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	56.58
Std Dev	6.056



B1L001S, U25-ch66, adc5

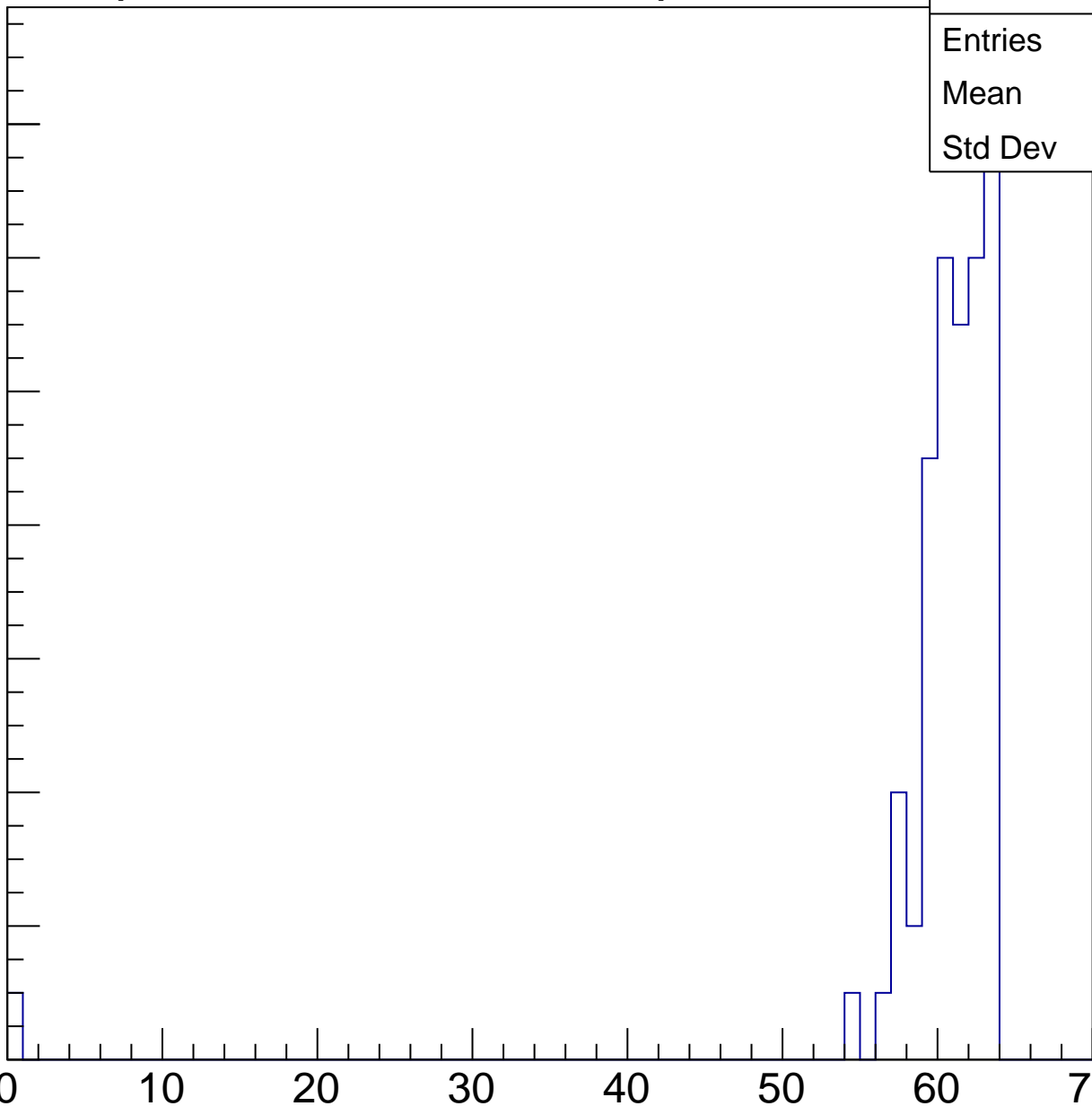
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	59.78
Std Dev	7.569

ampl



B1L001S, U25-ch66, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L001S, U25-ch66, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L001S, U25-ch67, adc0

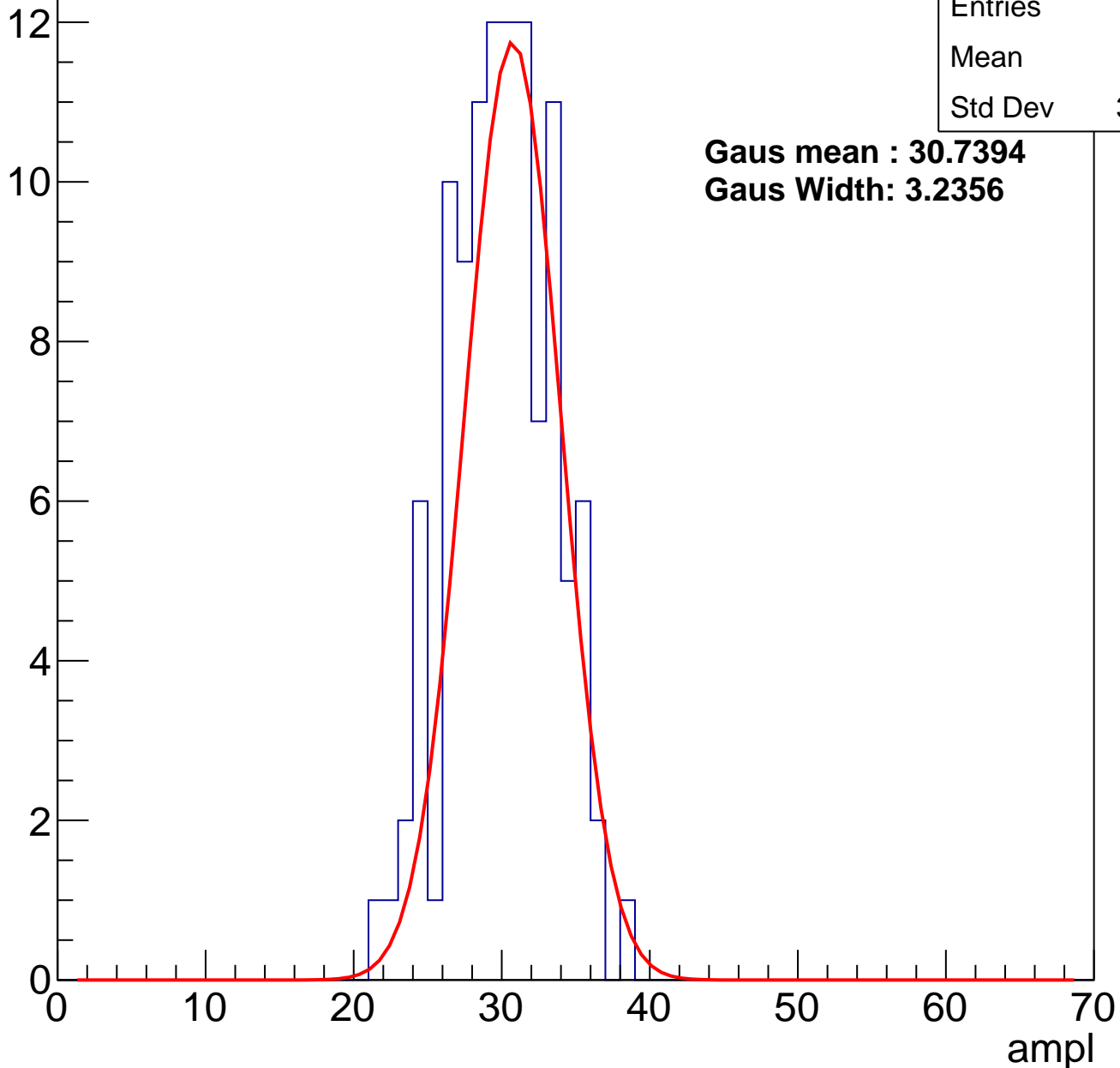
calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	29.6
Std Dev	3.411

Gaus mean : 30.7394

Gaus Width: 3.2356

Entry



B1L001S, U25-ch67, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	35.91
Std Dev	3.419

Gaus mean : 36.1683

Gaus Width: 3.5744

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

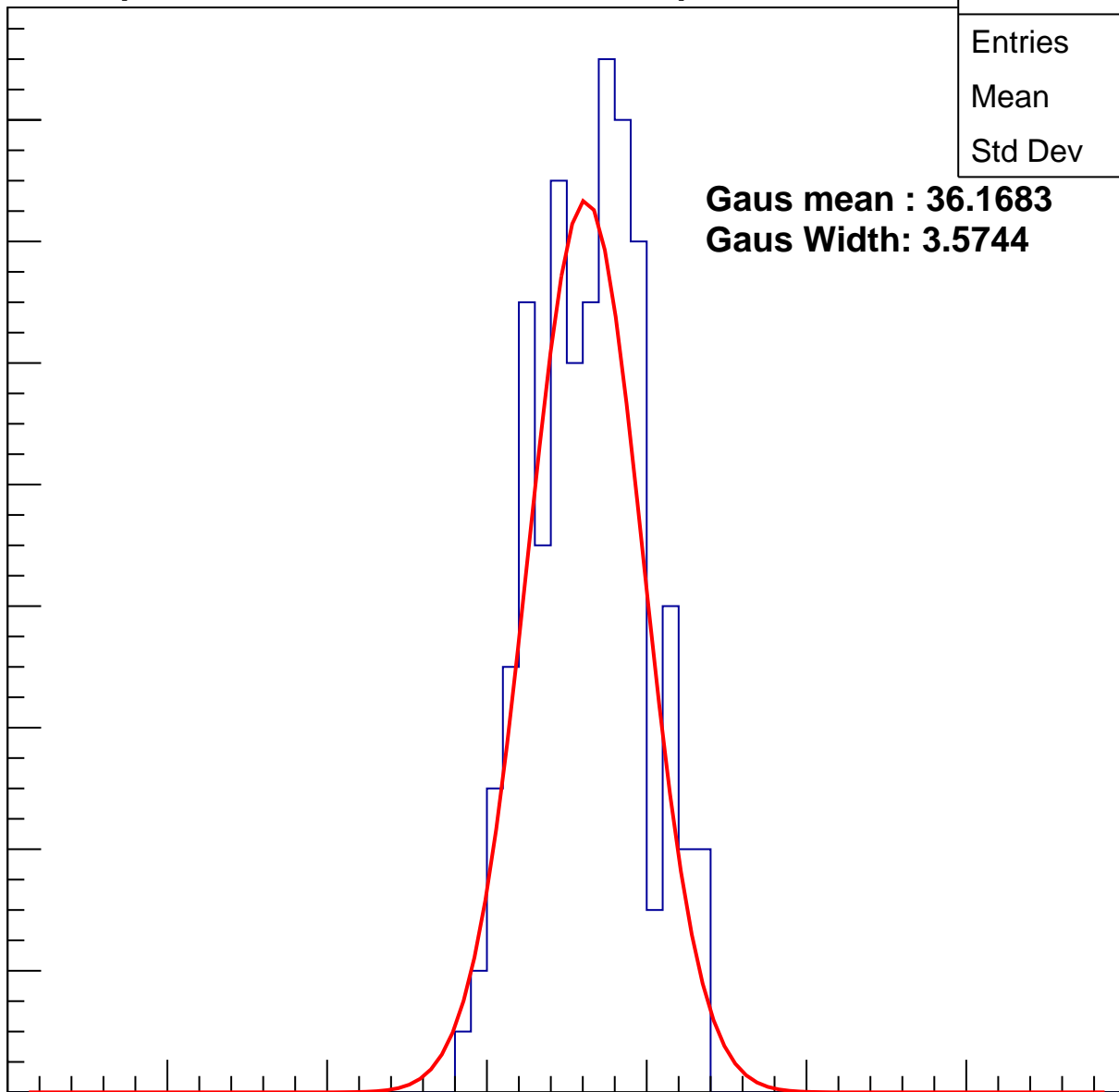
40

50

60

70

ampl



B1L001S, U25-ch67, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	42.51
Std Dev	3.427

Gaus mean : 43.2579

Gaus Width: 3.6255

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

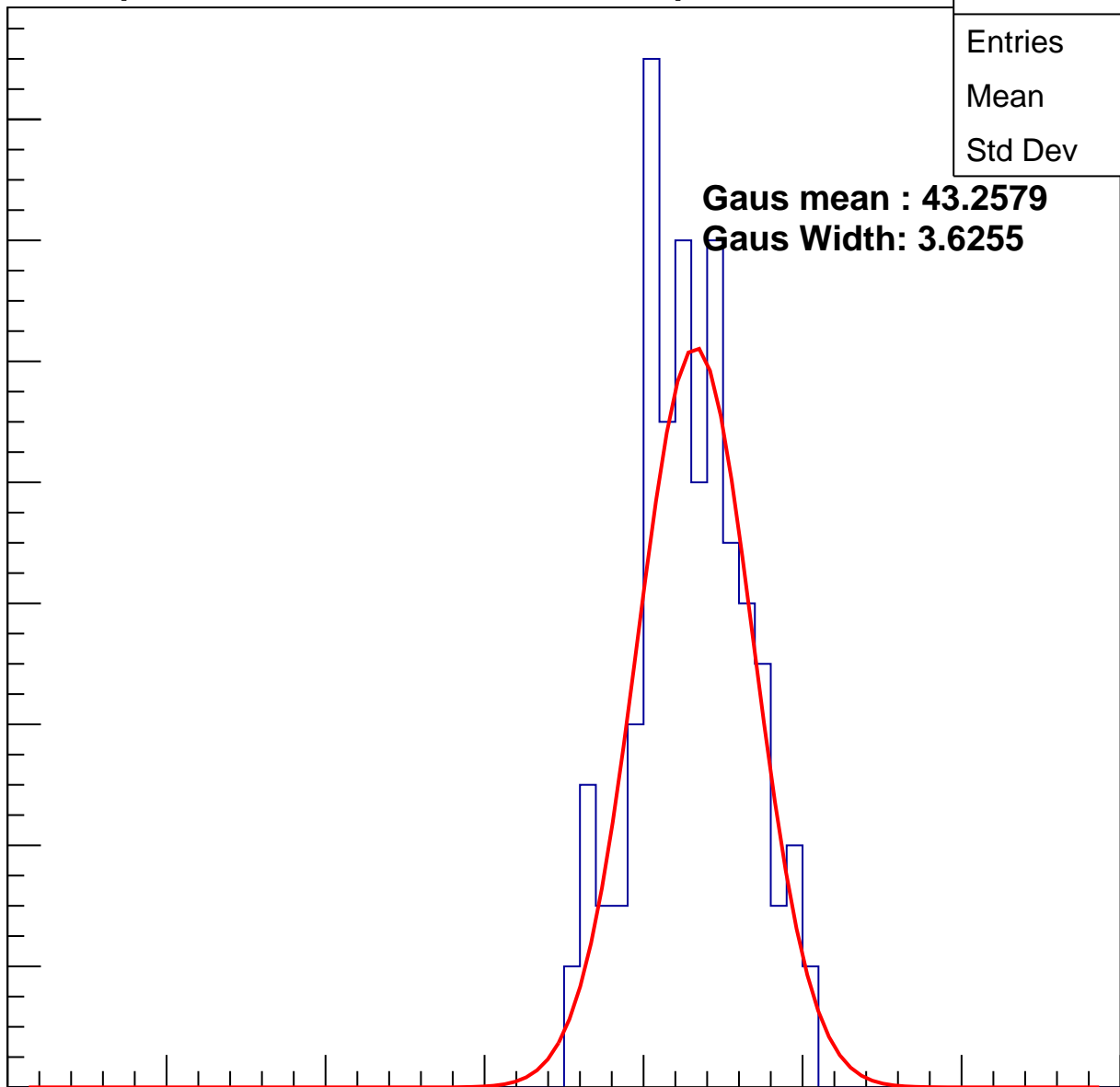
40

50

60

70

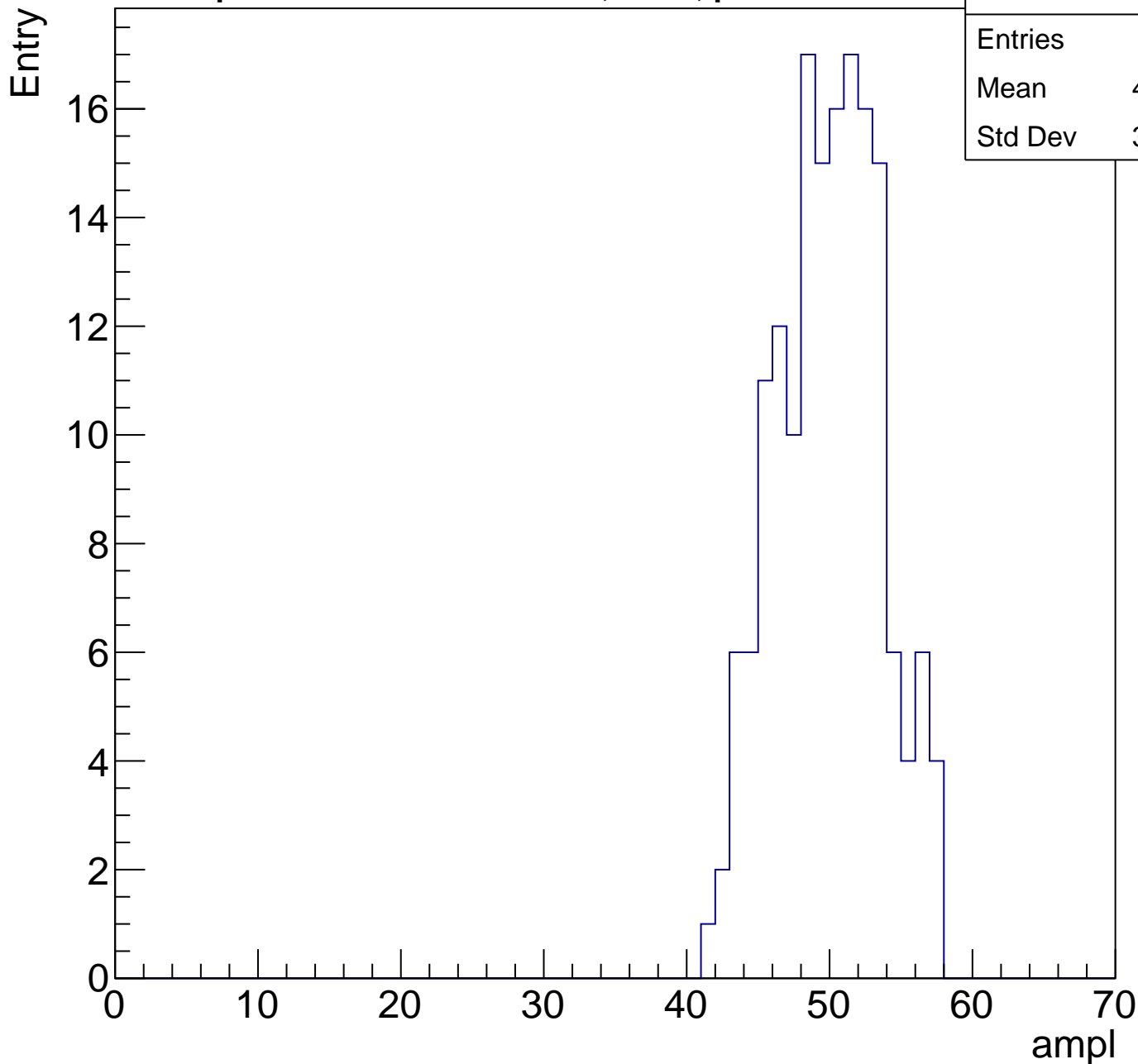
ampl



B1L001S, U25-ch67, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	49.49
Std Dev	3.613



B1L001S, U25-ch67, adc4

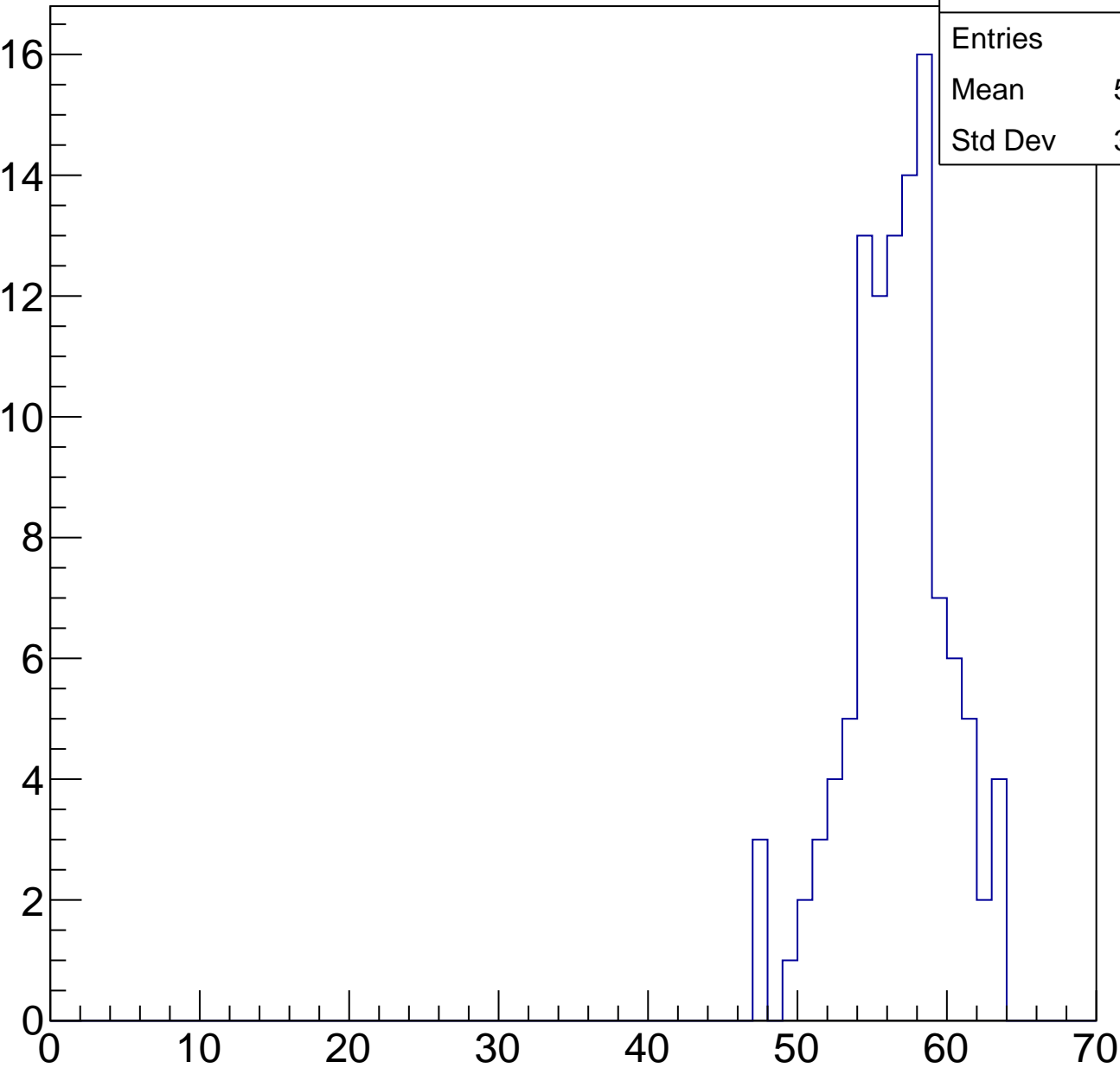
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	56.24
Std Dev	3.352

ampl



B1L001S, U25-ch67, adc5

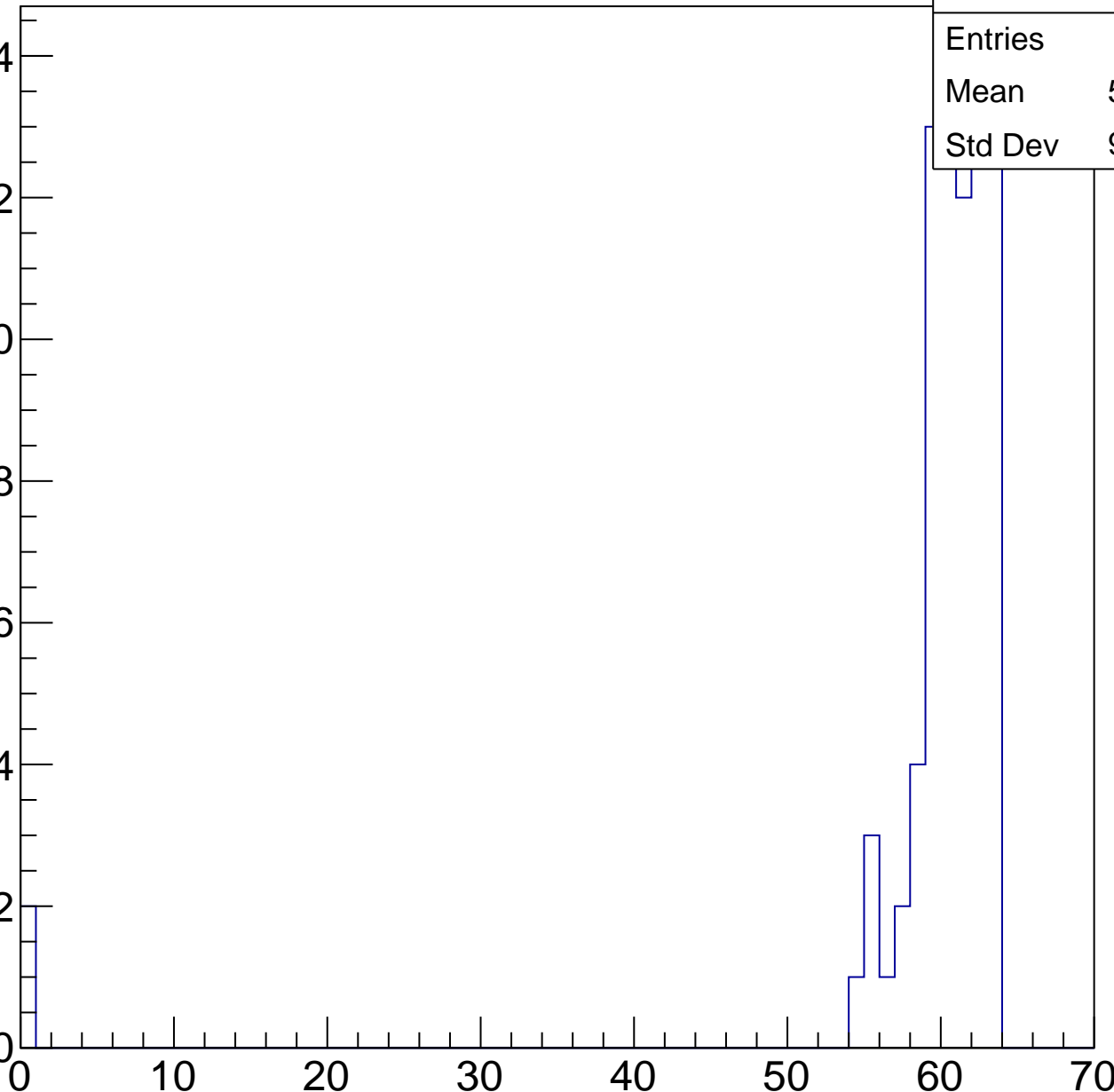
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	58.82
Std Dev	9.777

ampl

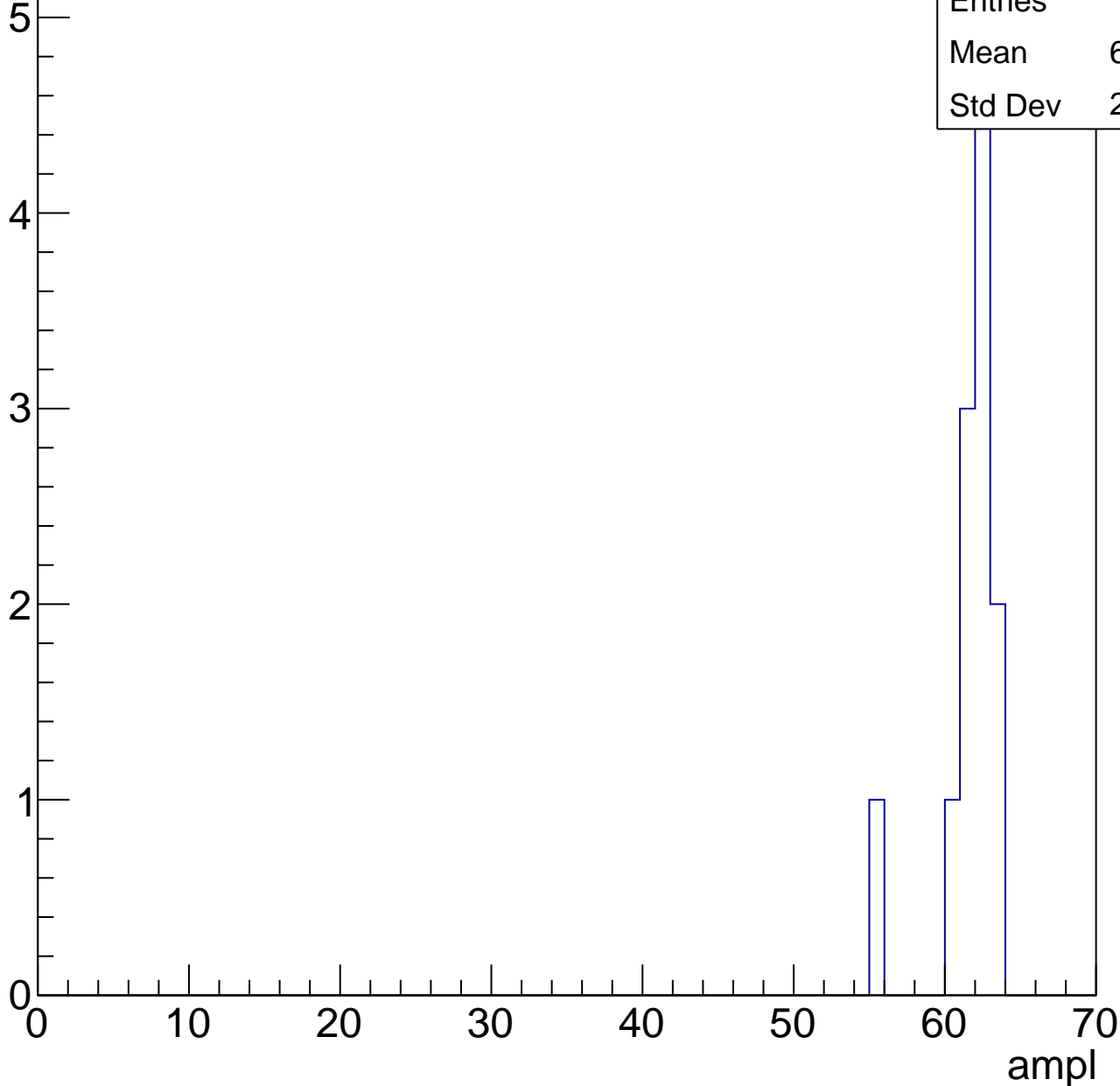


B1L001S, U25-ch67, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.17
Std Dev	2.034



B1L001S, U25-ch67, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch68, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	30.13
Std Dev	3.206

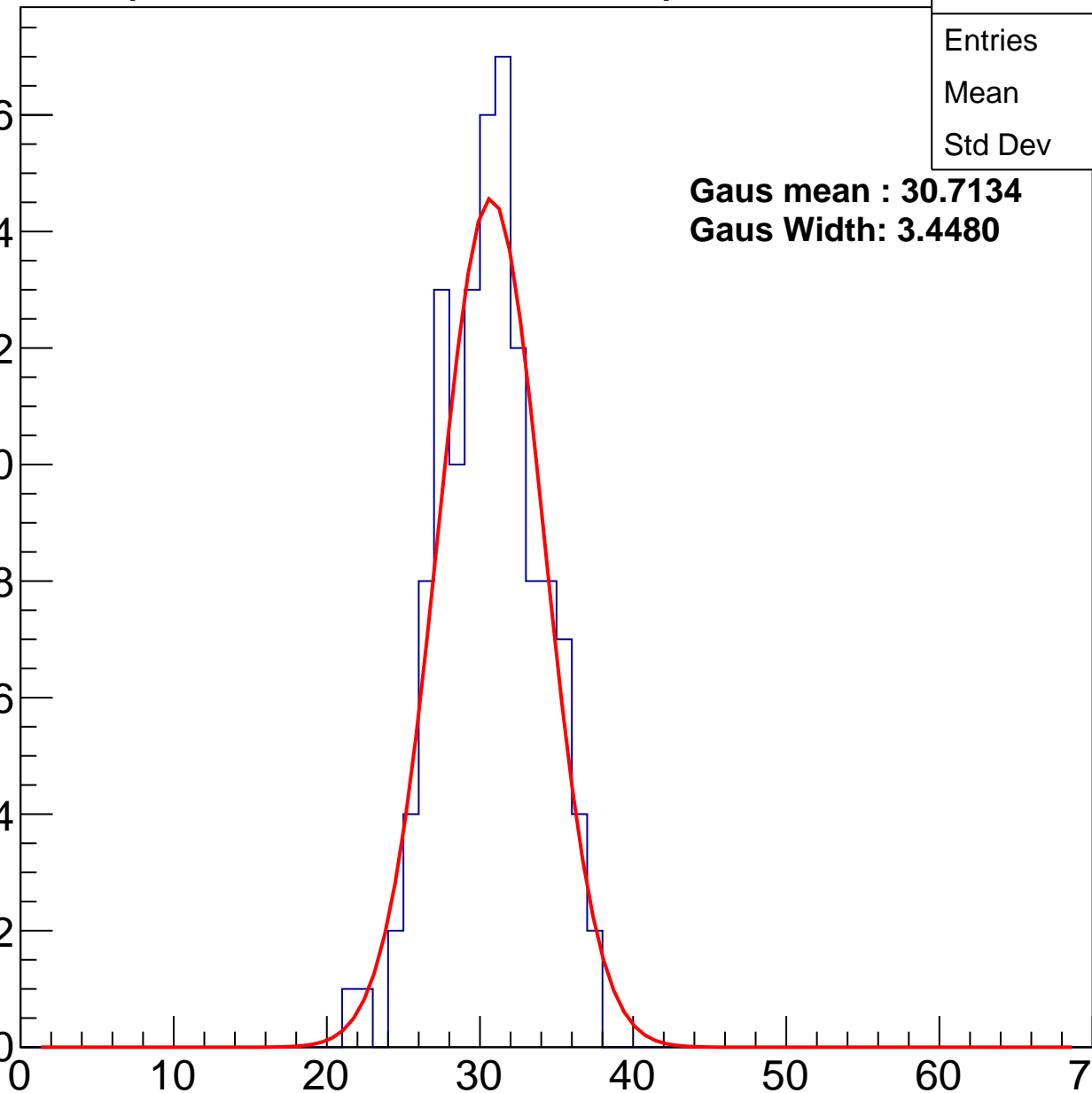
Gaus mean : 30.7134

Gaus Width: 3.4480

Entry

16
14
12
10
8
6
4
2
0

ampl



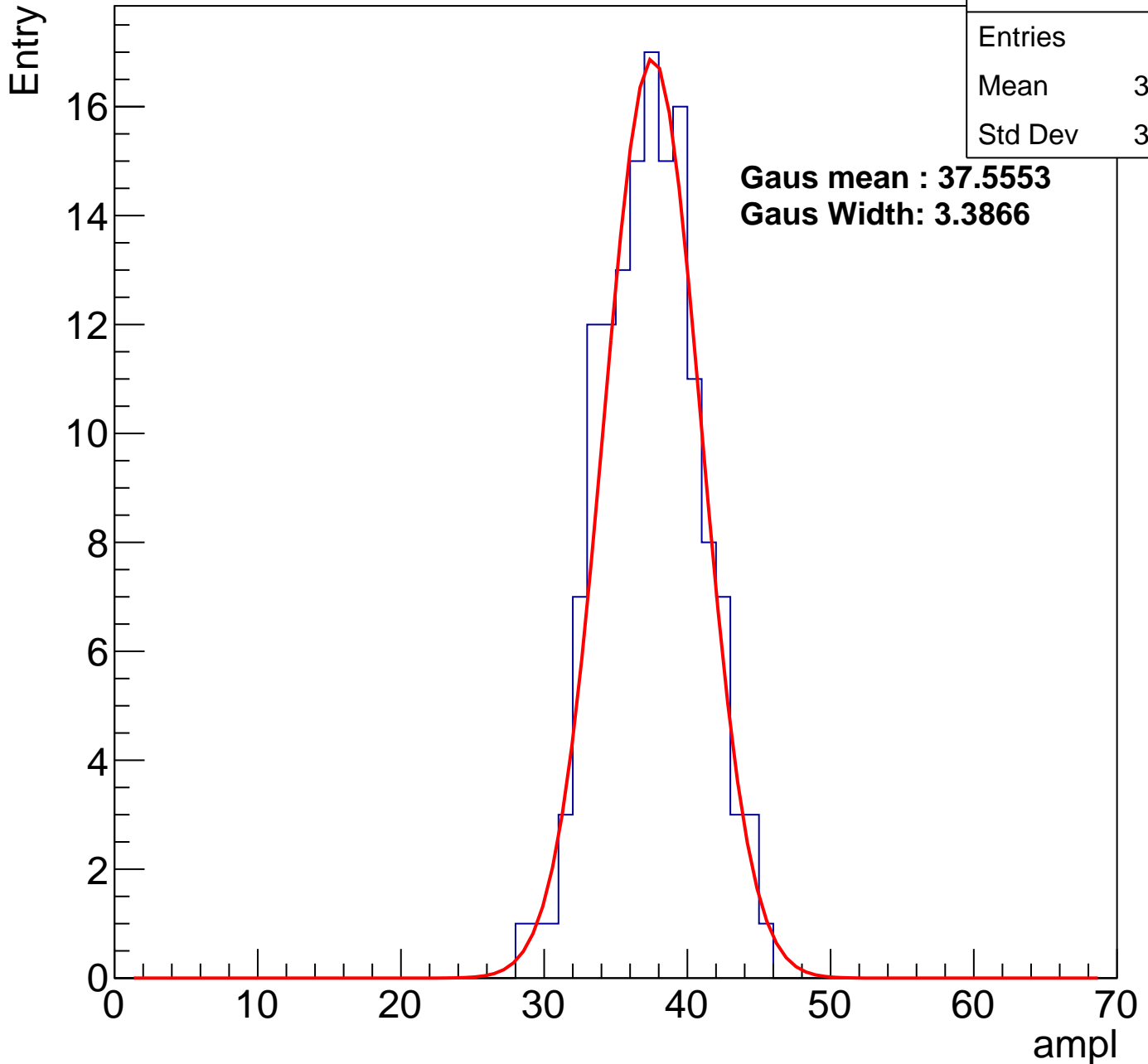
B1L001S, U25-ch68, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	36.95
Std Dev	3.355

Gaus mean : 37.5553

Gaus Width: 3.3866



B1L001S, U25-ch68, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

106

Mean

43.25

Std Dev

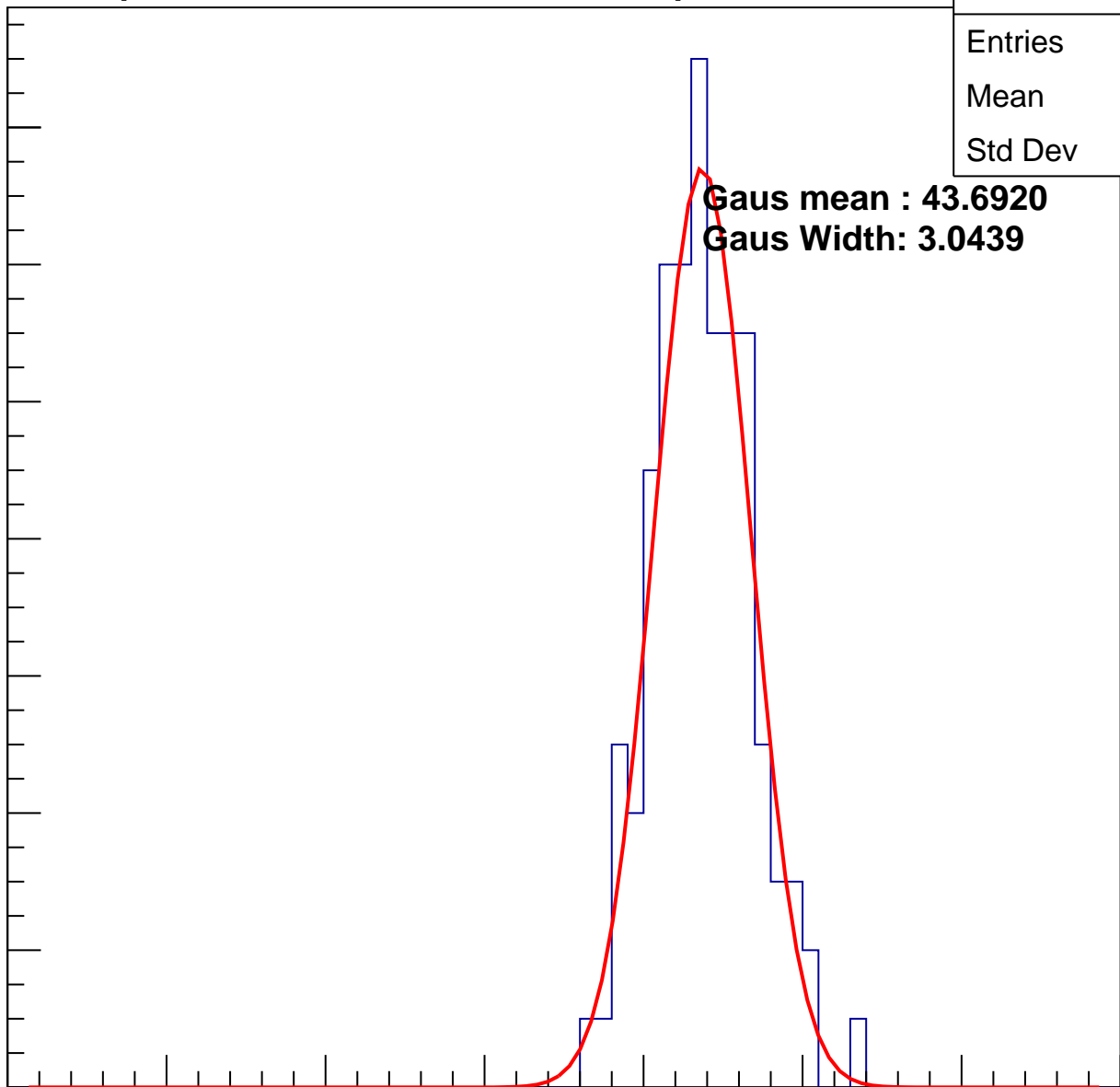
3.108

Gaus mean : 43.6920

Gaus Width: 3.0439

0 10 20 30 40 50 60 70

ampl

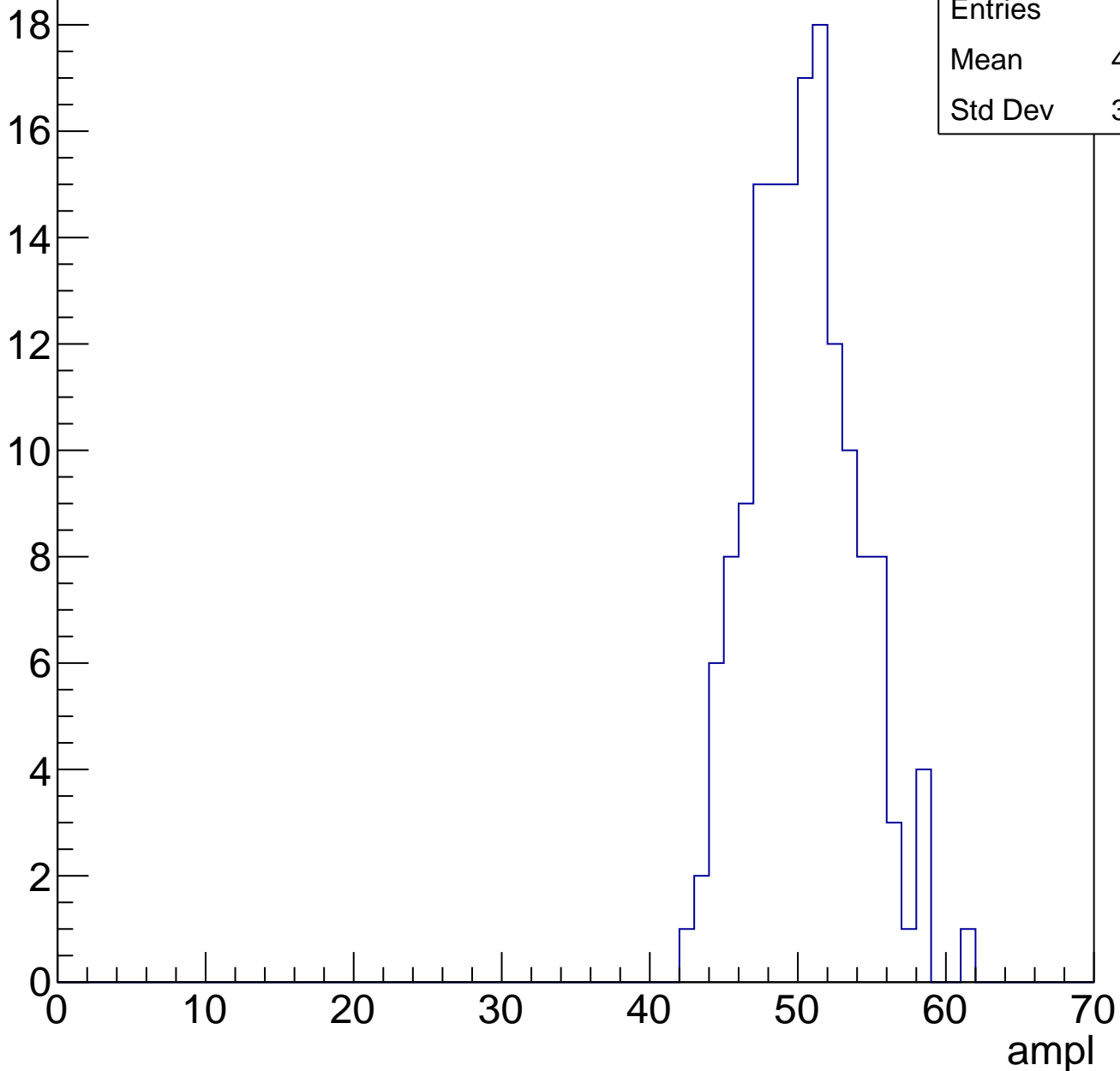


B1L001S, U25-ch68, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	49.92
Std Dev	3.569

Entry



B1L001S, U25-ch68, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	97
Mean	55.78
Std Dev	3.08

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

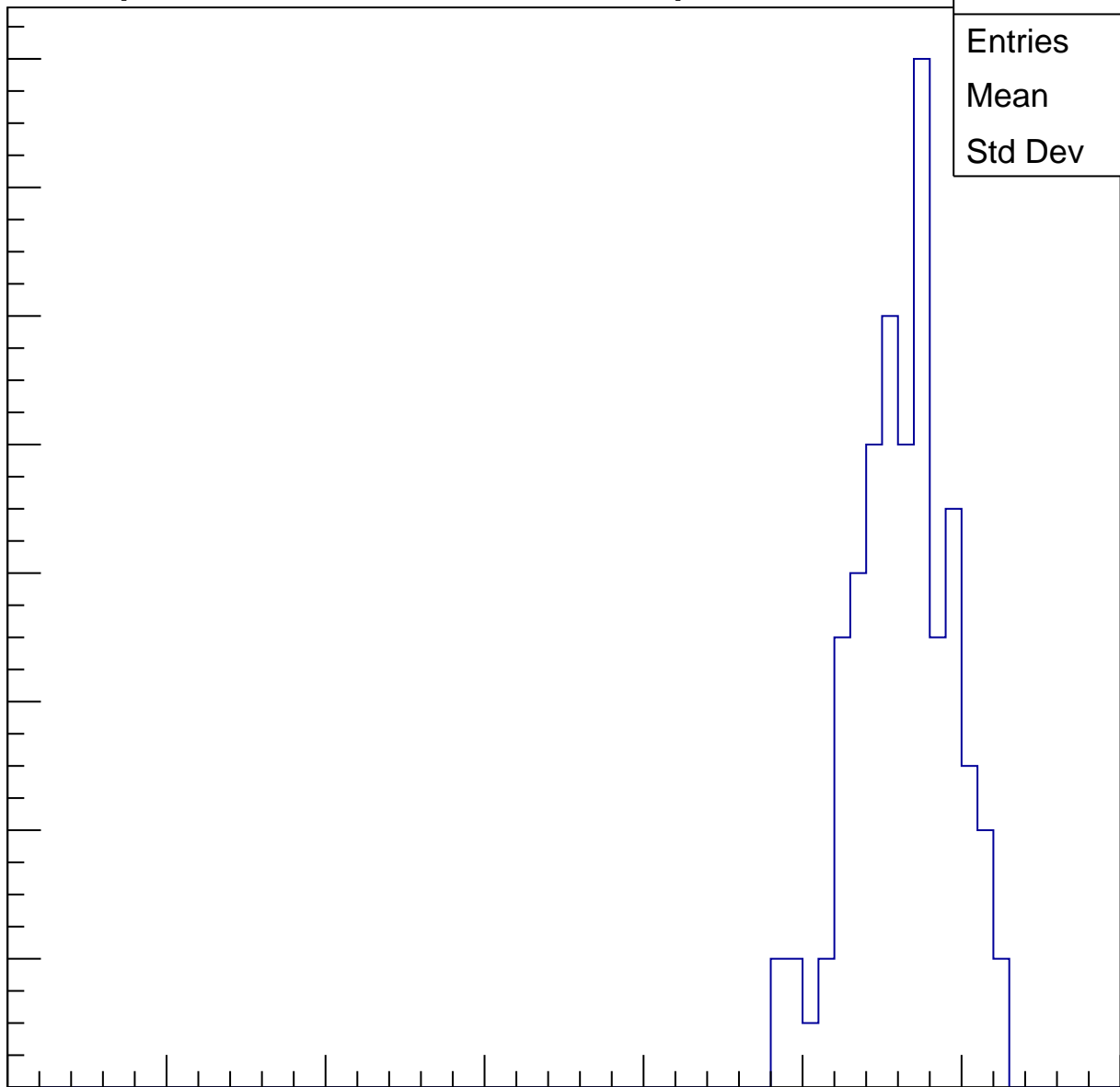
40

50

60

70

ampl



B1L001S, U25-ch68, adc5

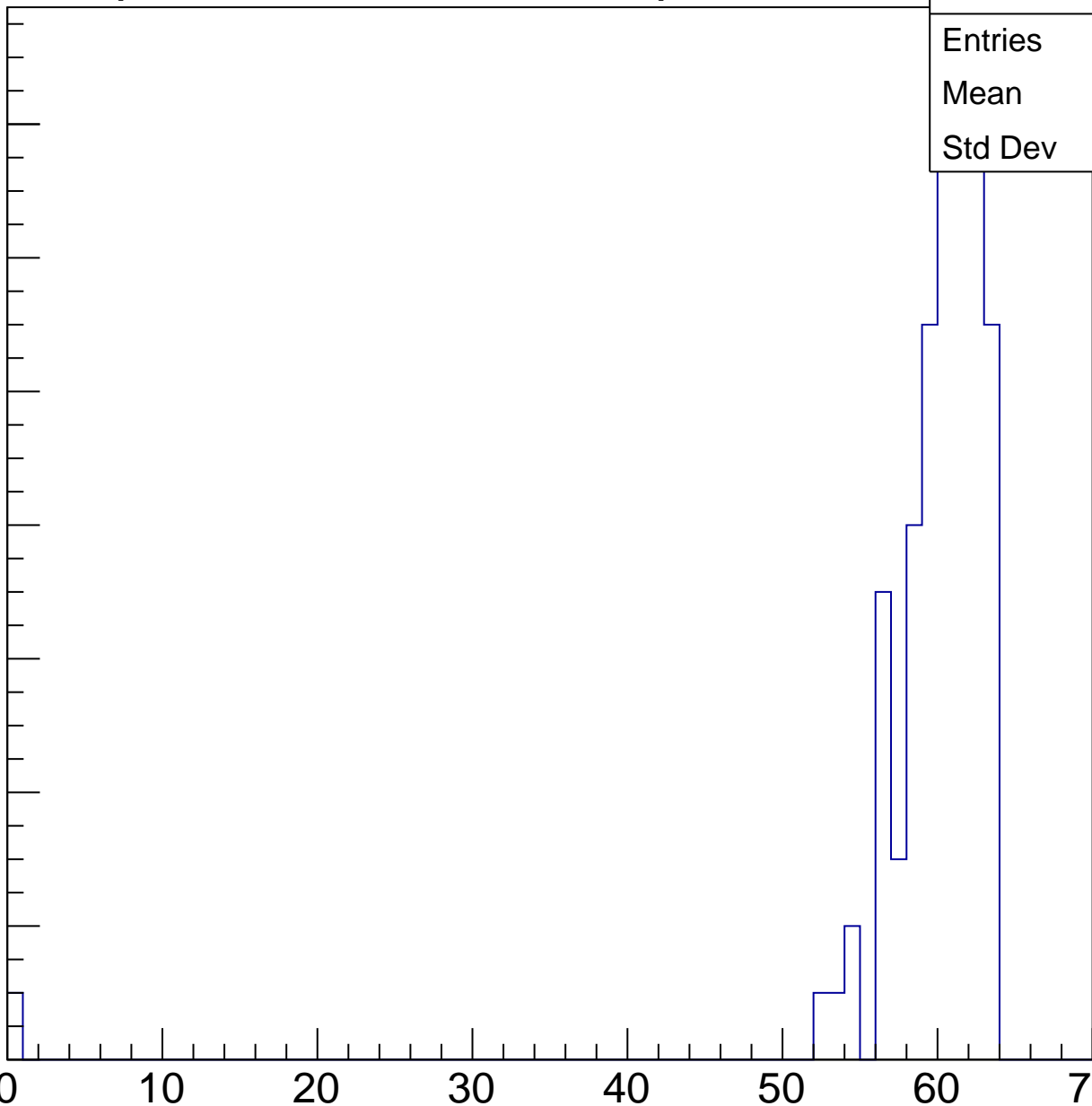
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	59.17
Std Dev	6.769

ampl

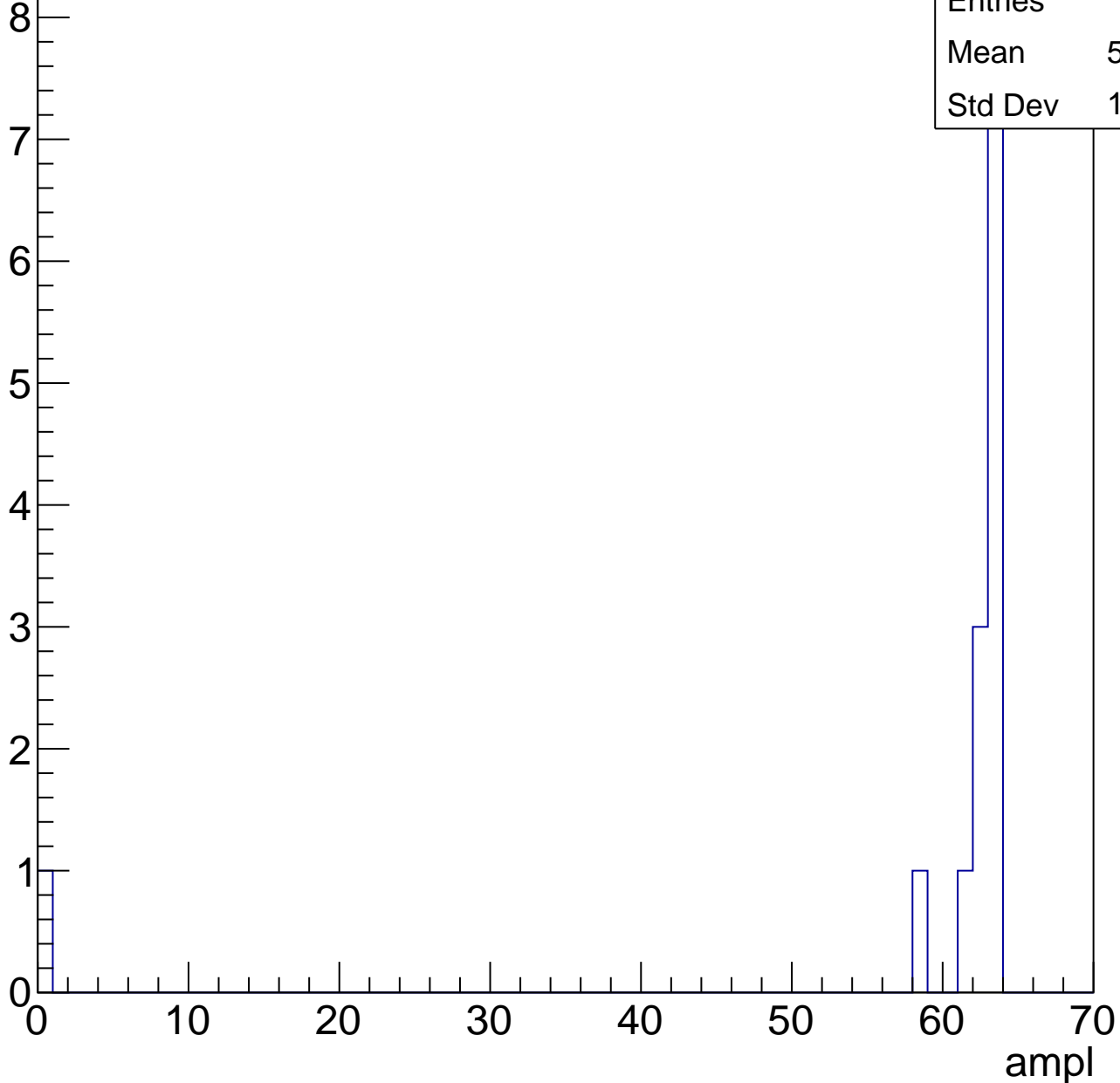


B1L001S, U25-ch68, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

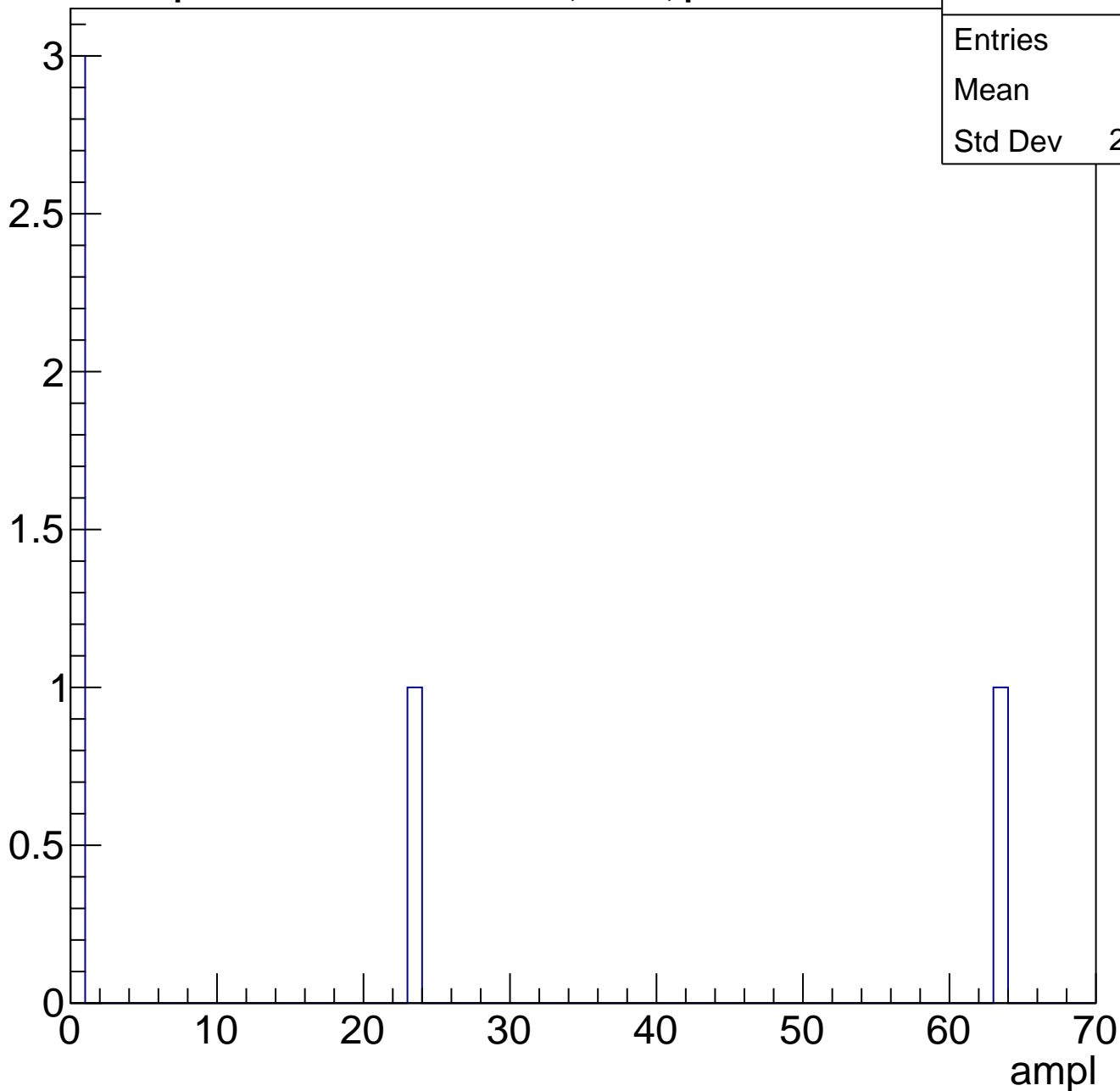
Entries	14
Mean	57.79
Std Dev	16.08



B1L001S, U25-ch68, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	17.2
Std Dev	24.57

B1L001S, U25-ch69, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

146

Mean

31.49

Std Dev

3.685

Gaus mean : 31.9936

Gaus Width: 3.7293

0

10

20

30

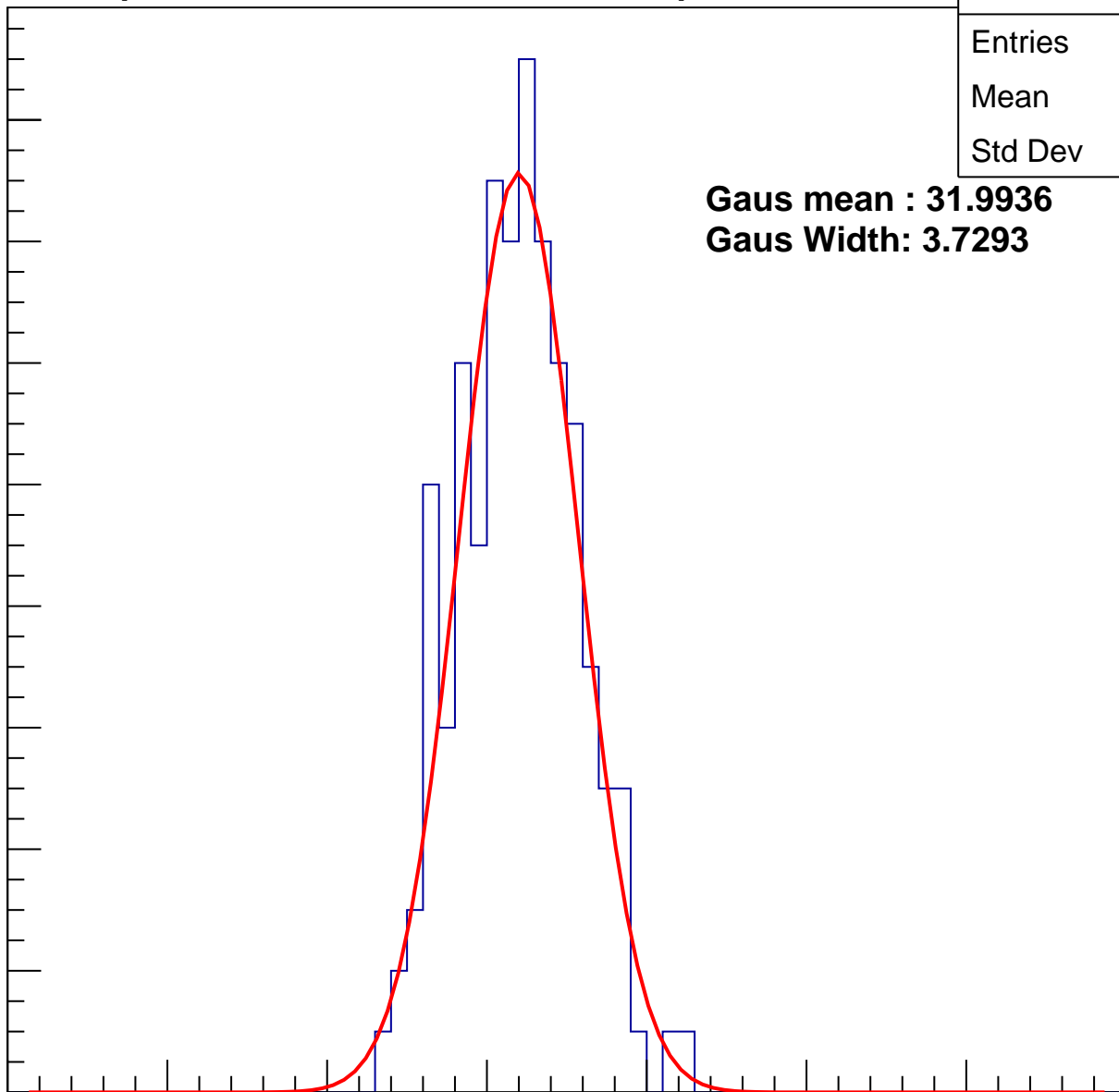
40

50

60

70

ampl



B1L001S, U25-ch69, adc1

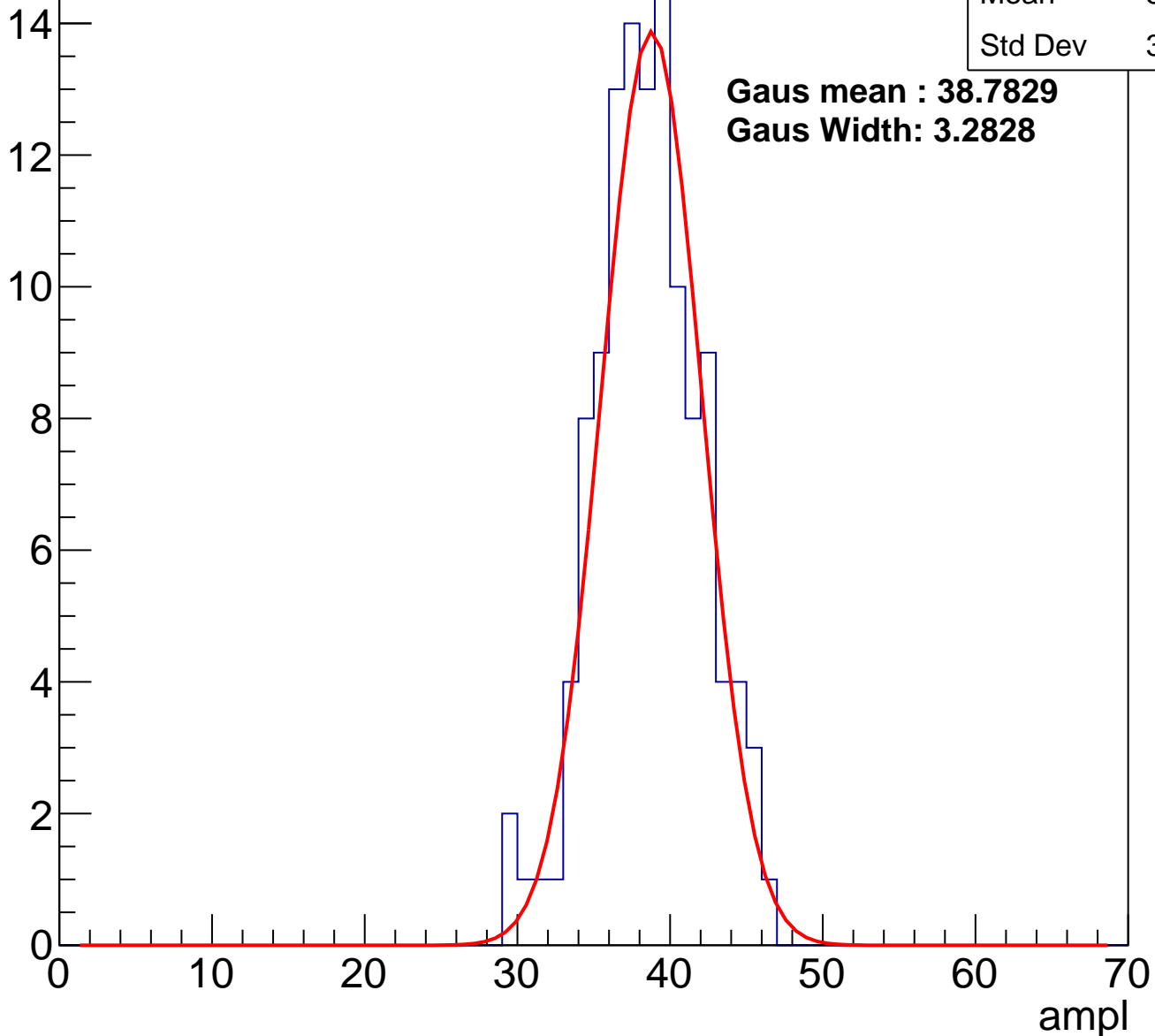
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	38.08
Std Dev	3.429

Gaus mean : 38.7829

Gaus Width: 3.2828

Entry



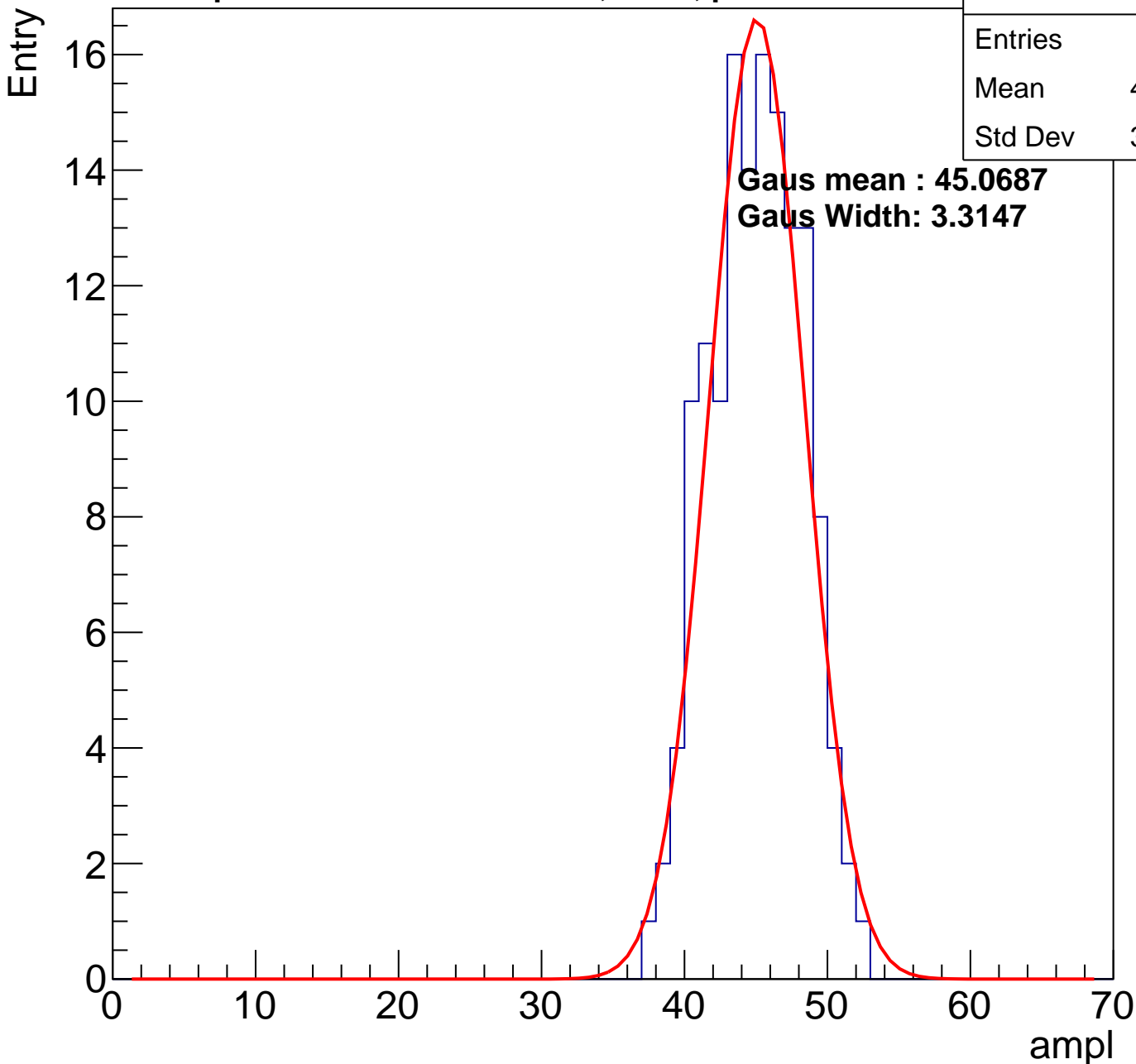
B1L001S, U25-ch69, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	44.54
Std Dev	3.165

Gaus mean : 45.0687

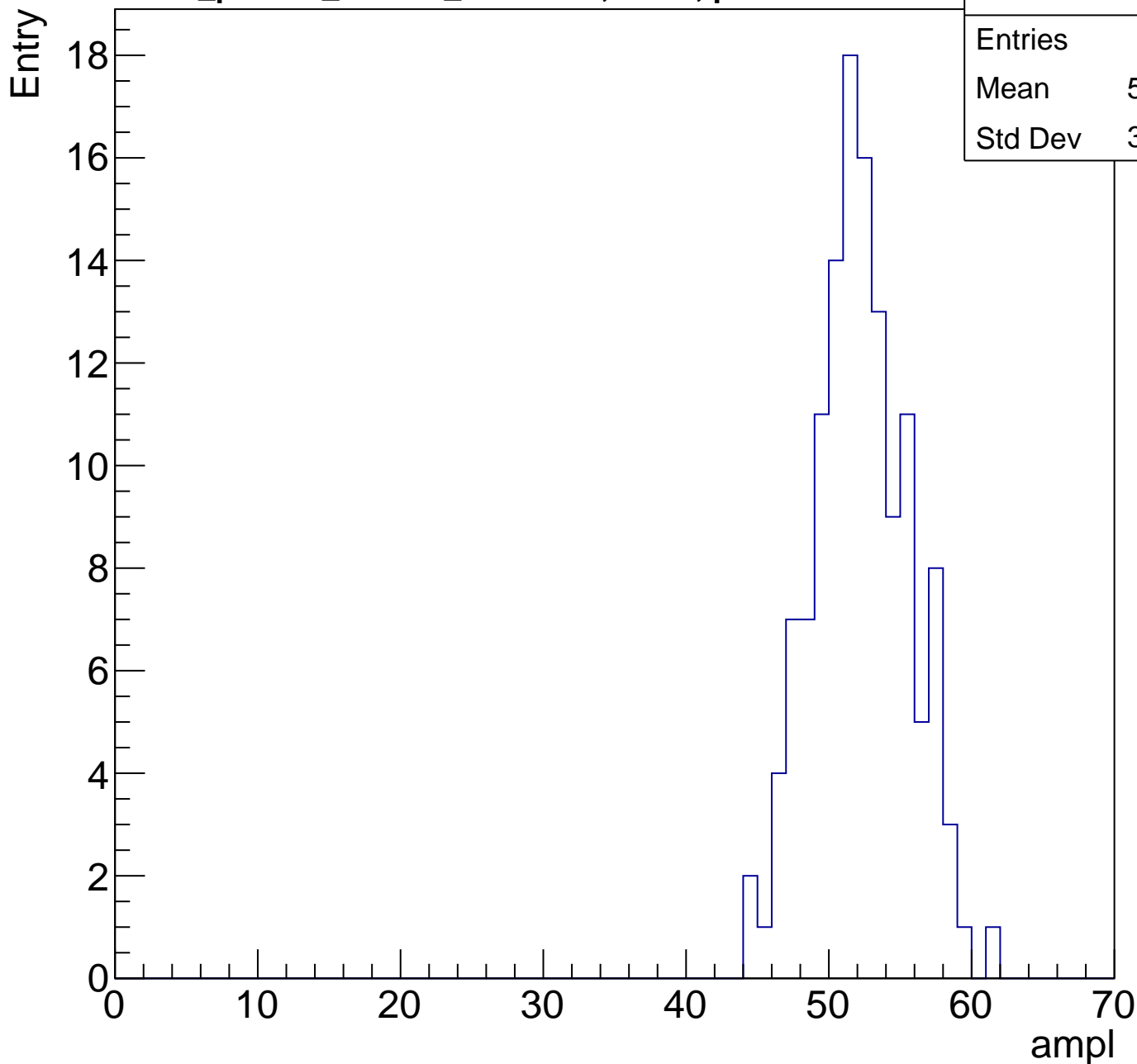
Gaus Width: 3.3147



B1L001S, U25-ch69, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	51.76
Std Dev	3.315



B1L001S, U25-ch69, adc4

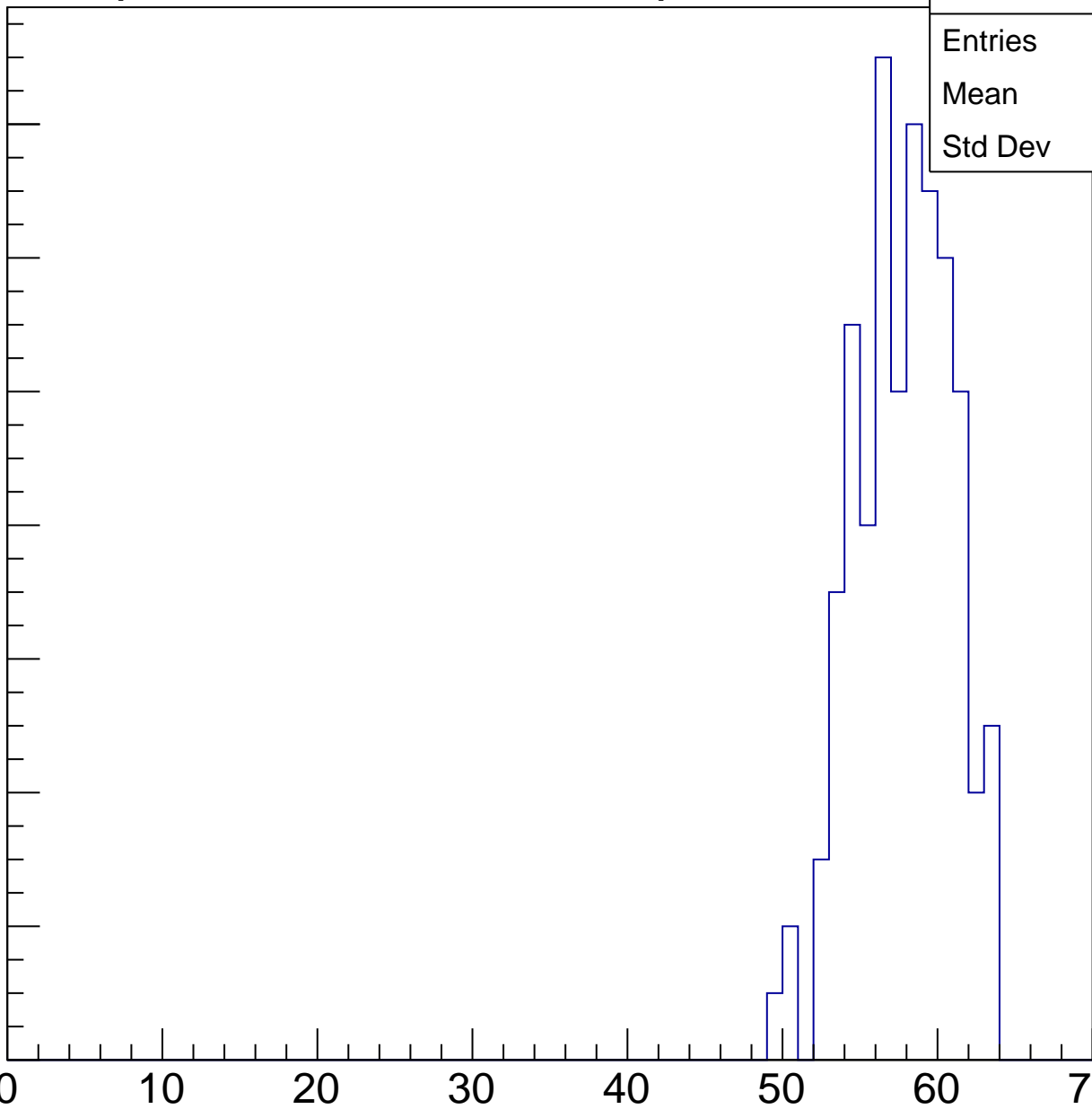
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	115
Mean	57.32
Std Dev	3.089

ampl



B1L001S, U25-ch69, adc5

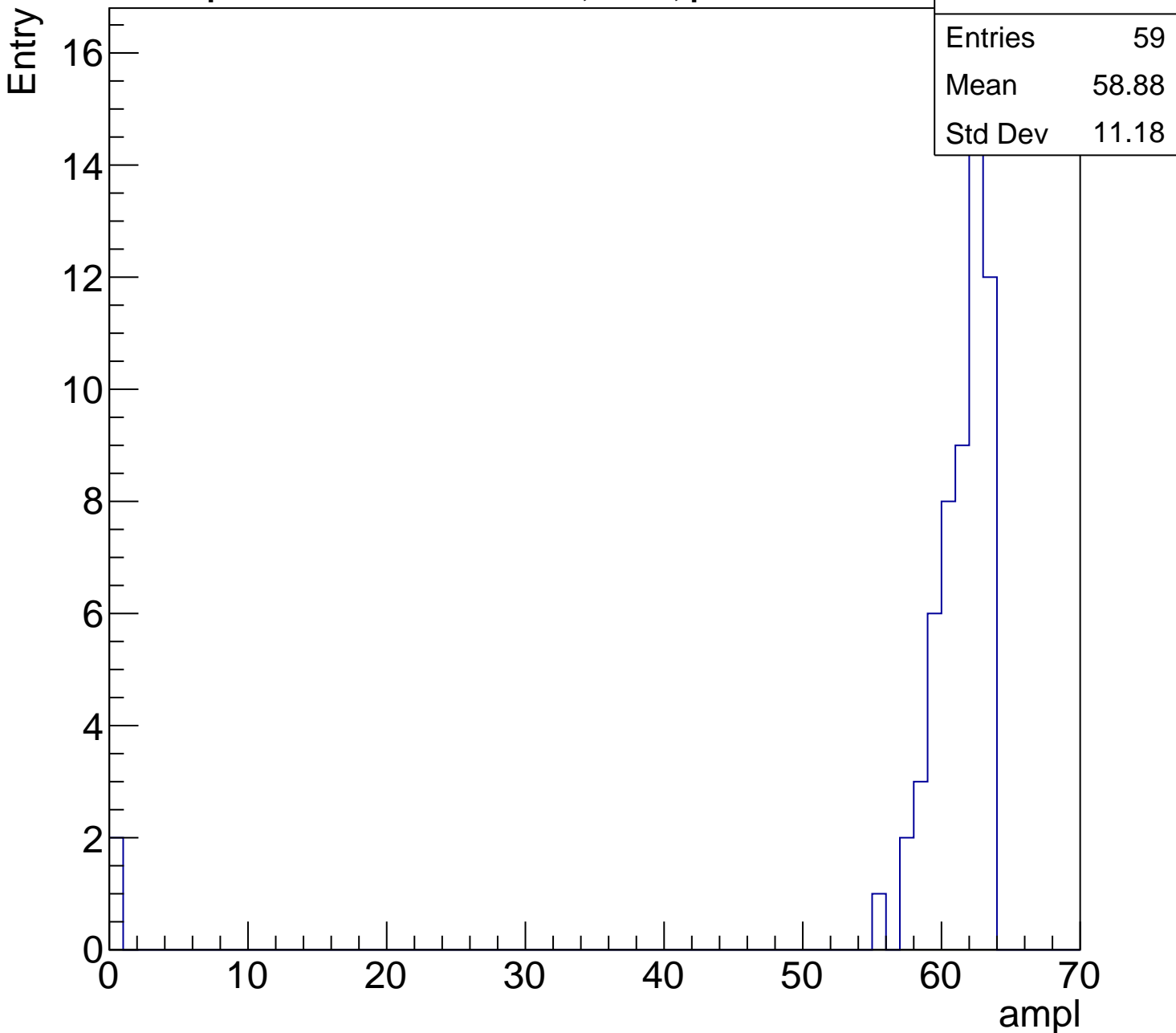
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	59
Mean	58.88
Std Dev	11.18

ampl



B1L001S, U25-ch69, adc6

calib_packv5_042523_0143.root, FC#2, port C2

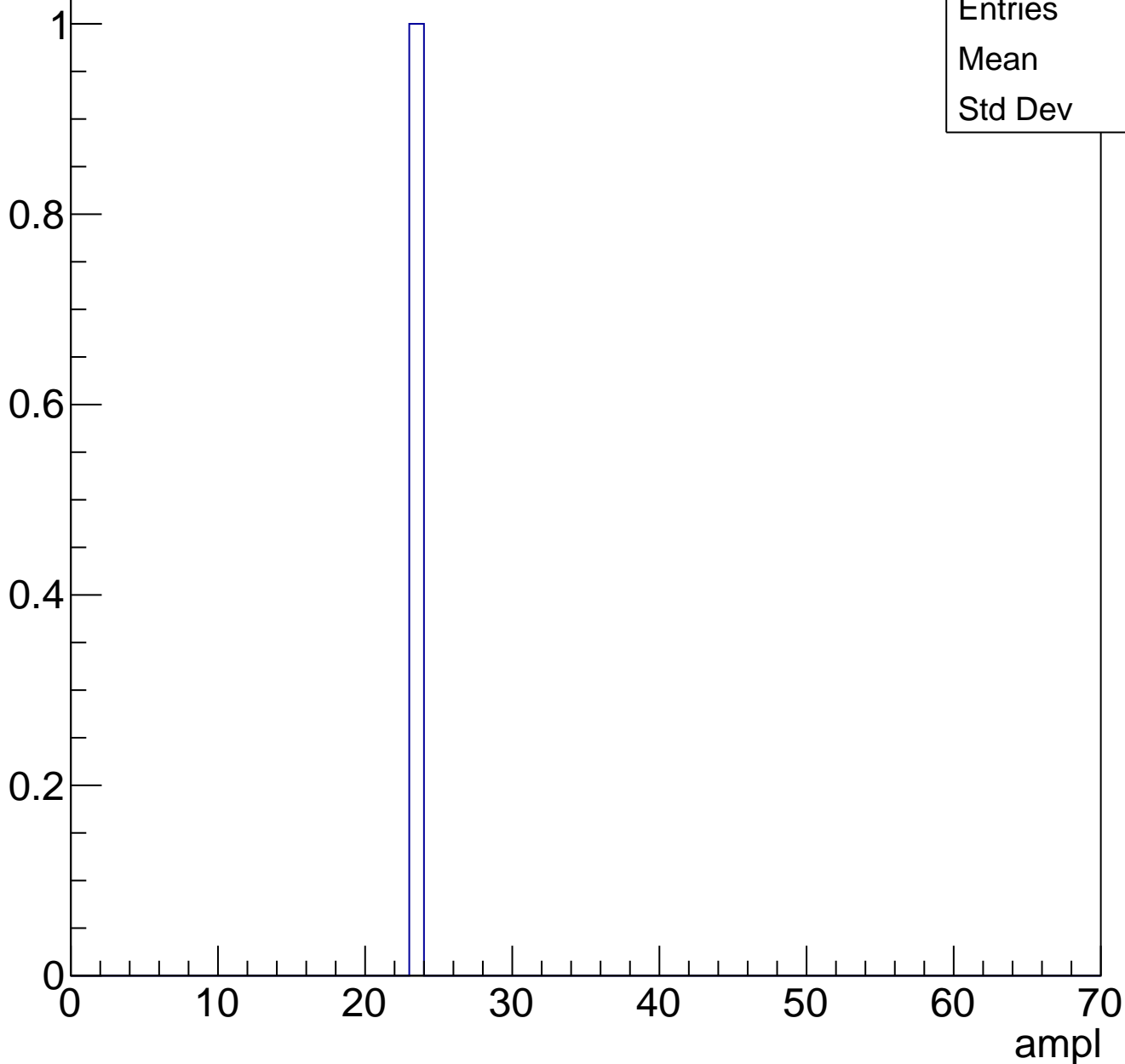
Entry



B1L001S, U25-ch69, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



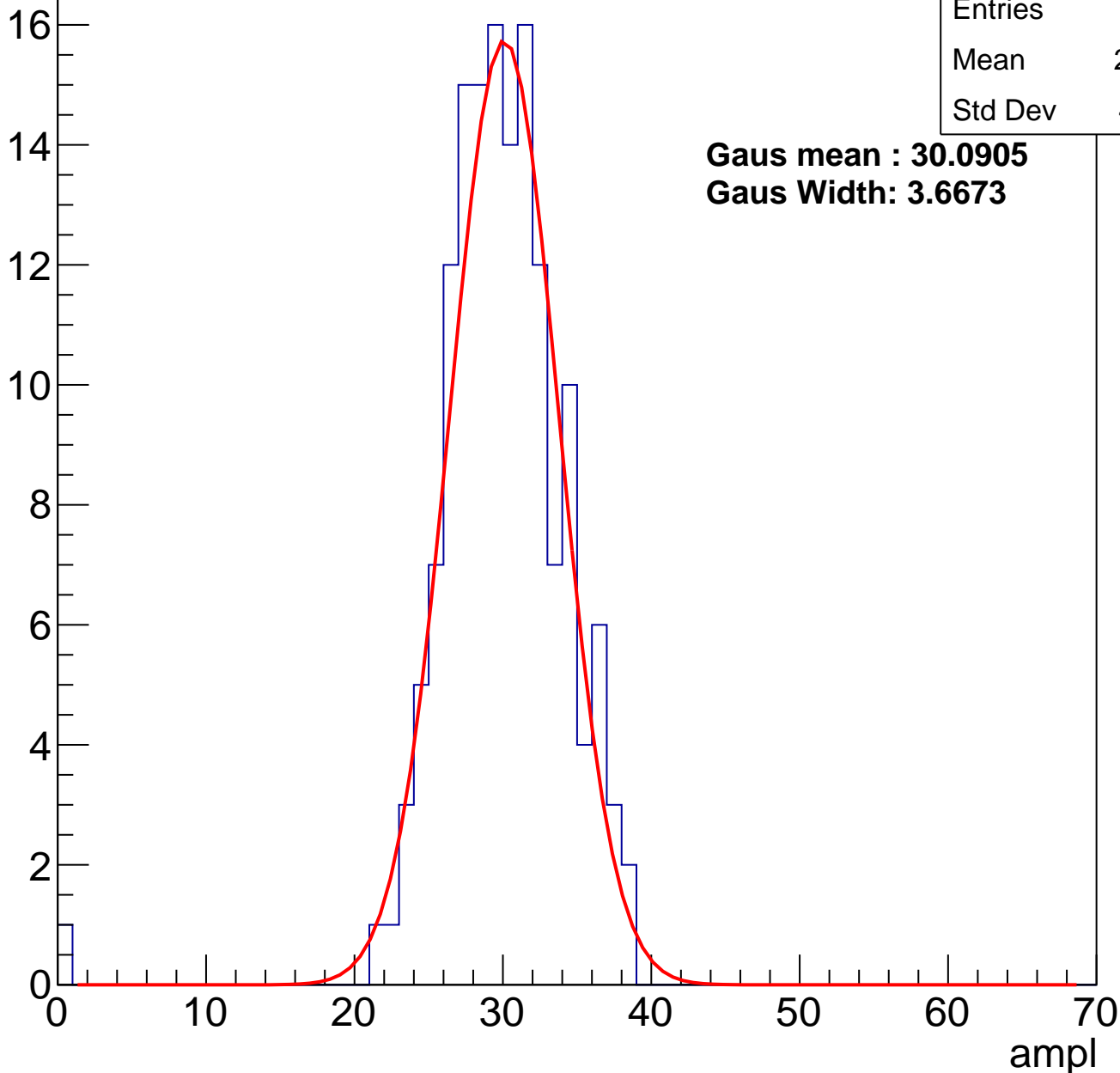
B1L001S, U25-ch70, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	29.48
Std Dev	4.311

Gaus mean : 30.0905
Gaus Width: 3.6673

Entry



B1L001S, U25-ch70, adc1

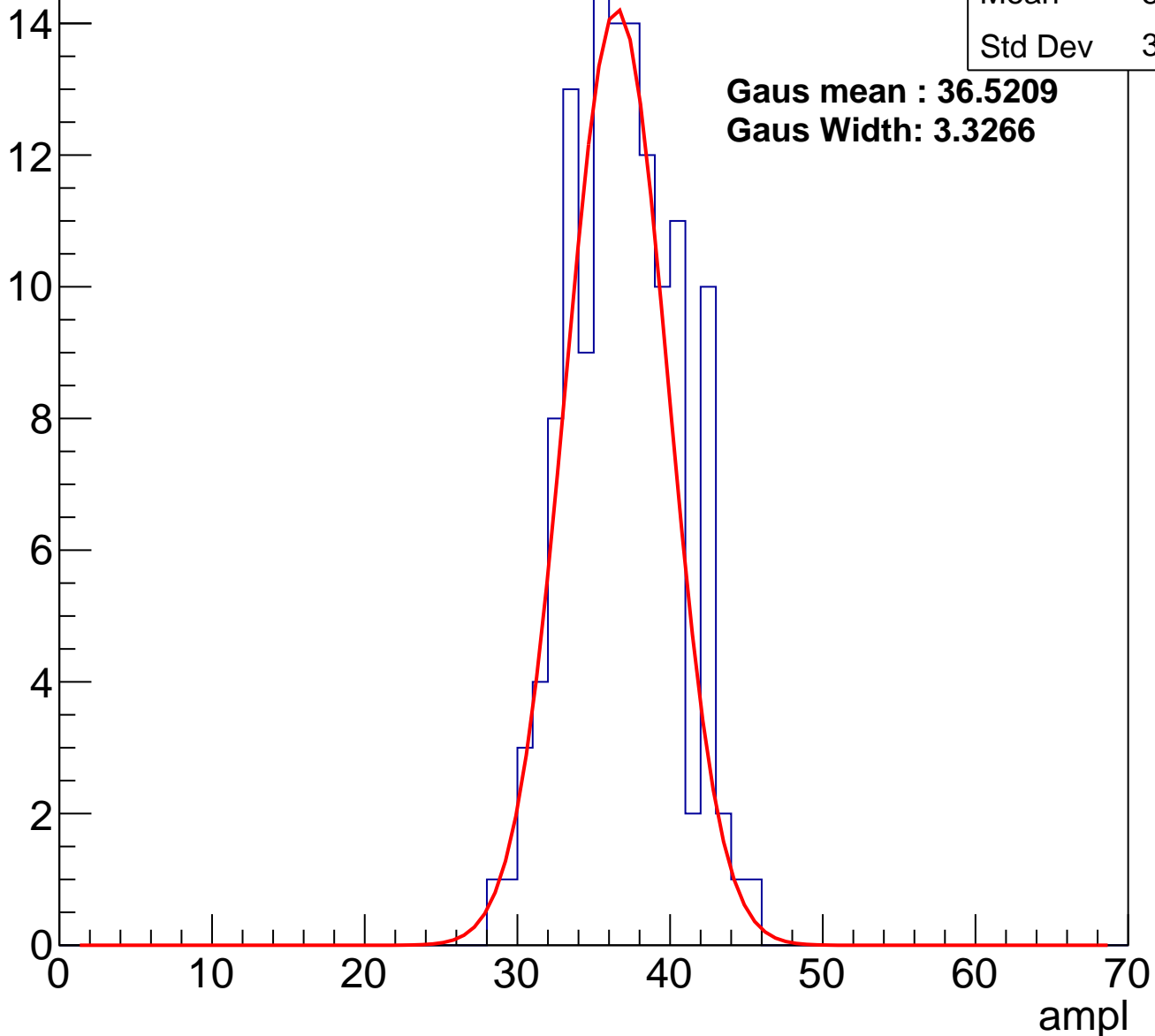
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.43
Std Dev	3.466

Gaus mean : 36.5209

Gaus Width: 3.3266

Entry



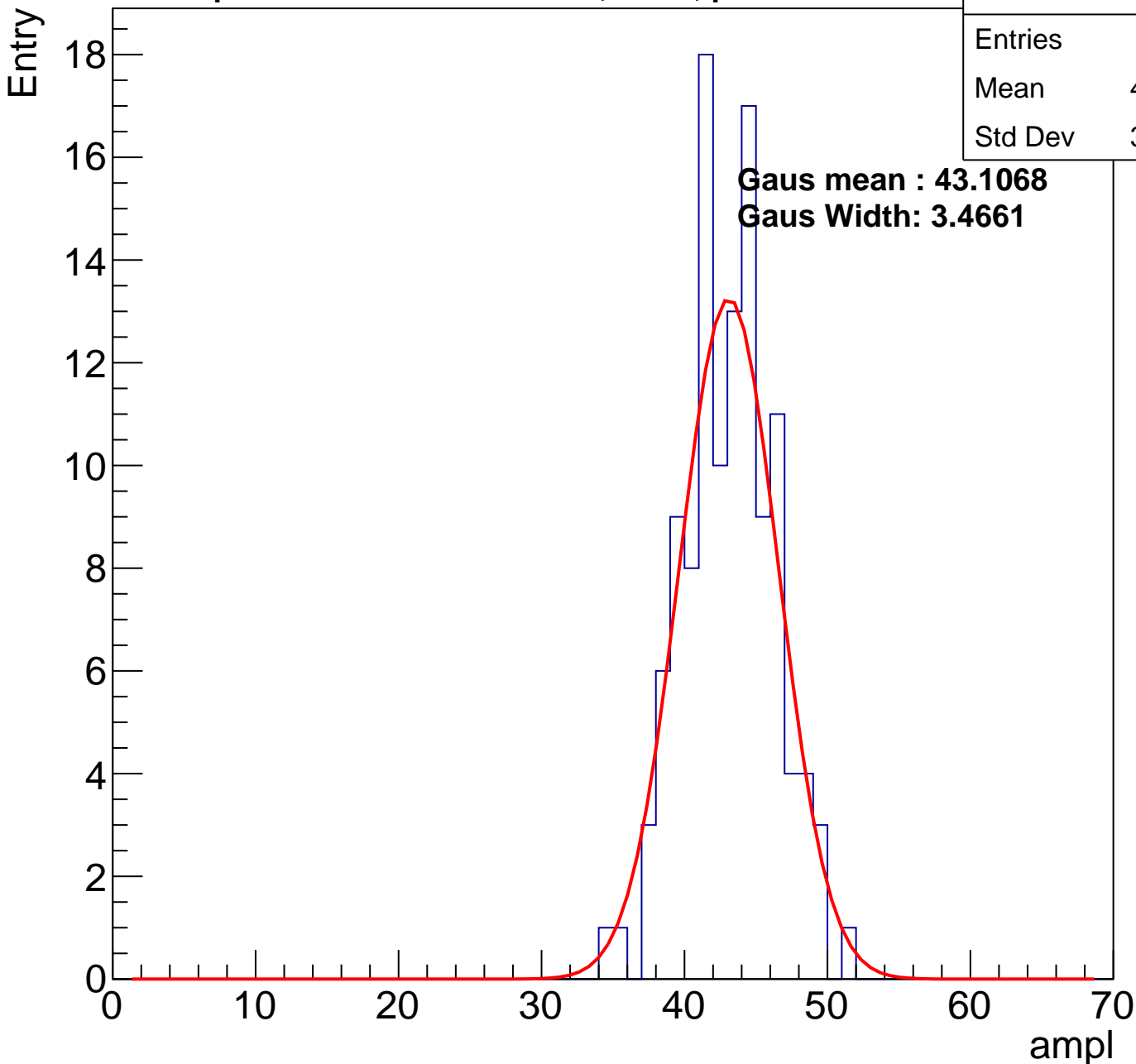
B1L001S, U25-ch70, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	42.65
Std Dev	3.155

Gaus mean : 43.1068

Gaus Width: 3.4661



B1L001S, U25-ch70, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	48.8
Std Dev	3.268

Entry

14

12

10

8

6

4

2

0

0

10

20

30

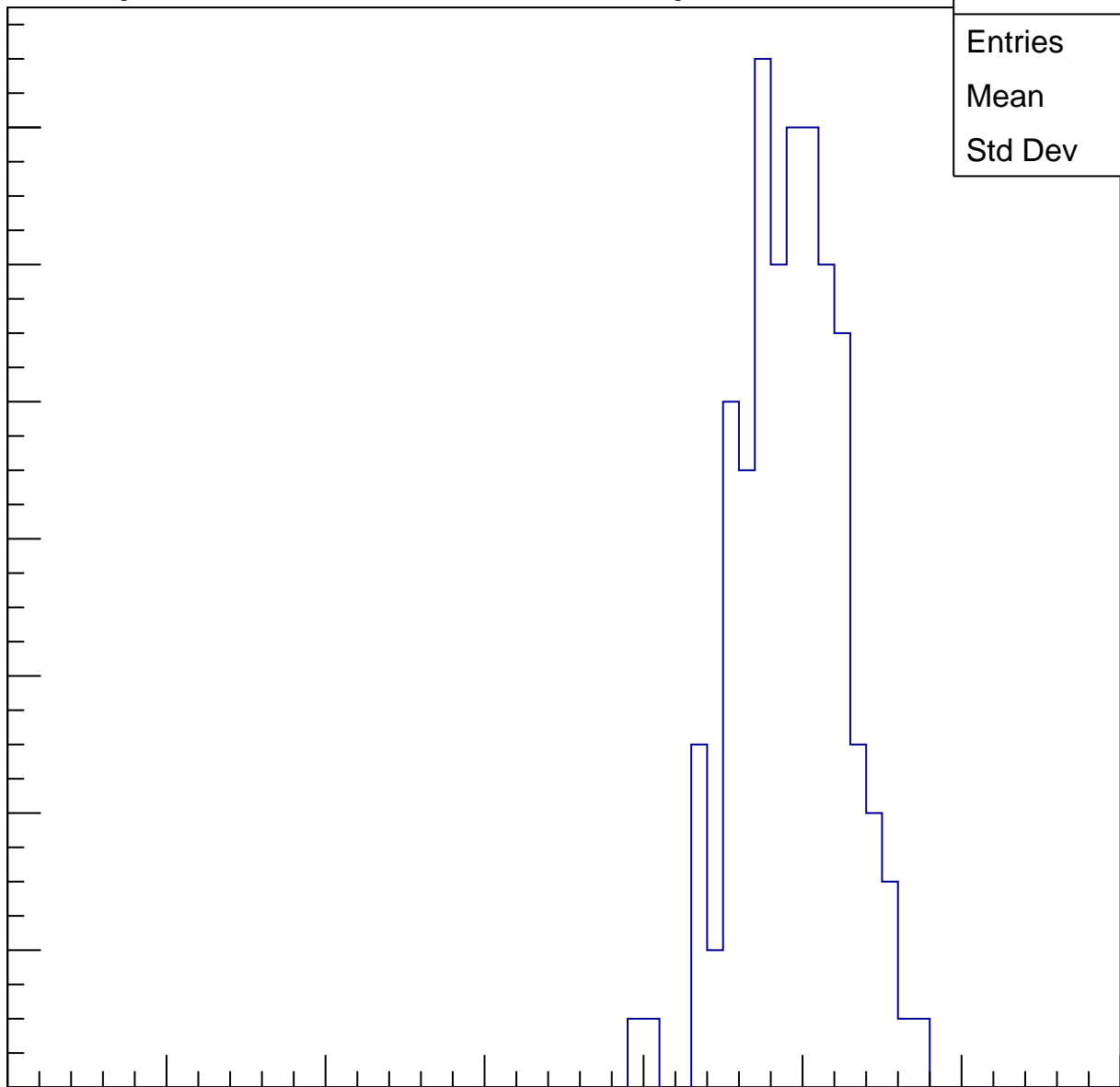
40

50

60

70

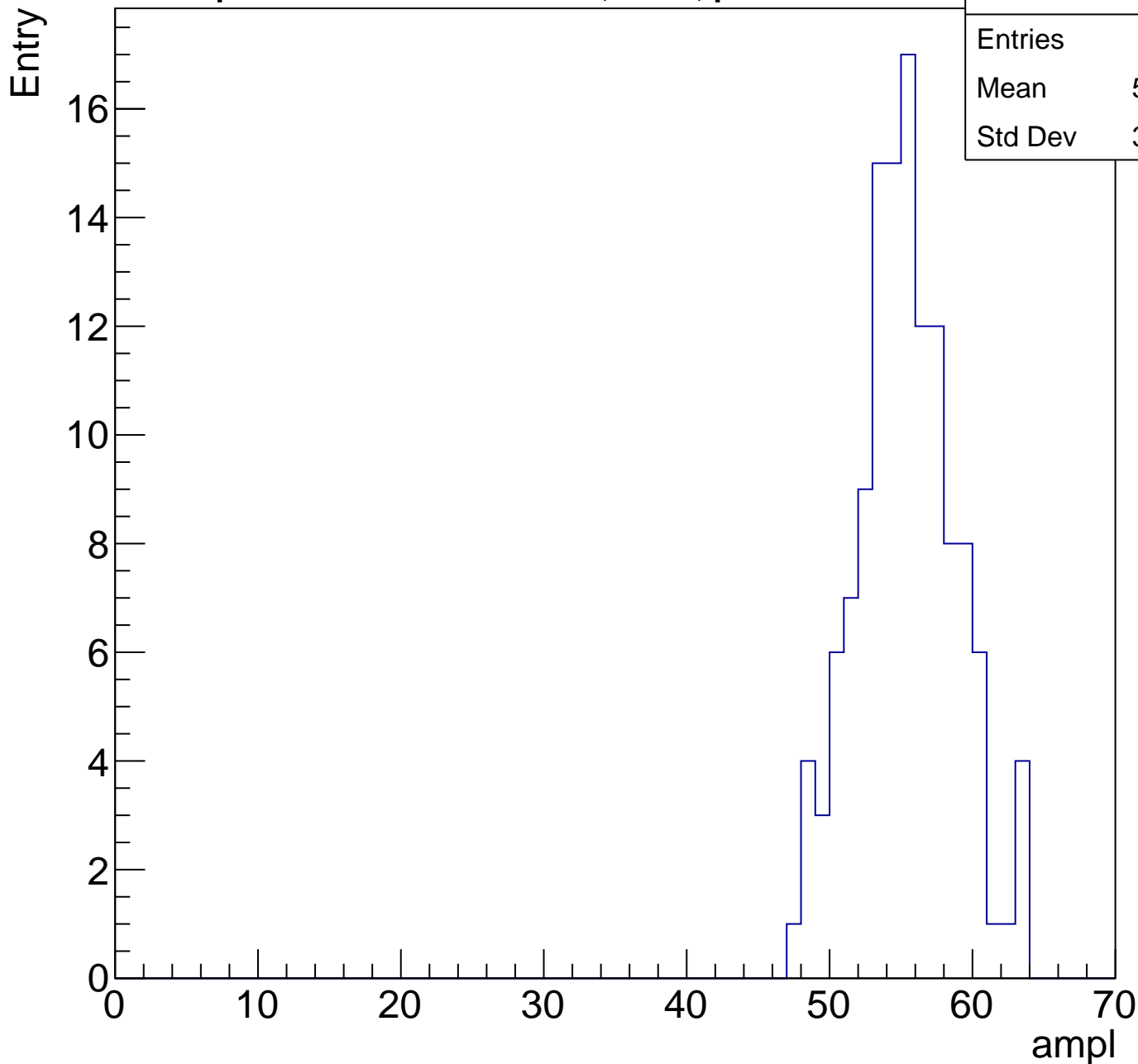
ampl



B1L001S, U25-ch70, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	54.87
Std Dev	3.449



B1L001S, U25-ch70, adc5

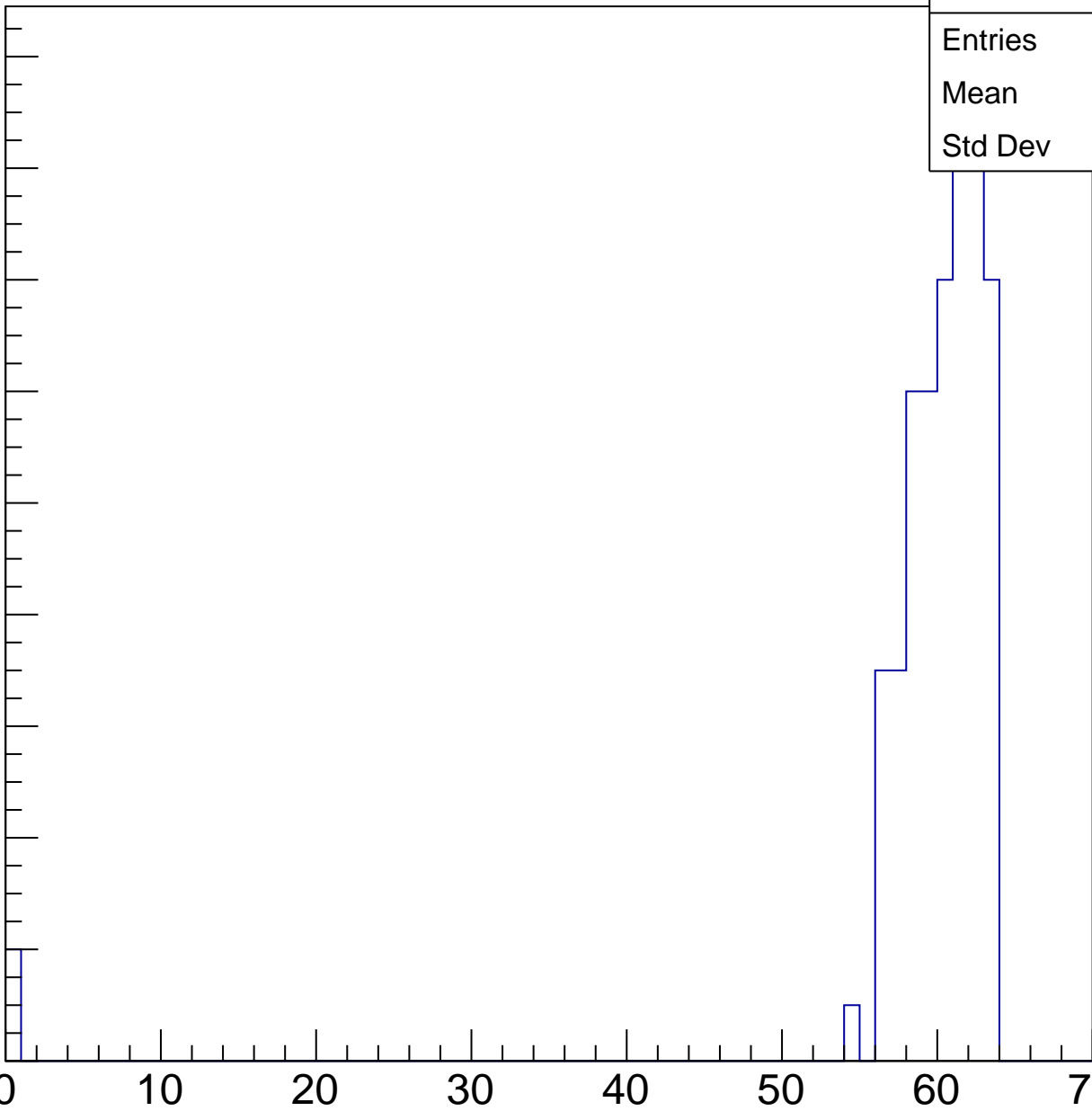
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	104
Mean	58.88
Std Dev	8.524

ampl



B1L001S, U25-ch70, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70

B1L001S, U25-ch70, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U25-ch71, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	30.06
Std Dev	3.398

Gaus mean : 30.4803

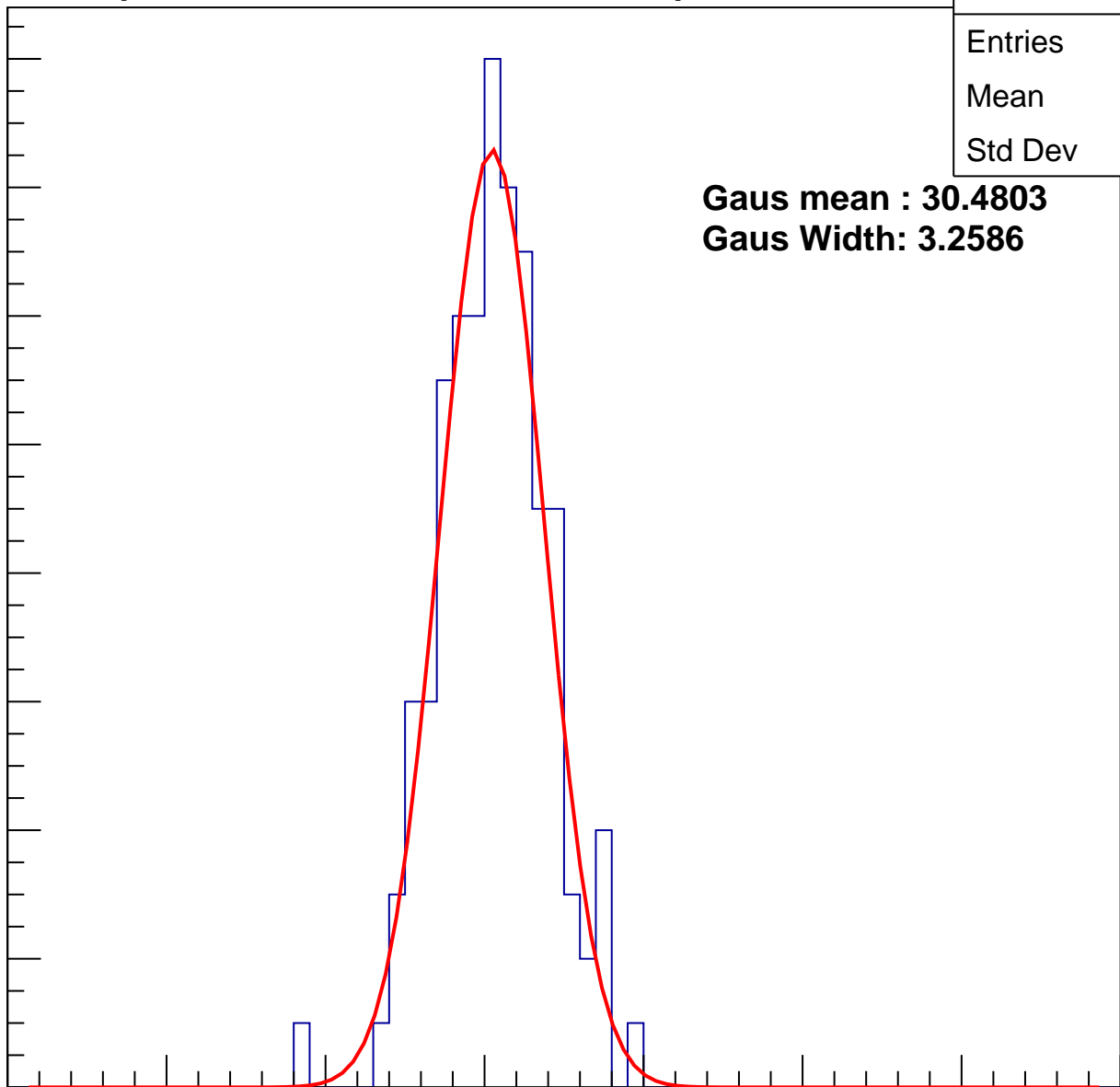
Gaus Width: 3.2586

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch71, adc1

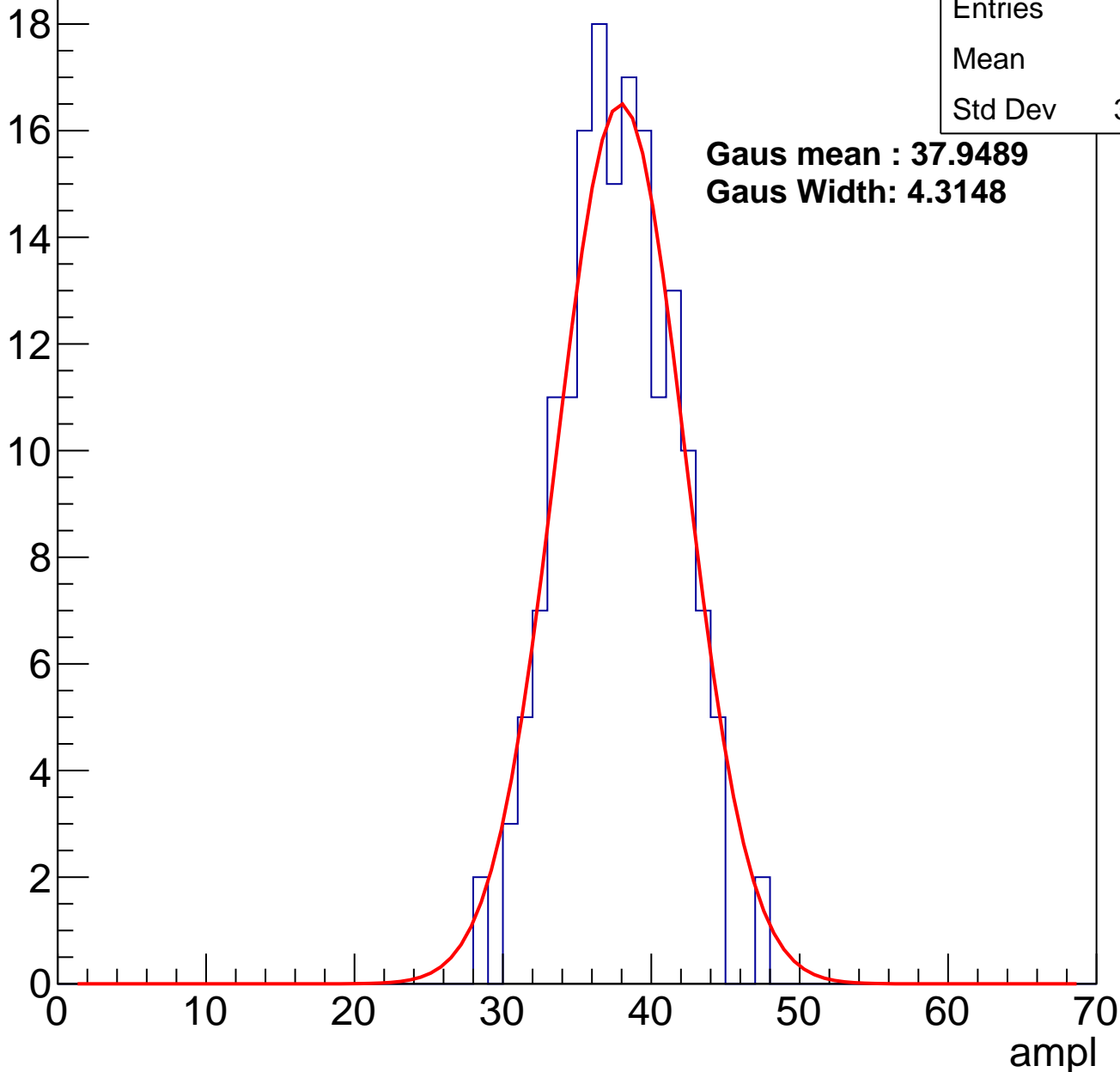
calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	37.3
Std Dev	3.722

Gaus mean : 37.9489

Gaus Width: 4.3148

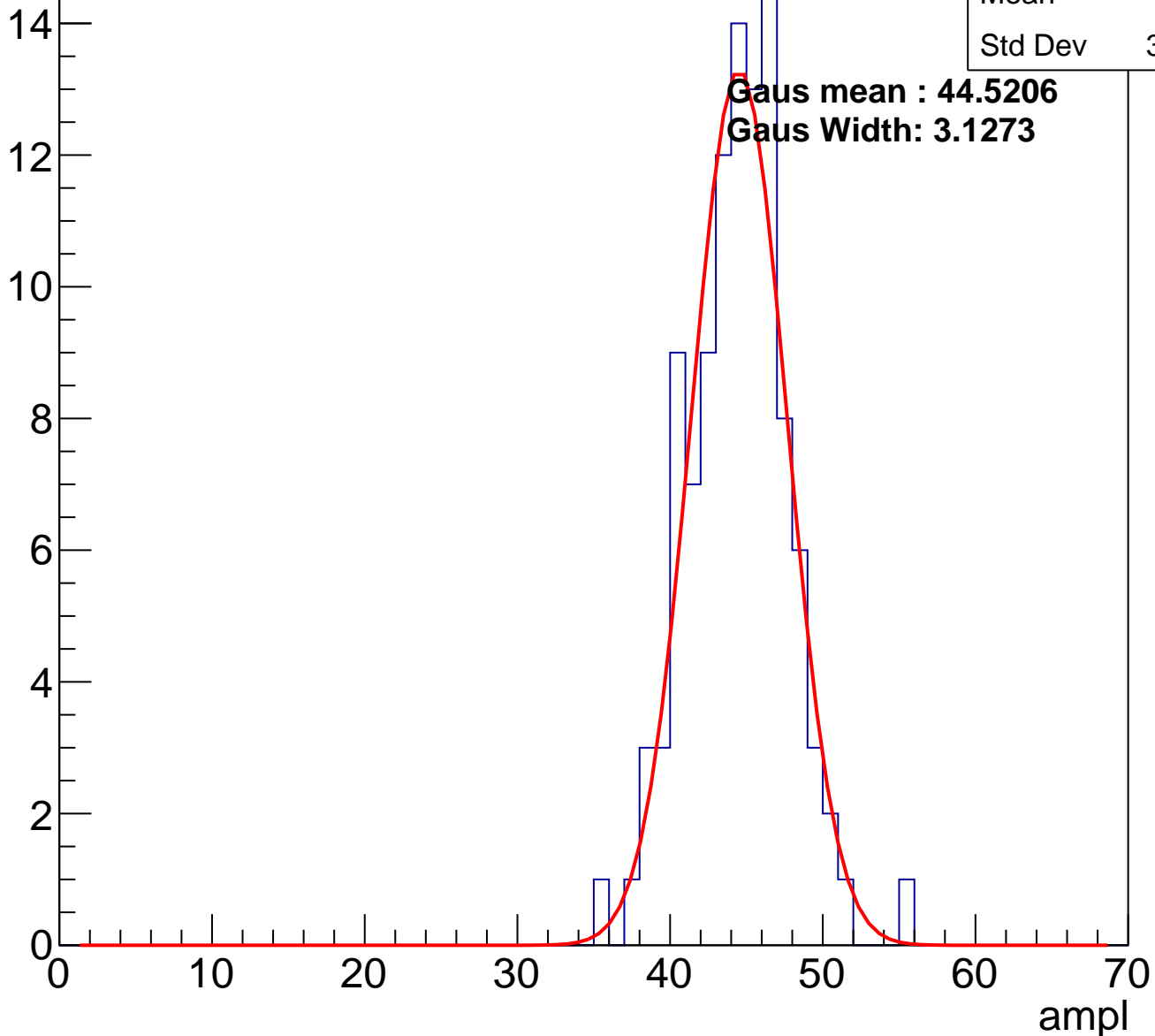
Entry



B1L001S, U25-ch71, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch71, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	50.44
Std Dev	3.236

Entry

16
14
12
10
8
6
4
2
0

0

10

20

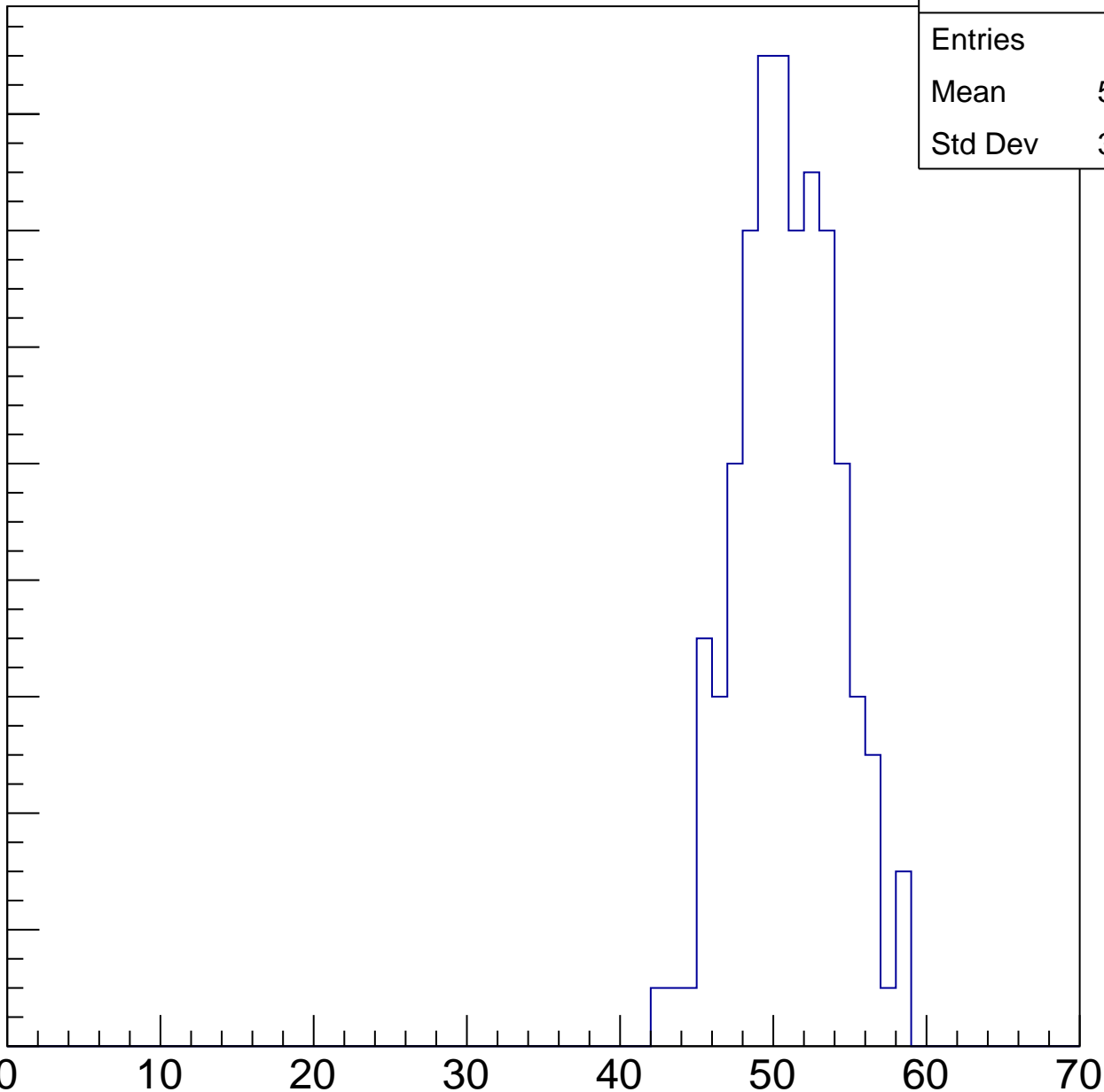
30

40

50

60

ampl



B1L001S, U25-ch71, adc4

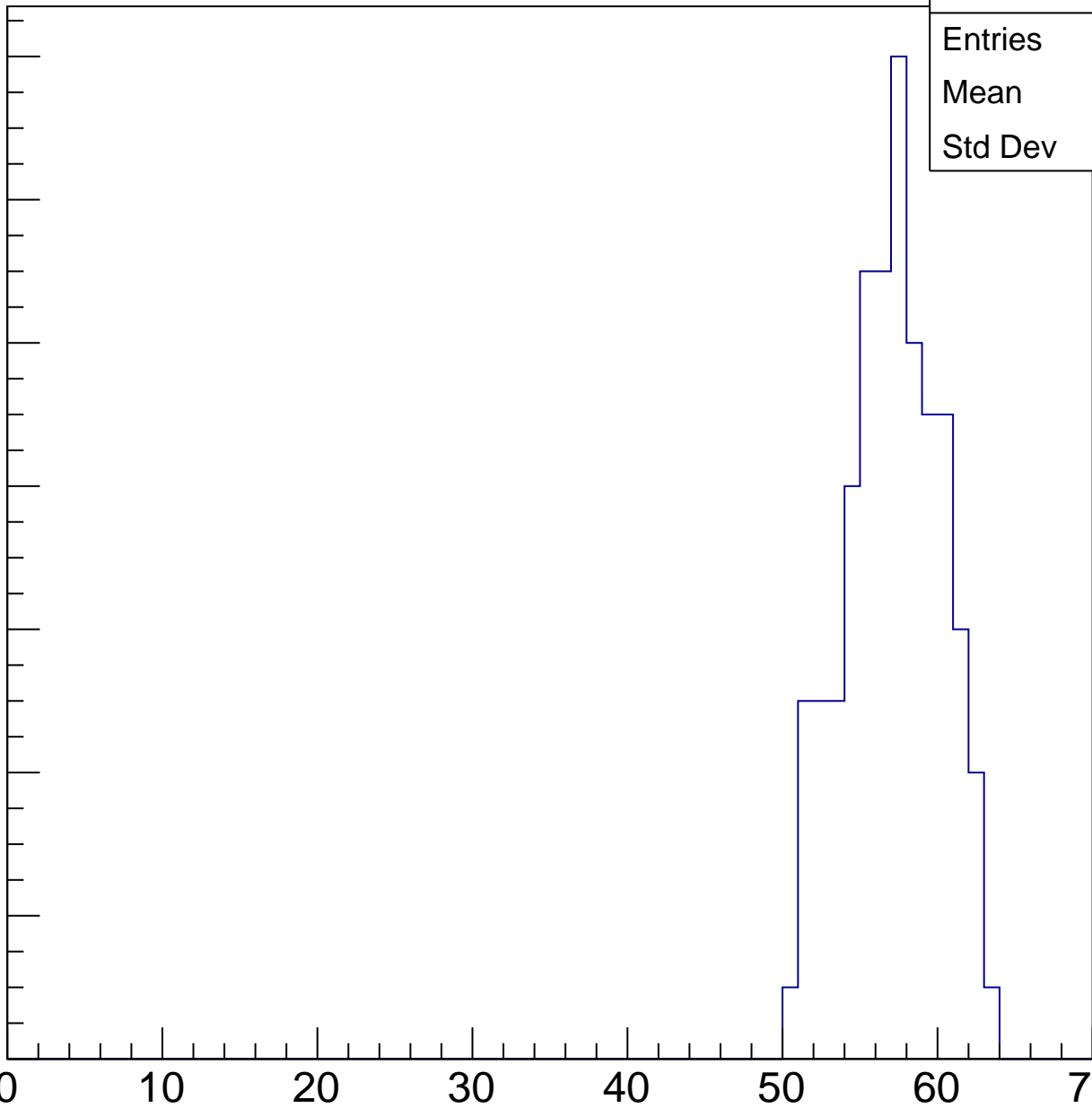
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	56.66
Std Dev	3.029

ampl



B1L001S, U25-ch71, adc5

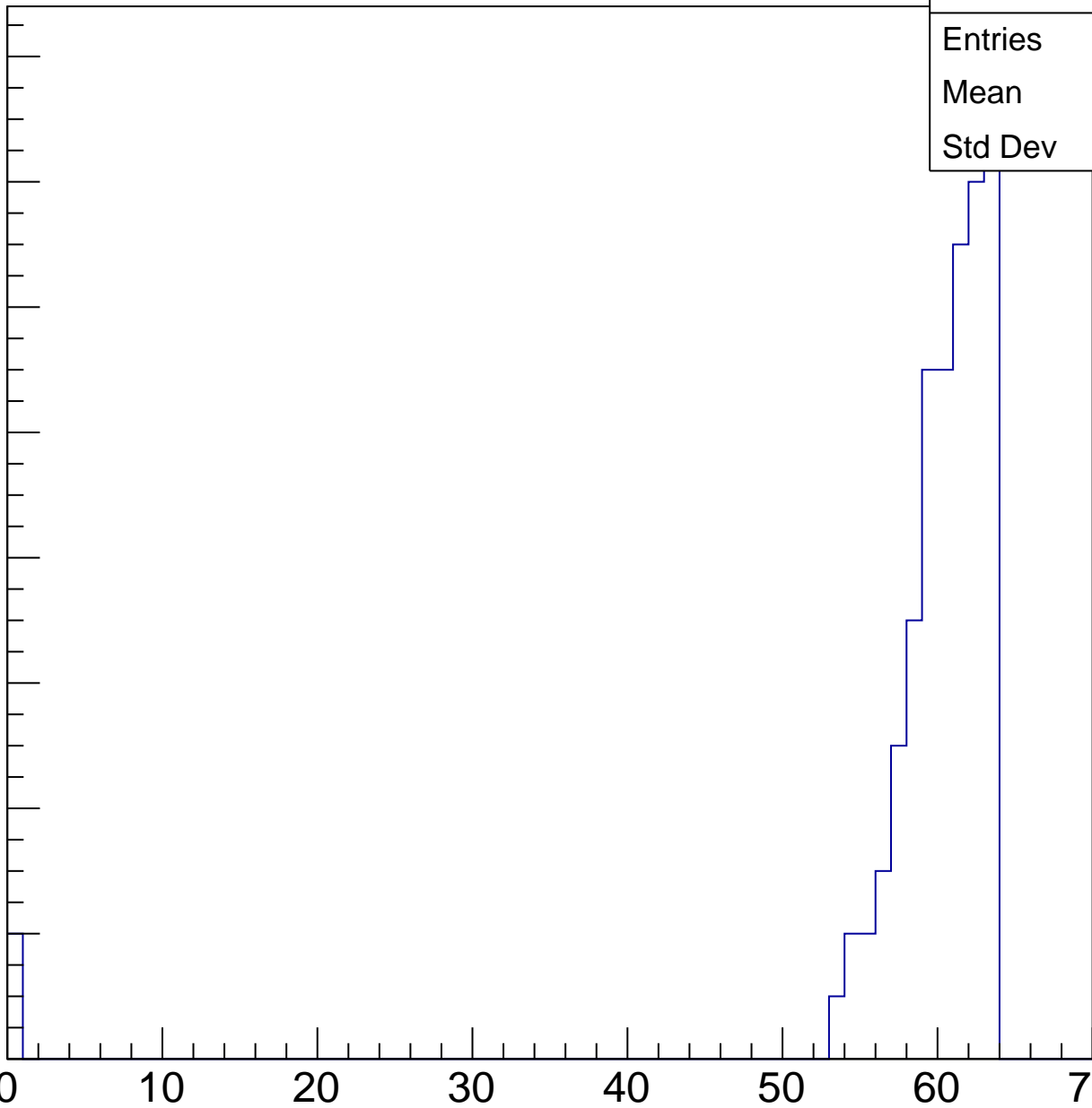
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.71
Std Dev	9.329

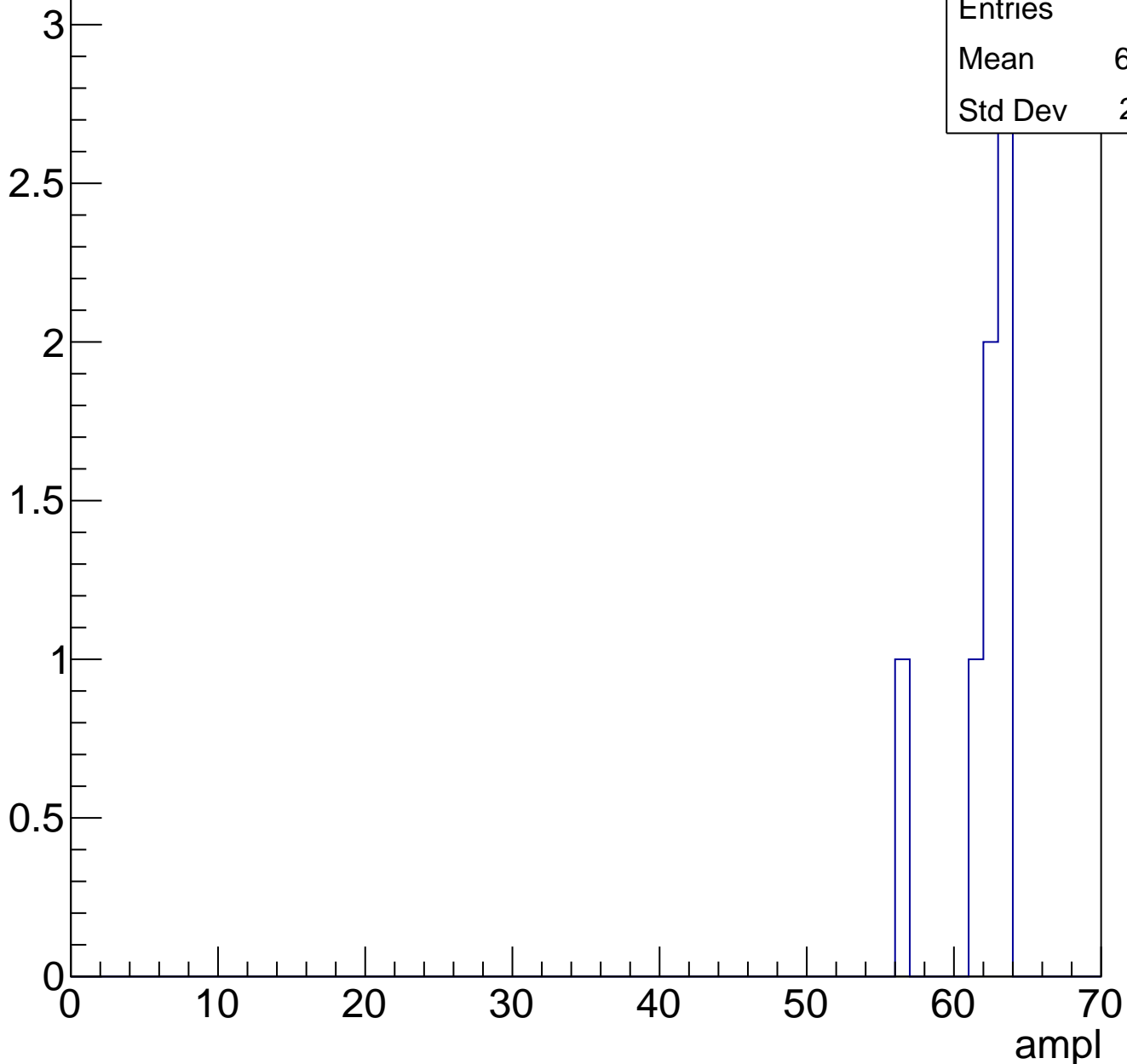
ampl



B1L001S, U25-ch71, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch71, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L001S, U25-ch72, adc0

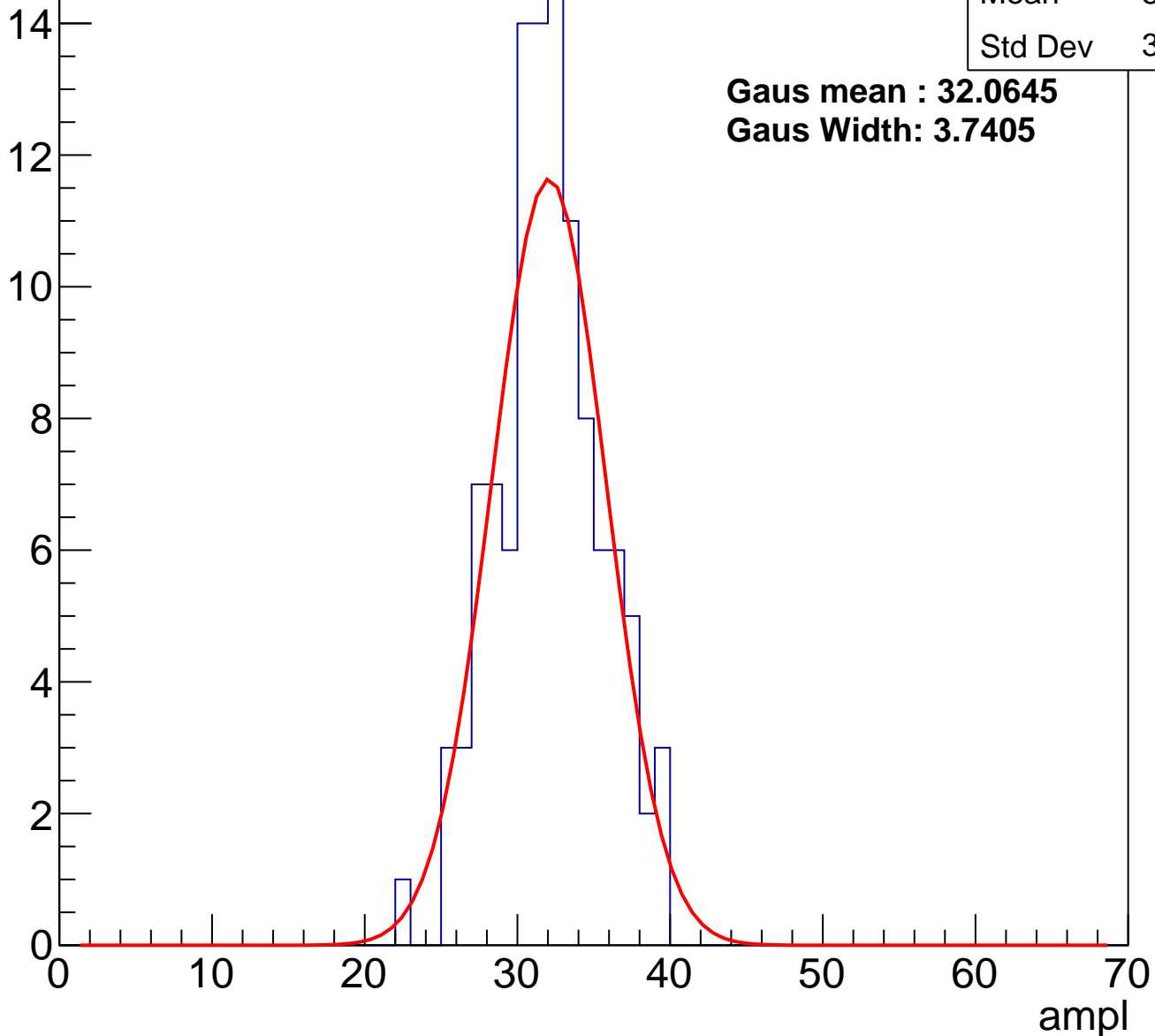
calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	31.59
Std Dev	3.402

Gaus mean : 32.0645

Gaus Width: 3.7405

Entry



B1L001S, U25-ch72, adc1

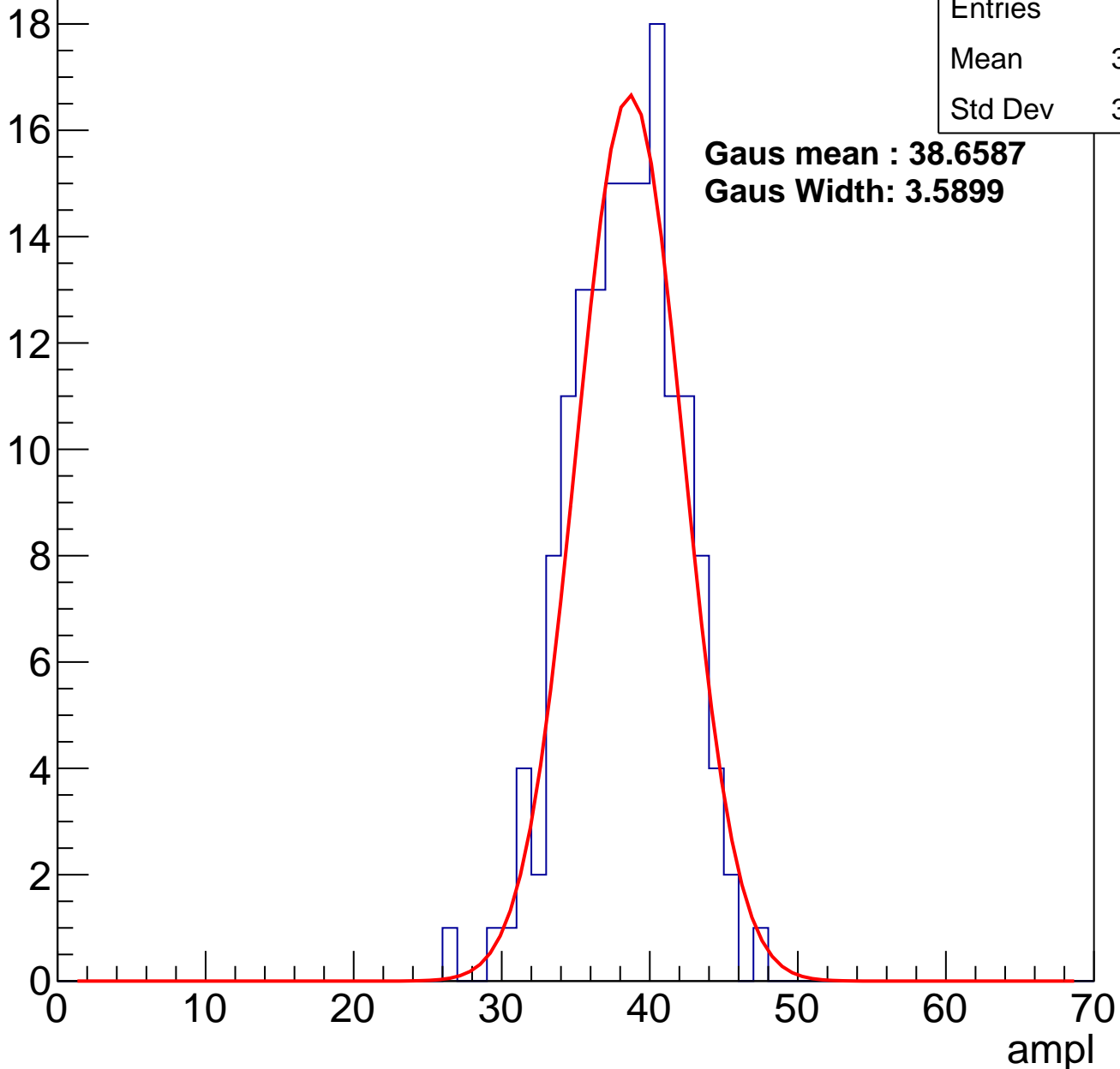
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	37.88
Std Dev	3.604

Gaus mean : 38.6587

Gaus Width: 3.5899

Entry



B1L001S, U25-ch72, adc2

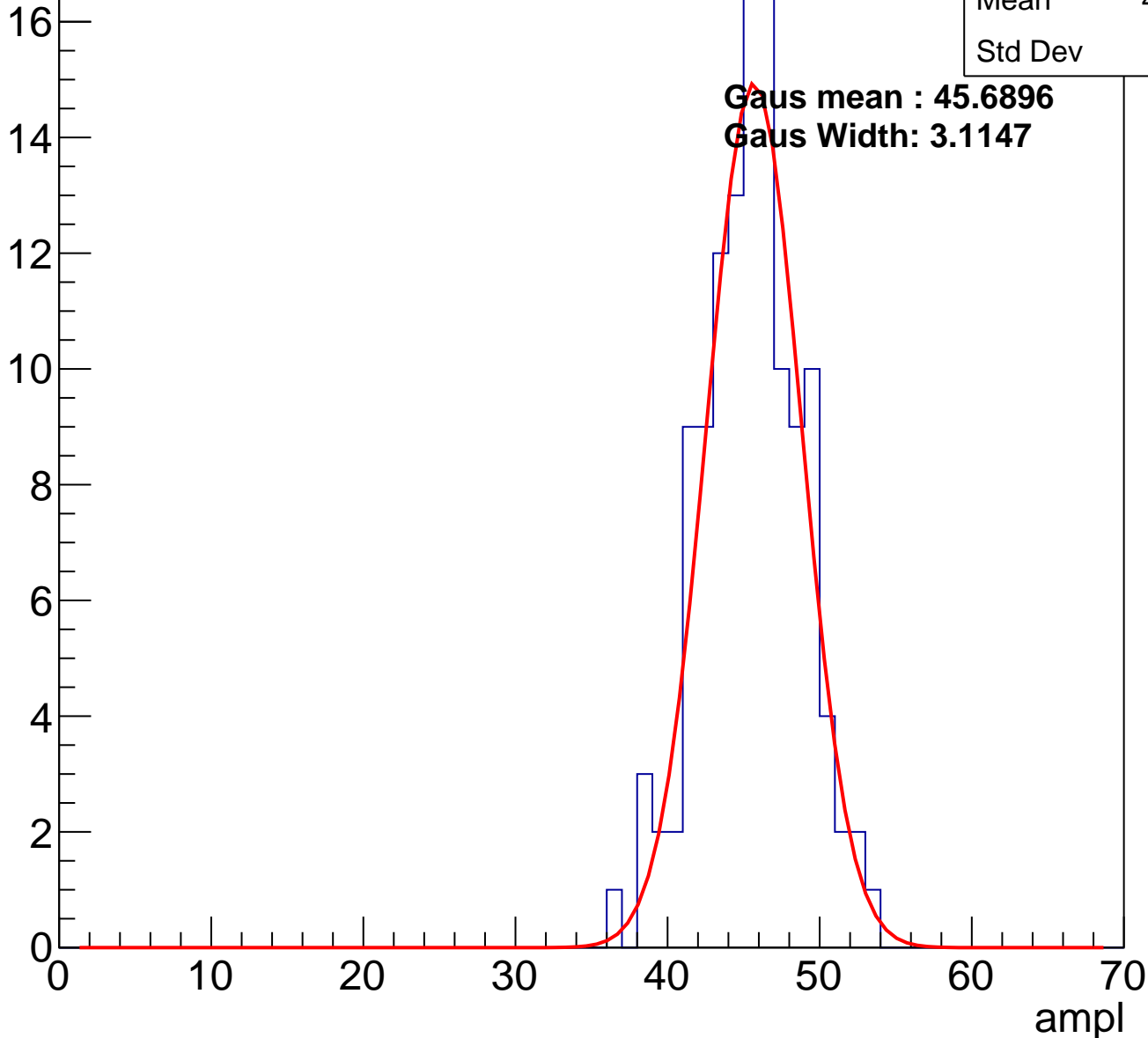
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	123
Mean	45.05
Std Dev	3.21

Gaus mean : 45.6896

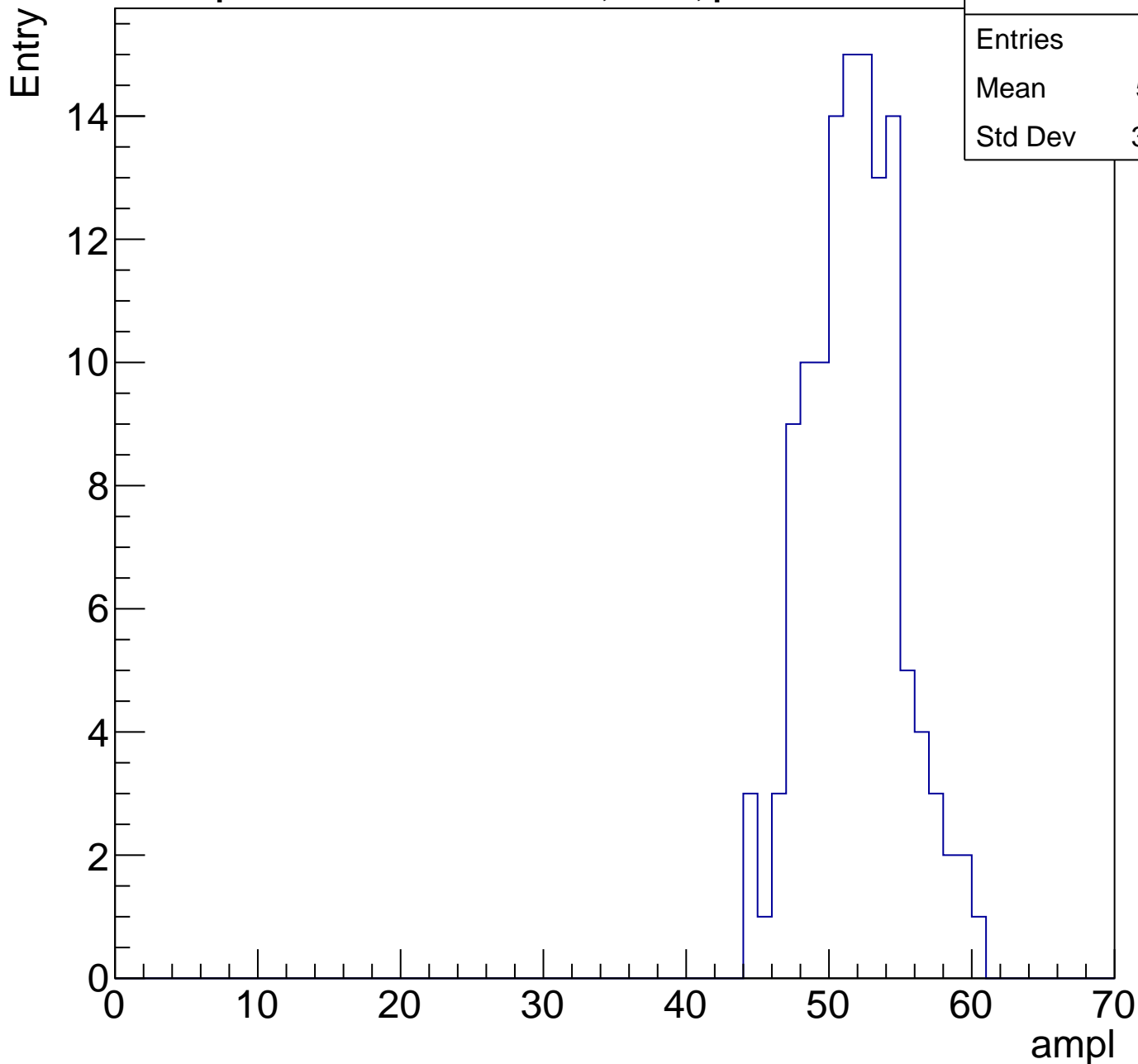
Gaus Width: 3.1147



B1L001S, U25-ch72, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	51.31
Std Dev	3.266



B1L001S, U25-ch72, adc4

calib_packv5_042523_0143.root, FC#2, port C2

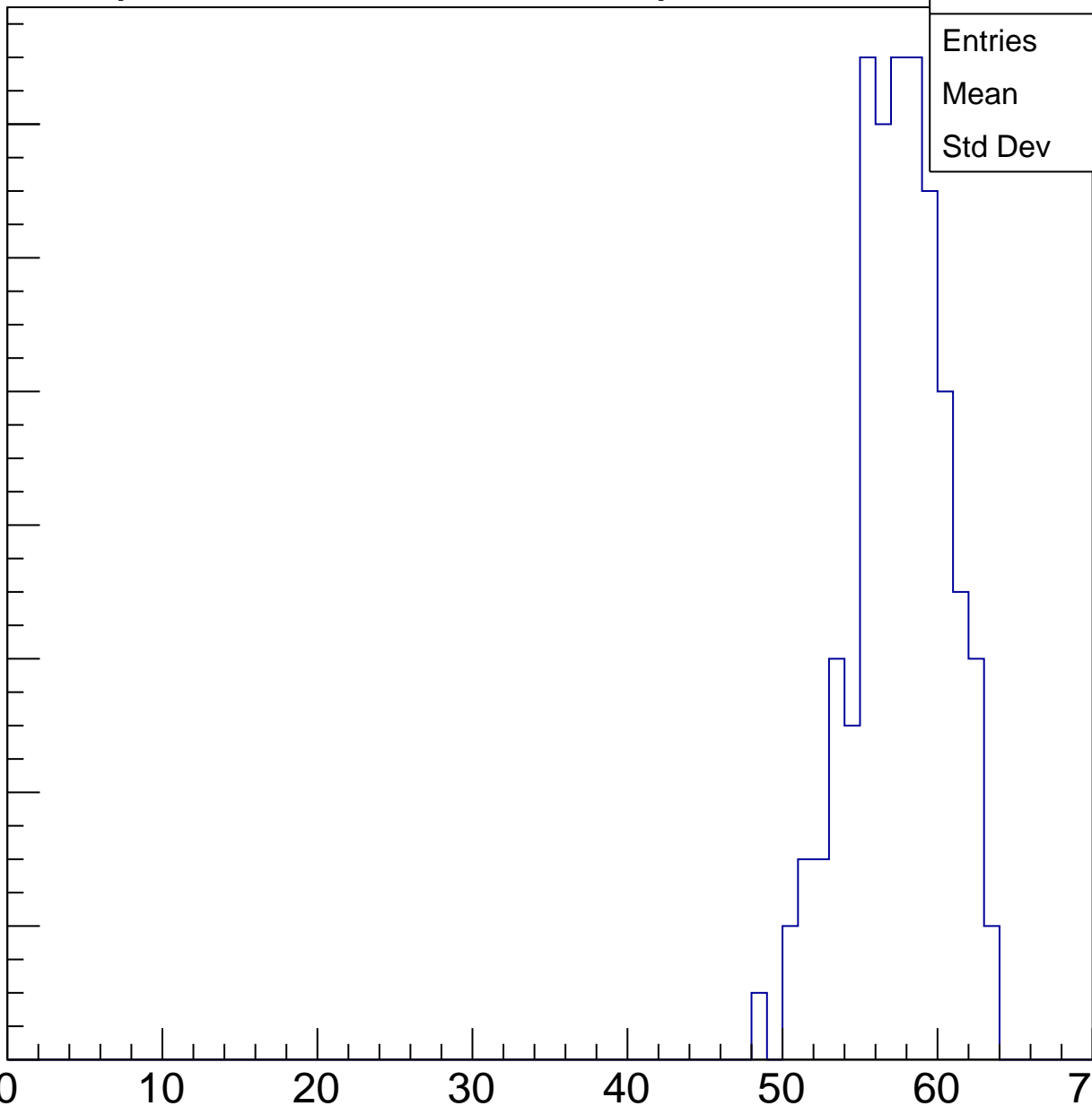
Entries	117
Mean	57.02
Std Dev	3.021

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch72, adc5

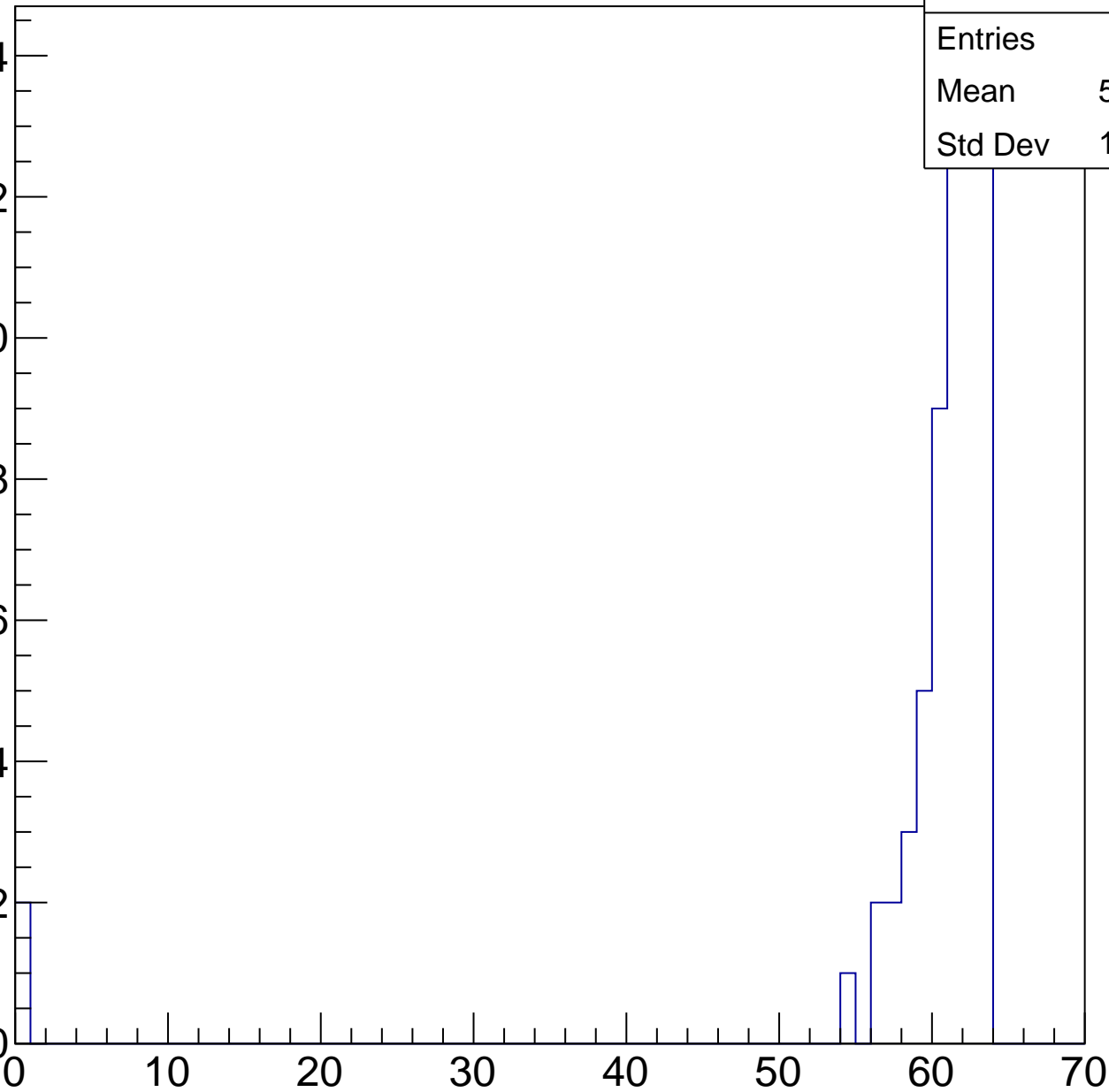
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	64
Mean	58.89
Std Dev	10.76

ampl



B1L001S, U25-ch72, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch72, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

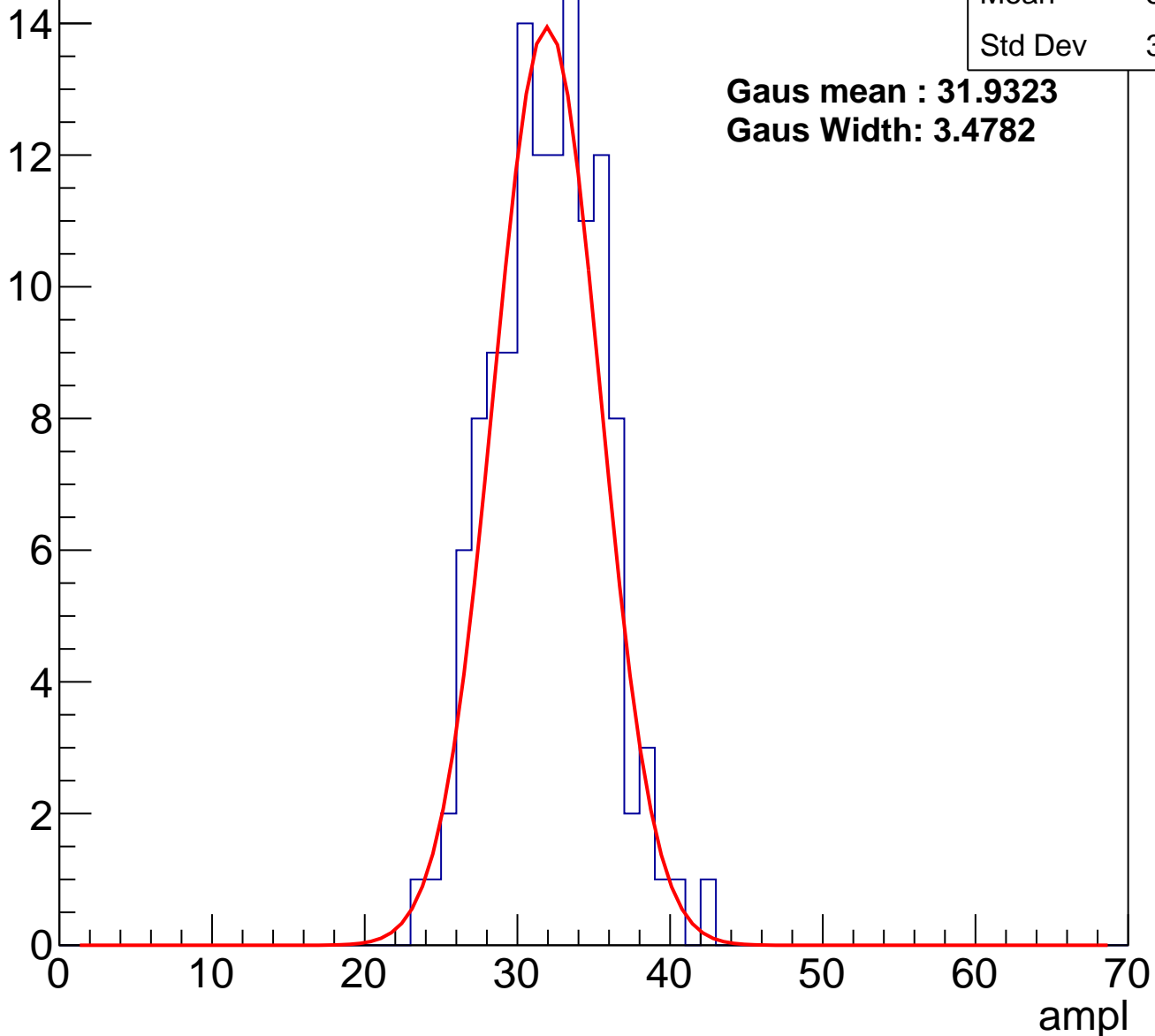


Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch73, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch73, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	38.36
Std Dev	3.633

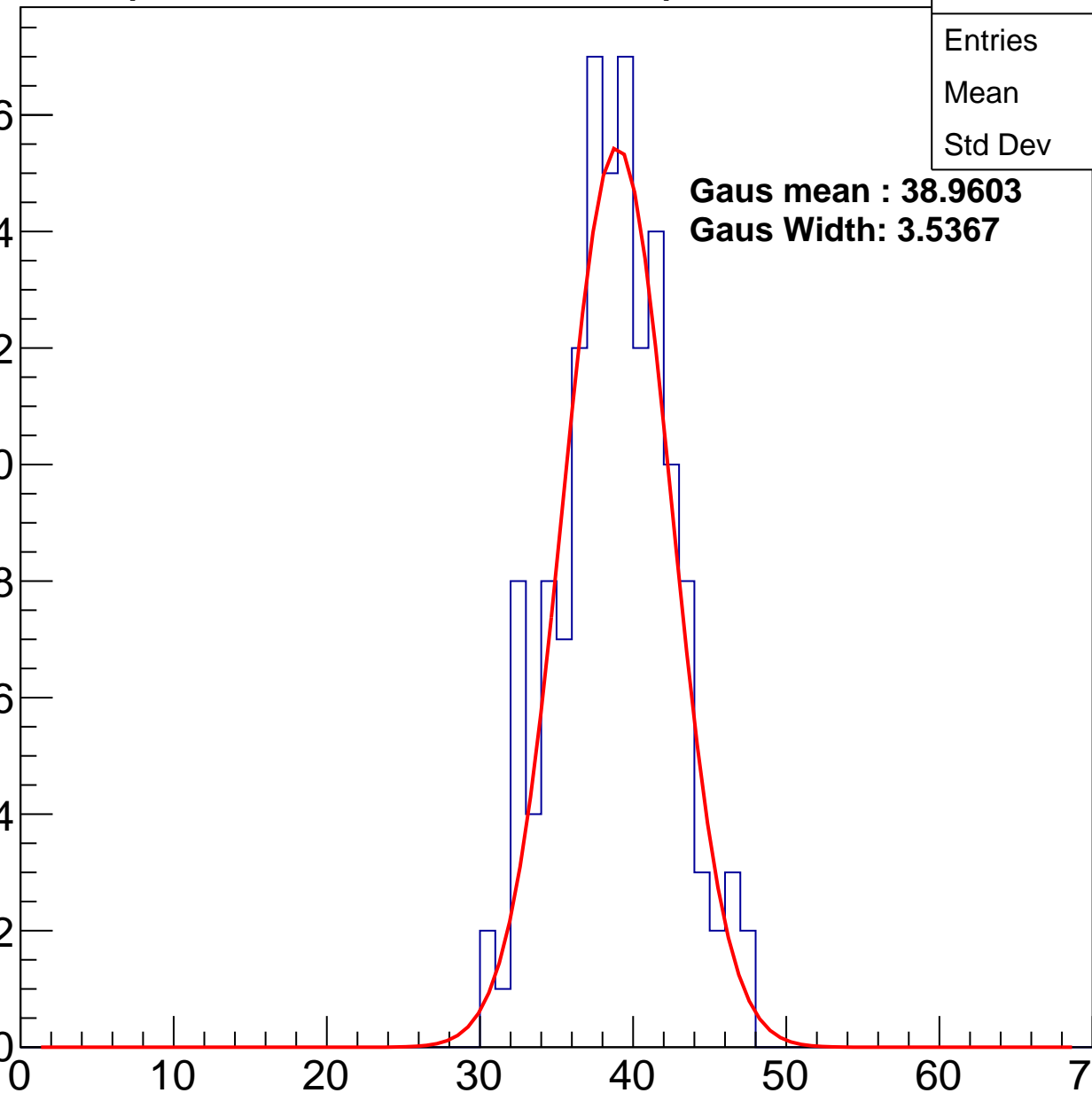
Gaus mean : 38.9603

Gaus Width: 3.5367

Entry

16
14
12
10
8
6
4
2
0

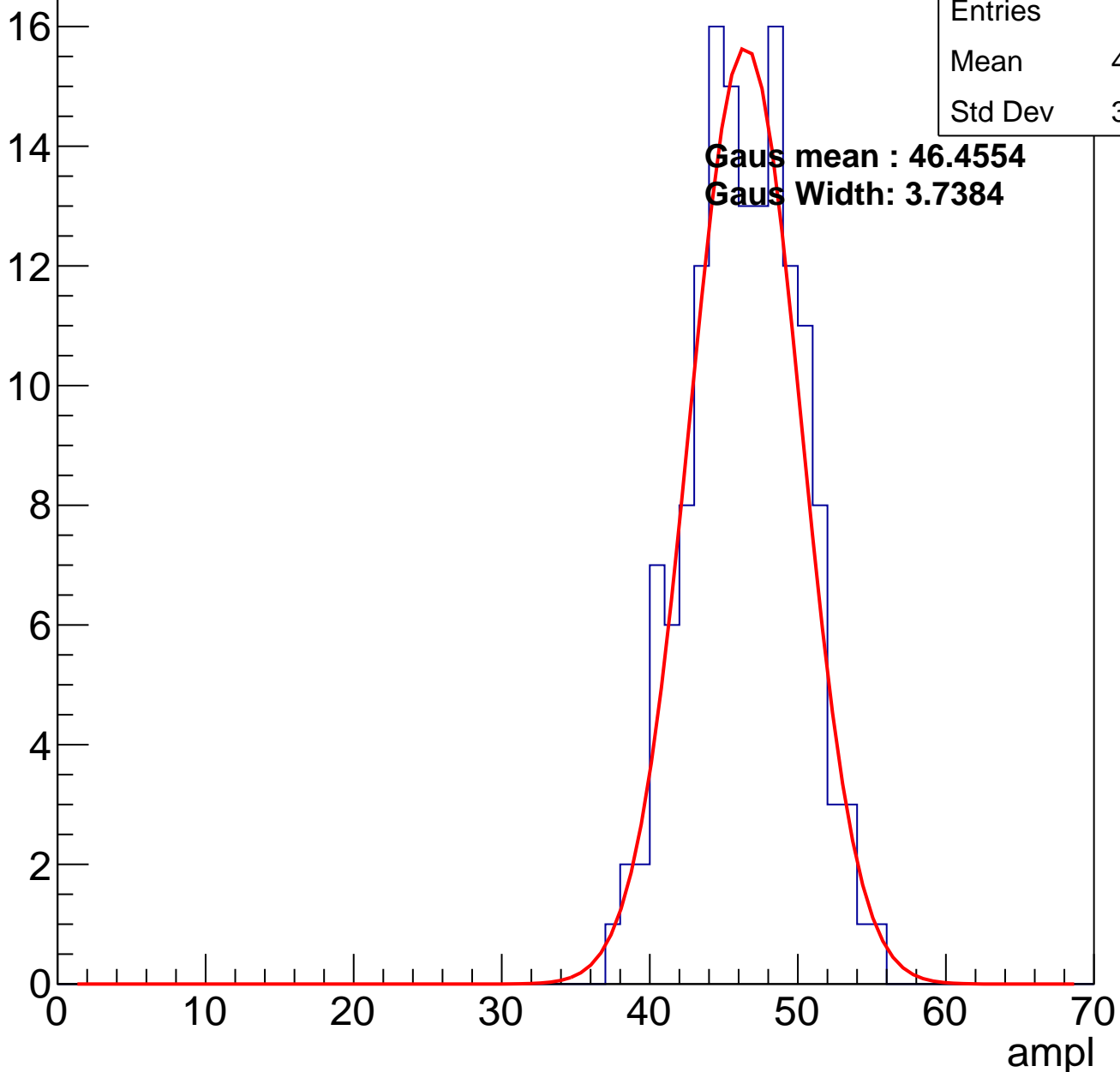
ampl



B1L001S, U25-ch73, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	150
Mean	45.97
Std Dev	3.638

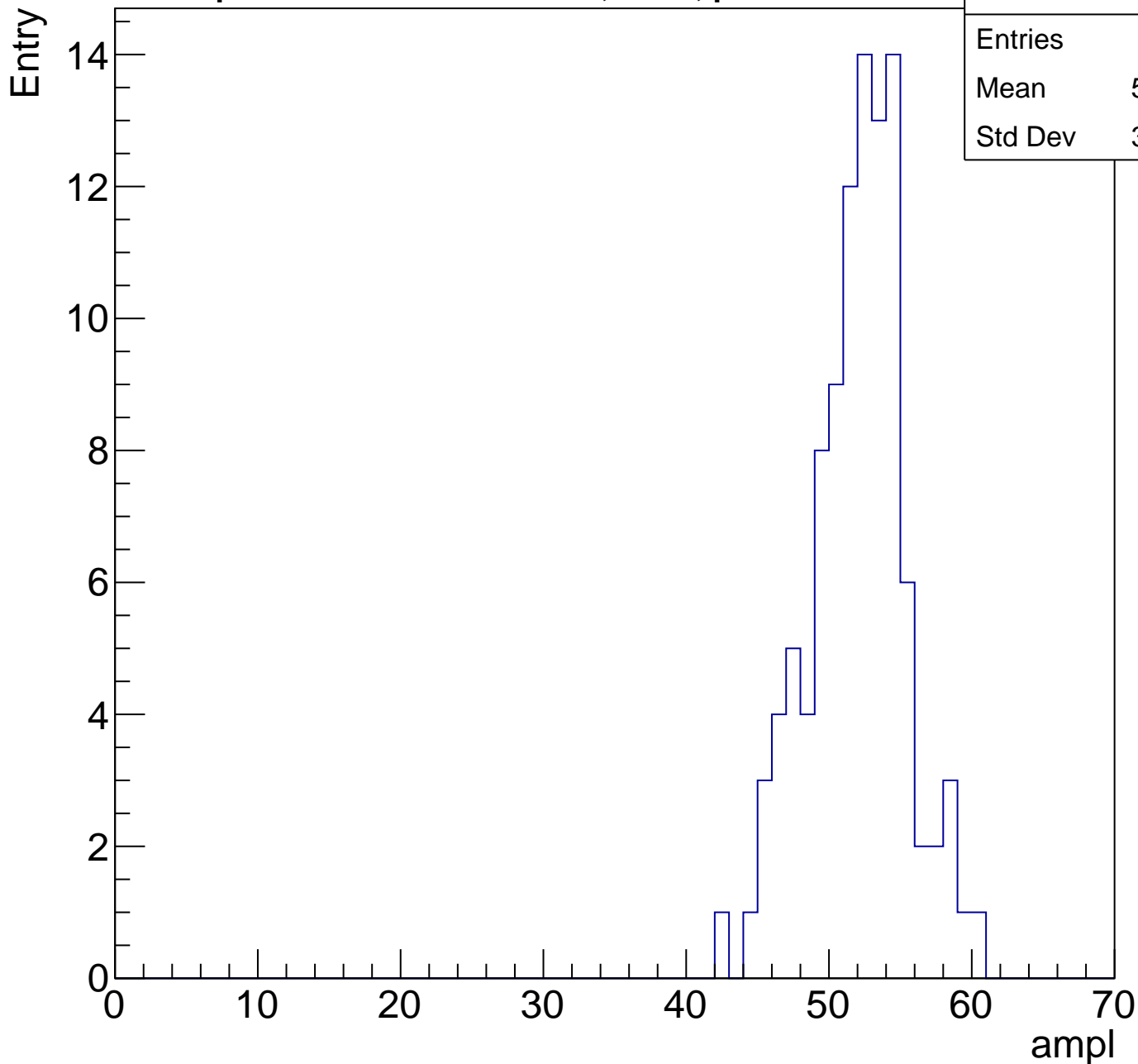
Gaus mean : 46.4554

Gaus Width: 3.7384

B1L001S, U25-ch73, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	51.53
Std Dev	3.367



B1L001S, U25-ch73, adc4

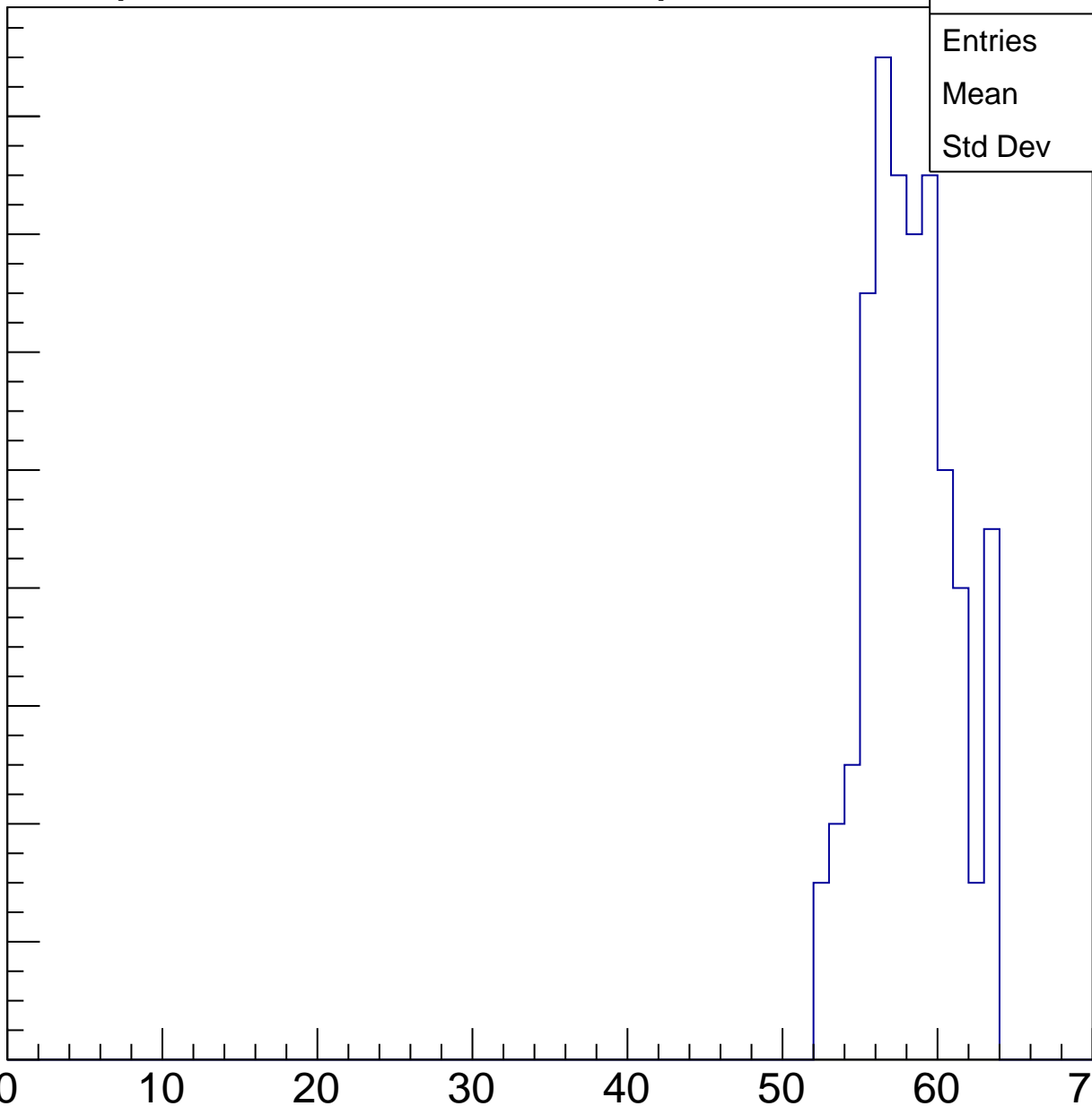
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	57.74
Std Dev	2.755

ampl



B1L001S, U25-ch73, adc5

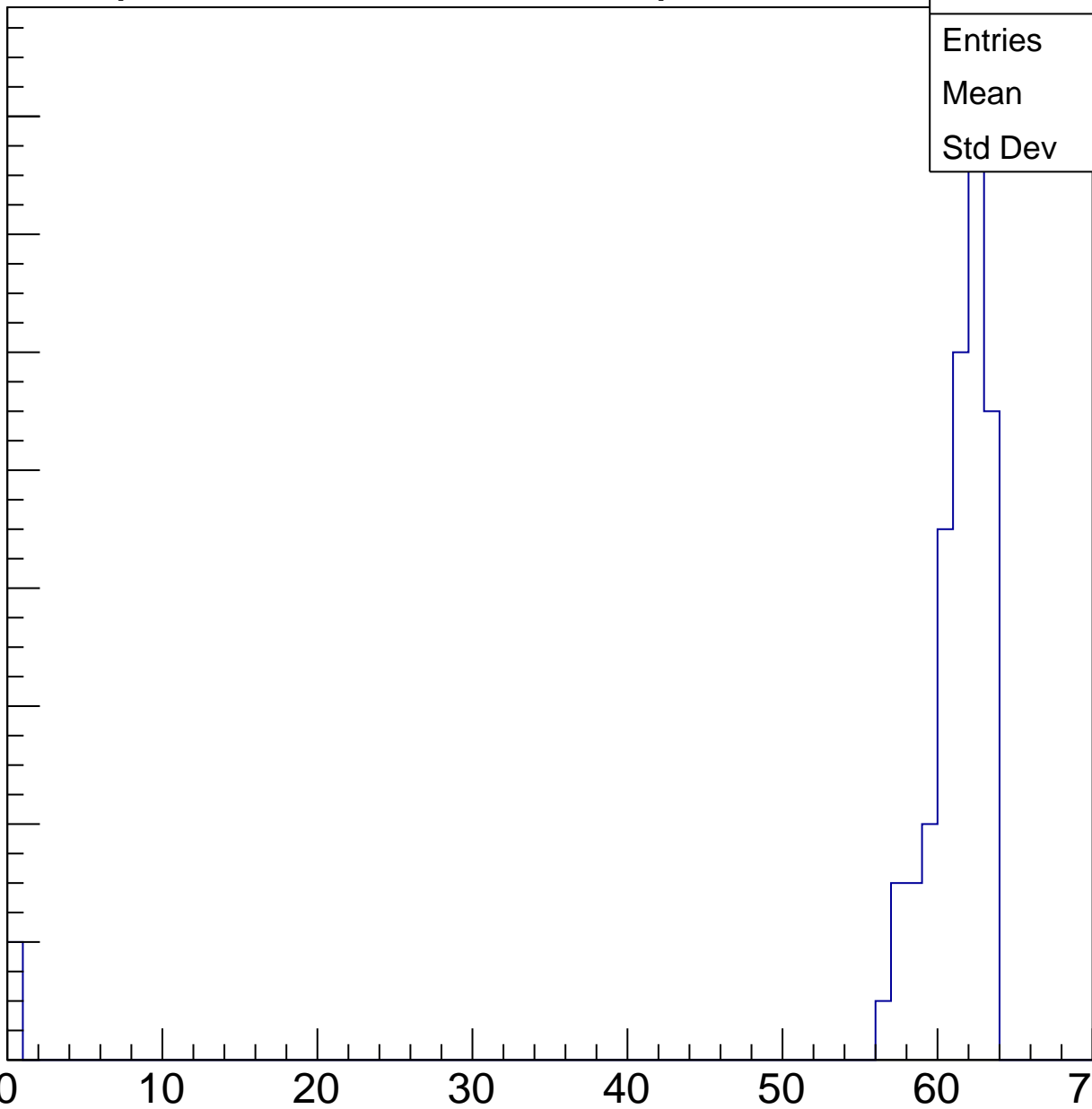
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	62
Mean	58.97
Std Dev	10.9

ampl



B1L001S, U25-ch73, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch73, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch74, adc0

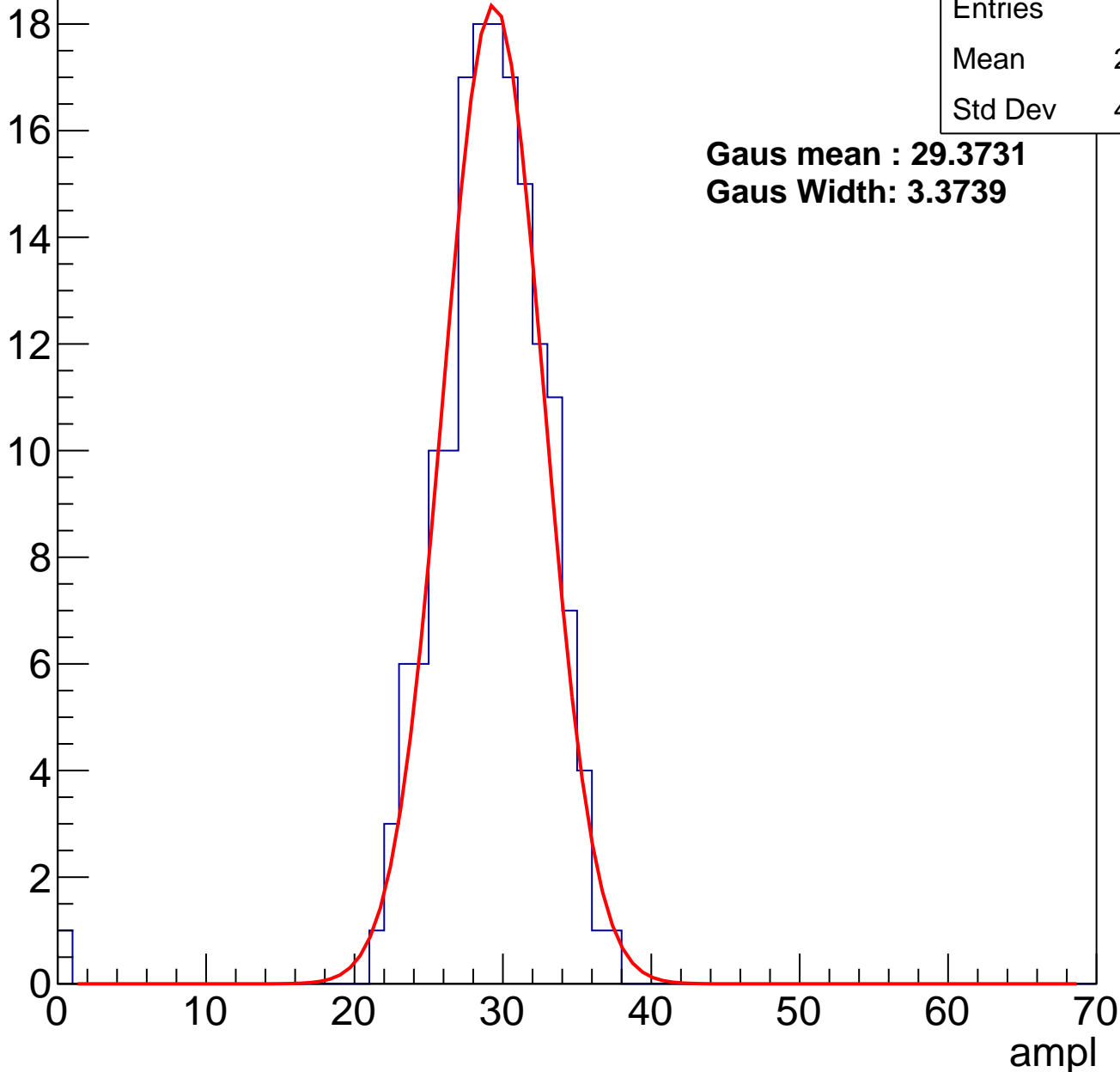
calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	28.72
Std Dev	4.005

Gaus mean : 29.3731

Gaus Width: 3.3739

Entry



B1L001S, U25-ch74, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.37
Std Dev	3.273

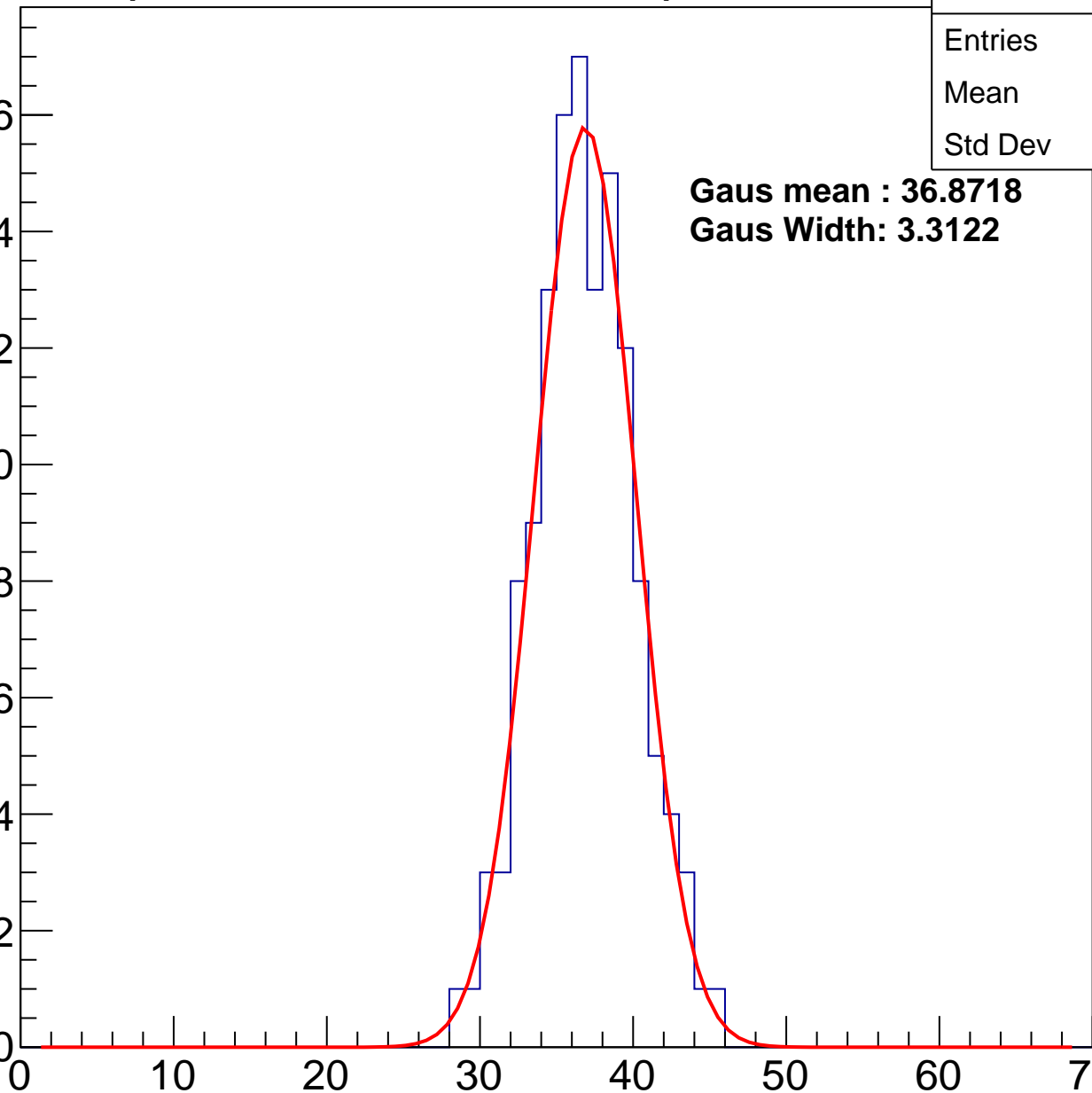
Gaus mean : 36.8718

Gaus Width: 3.3122

Entry

16
14
12
10
8
6
4
2
0

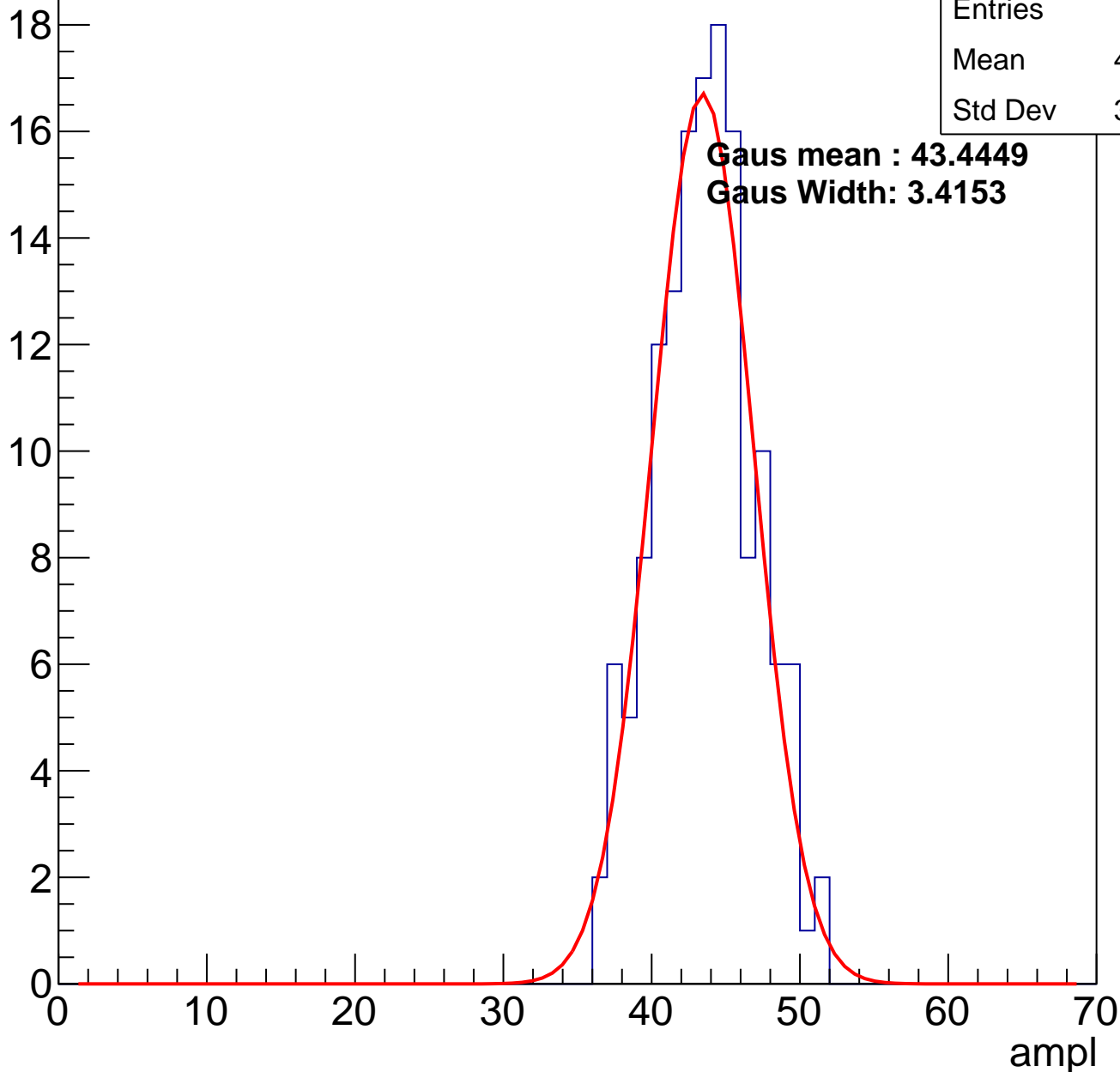
ampl



B1L001S, U25-ch74, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch74, adc3

calib_packv5_042523_0143.root, FC#2, port C2

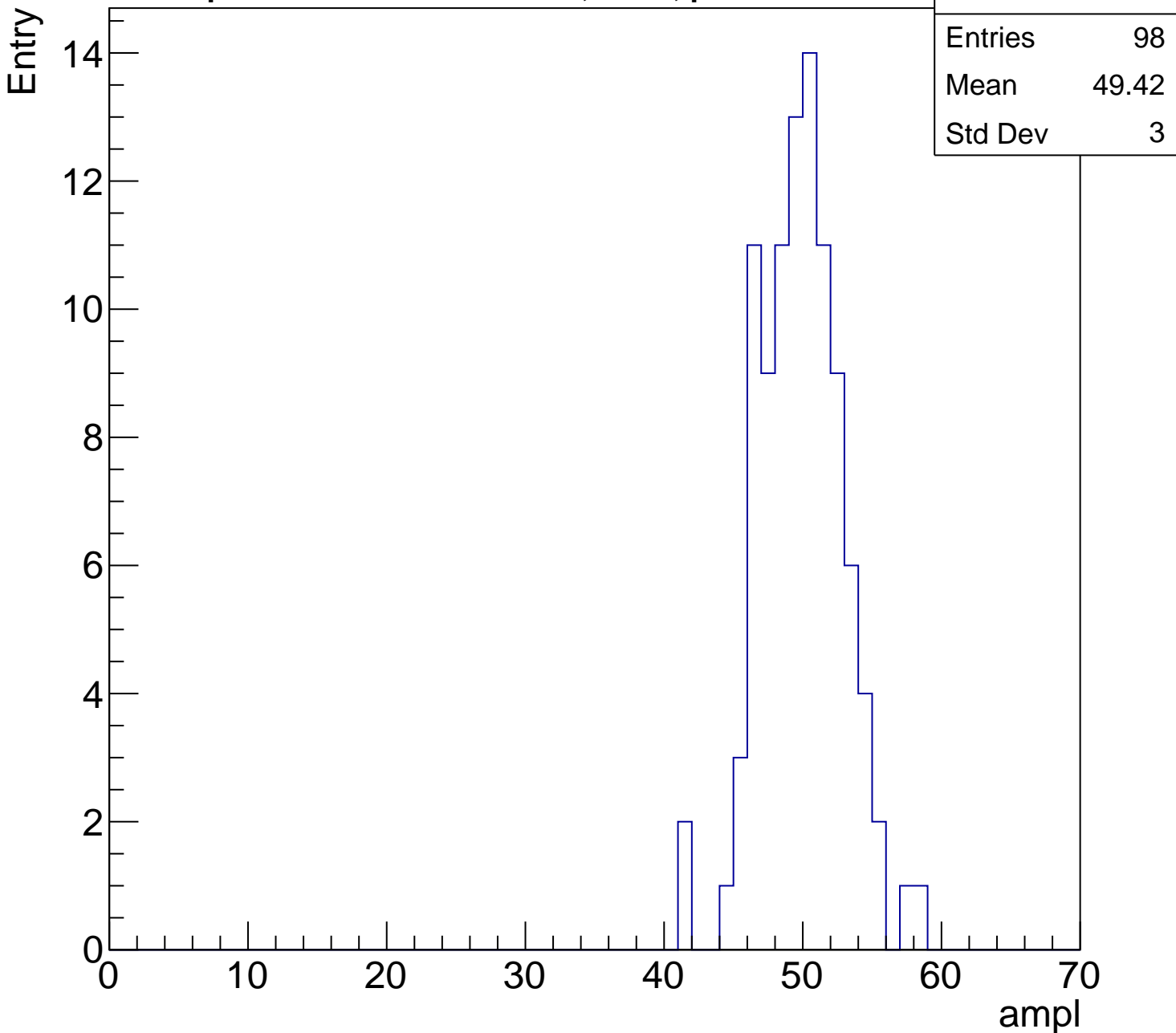
Entries	98
Mean	49.42
Std Dev	3

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

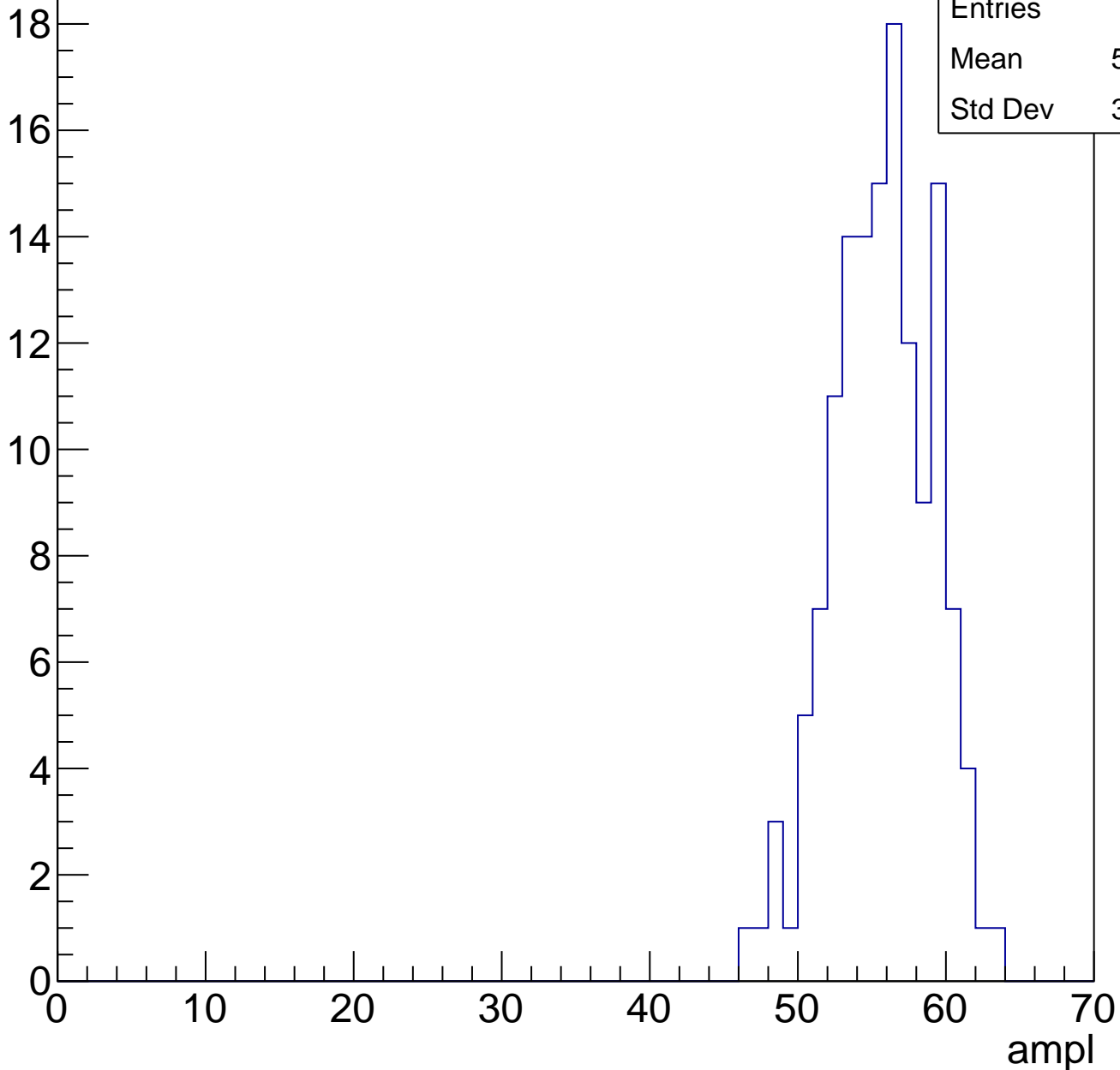
ampl



B1L001S, U25-ch74, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	139
Mean	55.22
Std Dev	3.335

B1L001S, U25-ch74, adc5

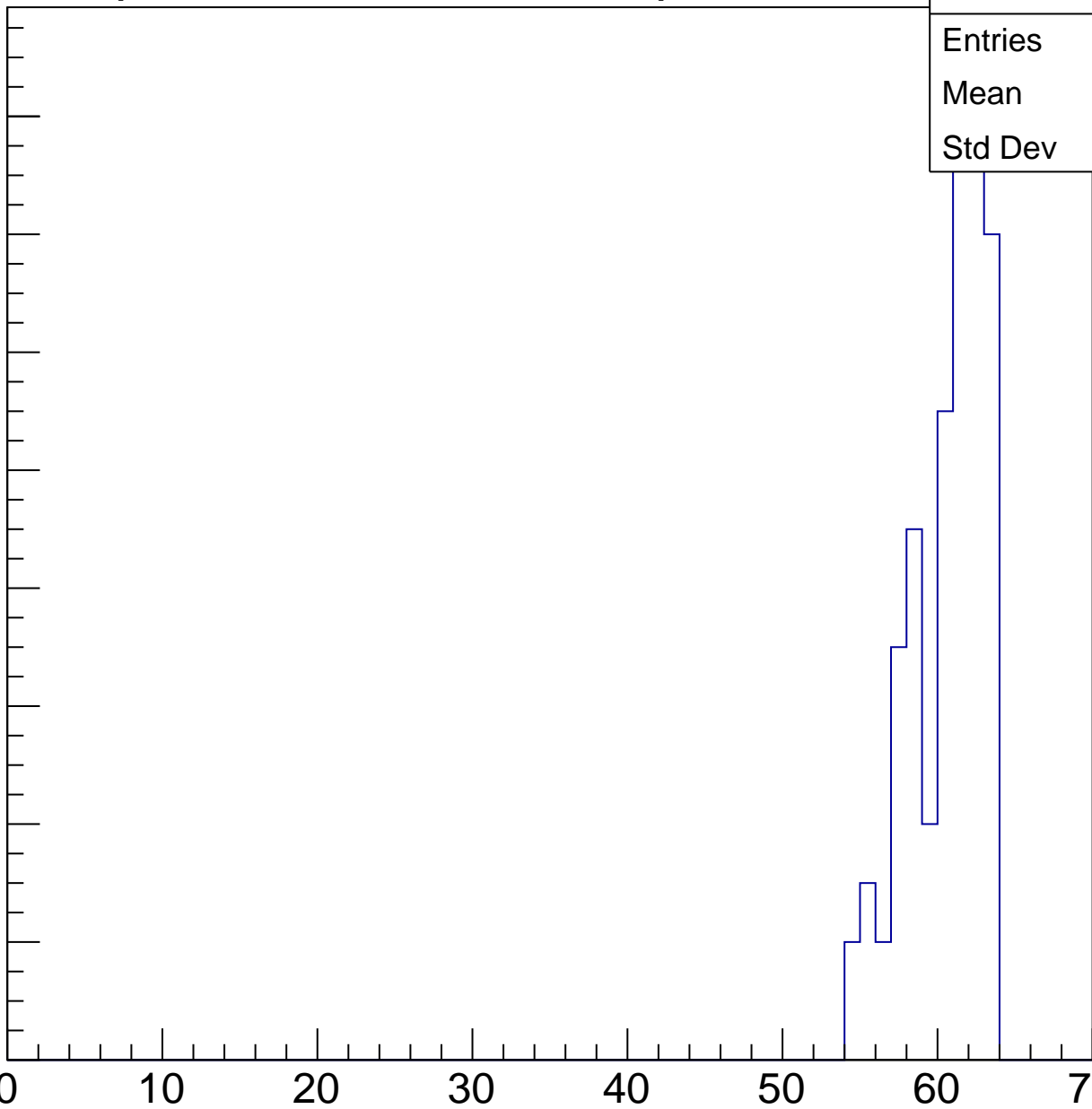
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	60.16
Std Dev	2.4

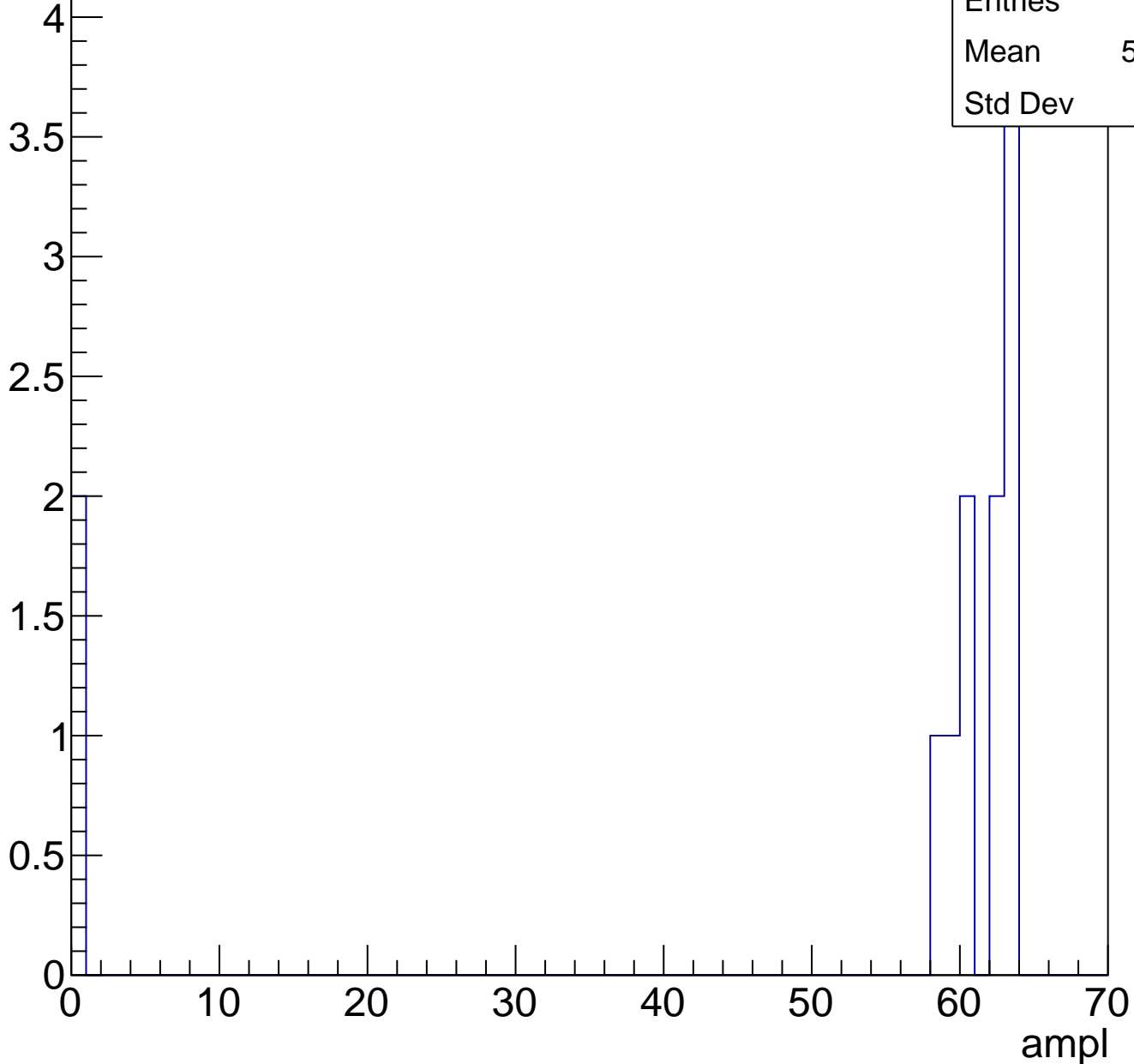
ampl



B1L001S, U25-ch74, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch74, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	63
Std Dev	0

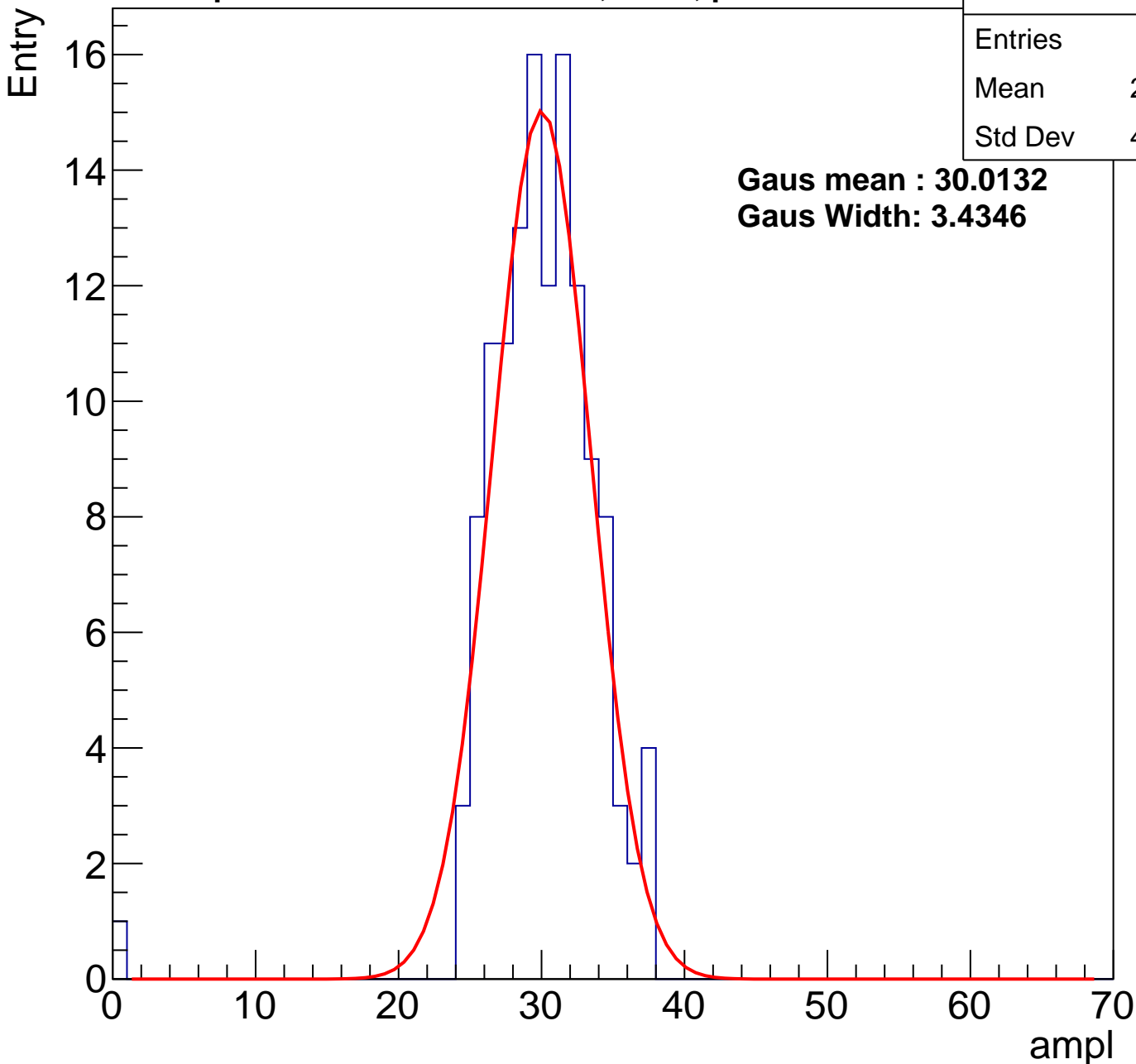
B1L001S, U25-ch75, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	29.59
Std Dev	4.078

Gaus mean : 30.0132

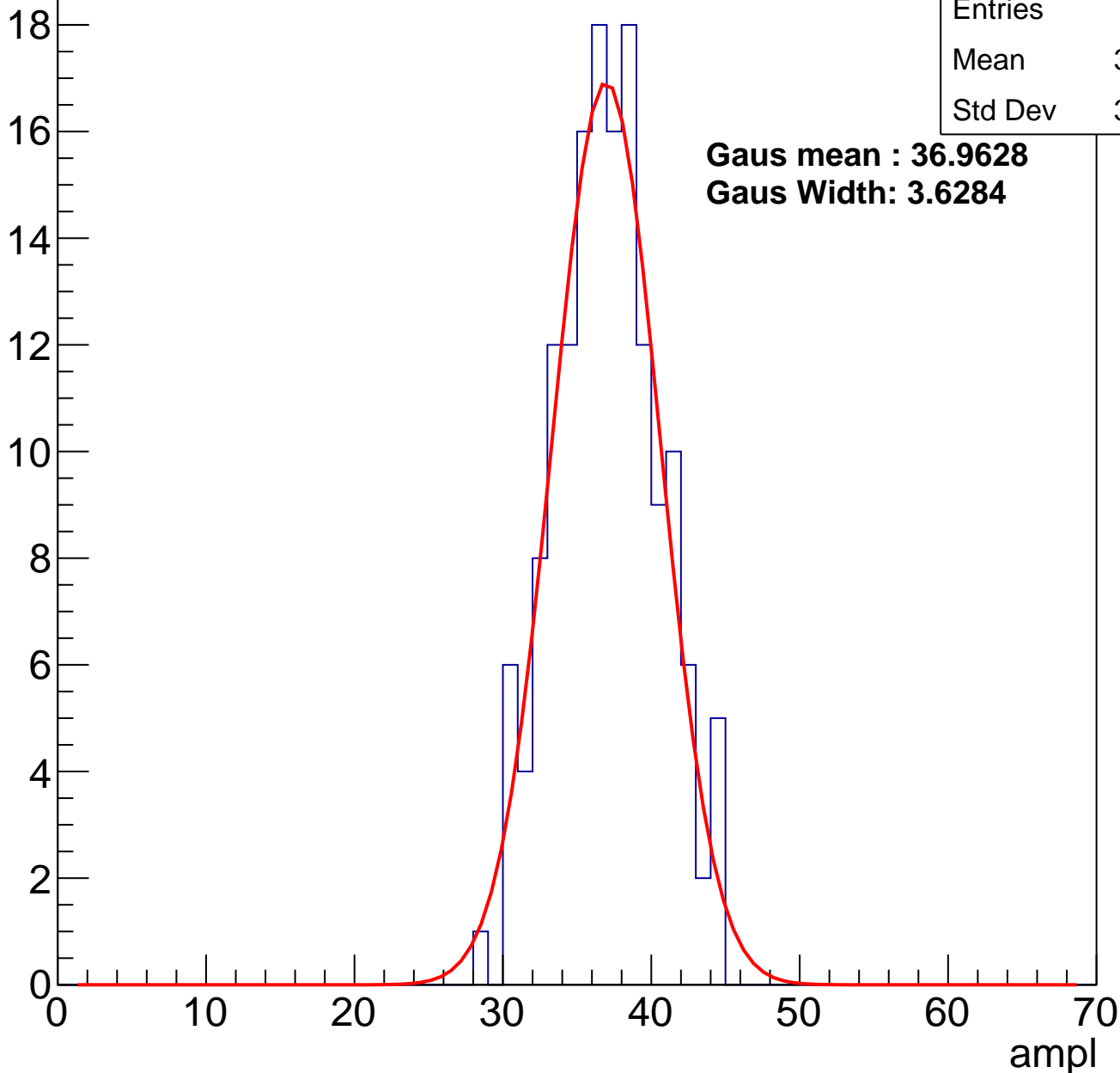
Gaus Width: 3.4346



B1L001S, U25-ch75, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch75, adc2

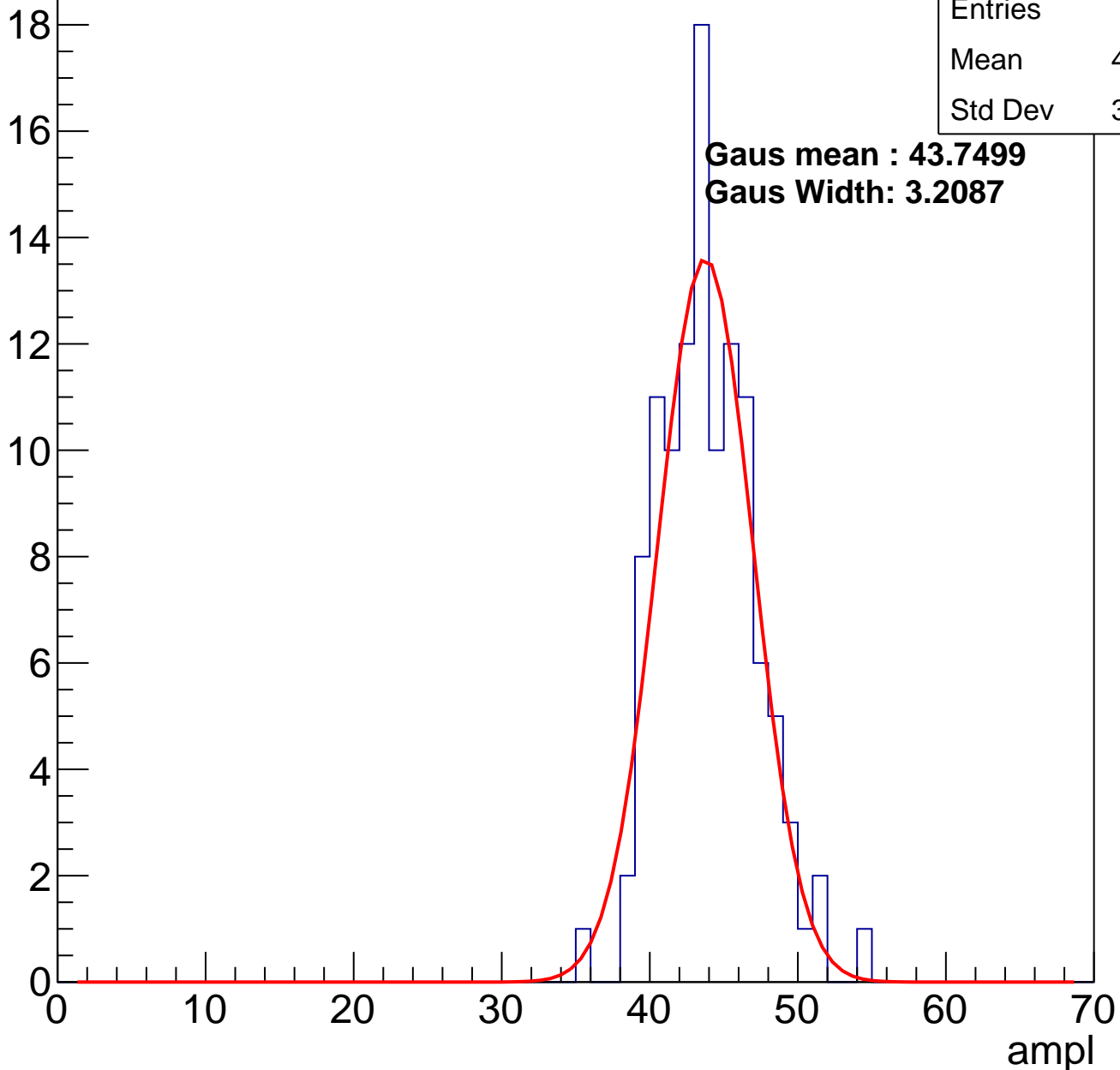
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	43.47
Std Dev	3.193

Gaus mean : 43.7499

Gaus Width: 3.2087

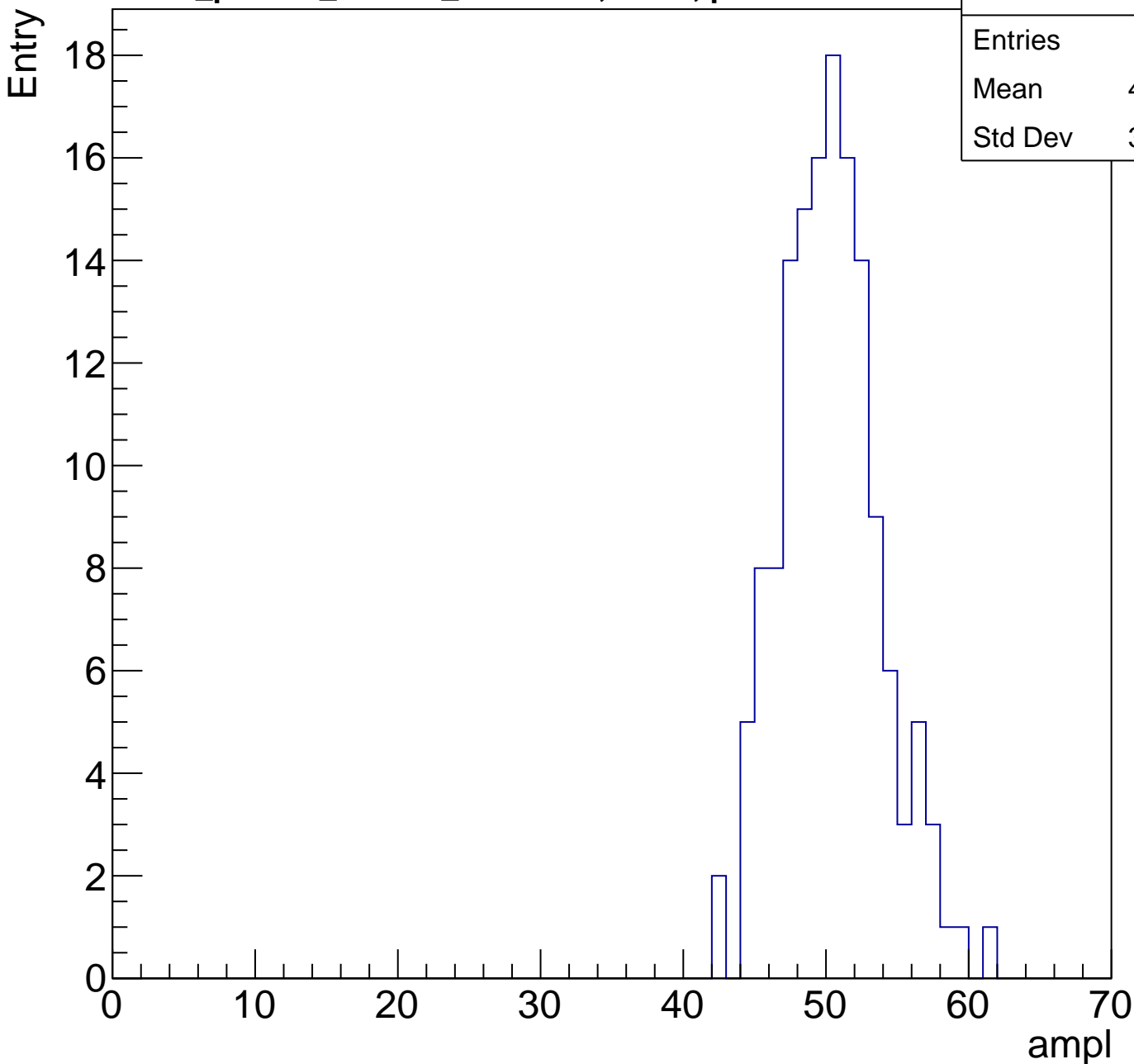
Entry



B1L001S, U25-ch75, adc3

calib_packv5_042523_0143.root, FC#2, port C2

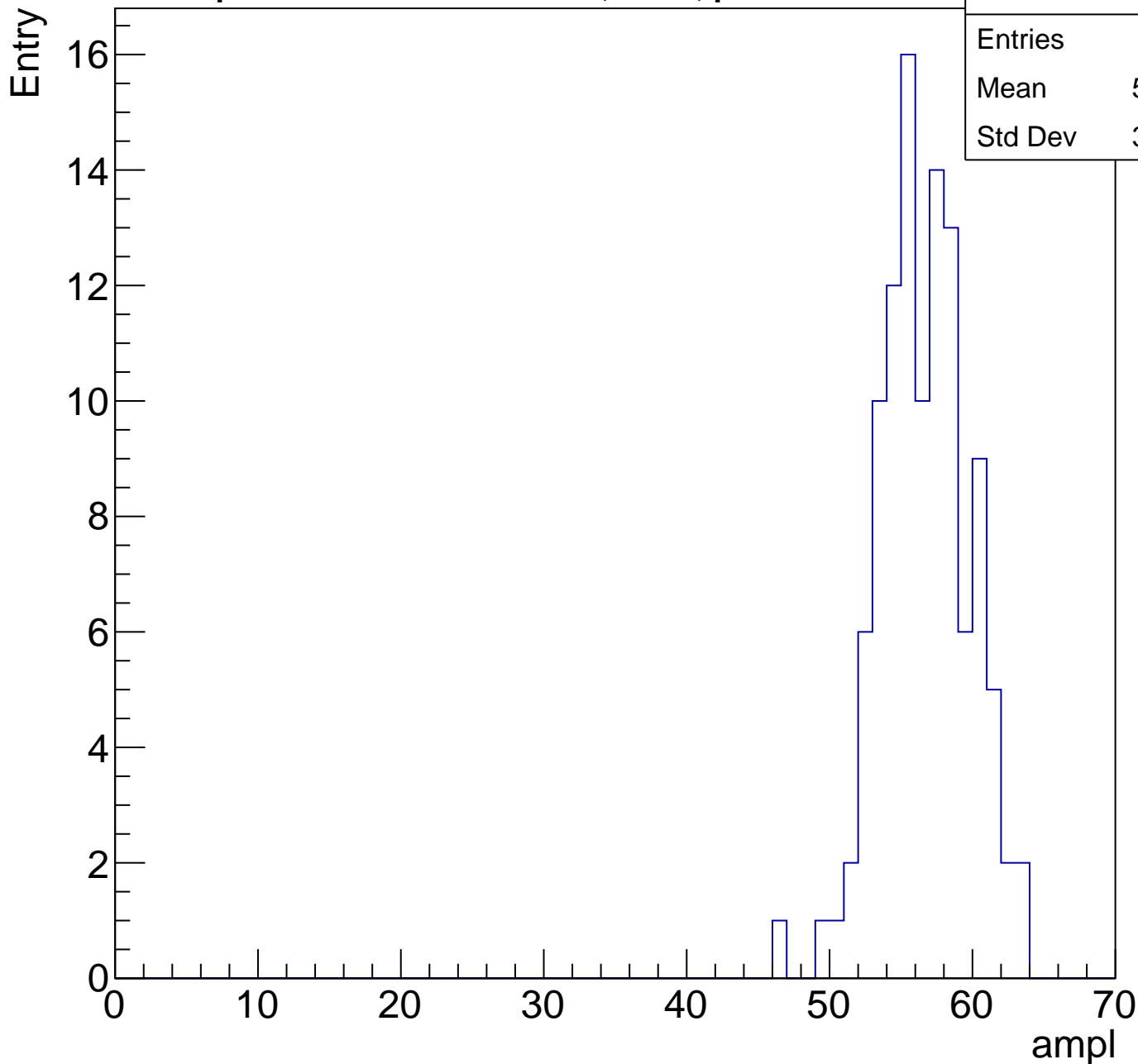
Entries	145
Mean	49.88
Std Dev	3.477



B1L001S, U25-ch75, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	56.16
Std Dev	3.076



B1L001S, U25-ch75, adc5

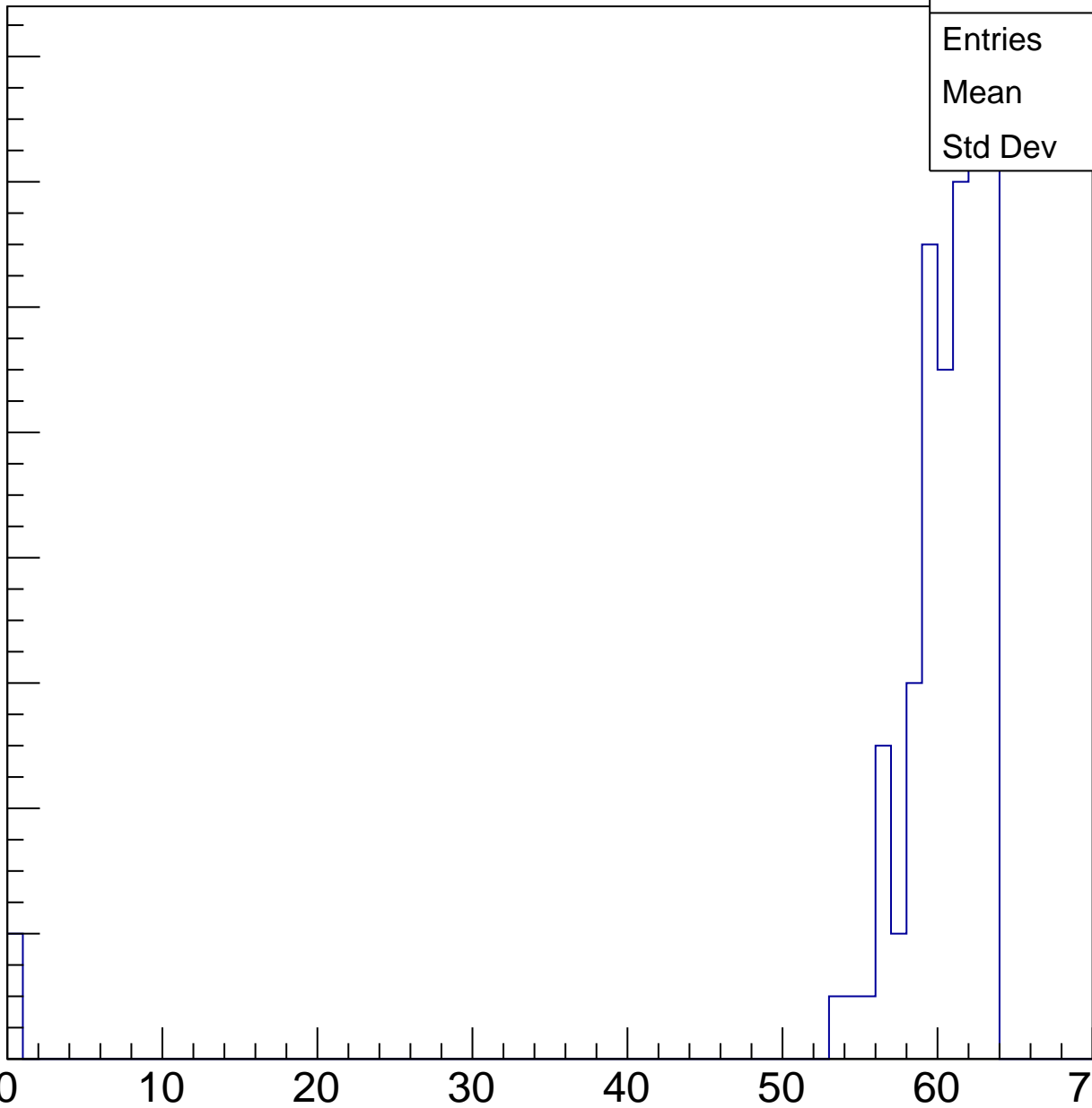
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	58.92
Std Dev	9.271

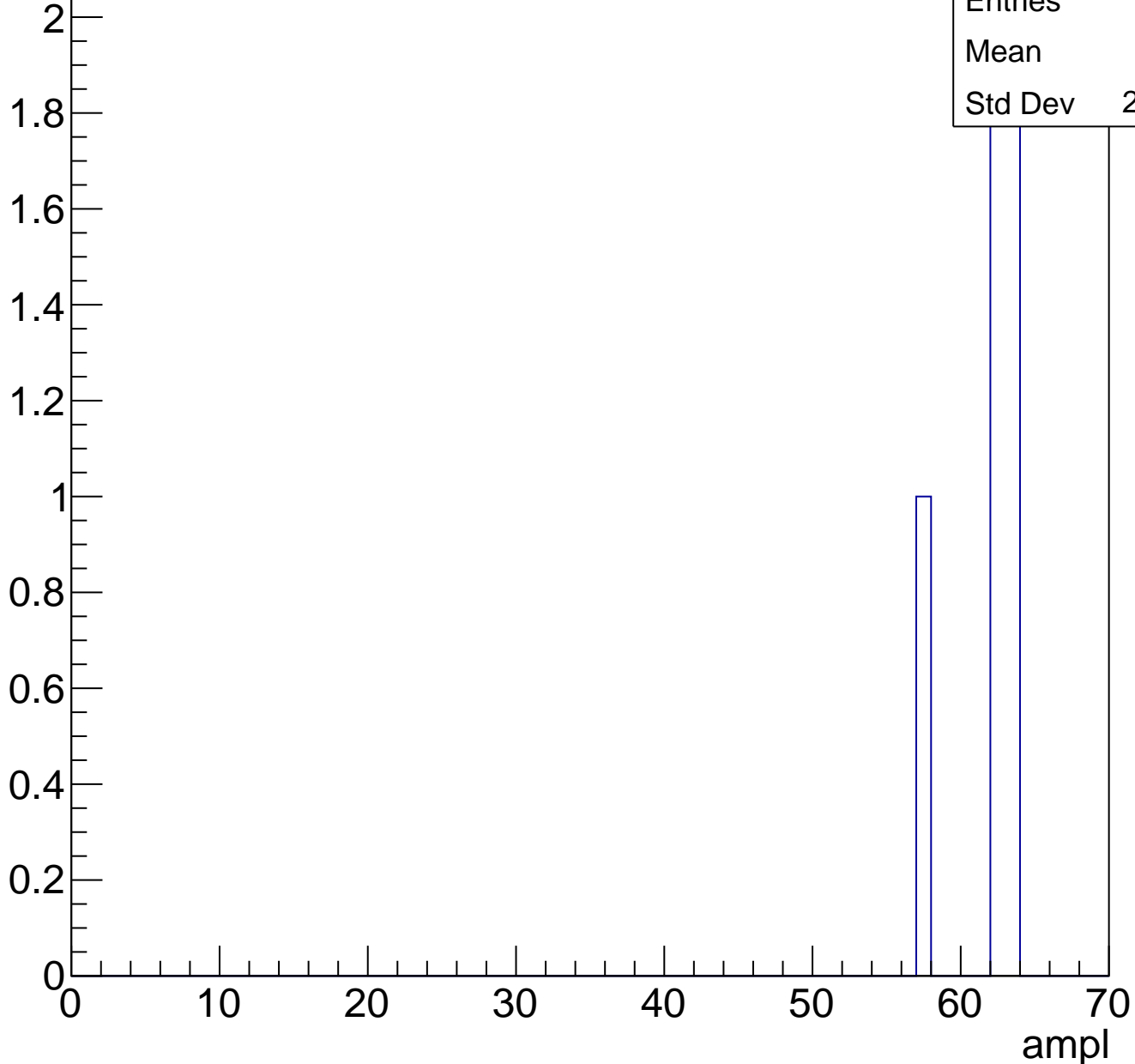
ampl



B1L001S, U25-ch75, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

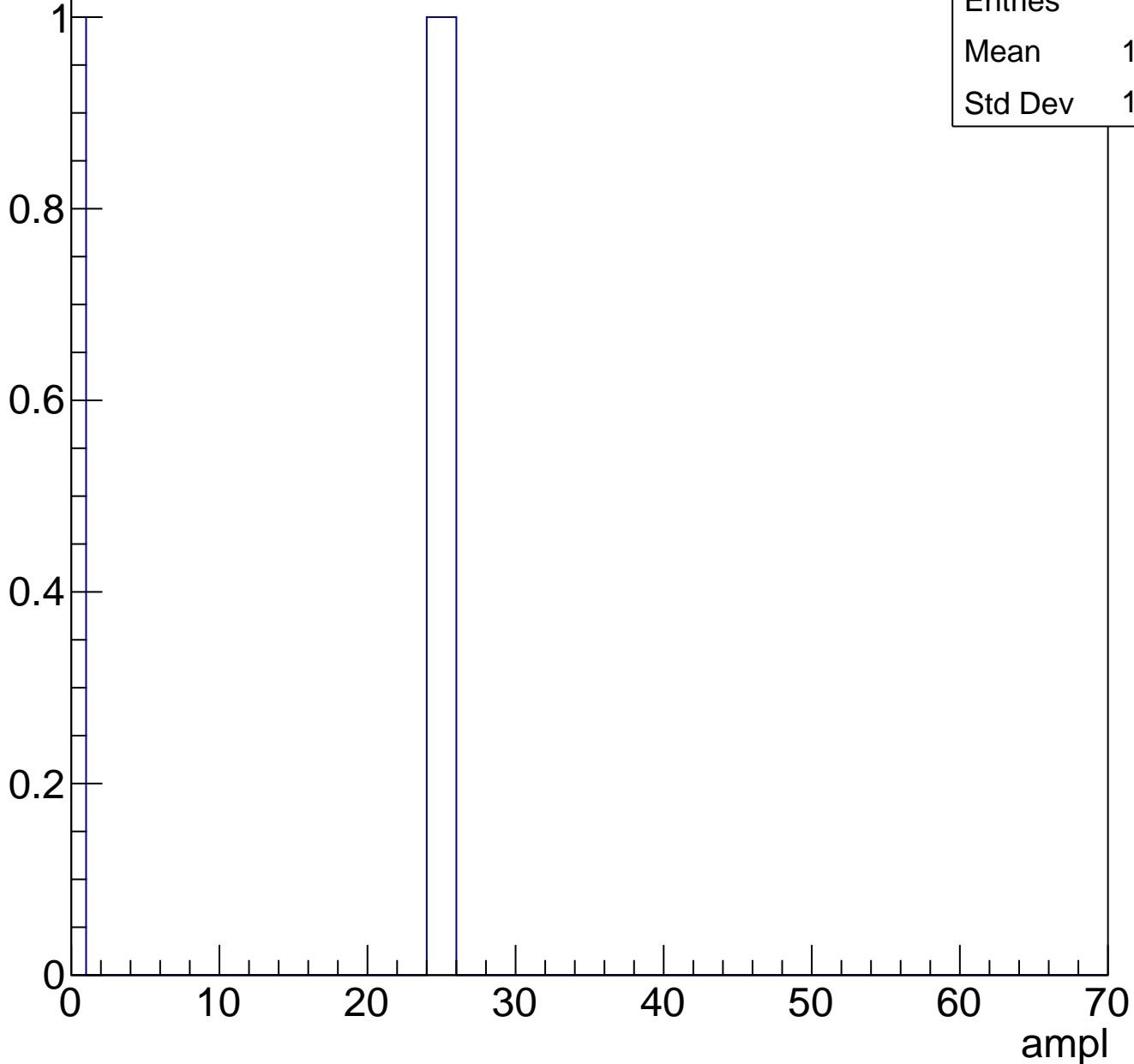


Entries	5
Mean	61.4
Std Dev	2.245

B1L001S, U25-ch75, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch76, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	30.18
Std Dev	4.439

Gaus mean : 30.8747

Gaus Width: 3.7027

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

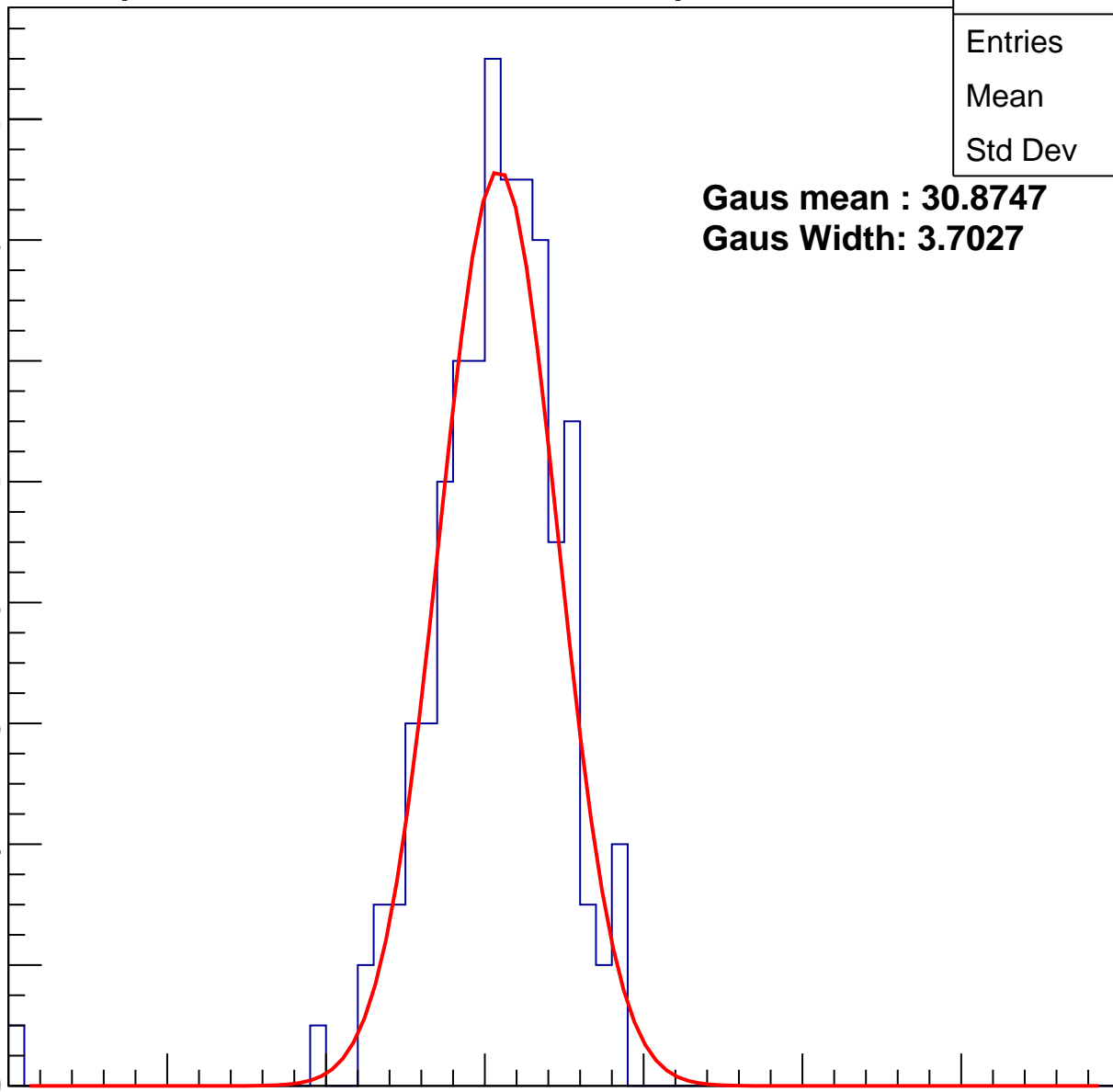
40

50

60

70

ampl



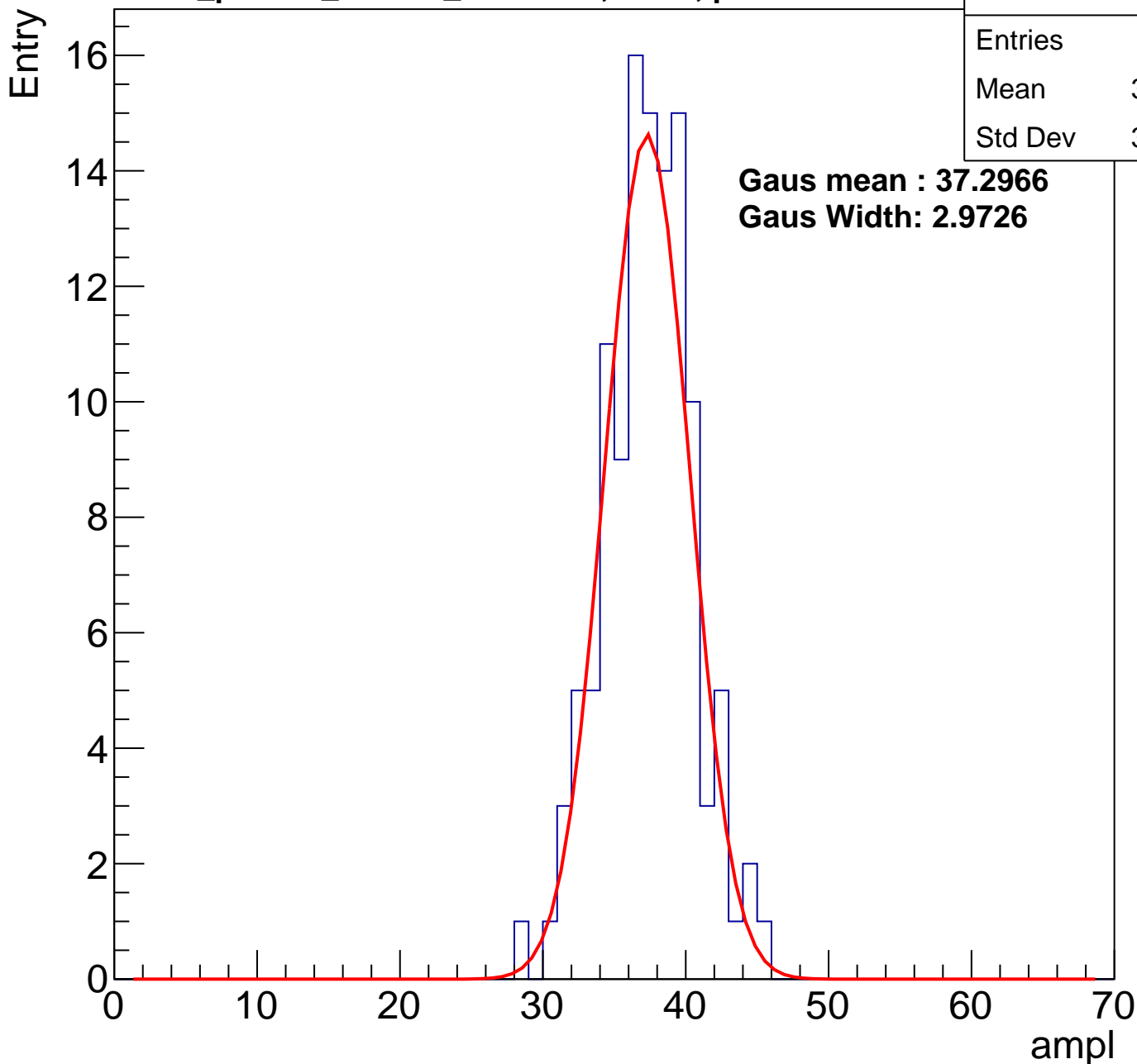
B1L001S, U25-ch76, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	36.94
Std Dev	3.106

Gaus mean : 37.2966

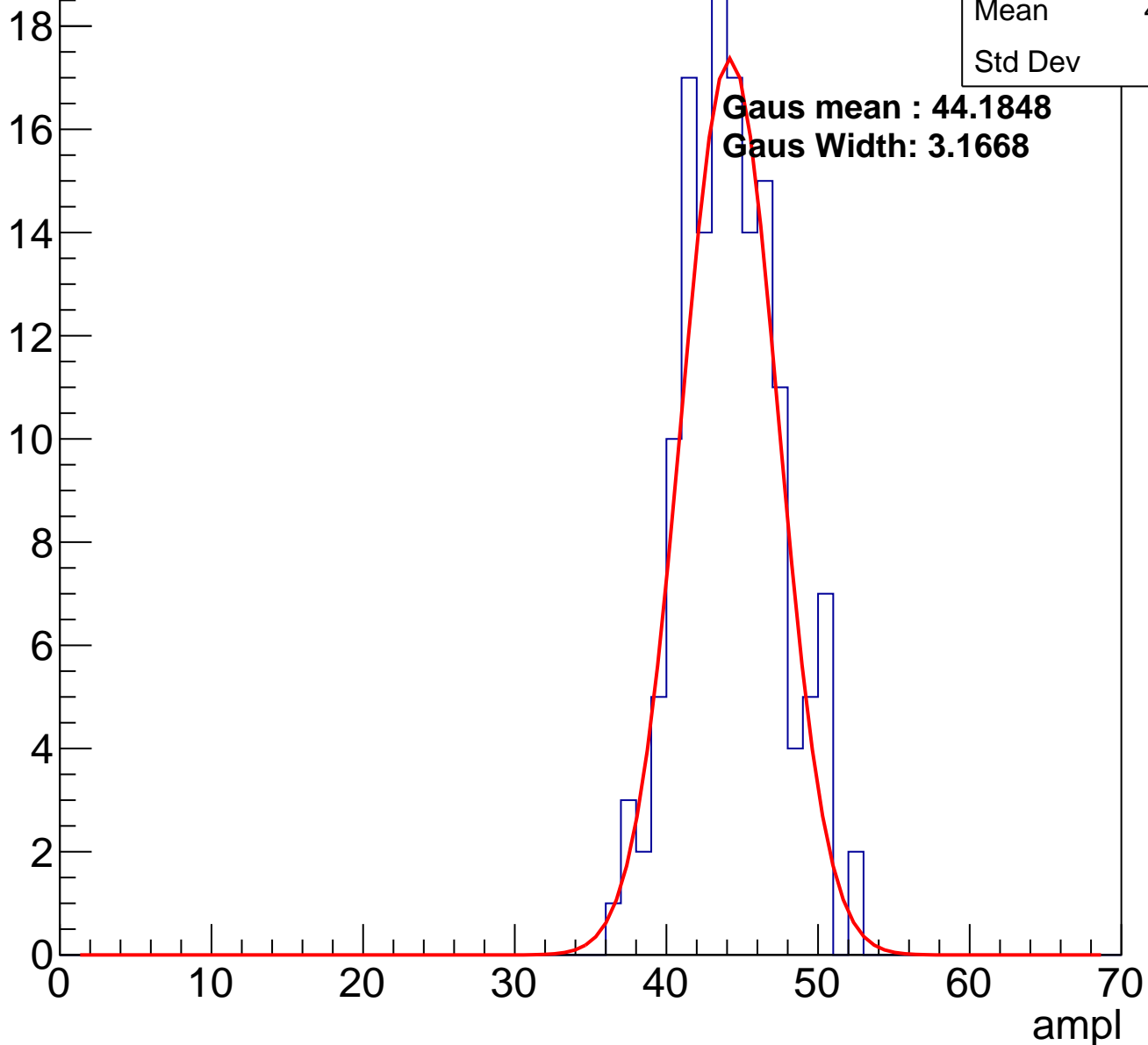
Gaus Width: 2.9726



B1L001S, U25-ch76, adc2

calib_packv5_042523_0143.root, FC#2, port C2

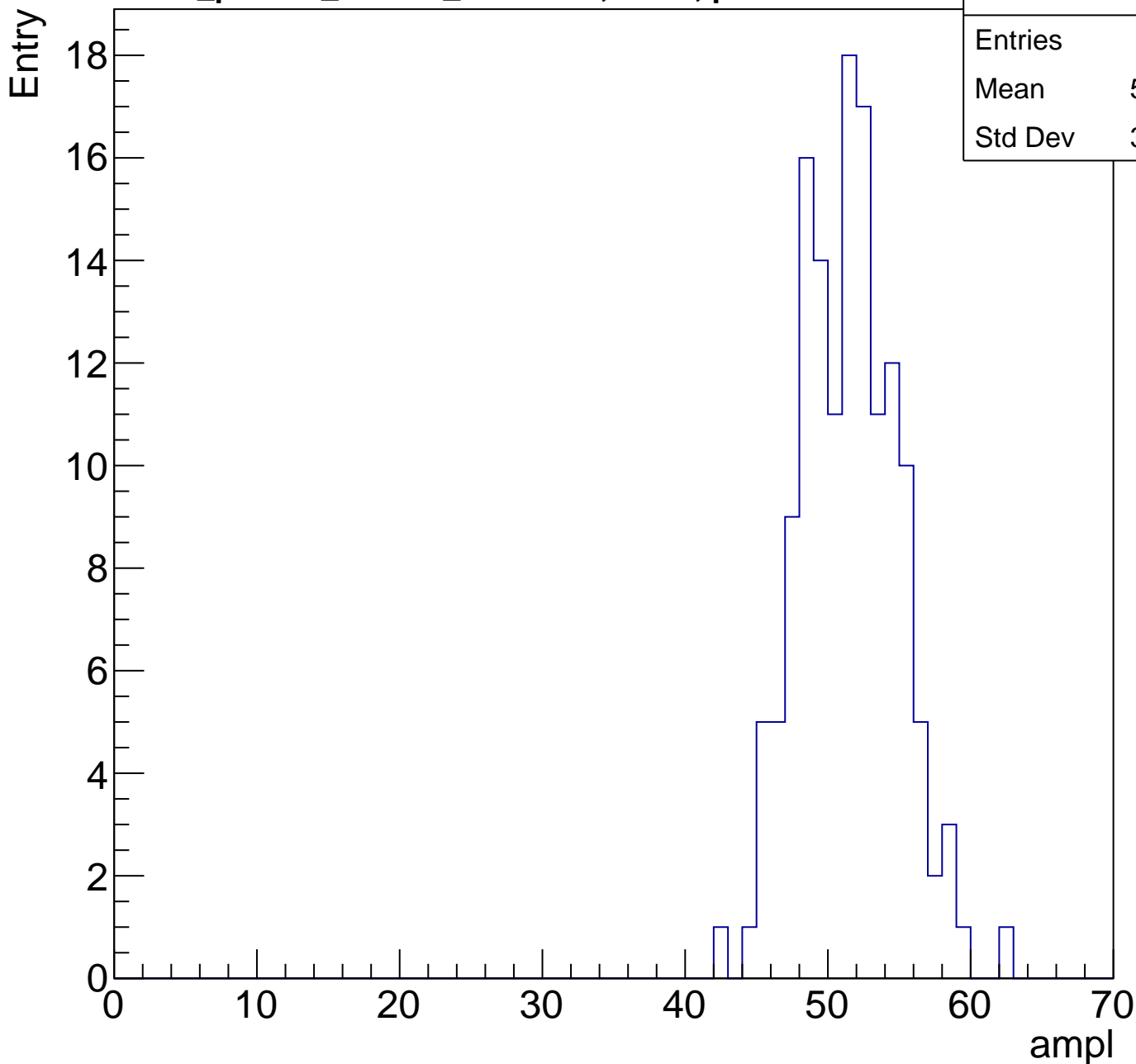
Entry



B1L001S, U25-ch76, adc3

calib_packv5_042523_0143.root, FC#2, port C2

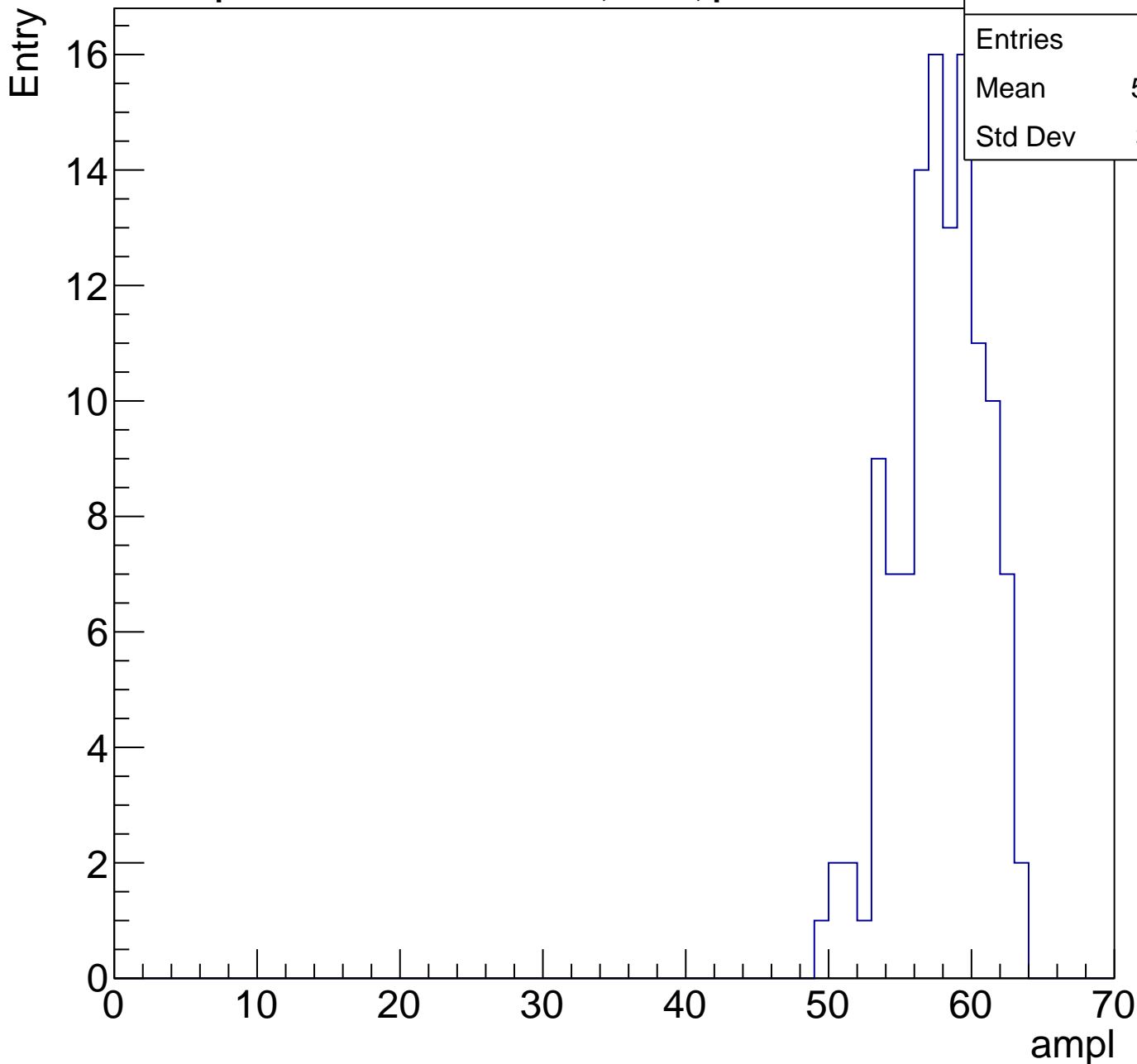
Entries	142
Mean	50.99
Std Dev	3.407



B1L001S, U25-ch76, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	57.35
Std Dev	3.021



B1L001S, U25-ch76, adc5

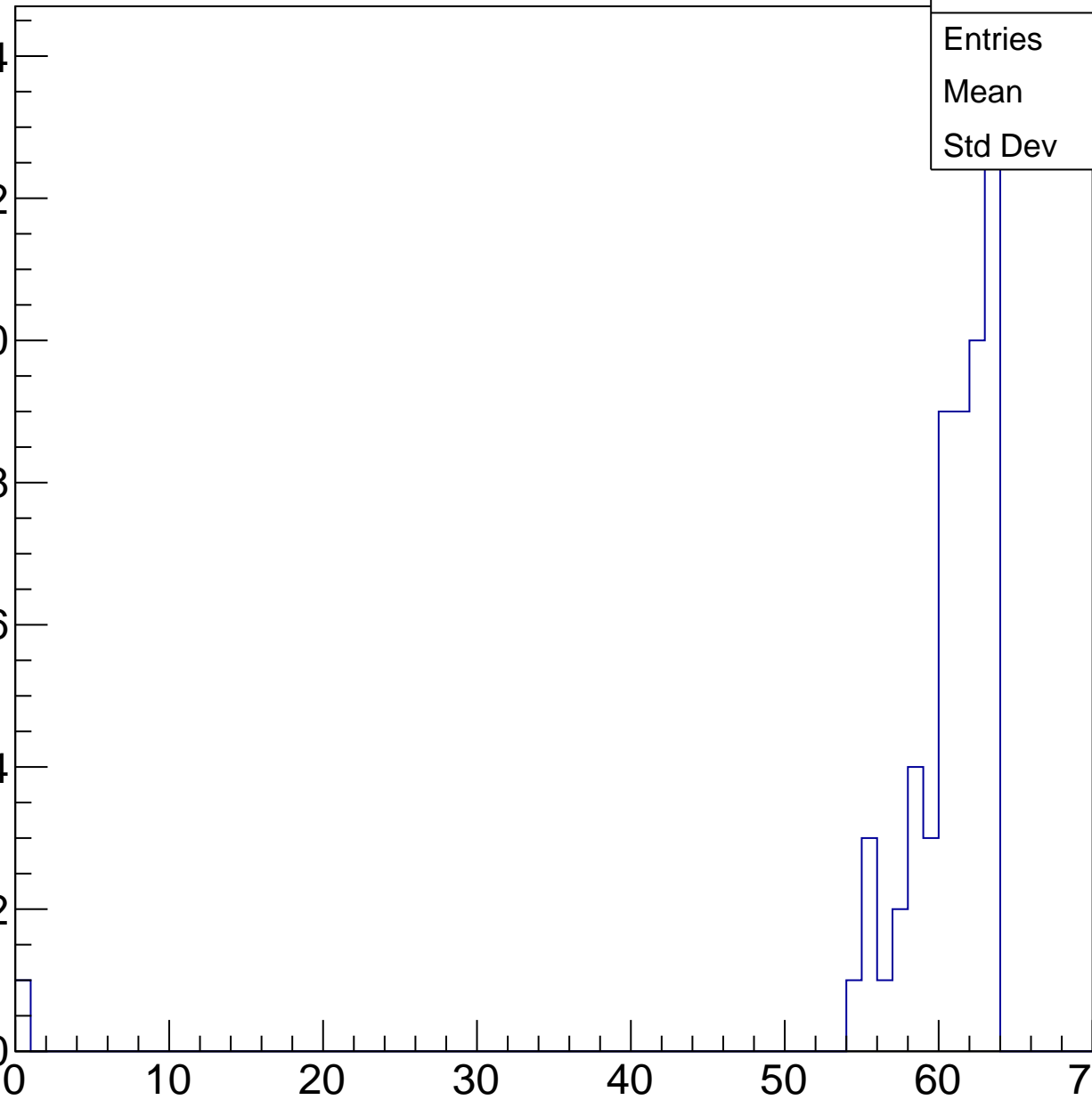
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	57
Mean	59.46
Std Dev	8.295

ampl



B1L001S, U25-ch76, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch76, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch77, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	30.58
Std Dev	4.28

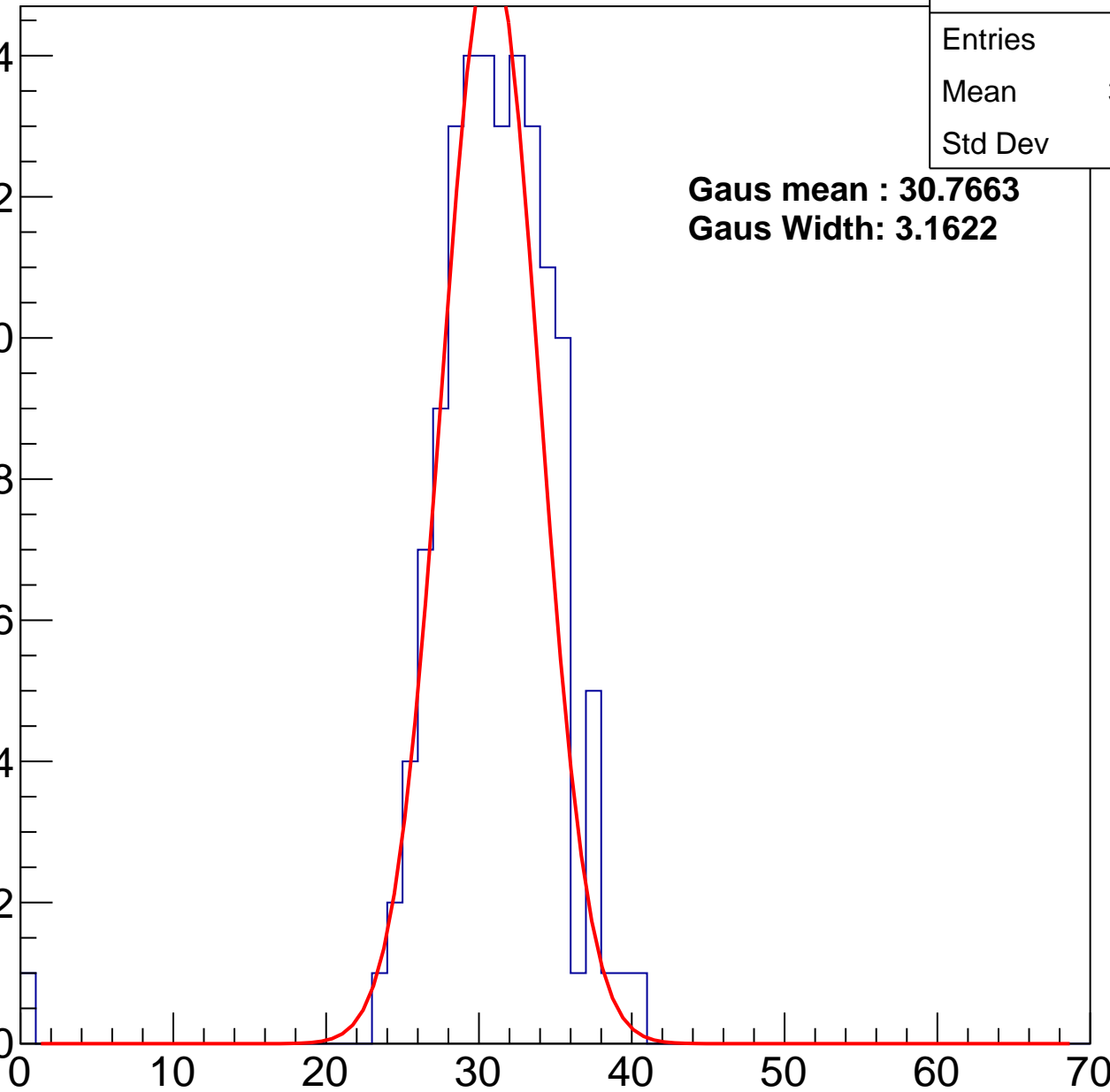
Gaus mean : 30.7663

Gaus Width: 3.1622

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch77, adc1

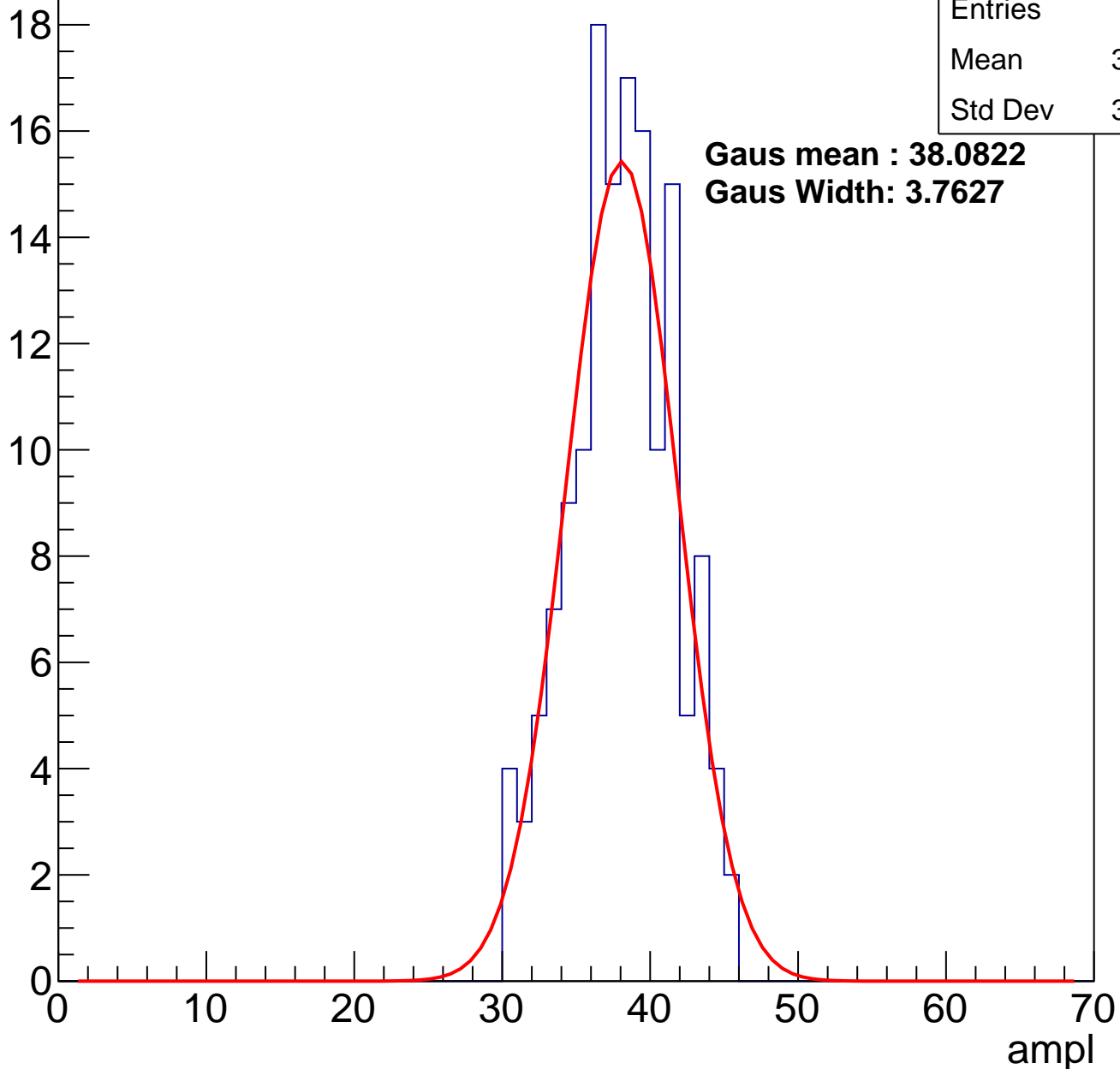
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	37.62
Std Dev	3.459

Gaus mean : 38.0822

Gaus Width: 3.7627

Entry



B1L001S, U25-ch77, adc2

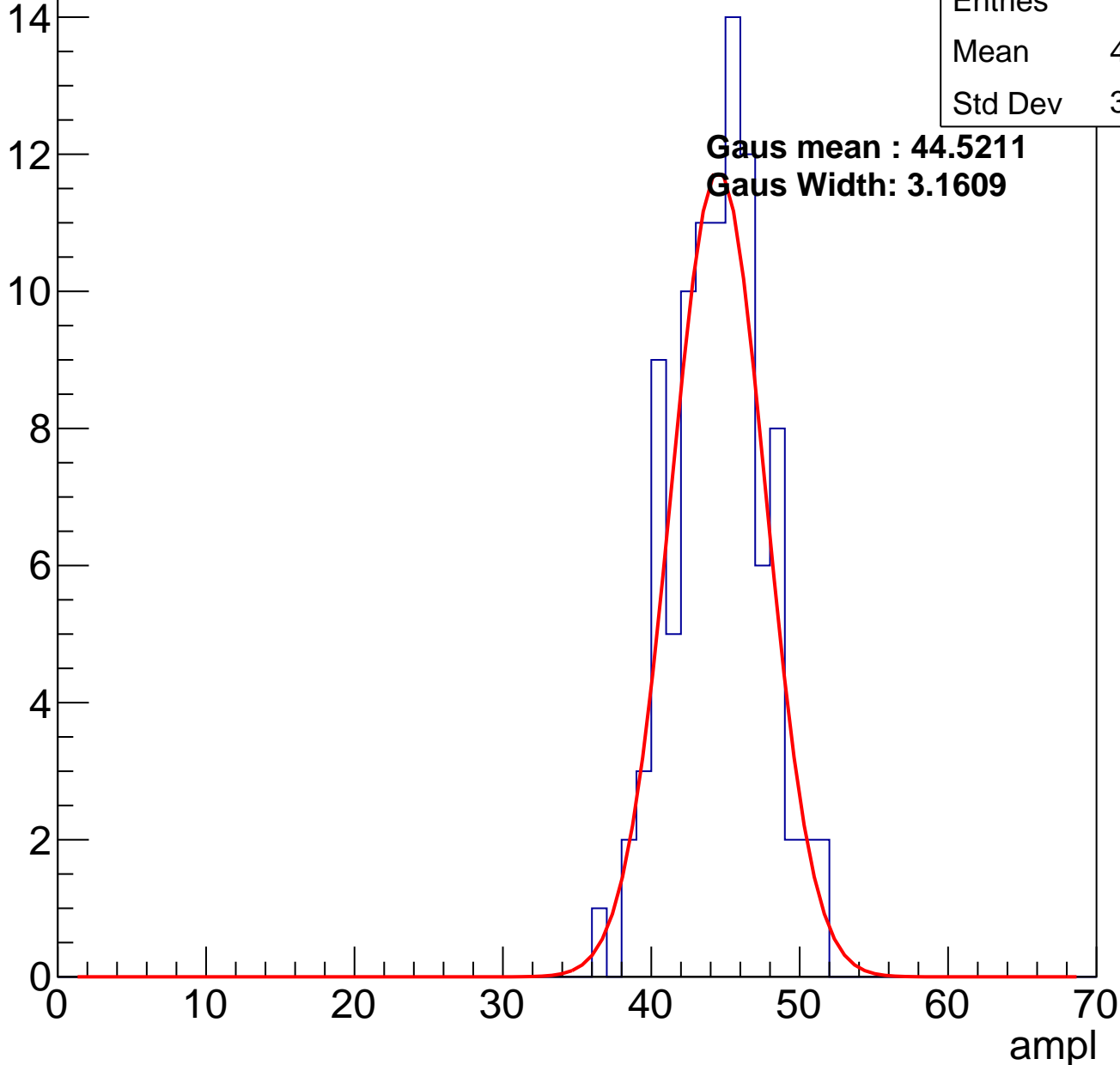
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	98
Mean	44.07
Std Dev	3.055

Gaus mean : 44.5211

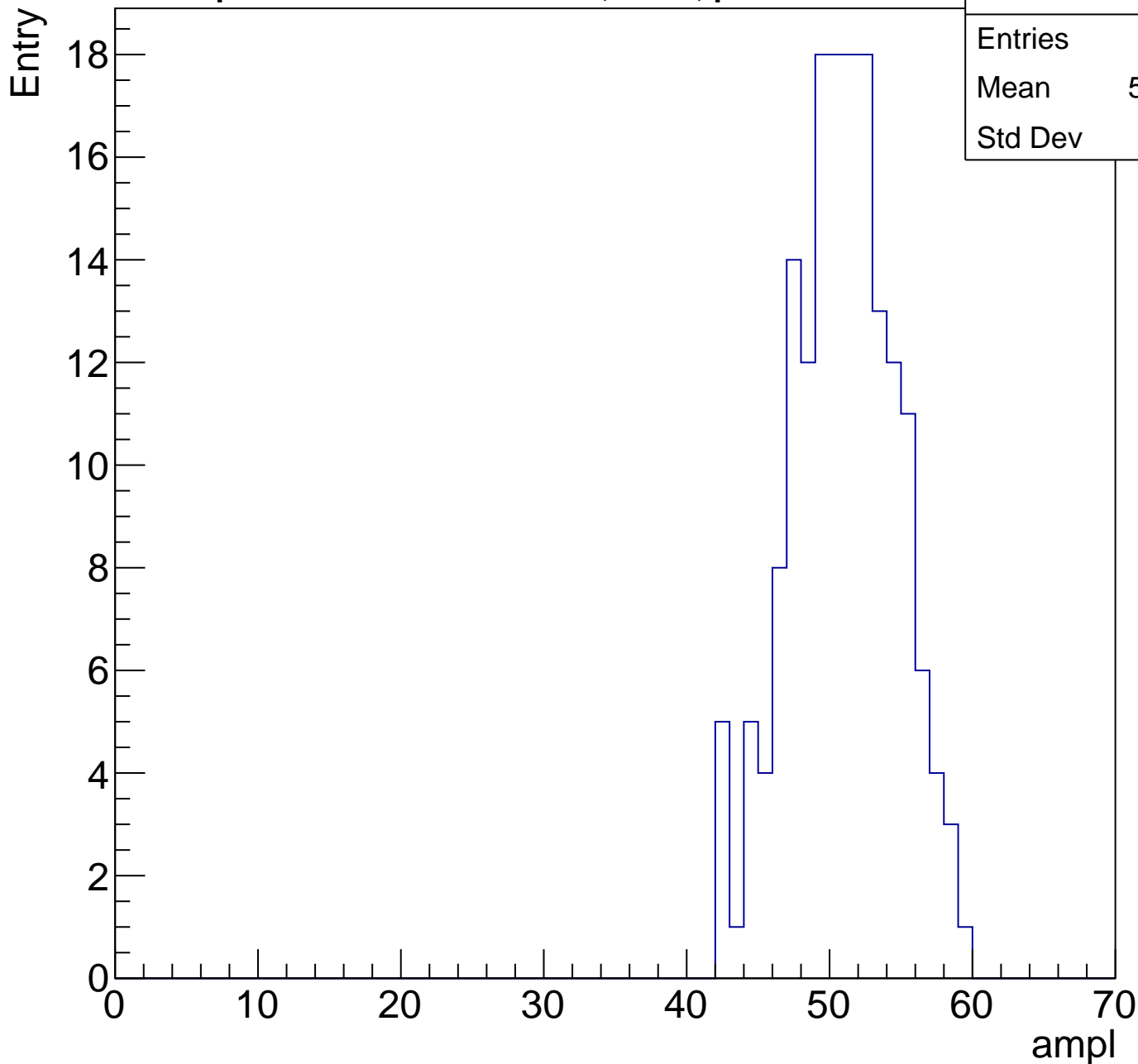
Gaus Width: 3.1609



B1L001S, U25-ch77, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	171
Mean	50.47
Std Dev	3.66



B1L001S, U25-ch77, adc4

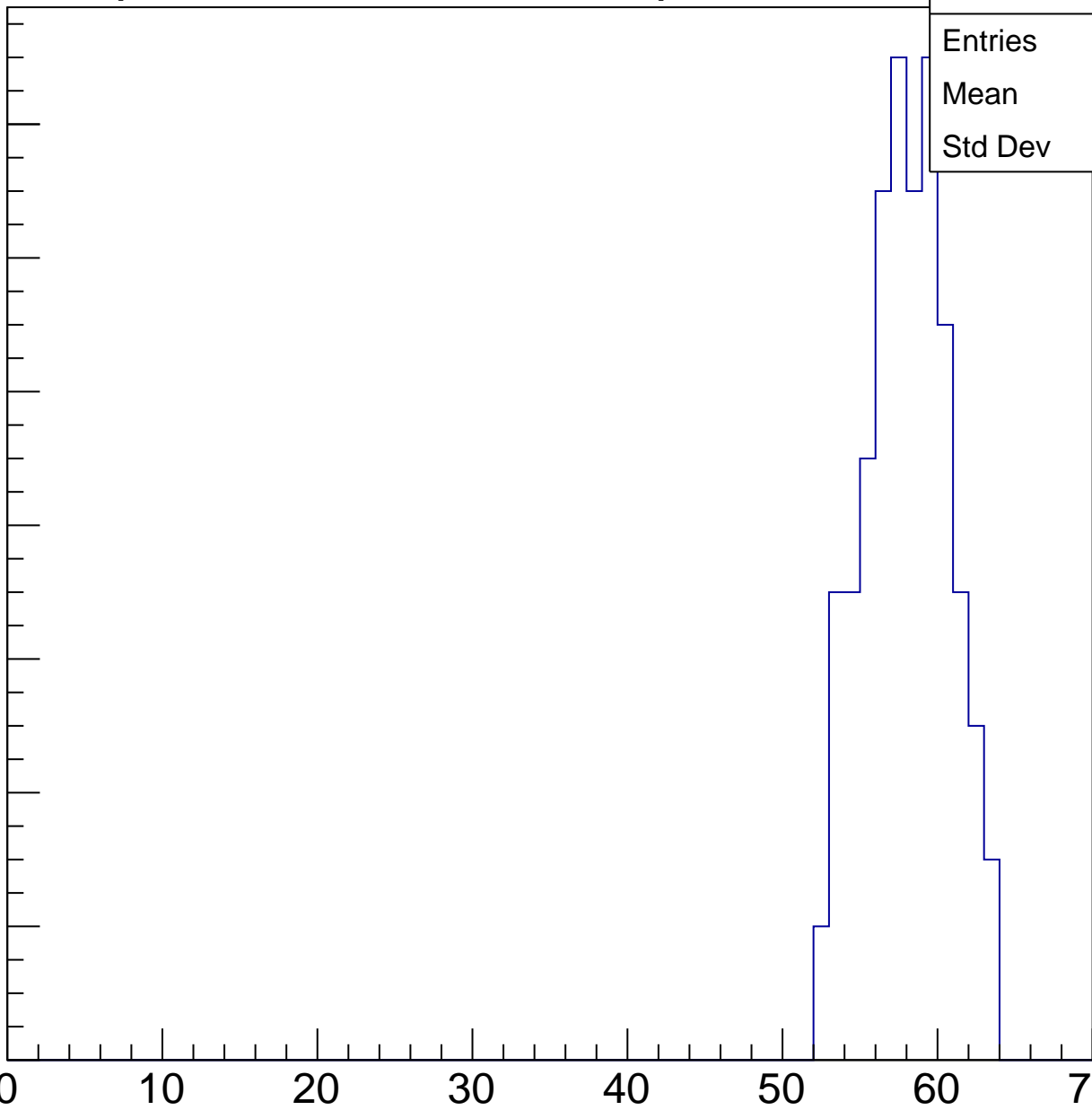
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	57.53
Std Dev	2.666

ampl



B1L001S, U25-ch77, adc5

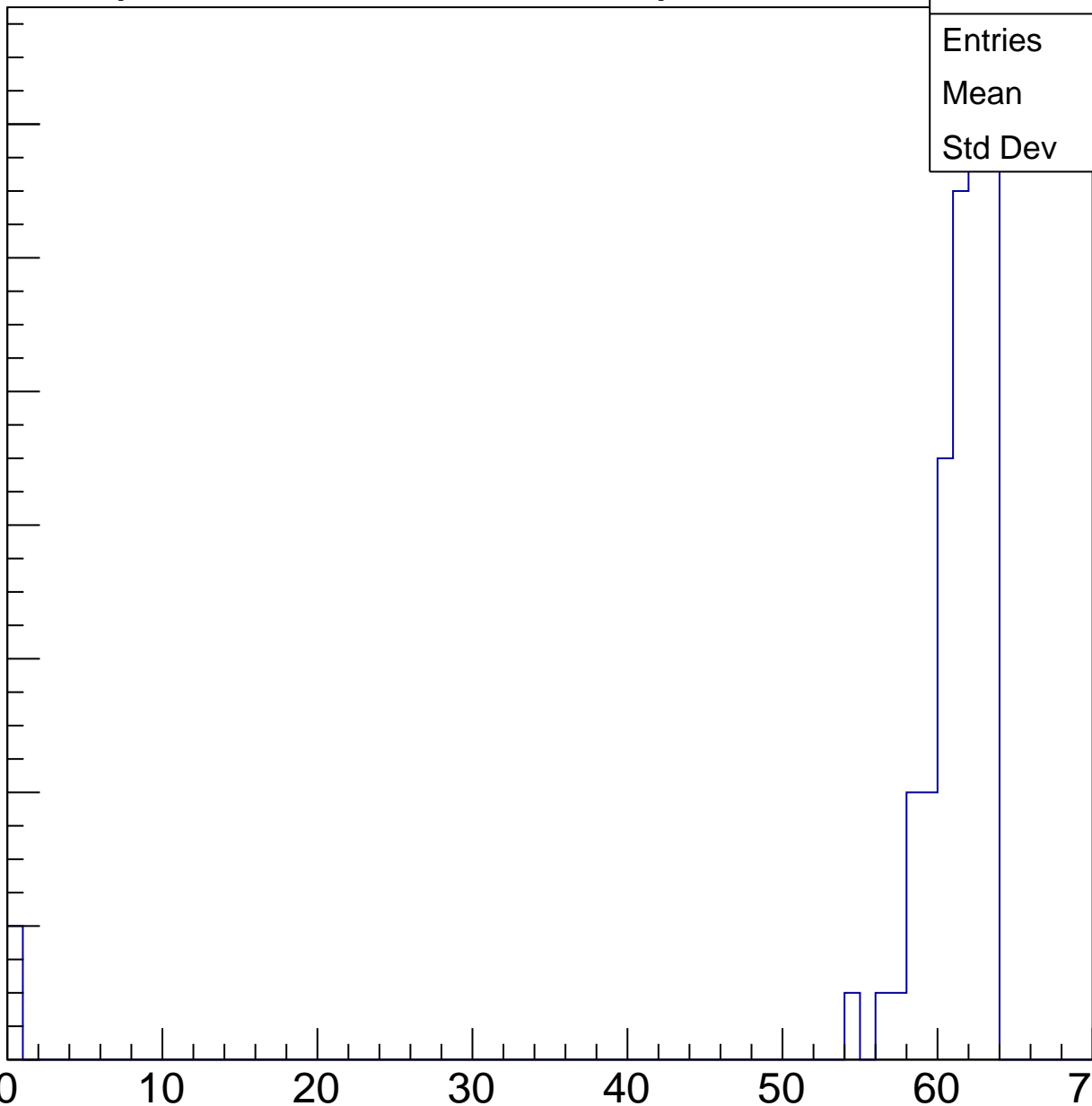
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	59.12
Std Dev	10.7

ampl



B1L001S, U25-ch77, adc6

calib_packv5_042523_0143.root, FC#2, port C2

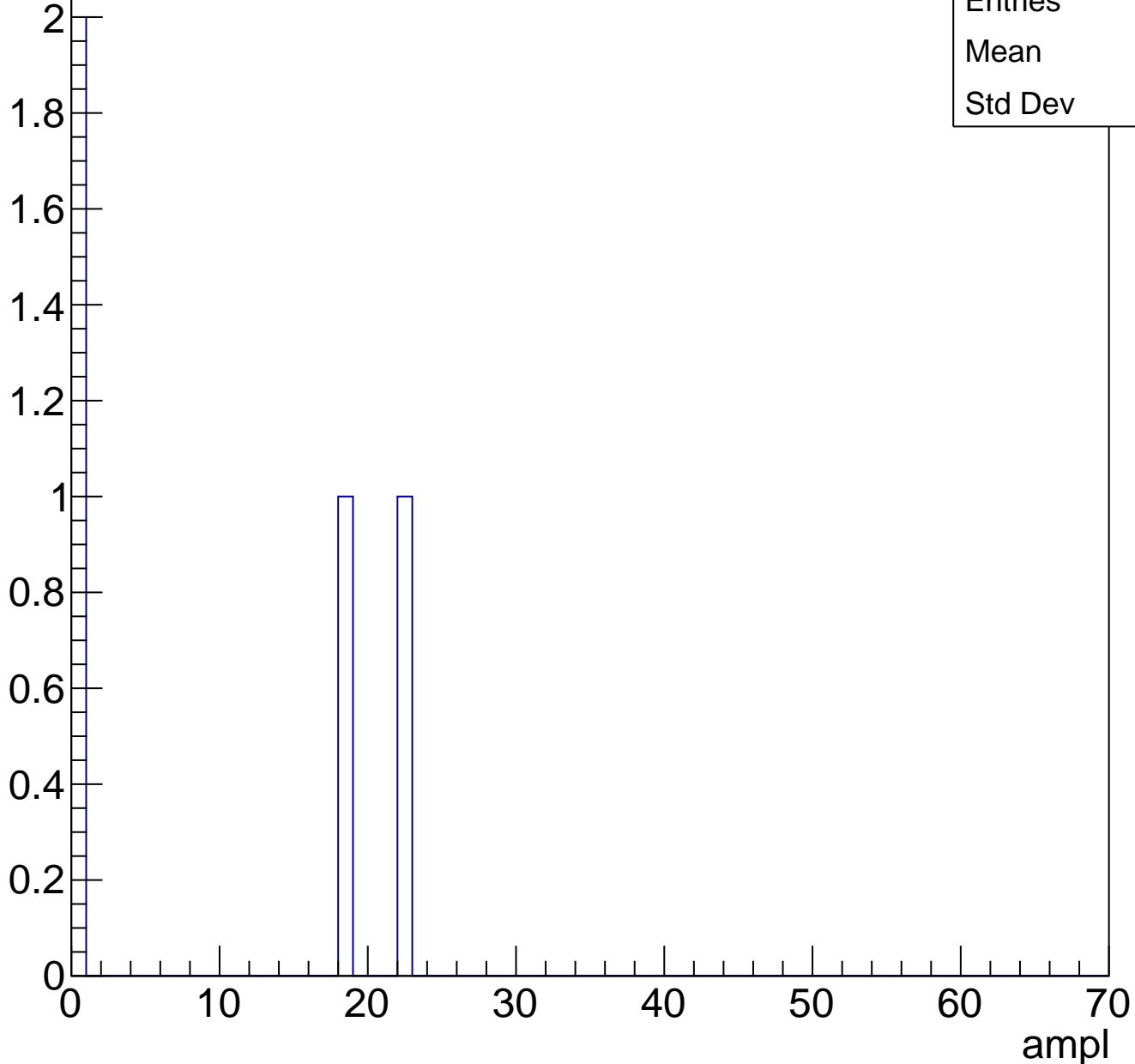
Entry



B1L001S, U25-ch77, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	10
Std Dev	10.1

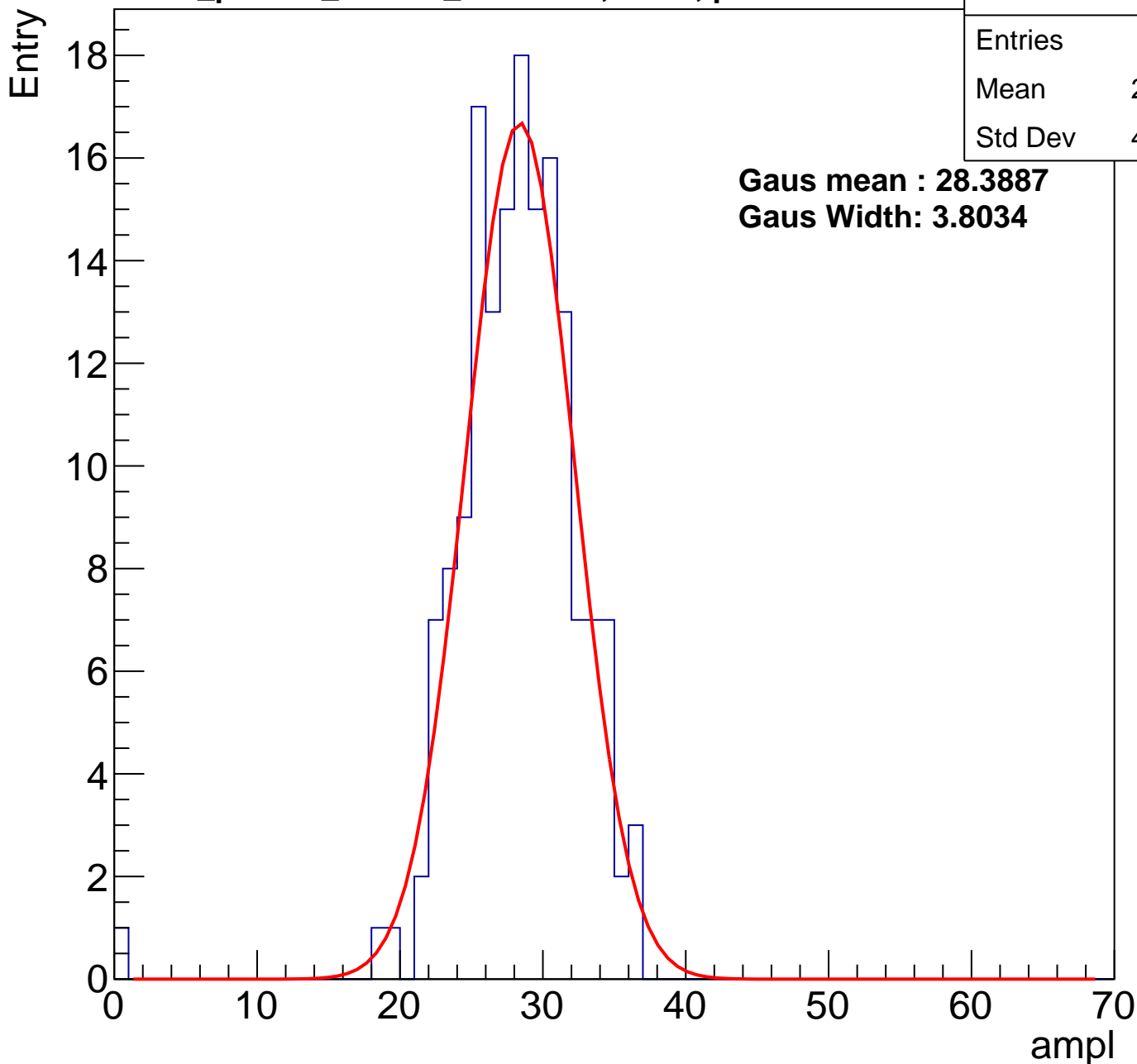
B1L001S, U25-ch78, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	27.74
Std Dev	4.226

Gaus mean : 28.3887

Gaus Width: 3.8034



B1L001S, U25-ch78, adc1

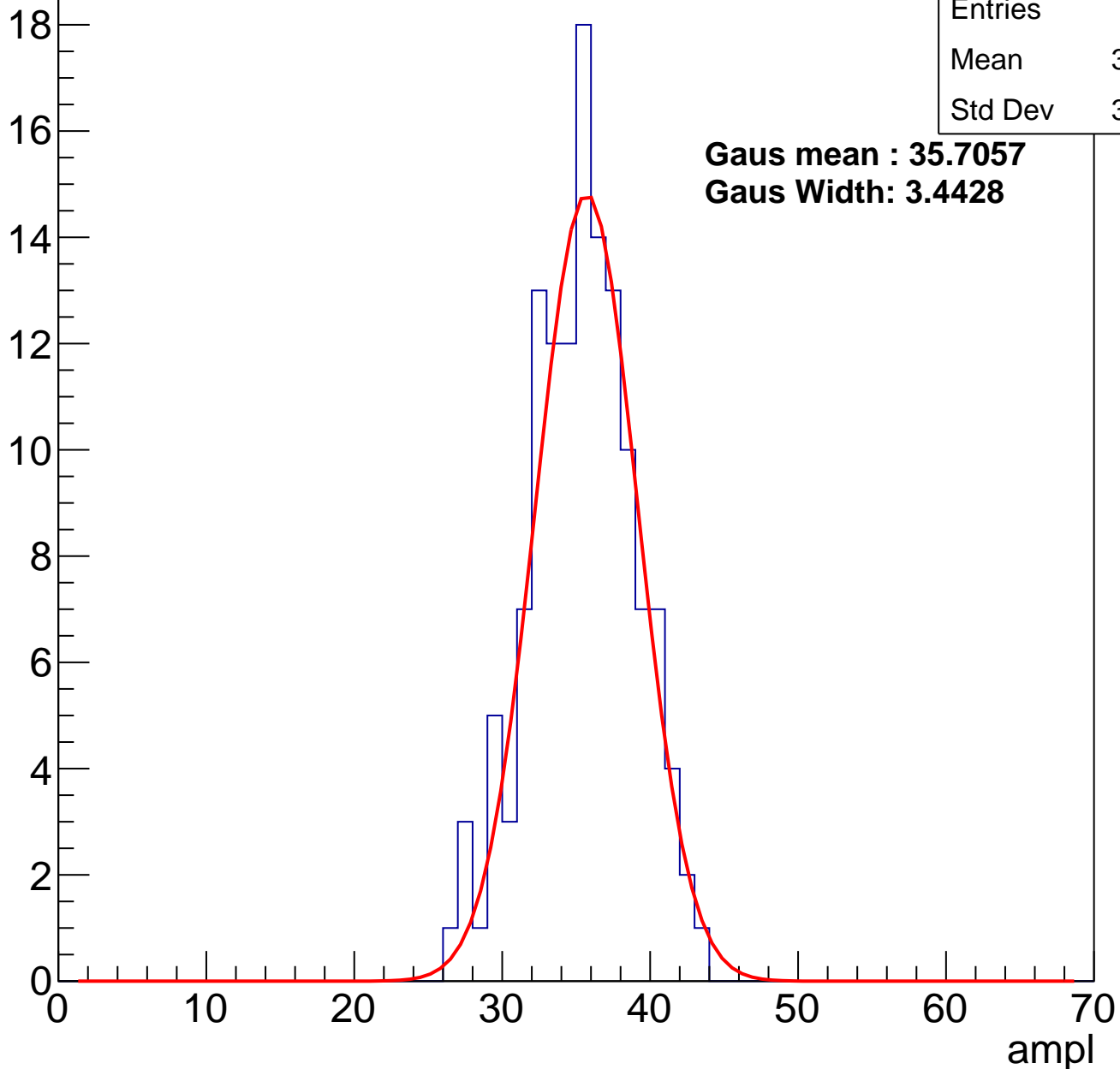
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	34.93
Std Dev	3.484

Gaus mean : 35.7057

Gaus Width: 3.4428

Entry



B1L001S, U25-ch78, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

126

Mean

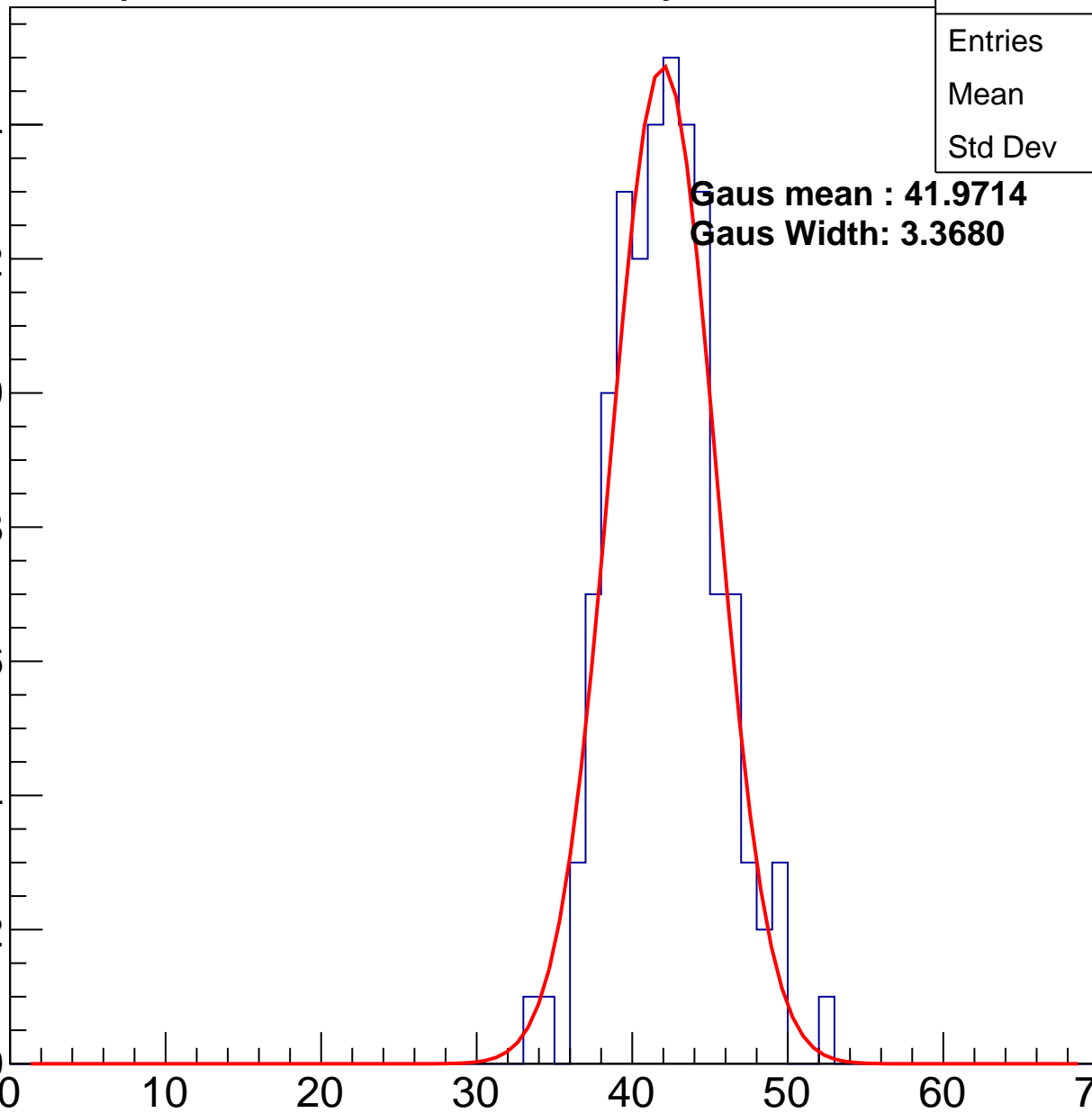
41.68

Std Dev

3.321

Gaus mean : 41.9714

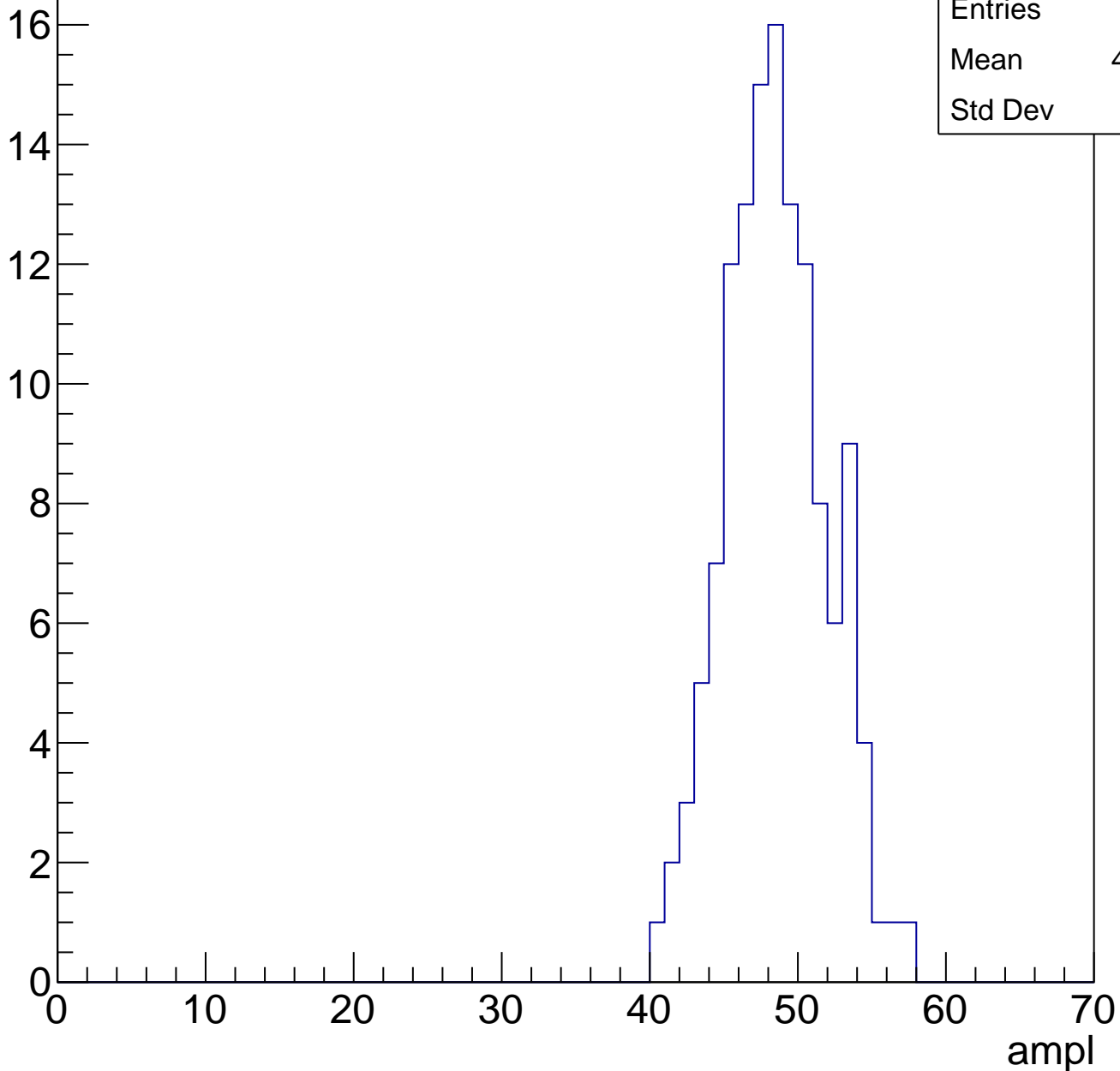
Gaus Width: 3.3680



B1L001S, U25-ch78, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch78, adc4

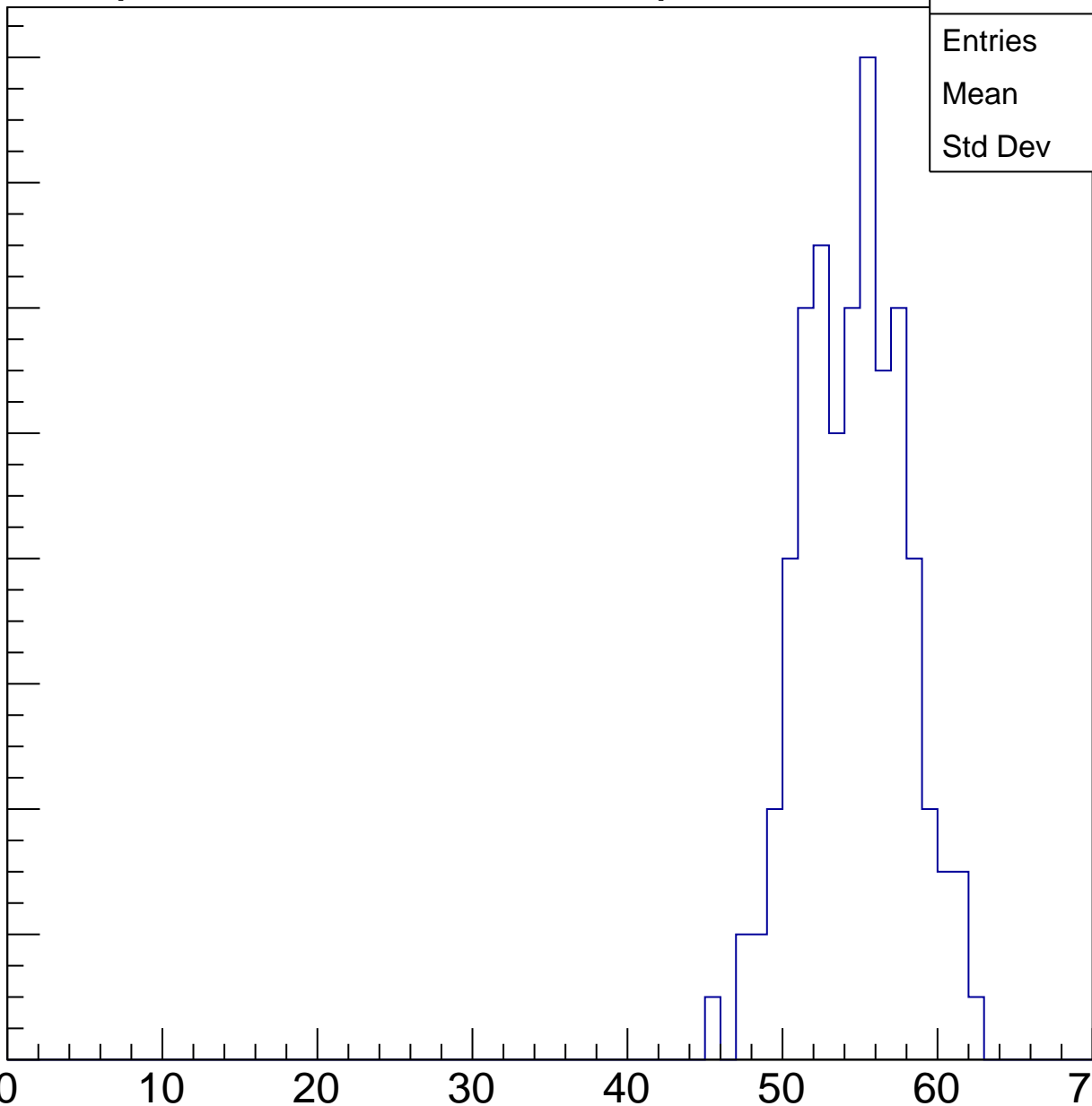
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	54.11
Std Dev	3.342

ampl



B1L001S, U25-ch78, adc5

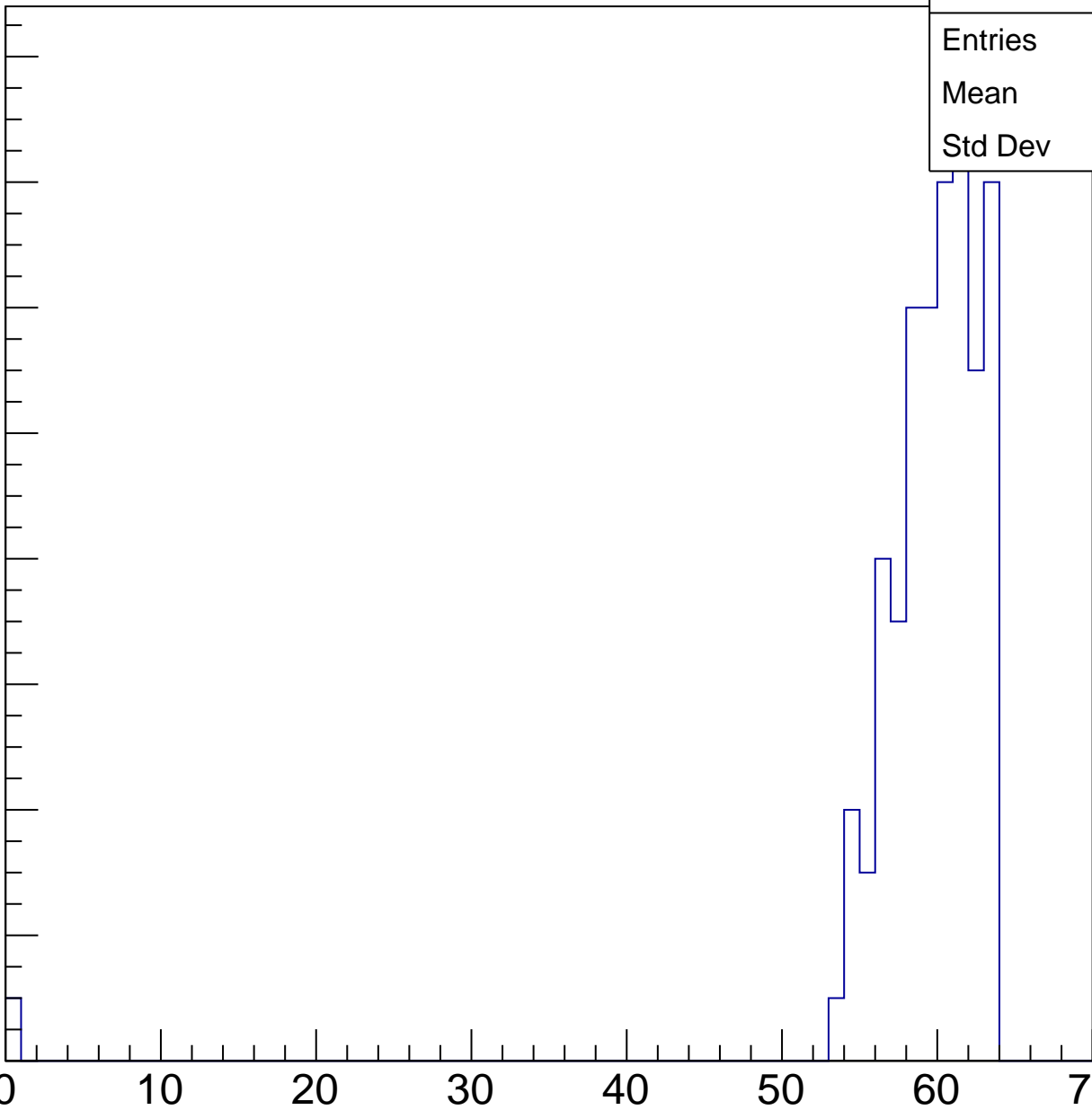
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.88
Std Dev	6.368

ampl

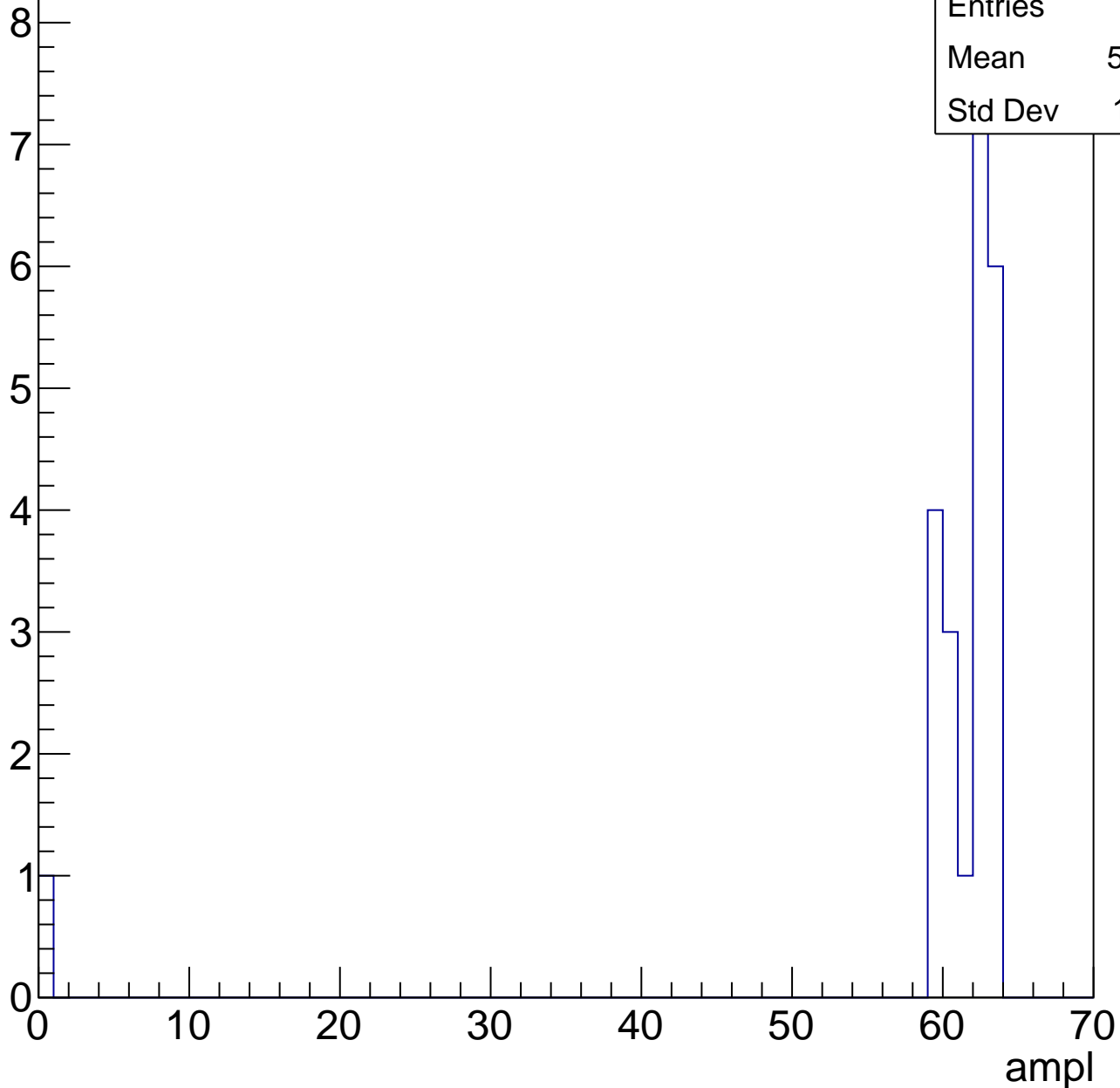


B1L001S, U25-ch78, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	23
Mean	58.74
Std Dev	12.61



B1L001S, U25-ch78, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch79, adc0

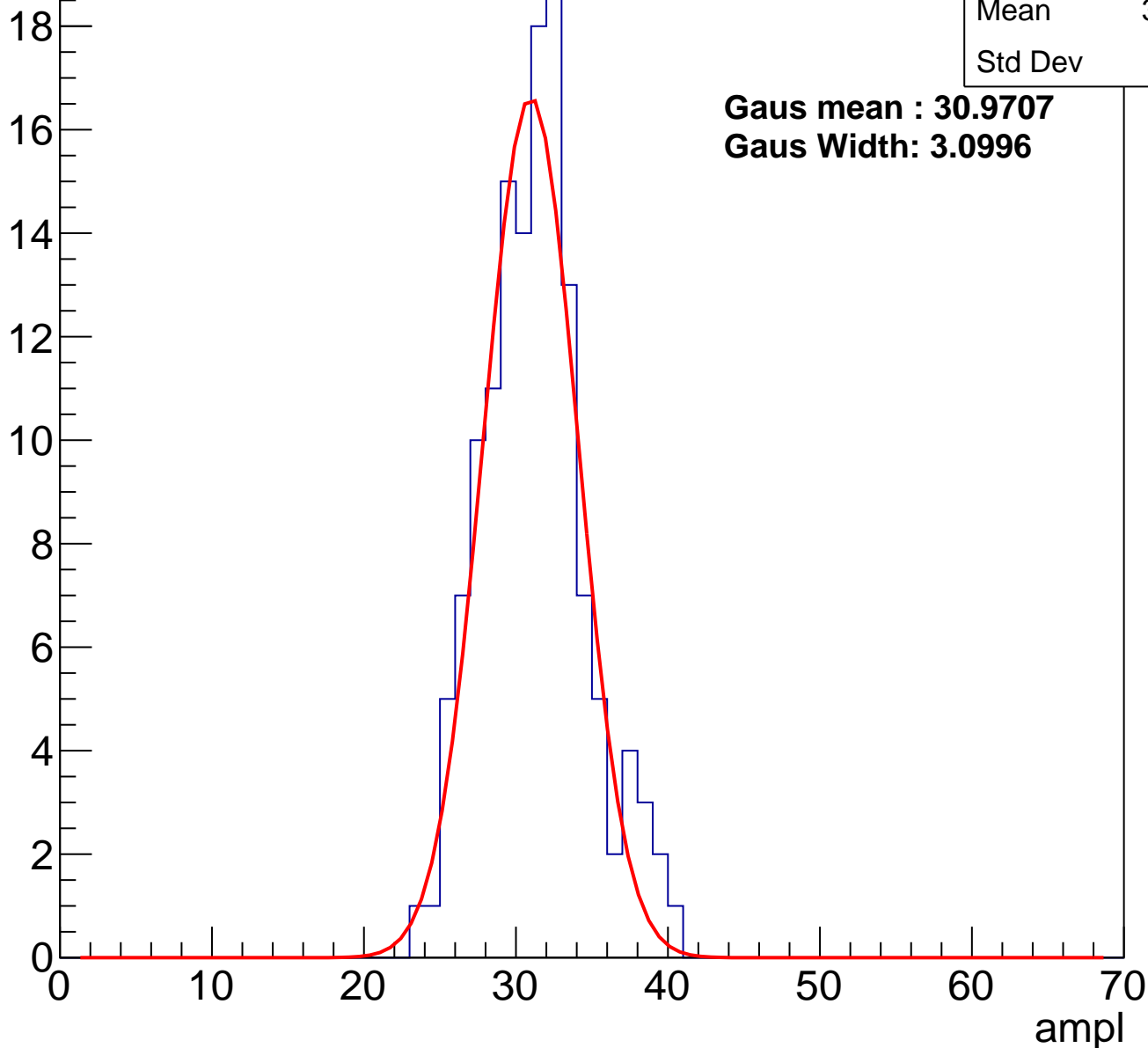
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	30.78
Std Dev	3.36

Gaus mean : 30.9707

Gaus Width: 3.0996

Entry

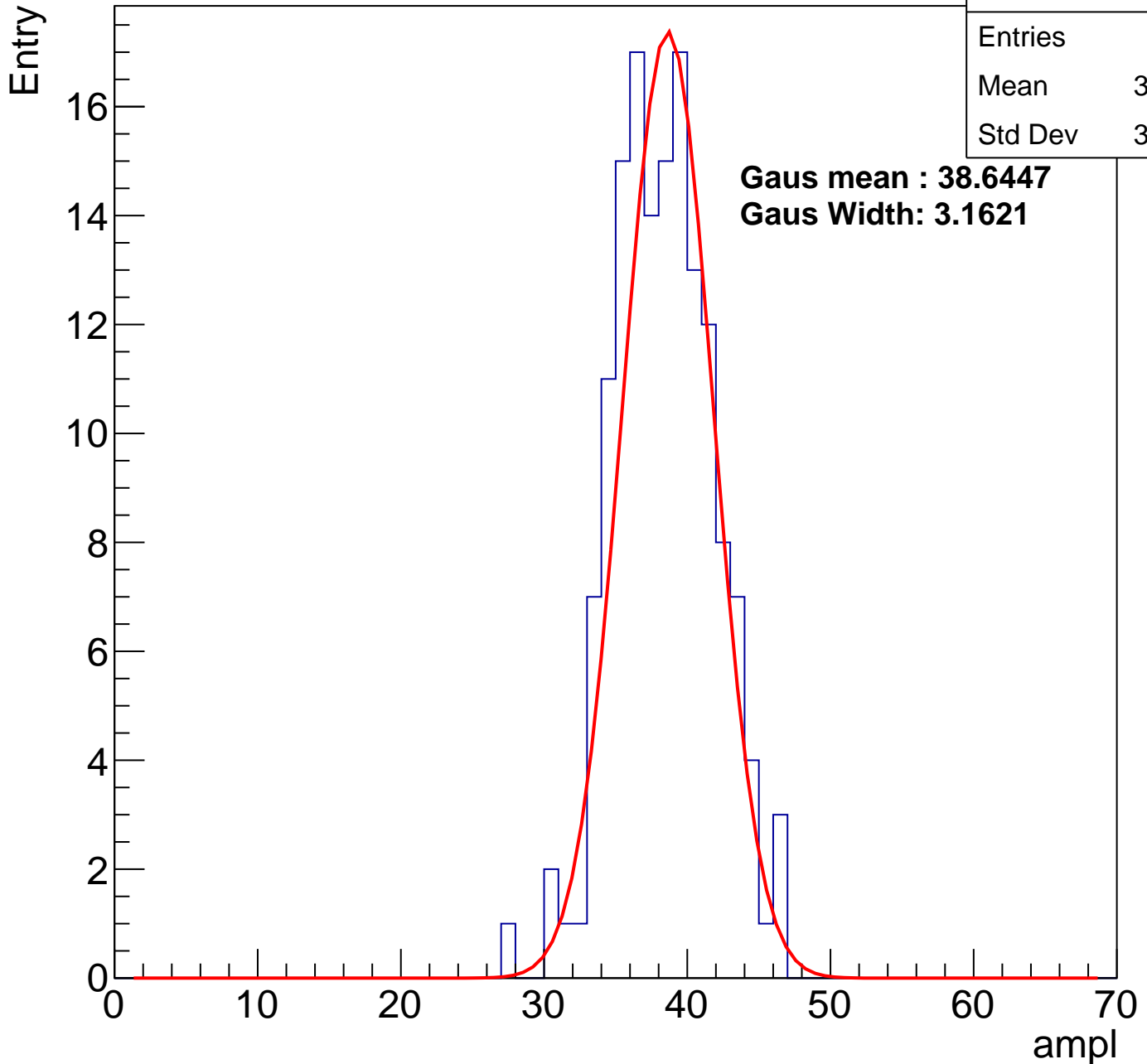


B1L001S, U25-ch79, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	37.93
Std Dev	3.443

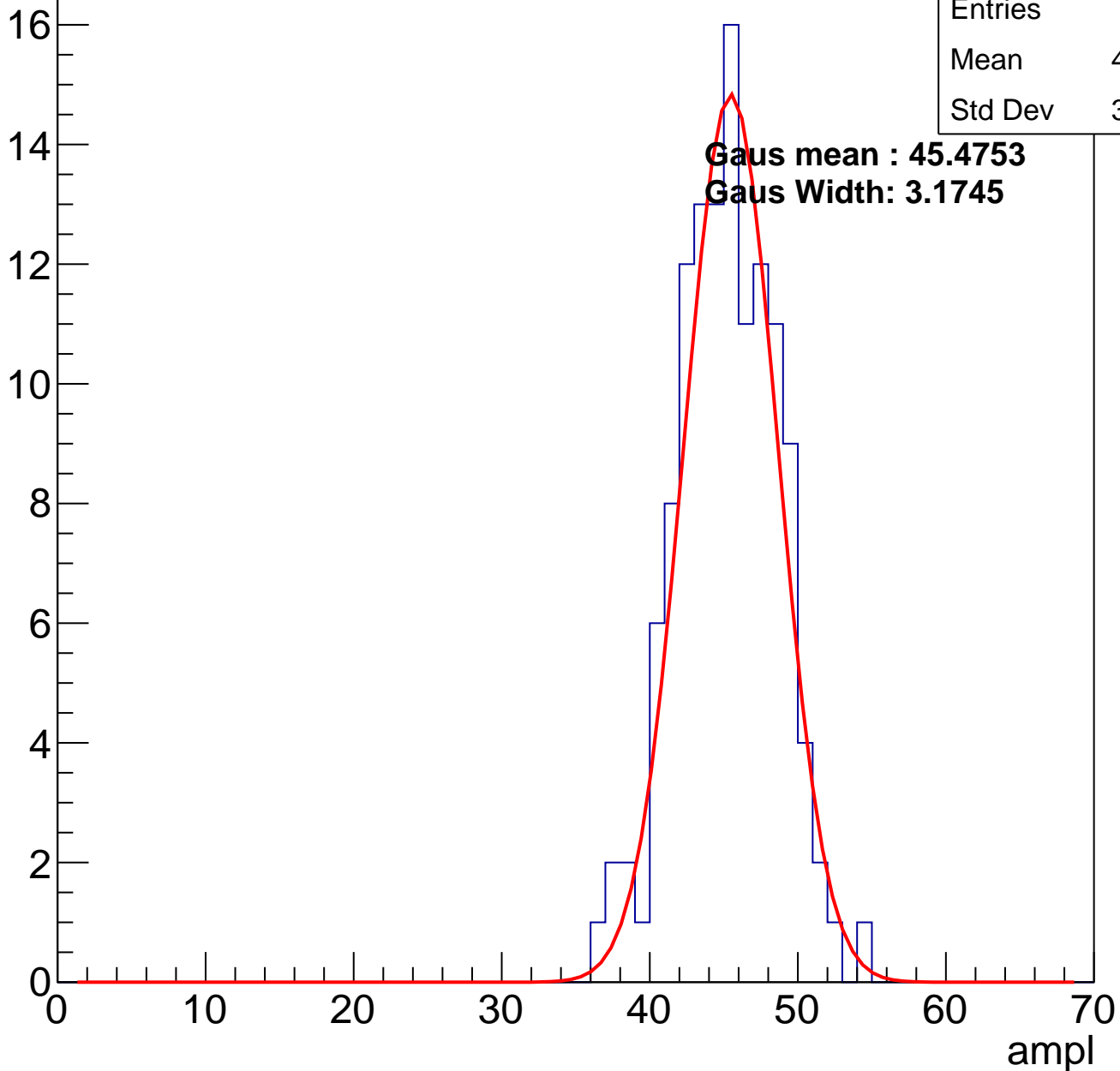
Gaus mean : 38.6447
Gaus Width: 3.1621



B1L001S, U25-ch79, adc2

calib_packv5_042523_0143.root, FC#2, port C2

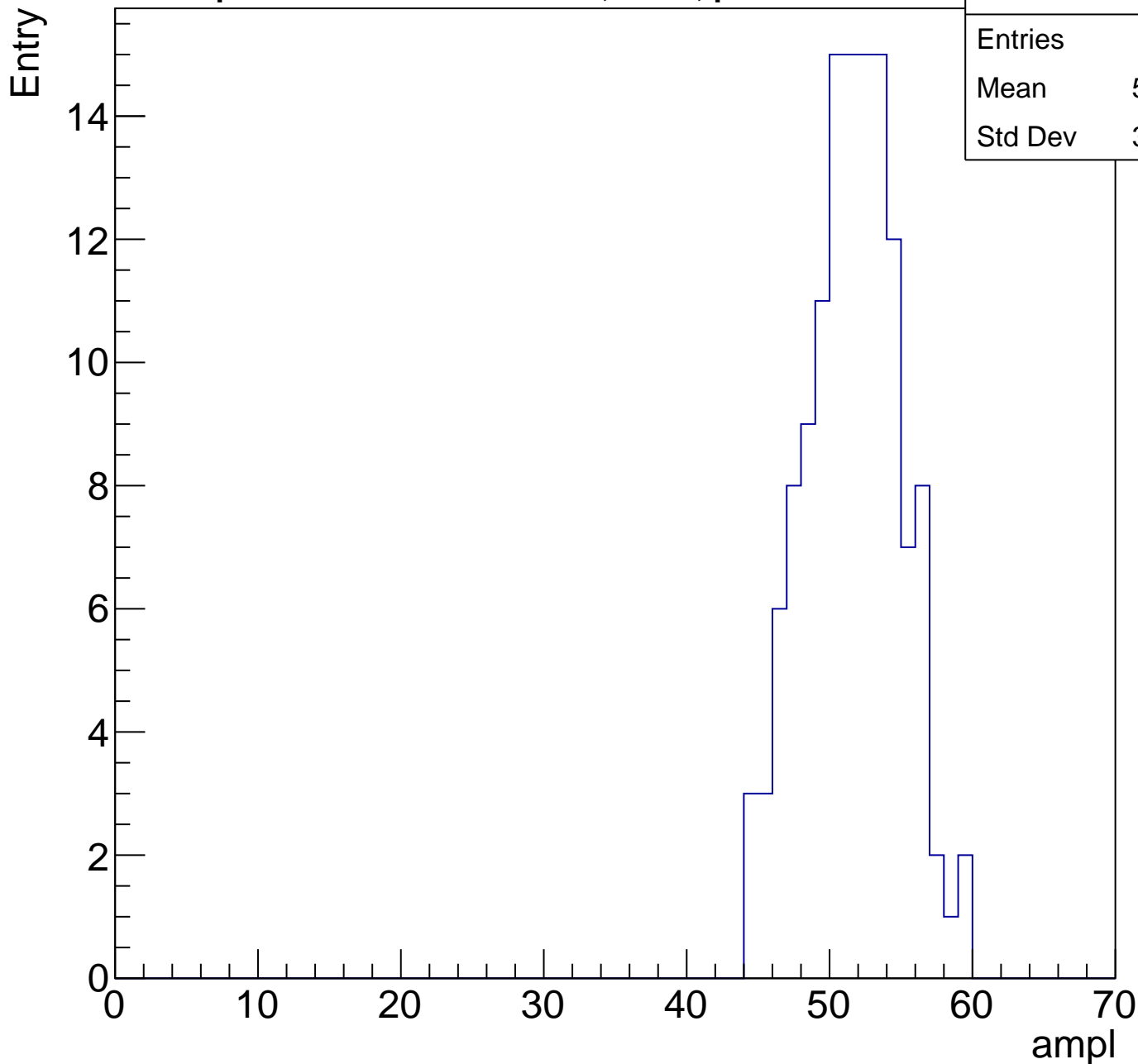
Entry



B1L001S, U25-ch79, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	51.14
Std Dev	3.273

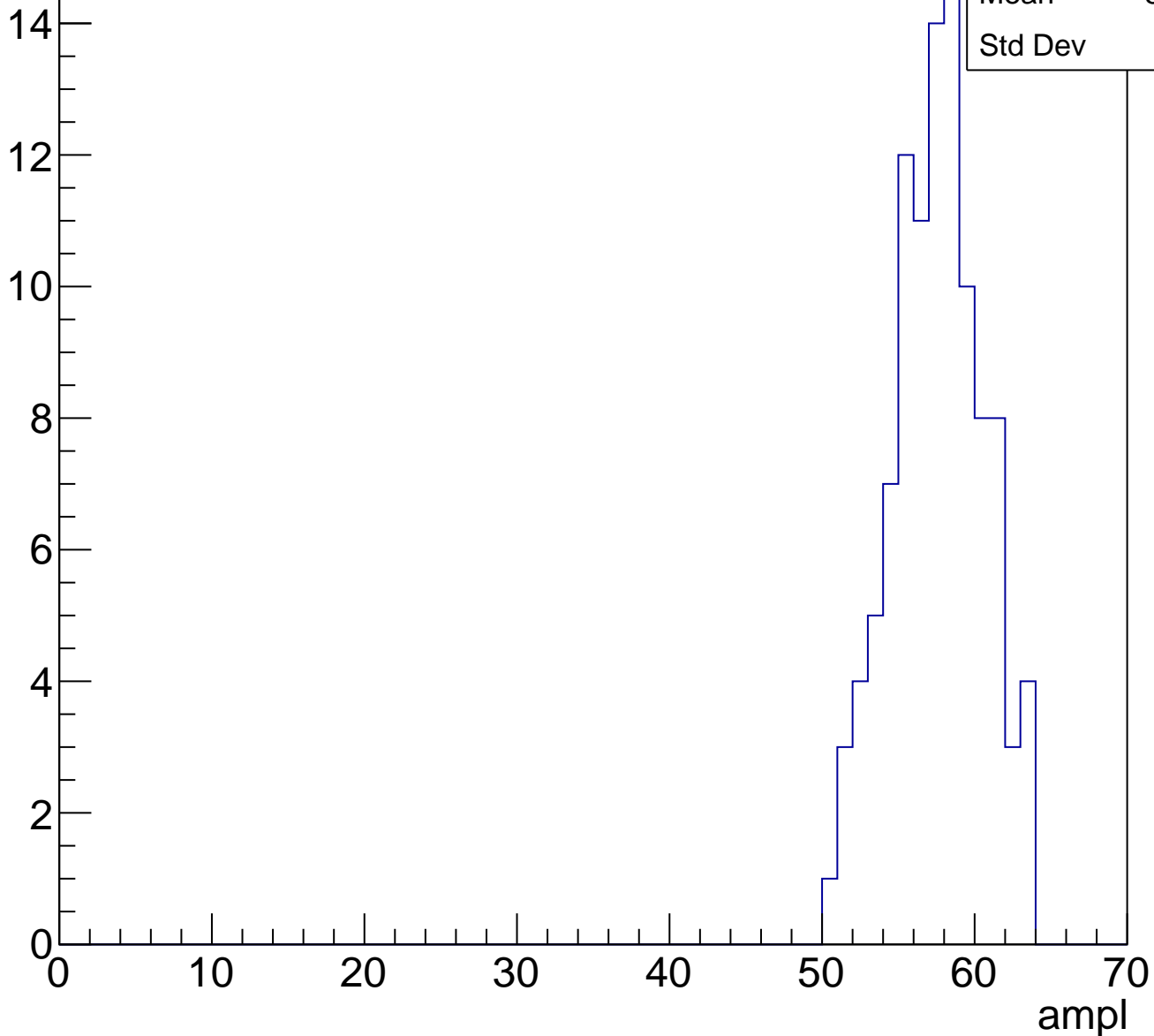


B1L001S, U25-ch79, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	57.09
Std Dev	2.98

Entry



B1L001S, U25-ch79, adc5

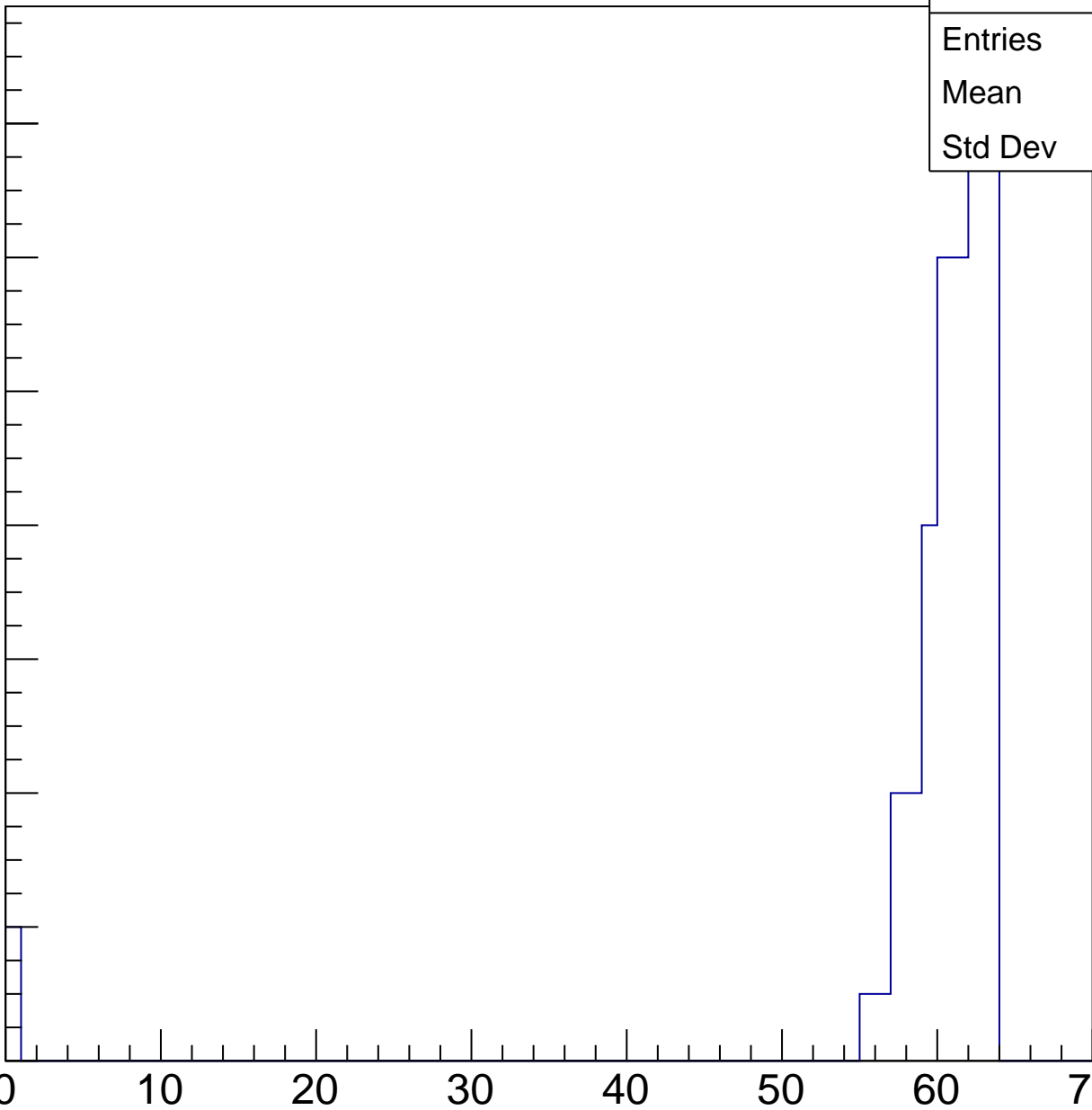
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	59
Std Dev	10.09

ampl



B1L001S, U25-ch79, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch79, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch80, adc0

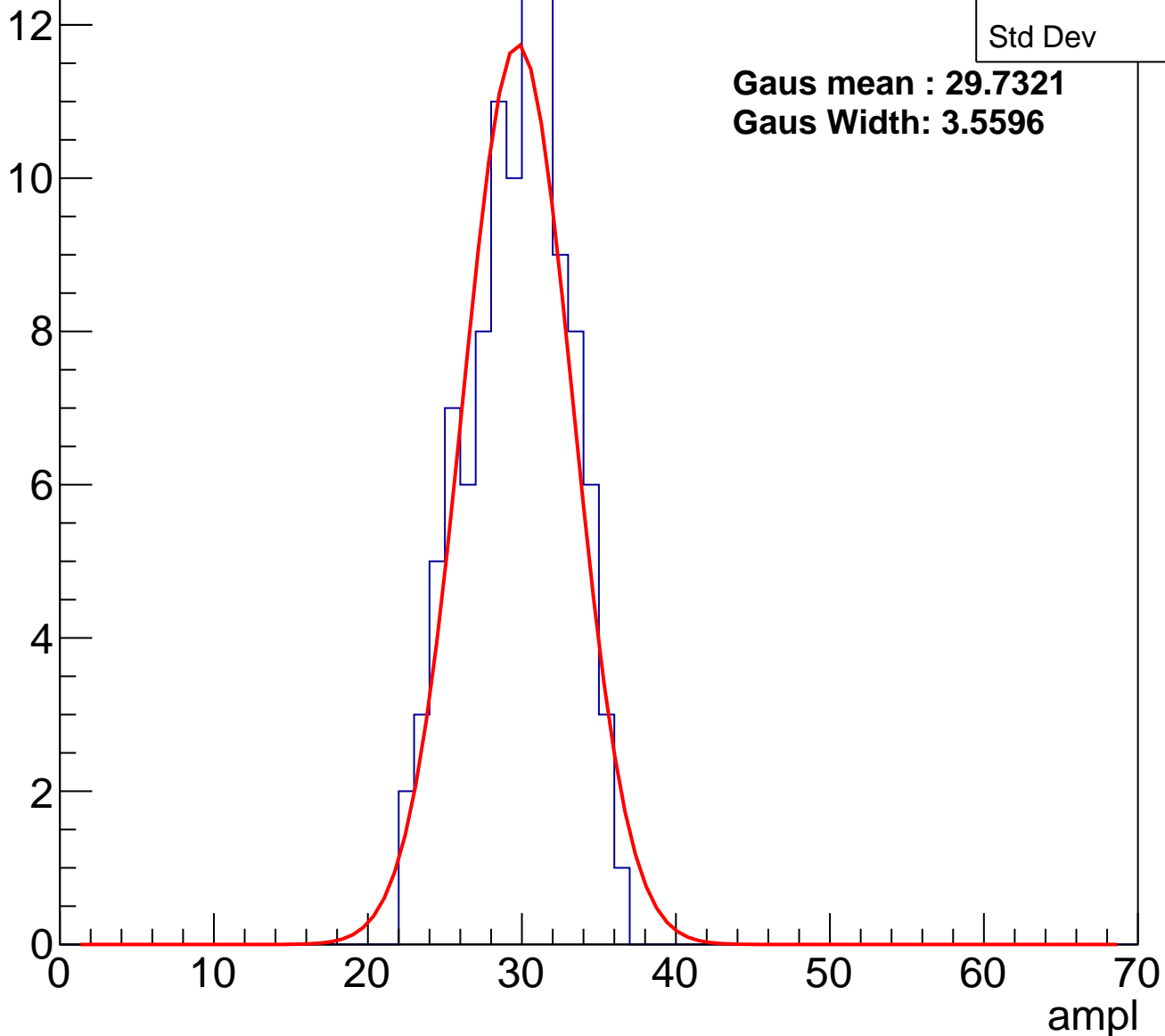
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	29.22
Std Dev	3.26

Gaus mean : 29.7321

Gaus Width: 3.5596

Entry



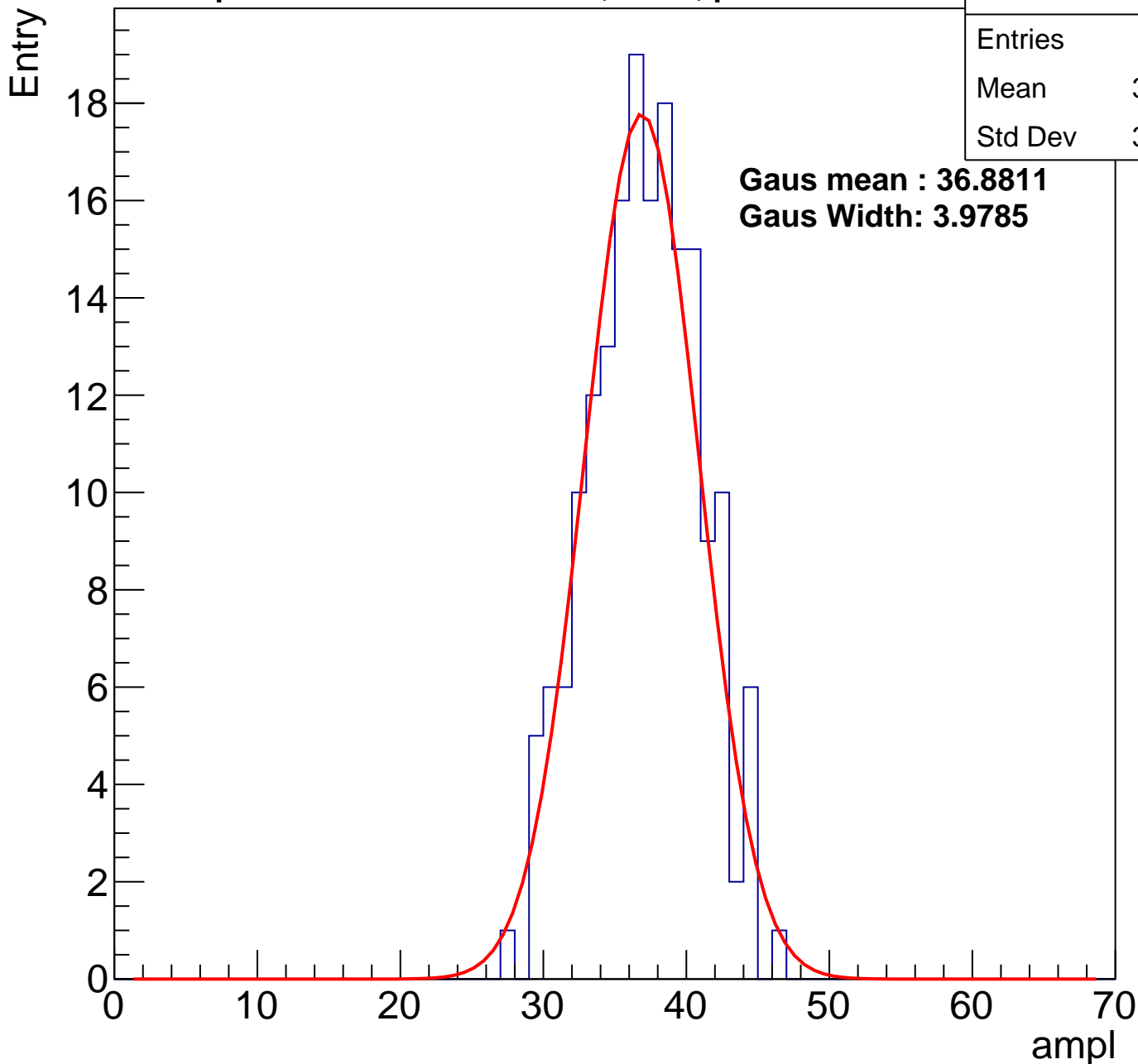
B1L001S, U25-ch80, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	180
Mean	36.59
Std Dev	3.797

Gaus mean : 36.8811

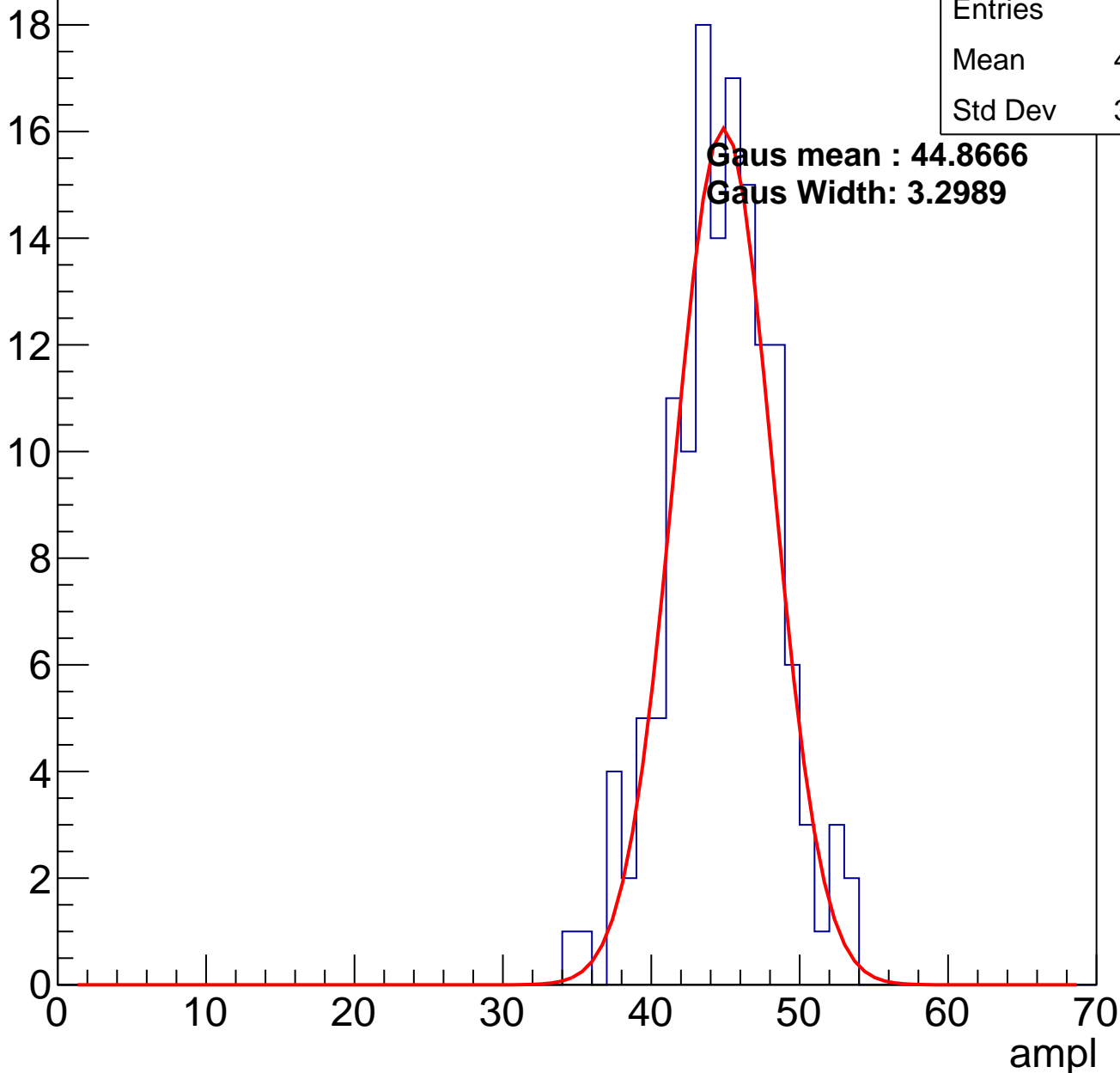
Gaus Width: 3.9785



B1L001S, U25-ch80, adc2

calib_packv5_042523_0143.root, FC#2, port C2

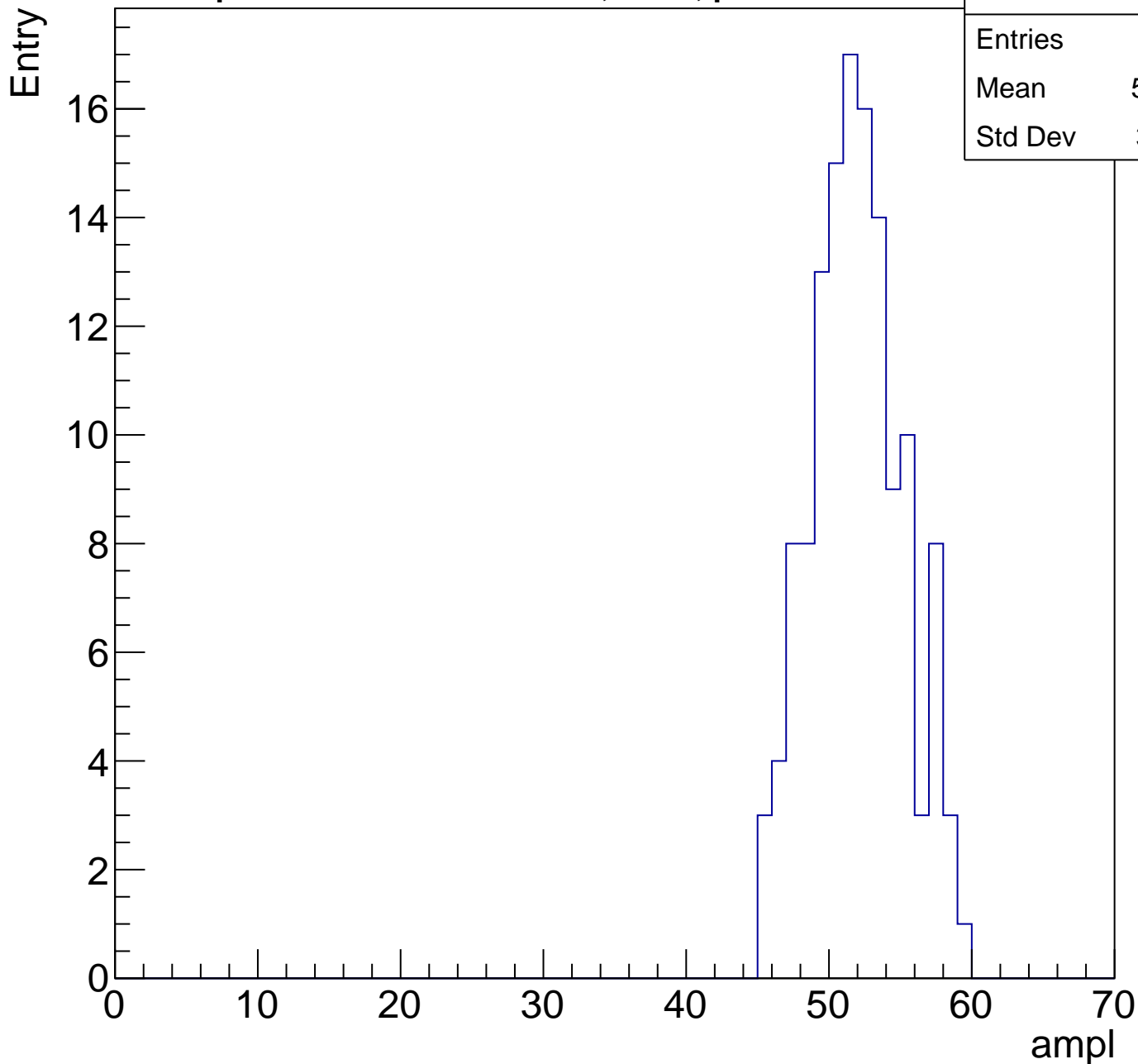
Entry



B1L001S, U25-ch80, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	51.52
Std Dev	3.171



B1L001S, U25-ch80, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	86
Mean	56.88
Std Dev	3.211

Entry

12

10

8

6

4

2

0

0

10

20

30

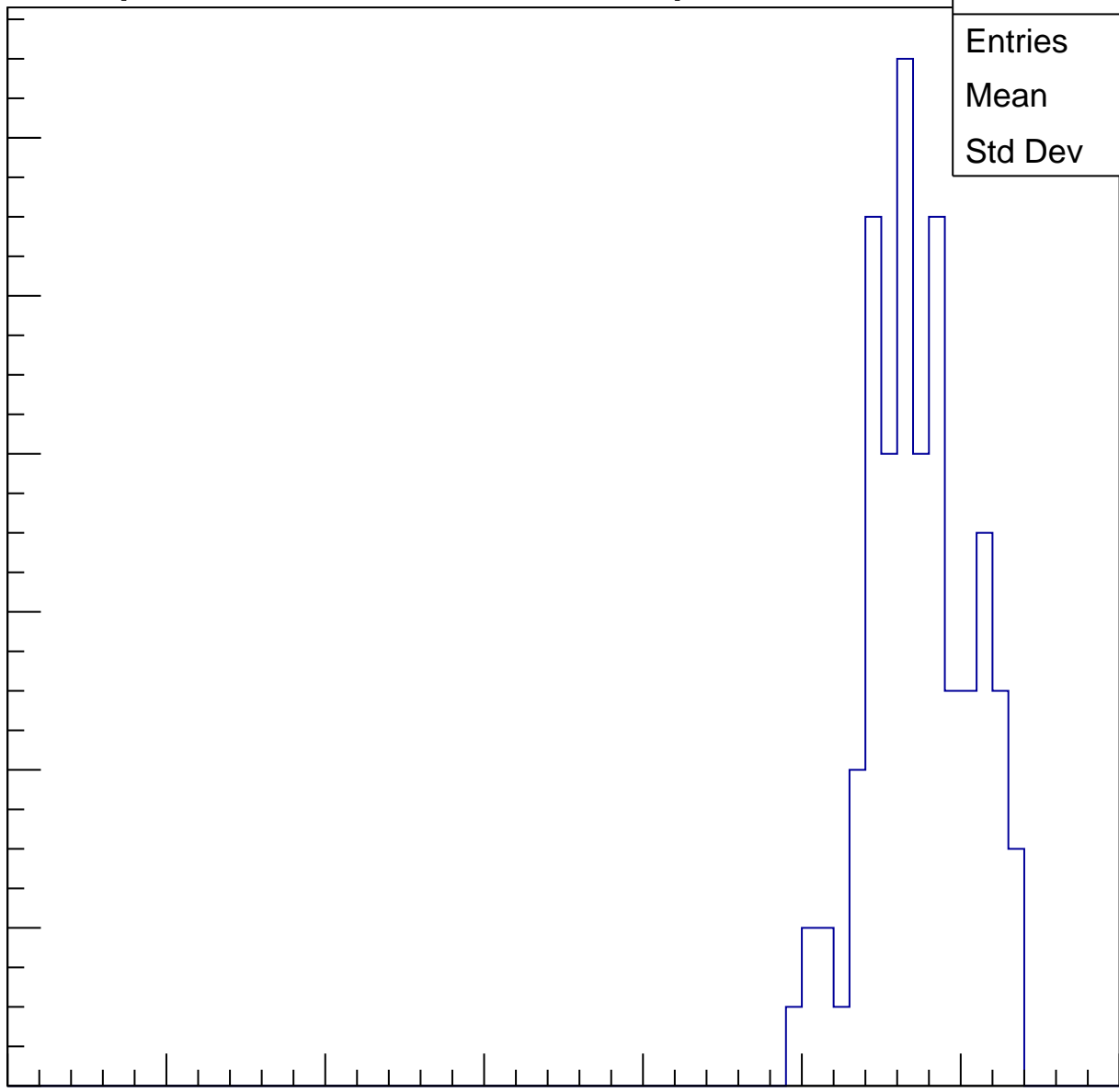
40

50

60

70

ampl



B1L001S, U25-ch80, adc5

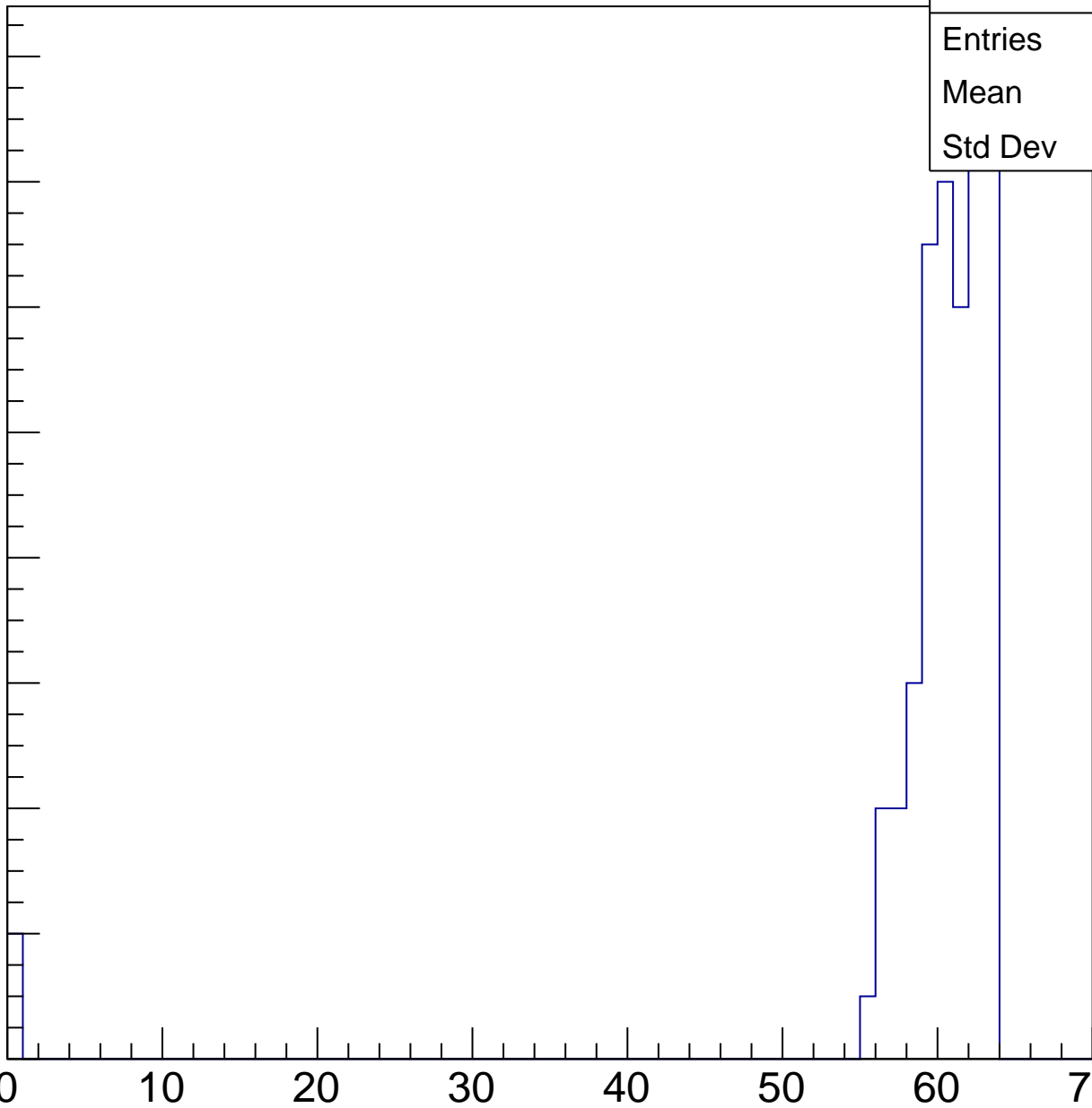
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.99
Std Dev	9.278

ampl



B1L001S, U25-ch80, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

ampl

Entries	5
Mean	59.6
Std Dev	2.653

B1L001S, U25-ch80, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch81, adc0

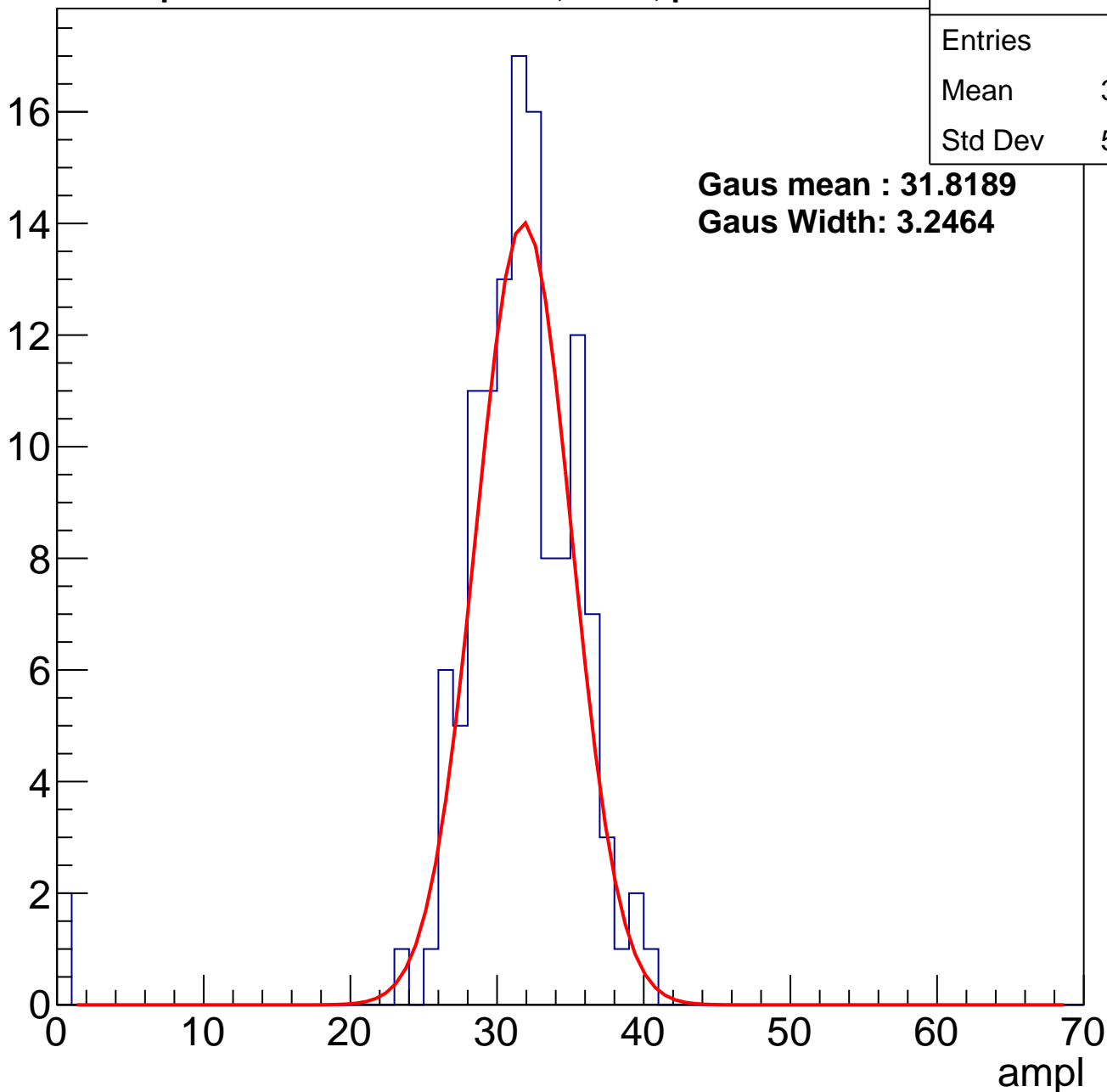
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	30.96
Std Dev	5.107

Gaus mean : 31.8189

Gaus Width: 3.2464

Entry



B1L001S, U25-ch81, adc1

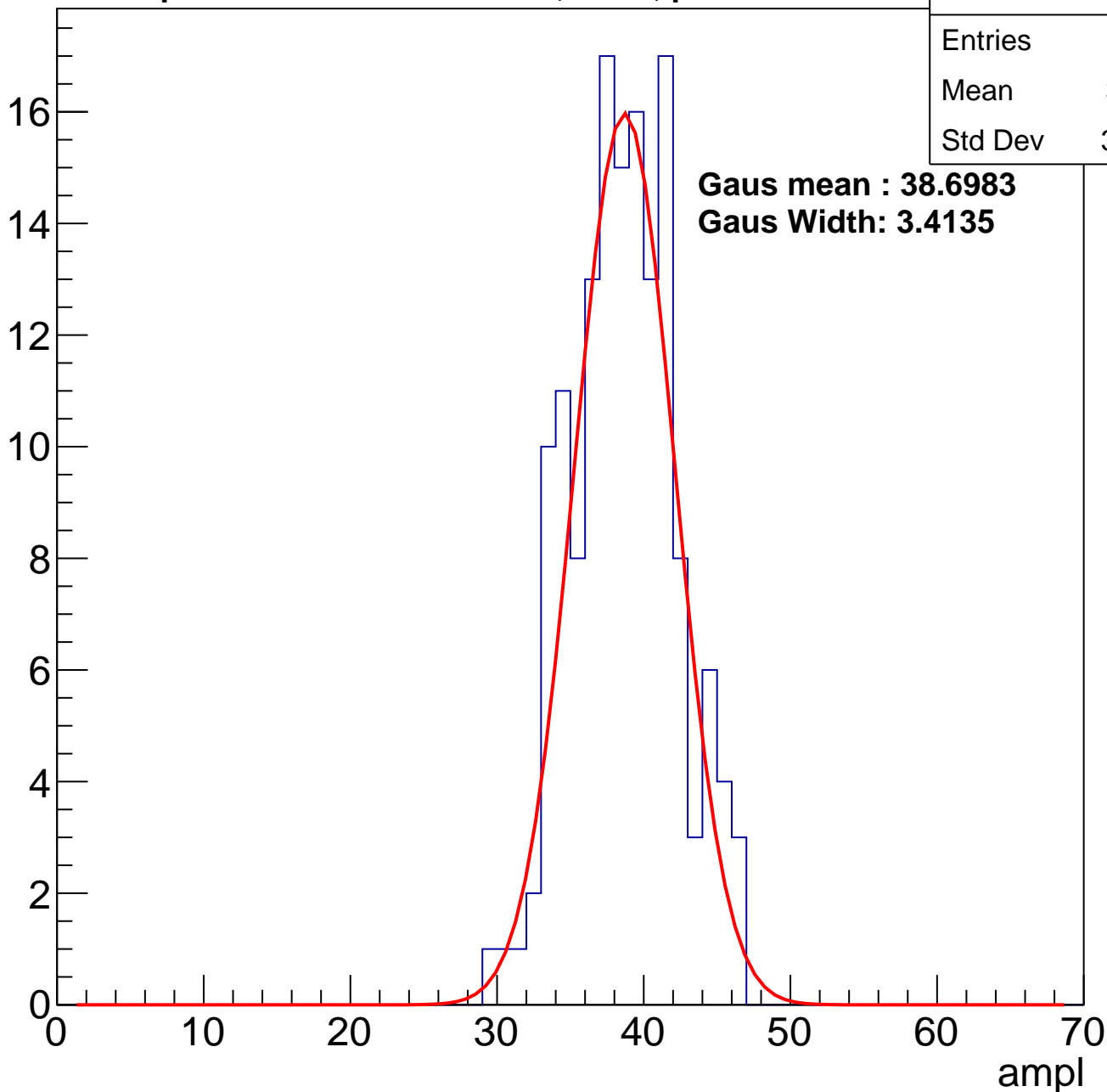
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	38.21
Std Dev	3.528

Gaus mean : 38.6983

Gaus Width: 3.4135

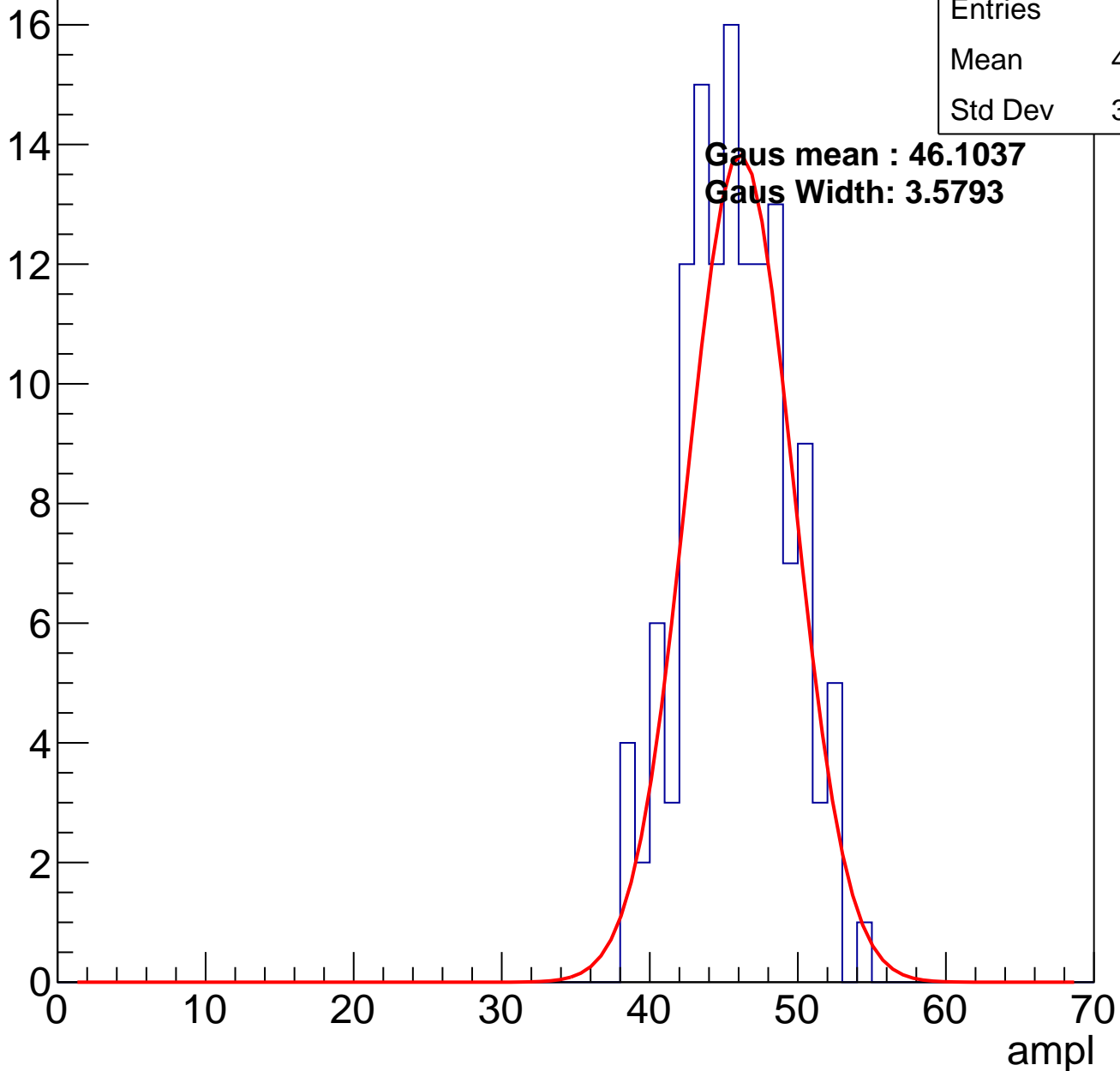
Entry



B1L001S, U25-ch81, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch81, adc3

calib_packv5_042523_0143.root, FC#2, port C2

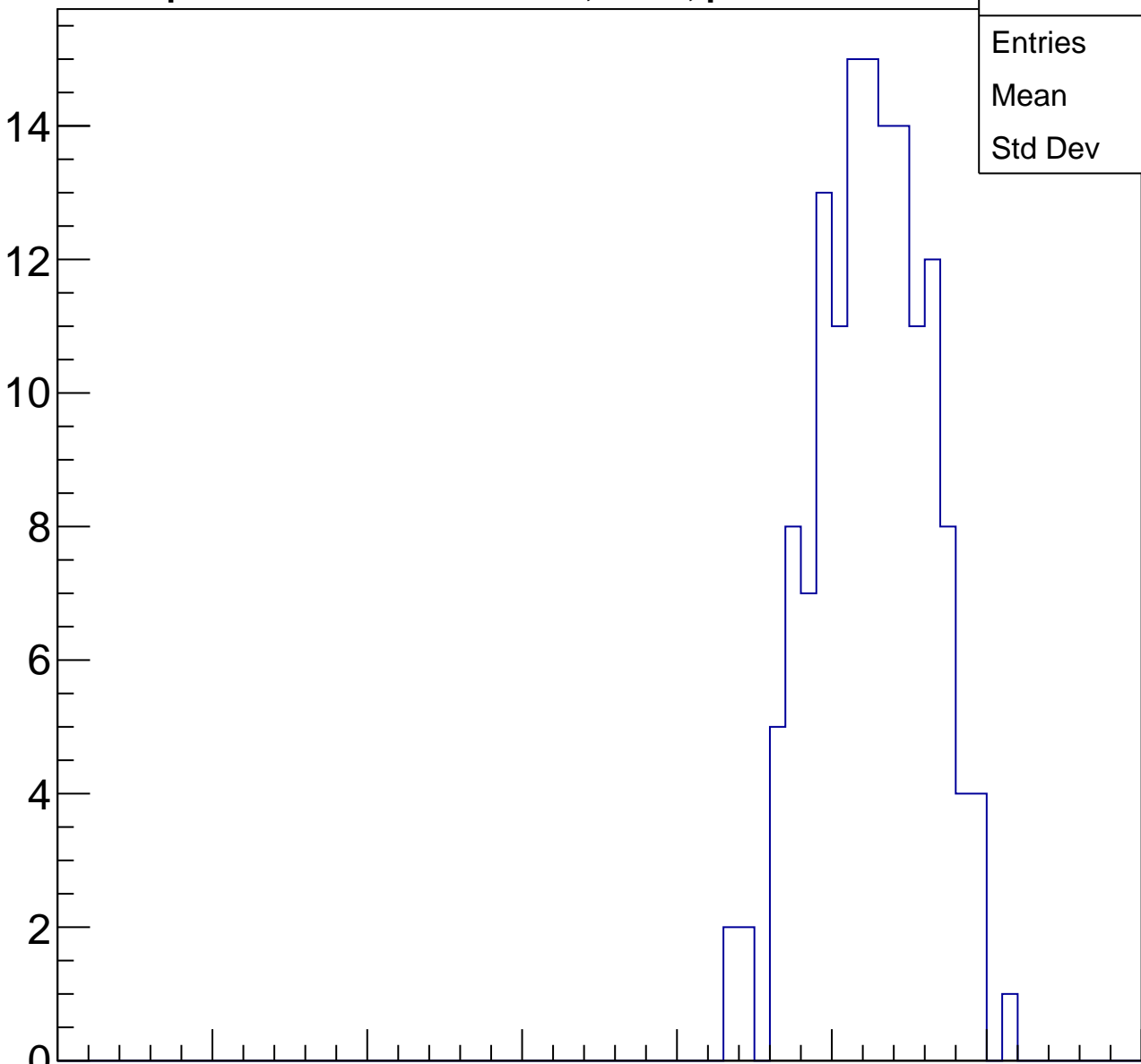
Entries	146
Mean	52.11
Std Dev	3.644

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

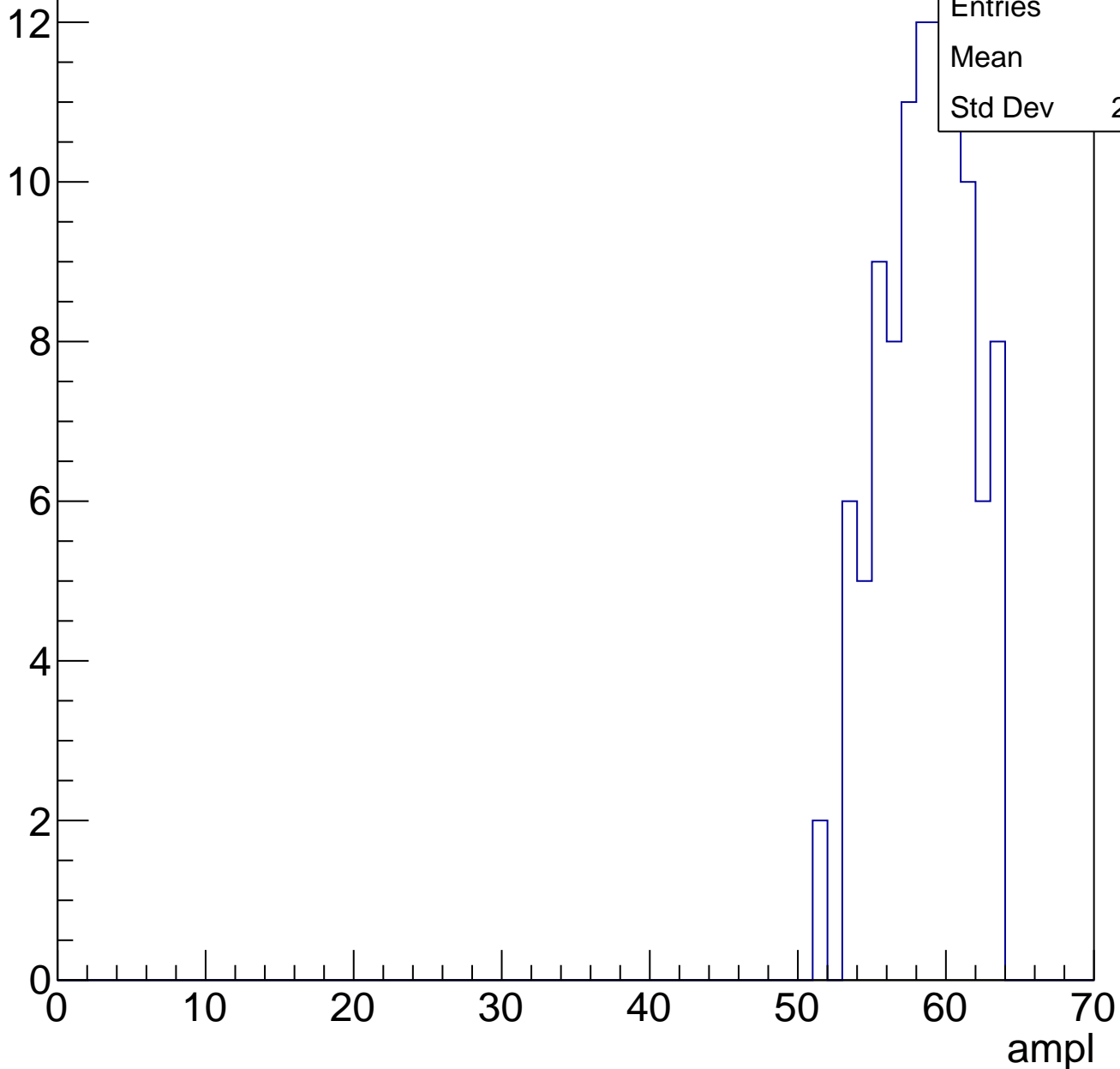


B1L001S, U25-ch81, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	58.1
Std Dev	2.988

Entry



B1L001S, U25-ch81, adc5

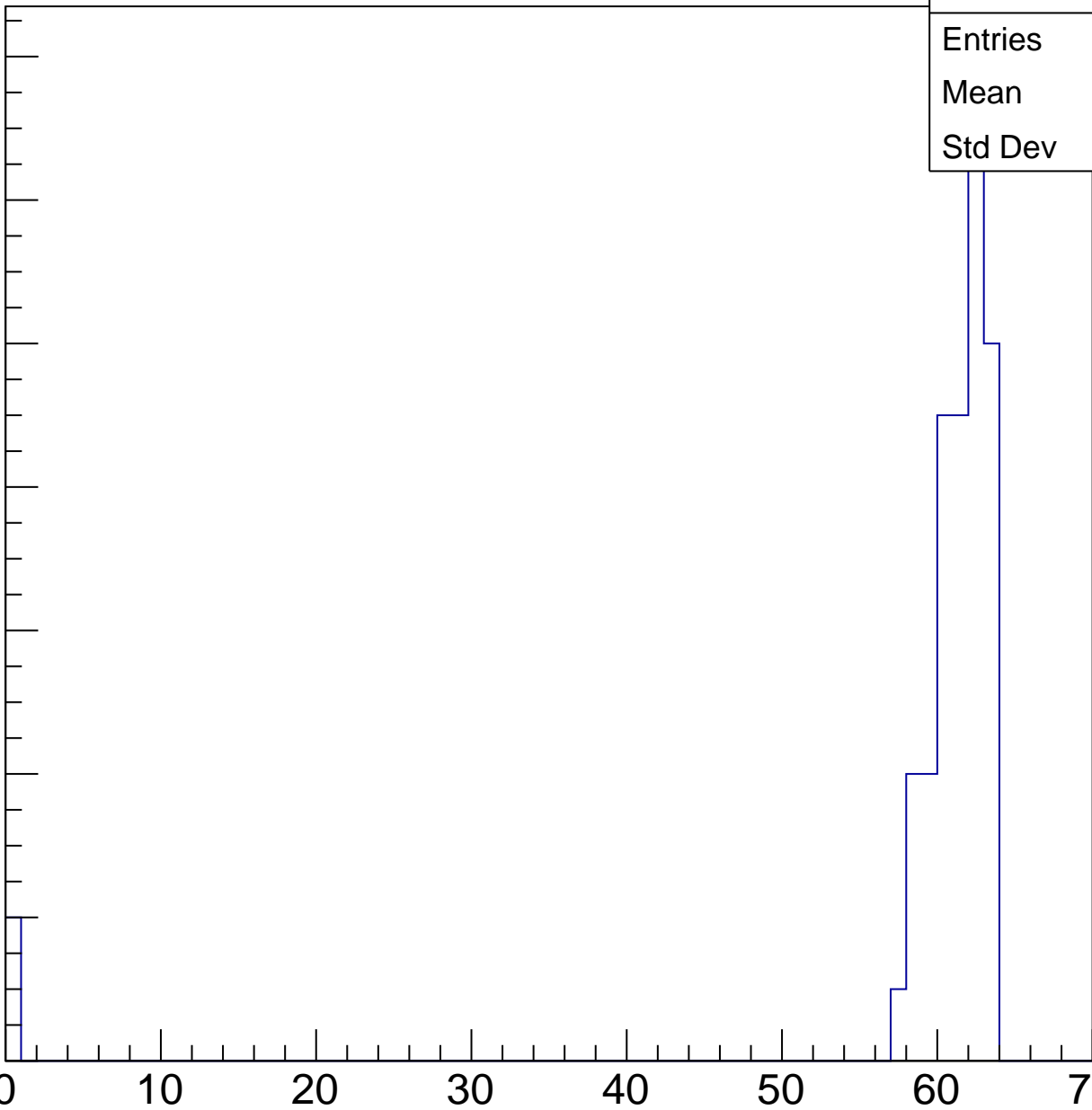
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	53
Mean	58.72
Std Dev	11.73

ampl



B1L001S, U25-ch81, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L001S, U25-ch81, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch82, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	29.96
Std Dev	4.233

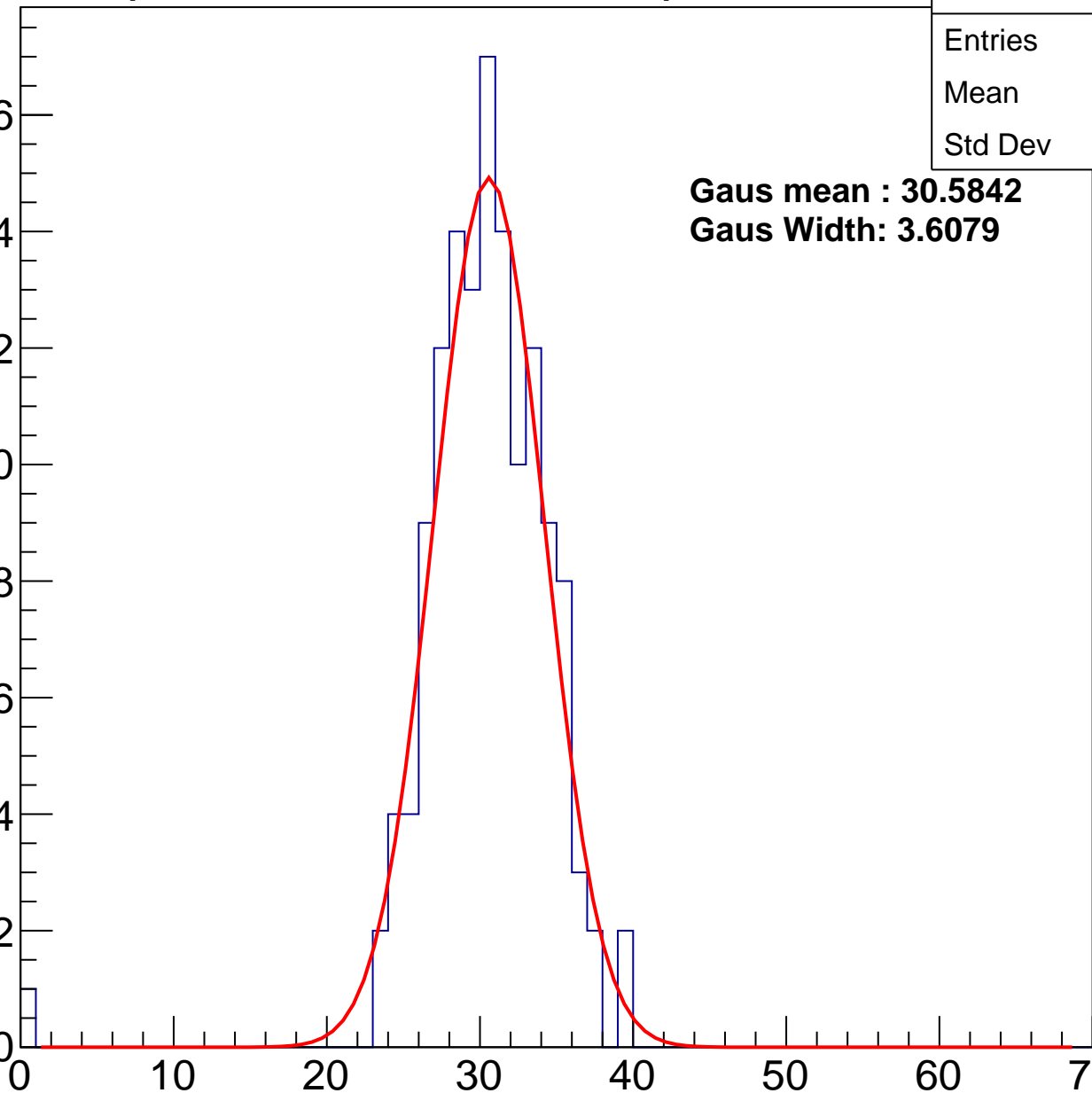
Gaus mean : 30.5842

Gaus Width: 3.6079

Entry

16
14
12
10
8
6
4
2
0

ampl



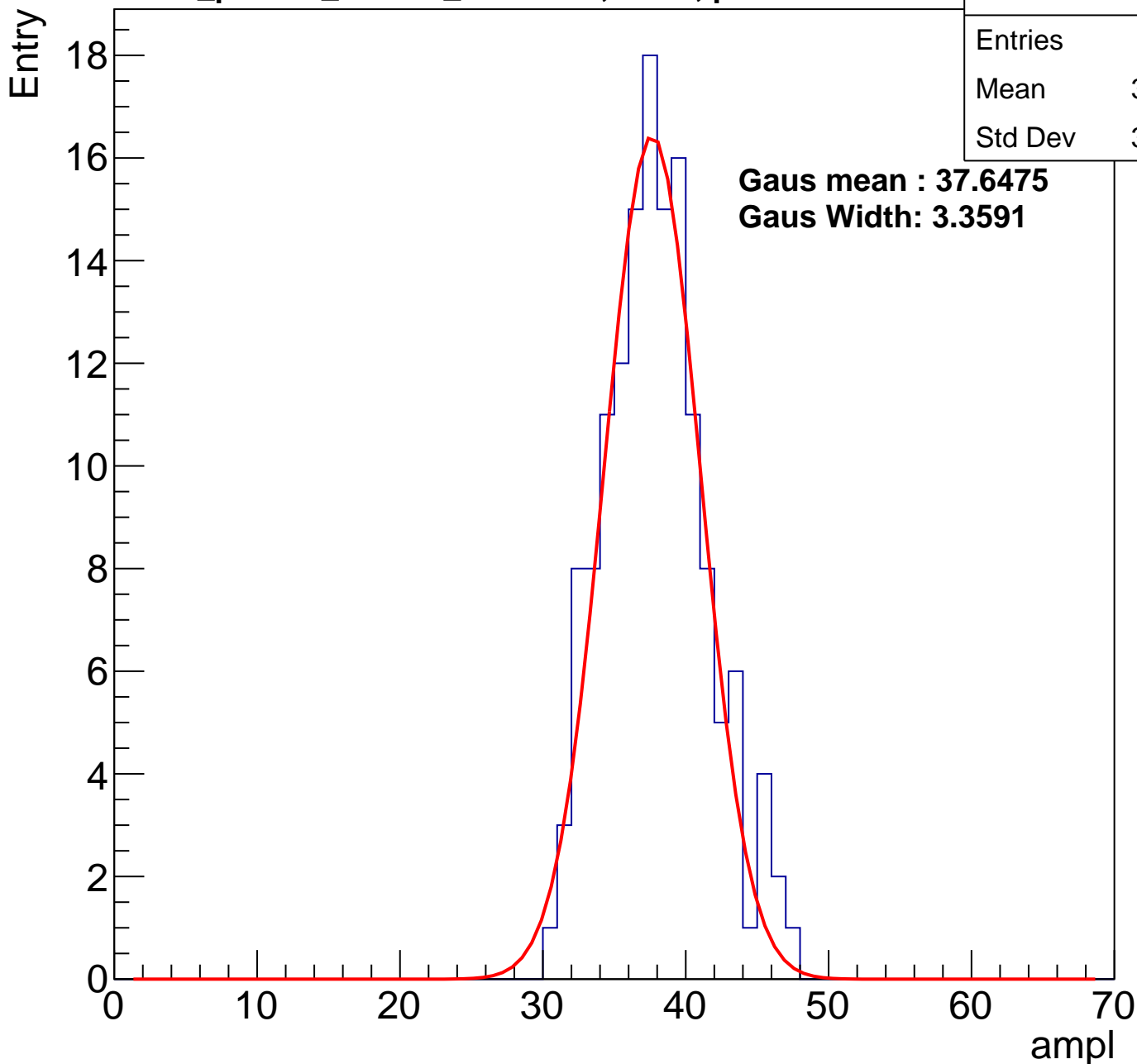
B1L001S, U25-ch82, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.49
Std Dev	3.549

Gaus mean : 37.6475

Gaus Width: 3.3591



B1L001S, U25-ch82, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	43.91
Std Dev	3.277

Gaus mean : 44.7100

Gaus Width: 3.6701

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

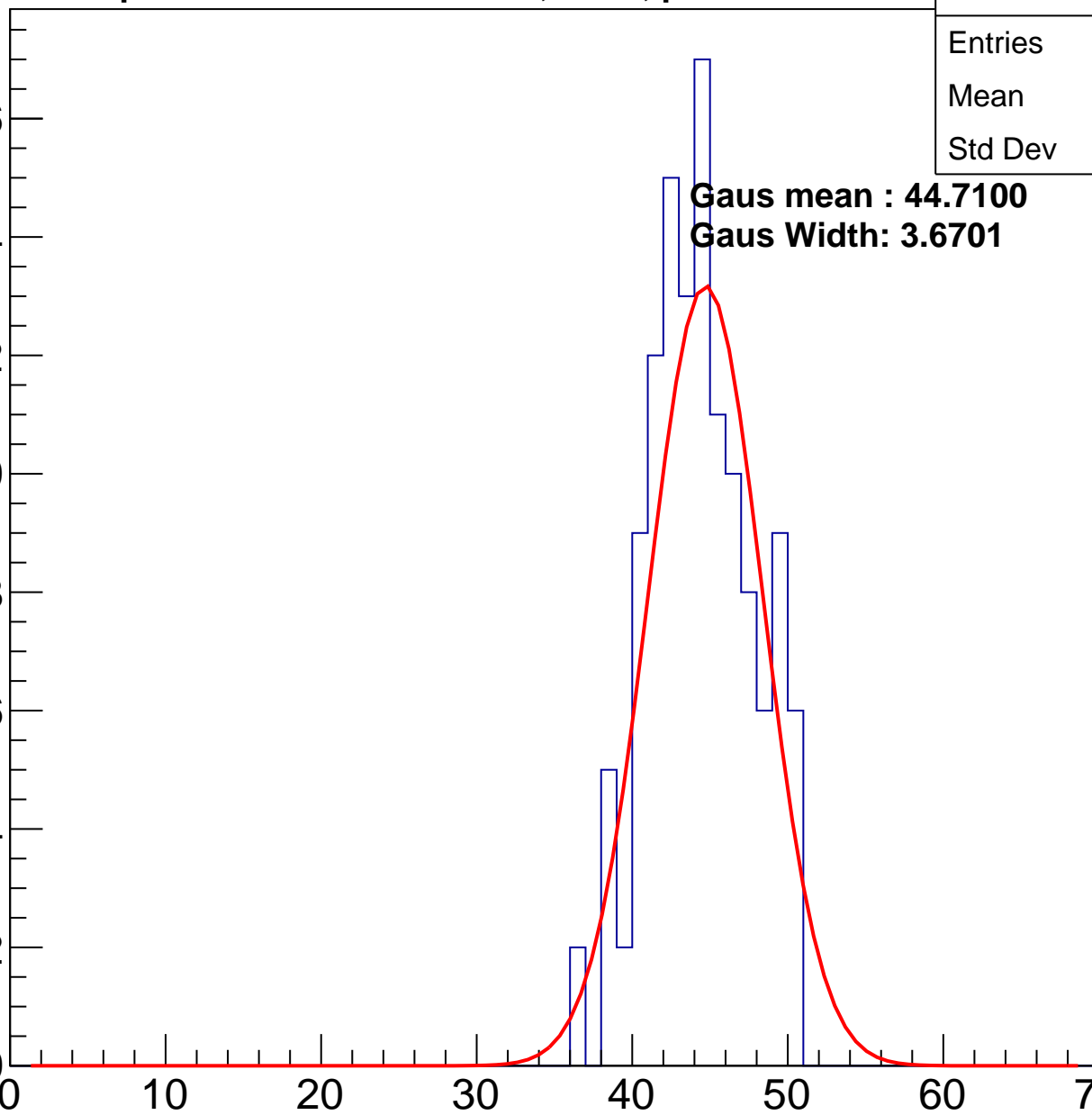
40

50

60

70

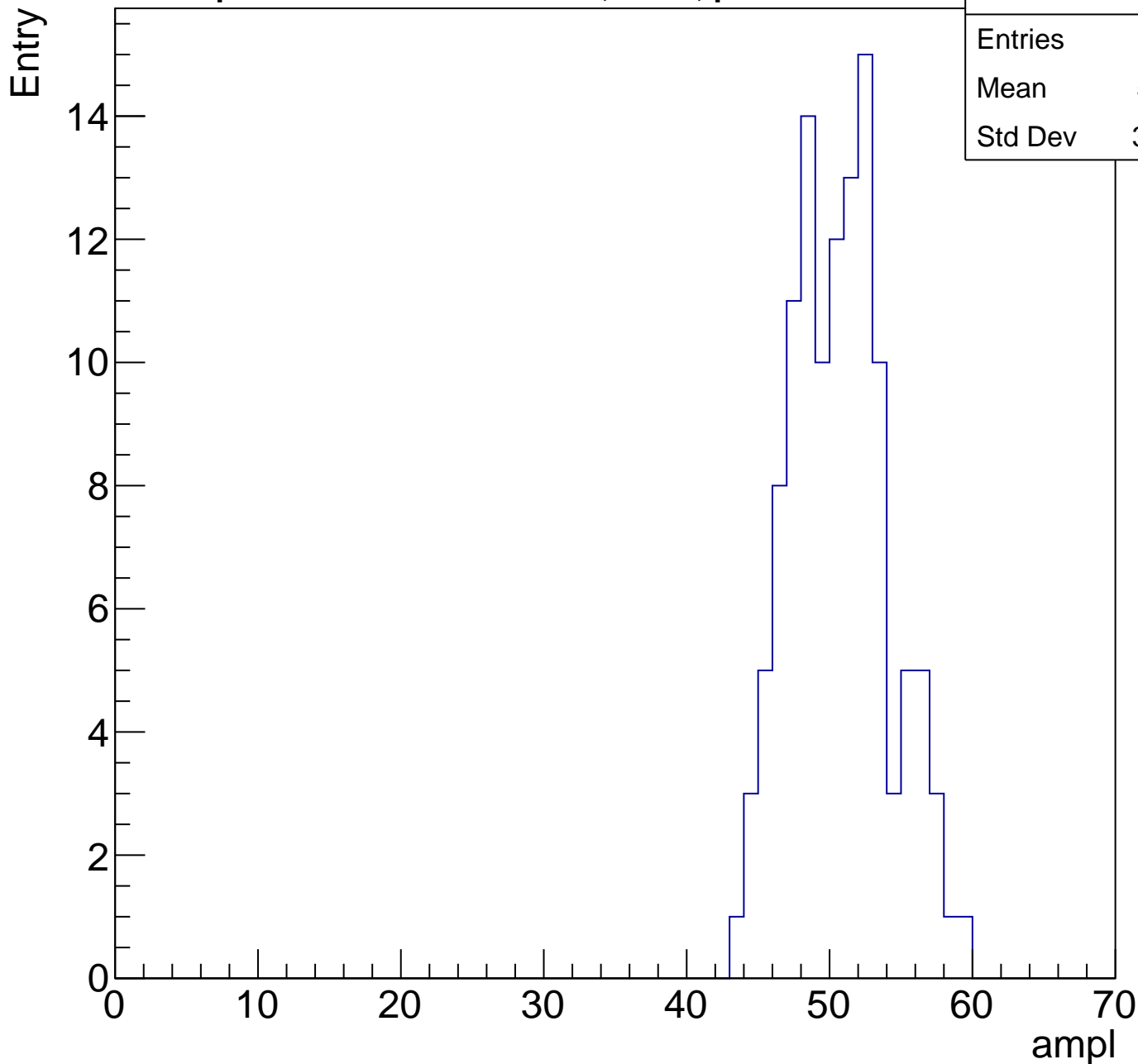
ampl



B1L001S, U25-ch82, adc3

calib_packv5_042523_0143.root, FC#2, port C2

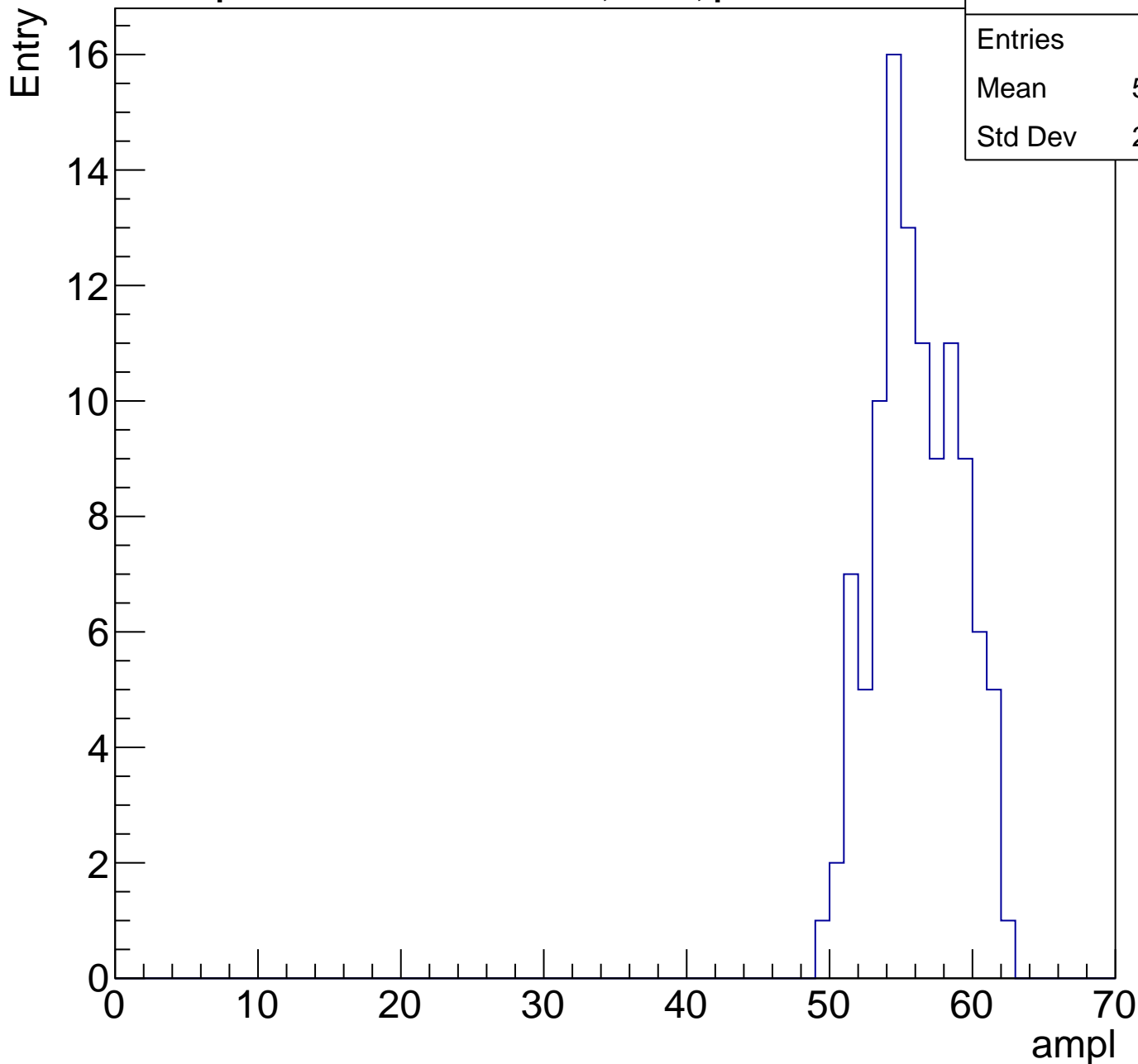
Entries	120
Mean	50.21
Std Dev	3.388



B1L001S, U25-ch82, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	55.66
Std Dev	2.949



B1L001S, U25-ch82, adc5

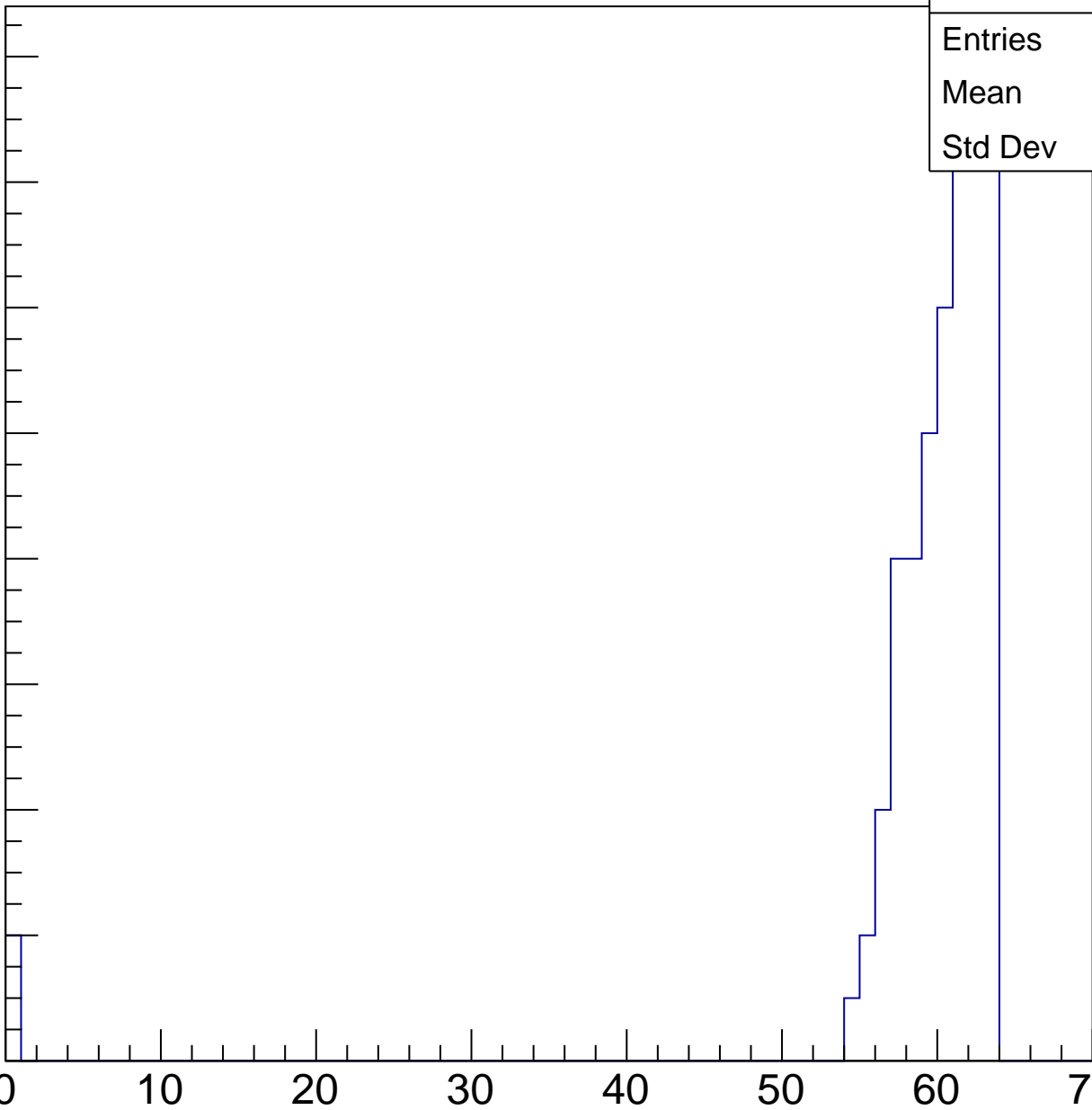
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.82
Std Dev	9.009

ampl

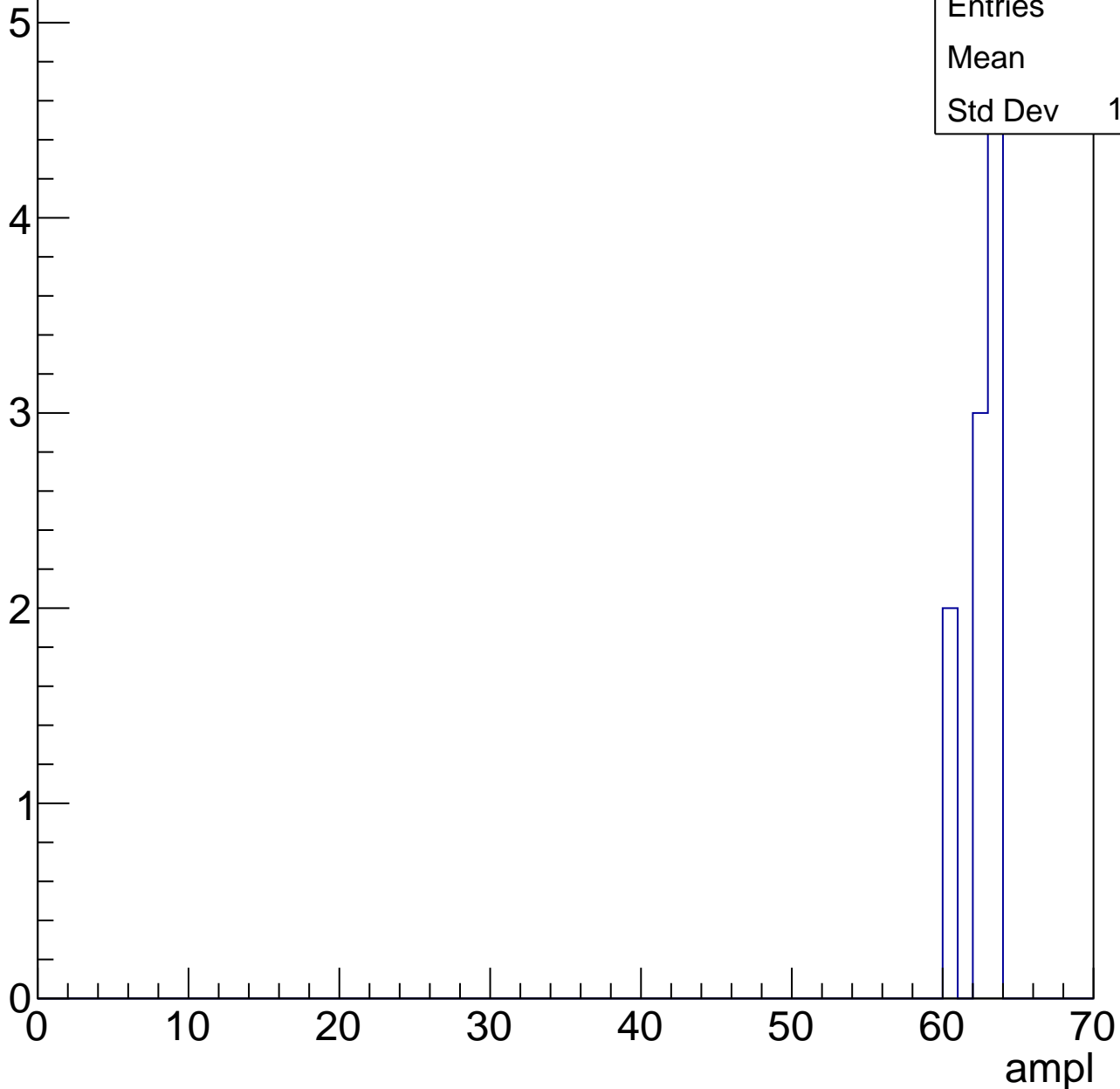


B1L001S, U25-ch82, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	62.1
Std Dev	1.136



B1L001S, U25-ch82, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

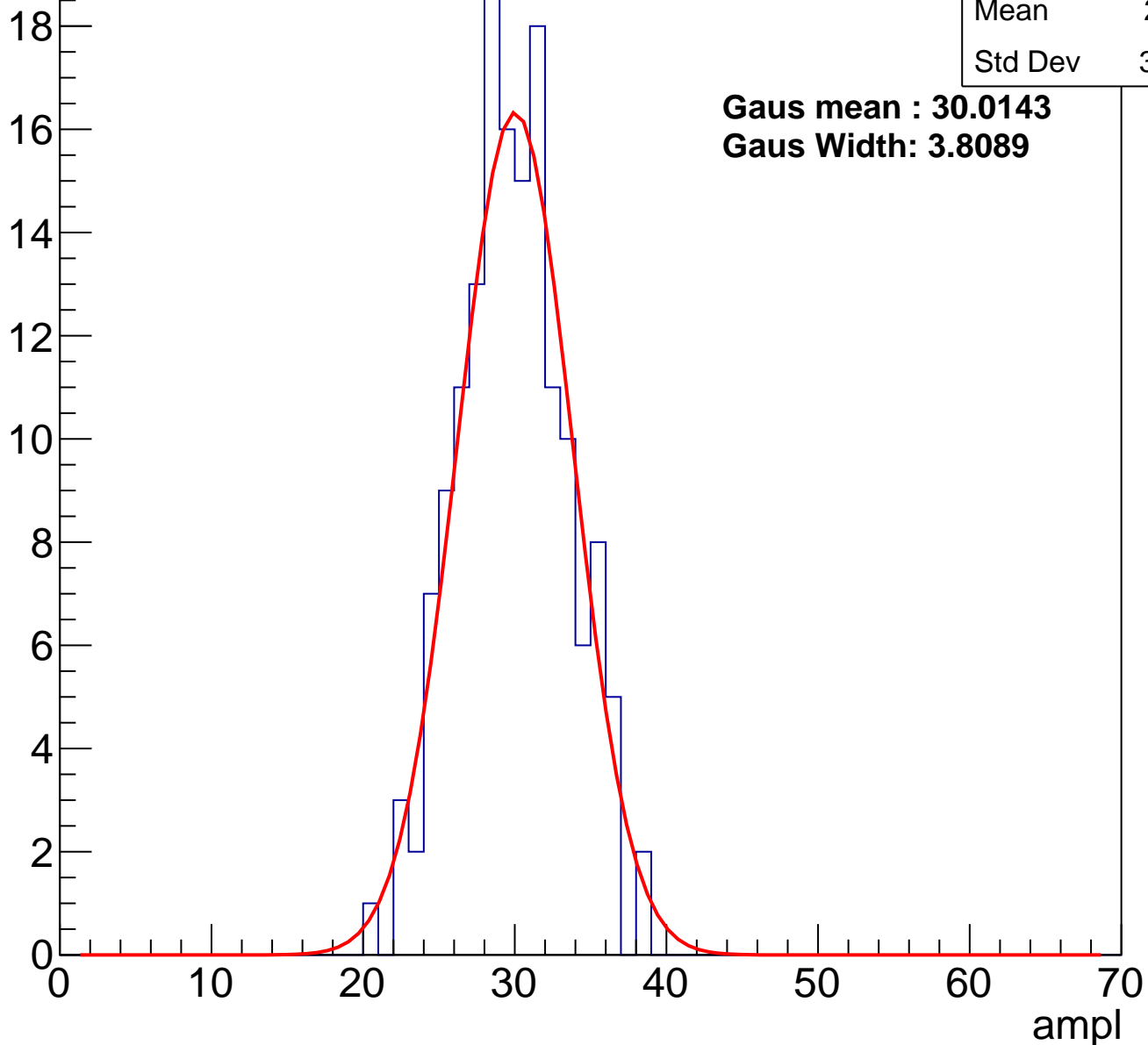


Entries	3
Mean	7.667
Std Dev	10.84

B1L001S, U25-ch83, adc0

calib_packv5_042523_0143.root, FC#2, port C2

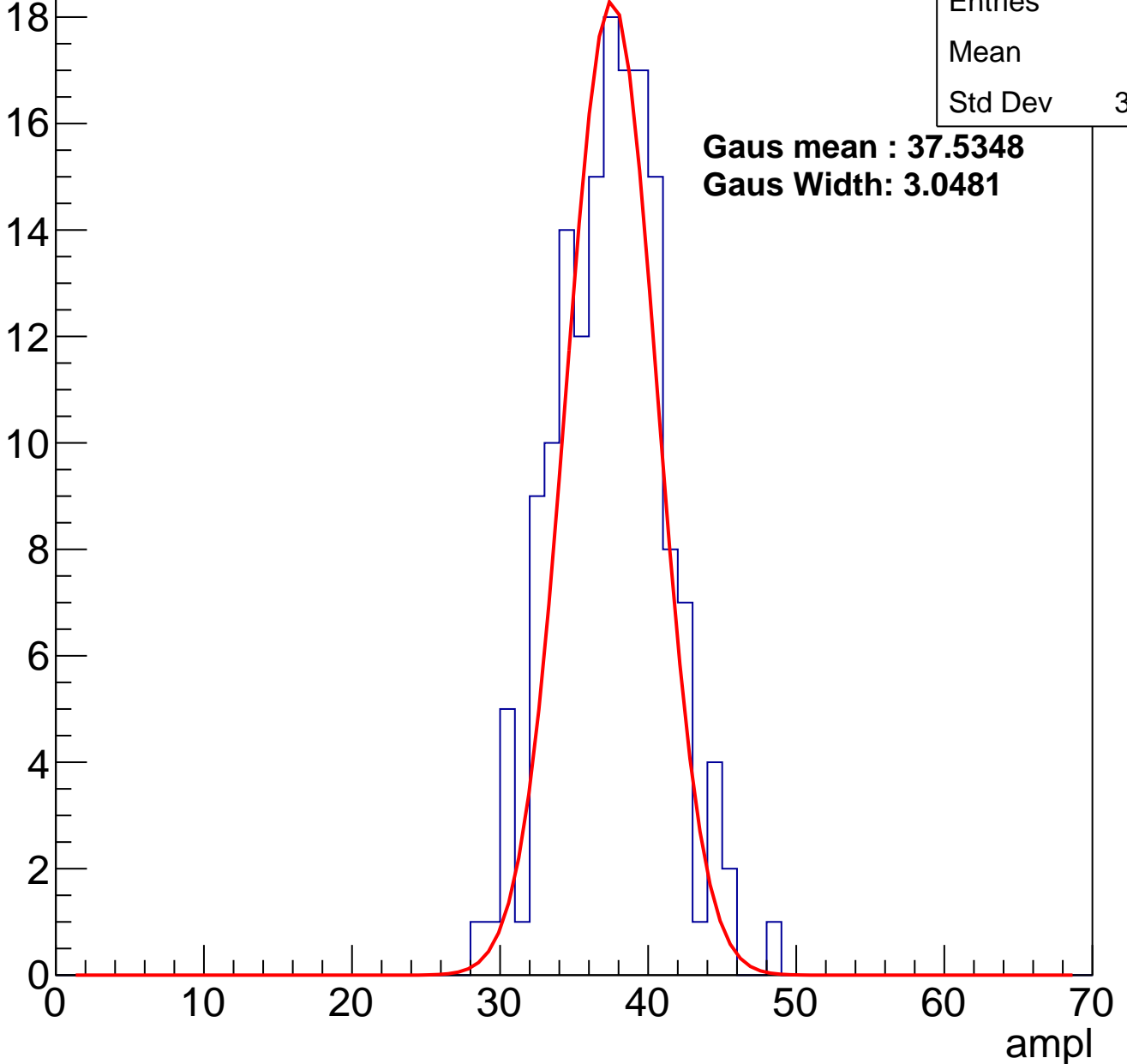
Entry



B1L001S, U25-ch83, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch83, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

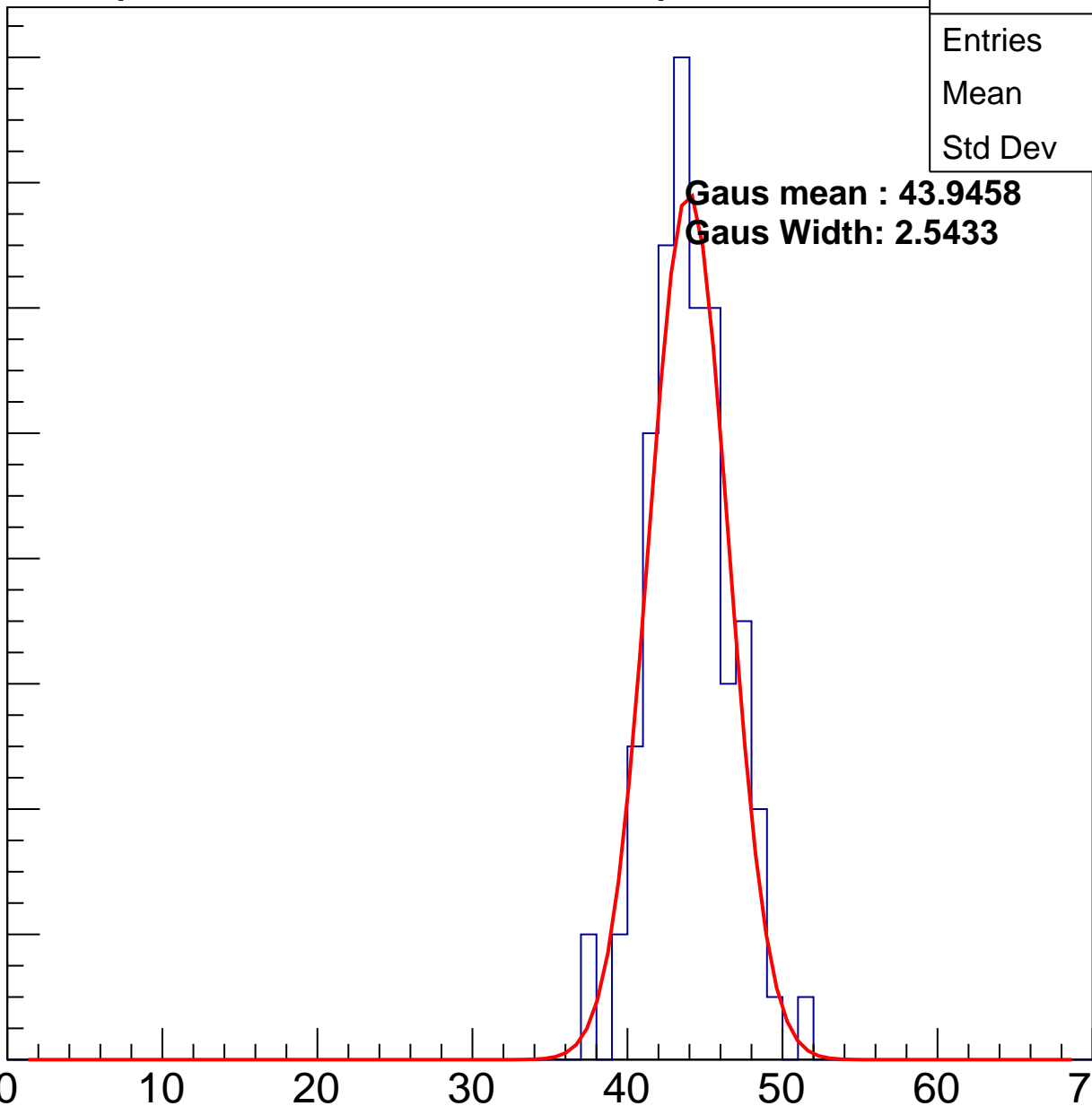
16
14
12
10
8
6
4
2
0

Entries	91
Mean	43.53
Std Dev	2.573

Gaus mean : 43.9458

Gaus Width: 2.5433

0 10 20 30 40 50 60 70
ampl

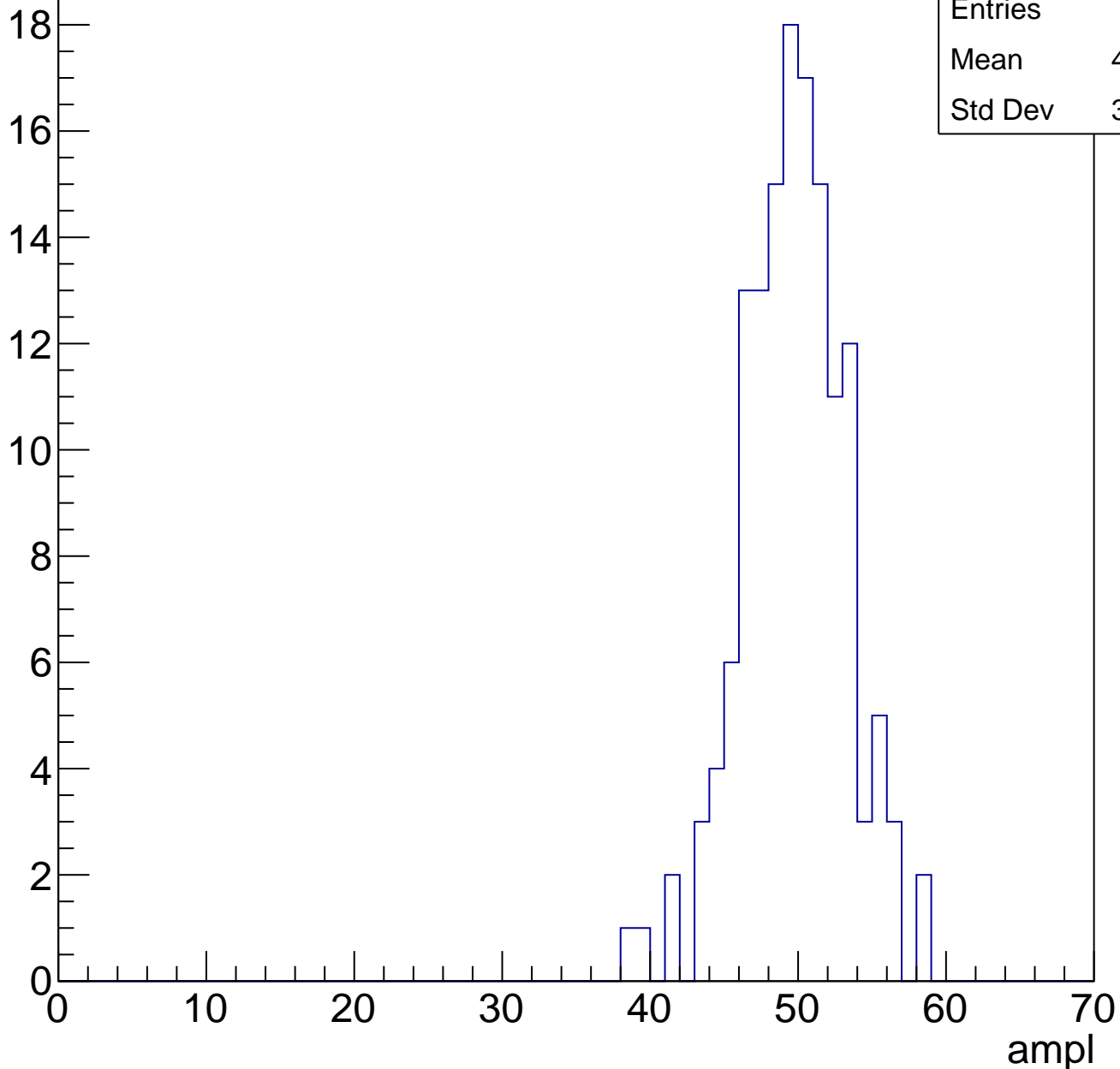


B1L001S, U25-ch83, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	49.23
Std Dev	3.497

Entry



B1L001S, U25-ch83, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

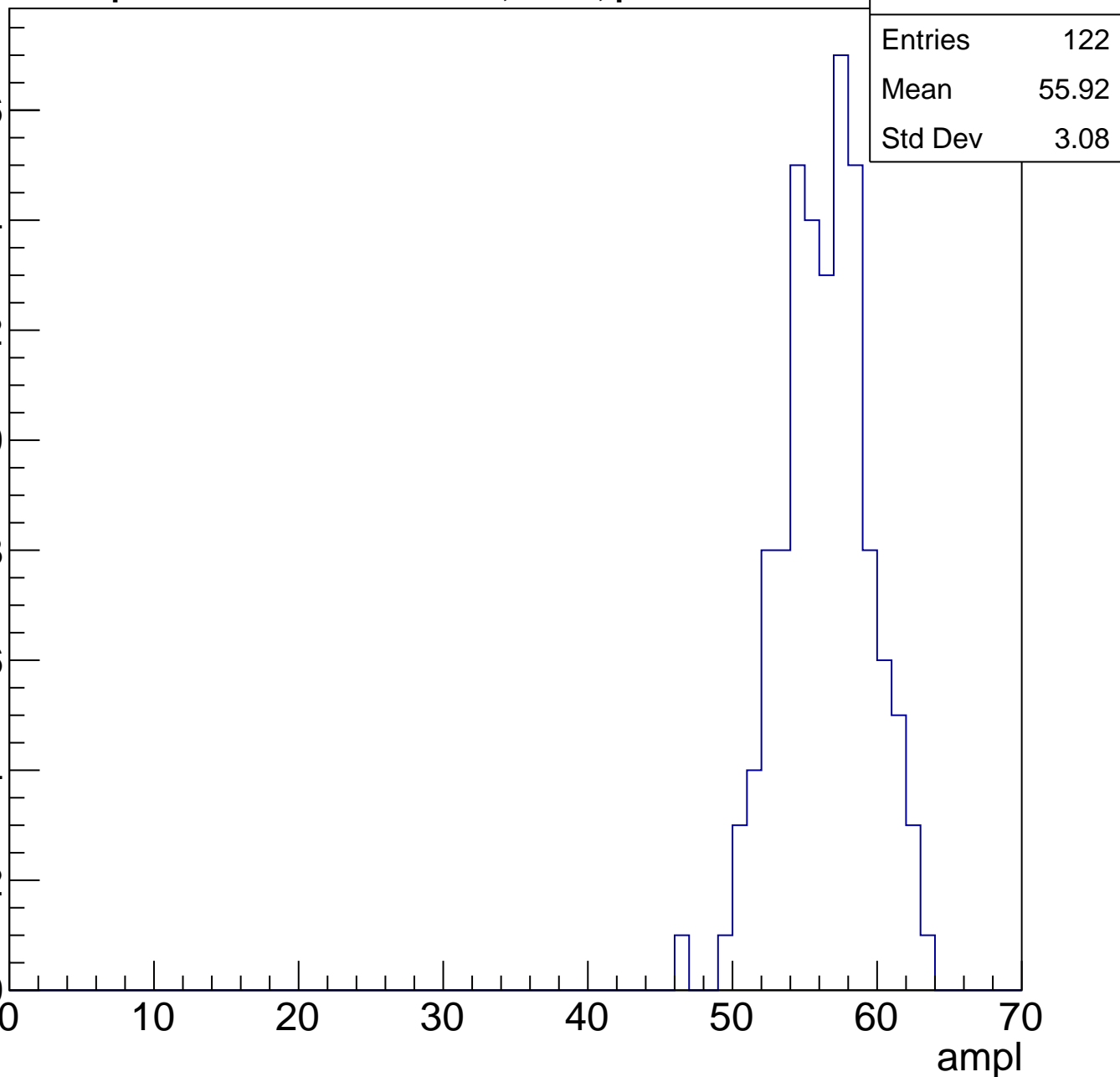
122

Mean

55.92

Std Dev

3.08



B1L001S, U25-ch83, adc5

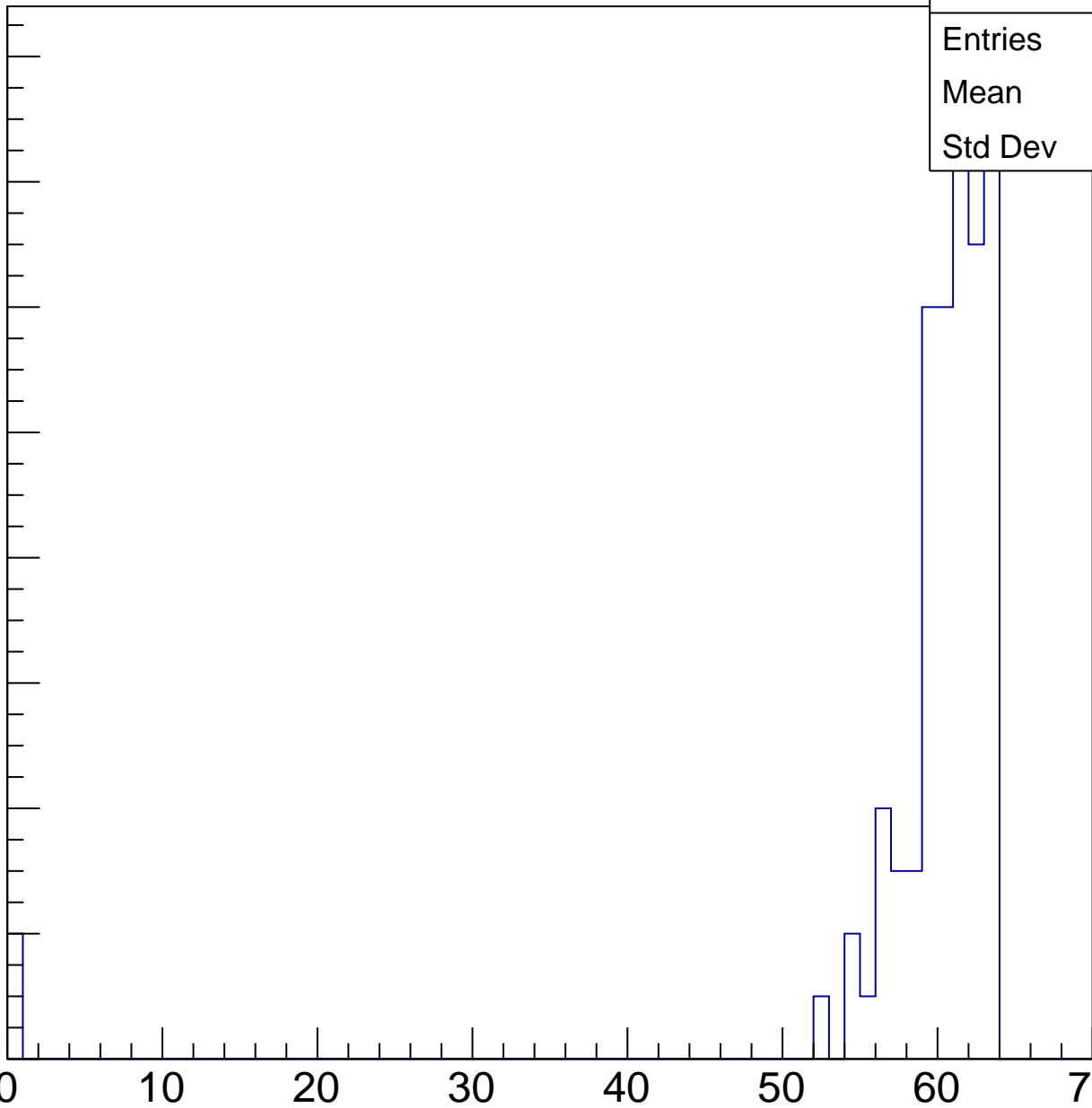
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.82
Std Dev	9.493

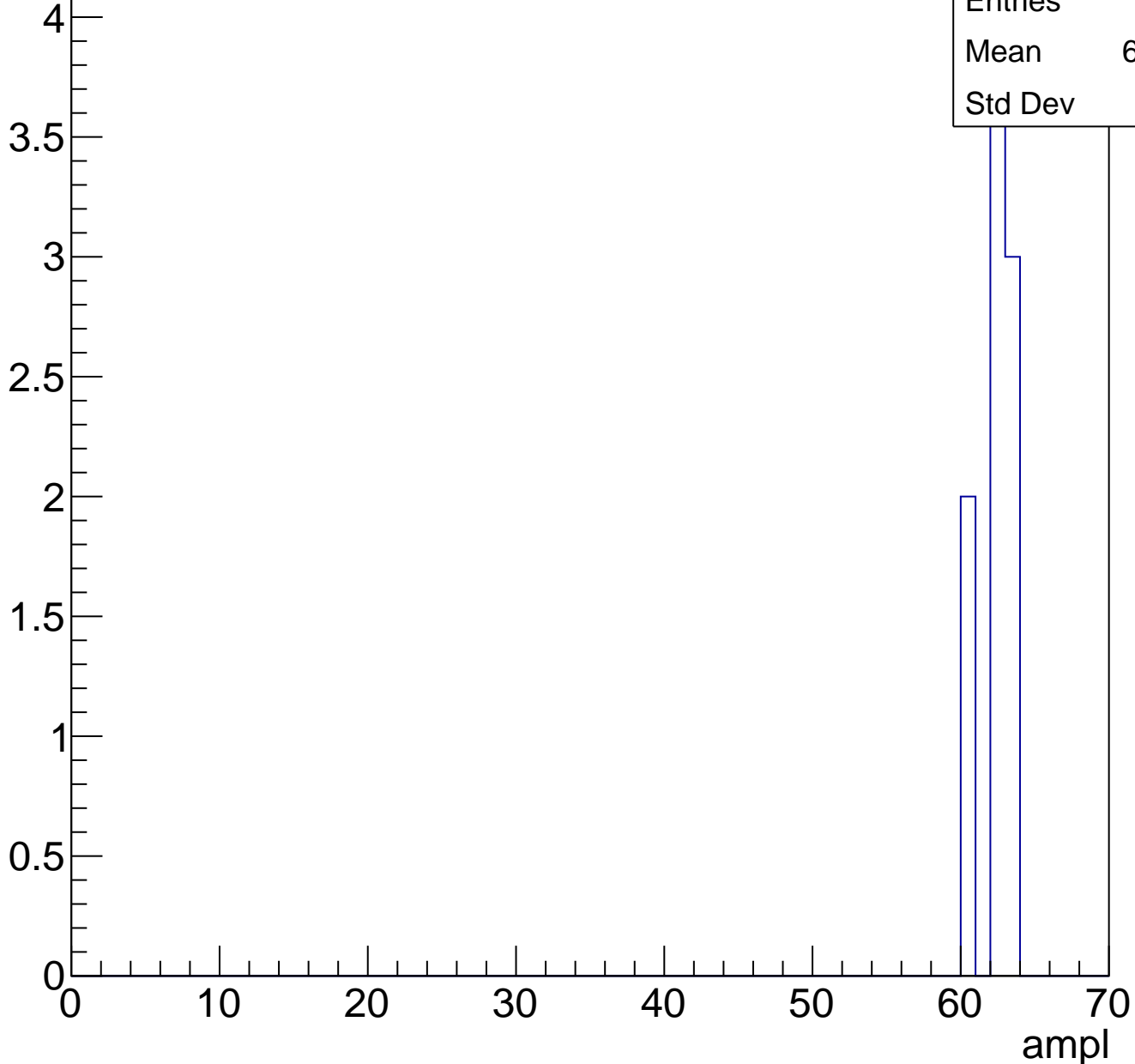
ampl



B1L001S, U25-ch83, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch83, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch84, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	29.9
Std Dev	3.384

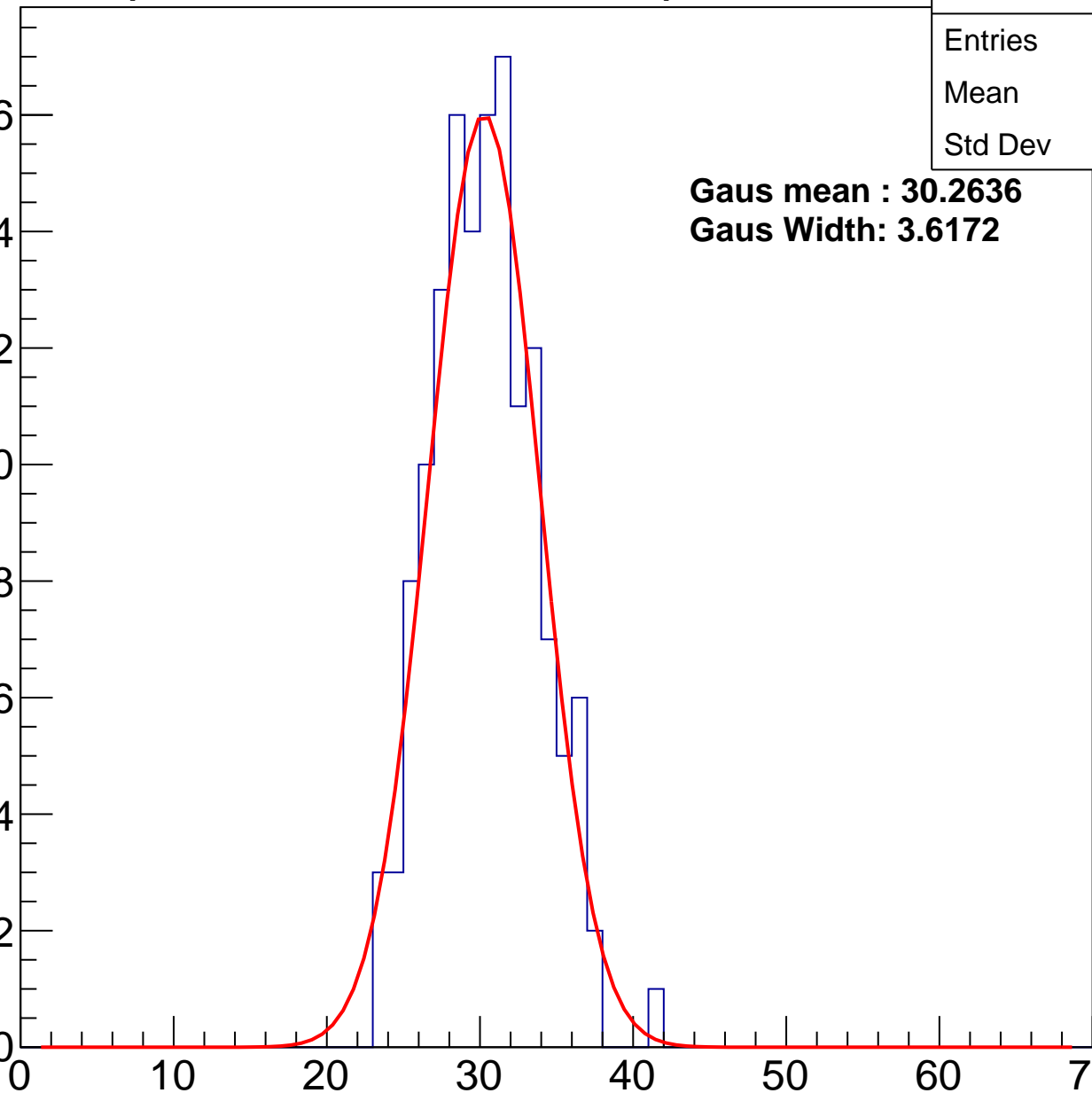
Gaus mean : 30.2636

Gaus Width: 3.6172

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch84, adc1

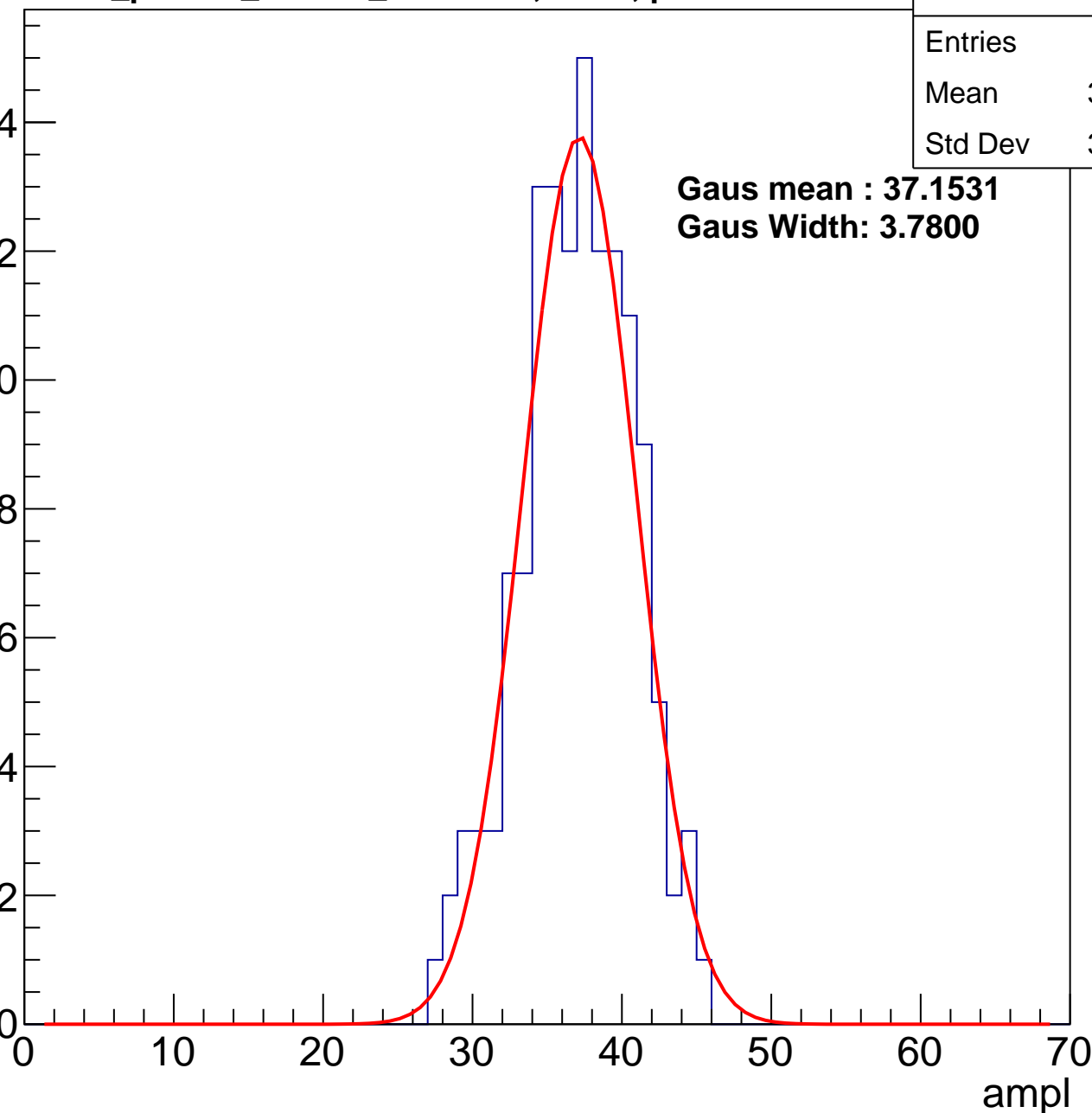
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	134
Mean	36.55
Std Dev	3.727

Gaus mean : 37.1531
Gaus Width: 3.7800



B1L001S, U25-ch84, adc2

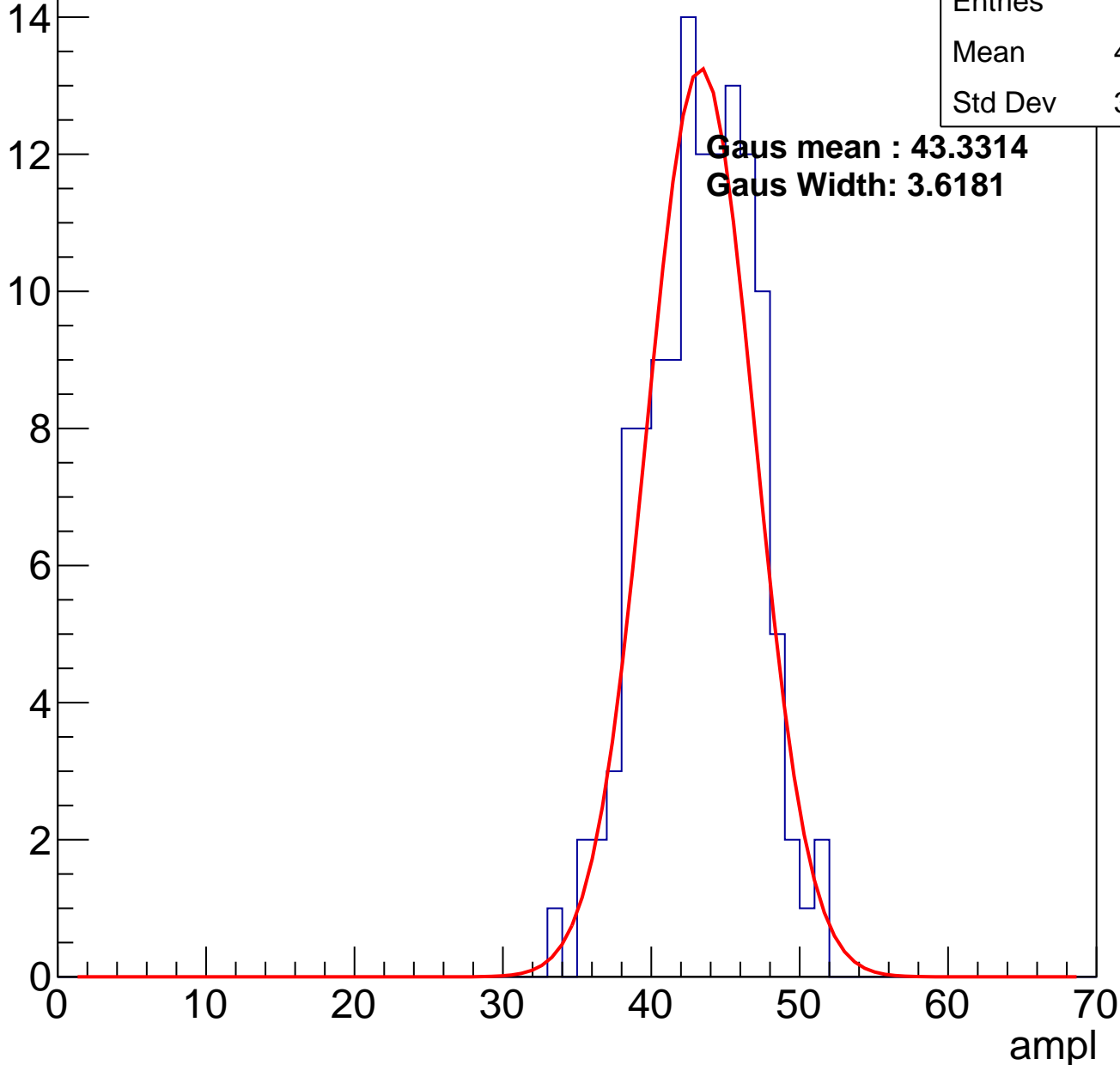
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	42.88
Std Dev	3.575

Gaus mean : 43.3314

Gaus Width: 3.6181

Entry



B1L001S, U25-ch84, adc3

calib_packv5_042523_0143.root, FC#2, port C2

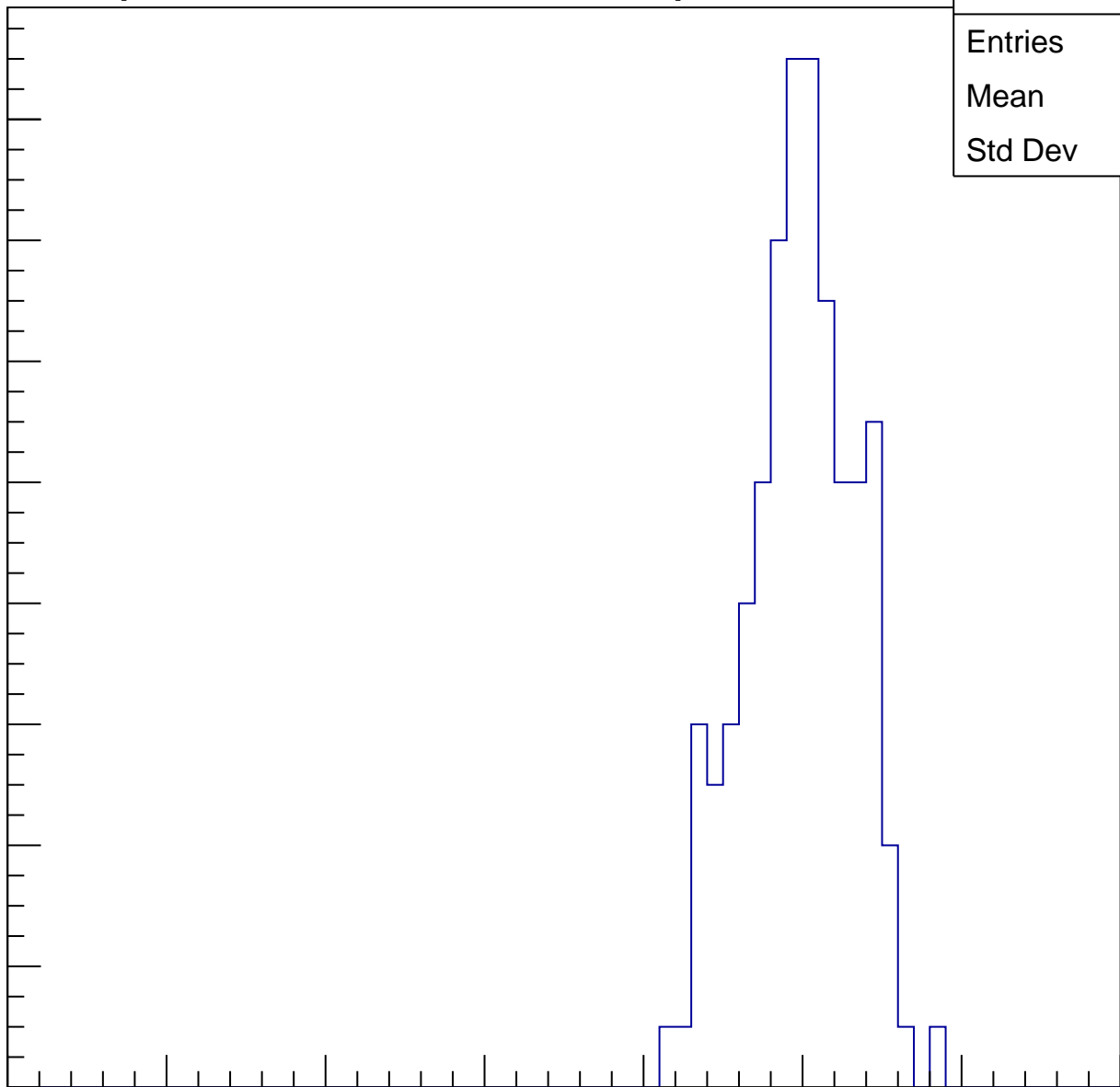
Entries	135
Mean	49.37
Std Dev	3.362

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch84, adc4

calib_packv5_042523_0143.root, FC#2, port C2

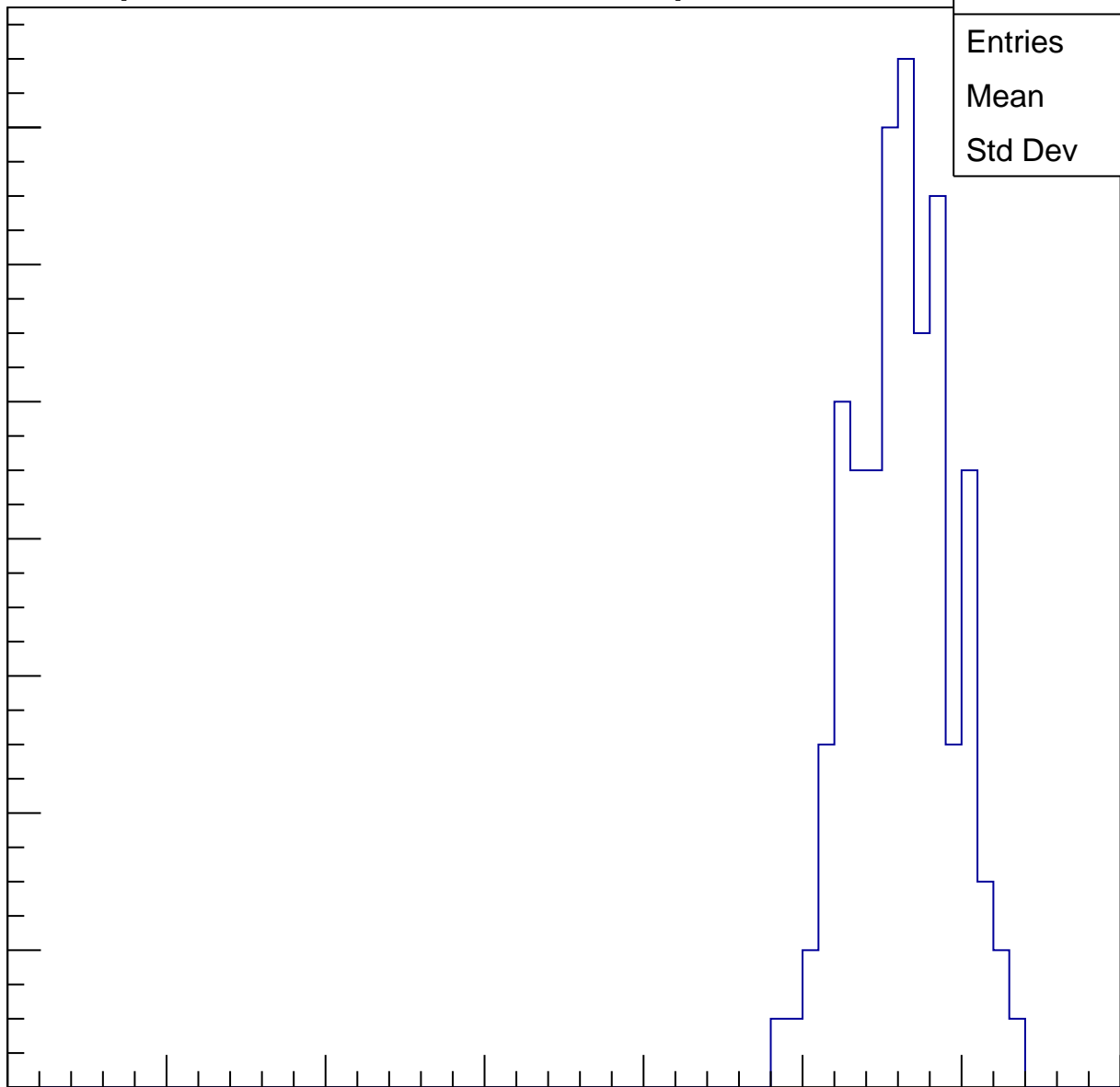
Entries	110
Mean	55.74
Std Dev	3.077

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch84, adc5

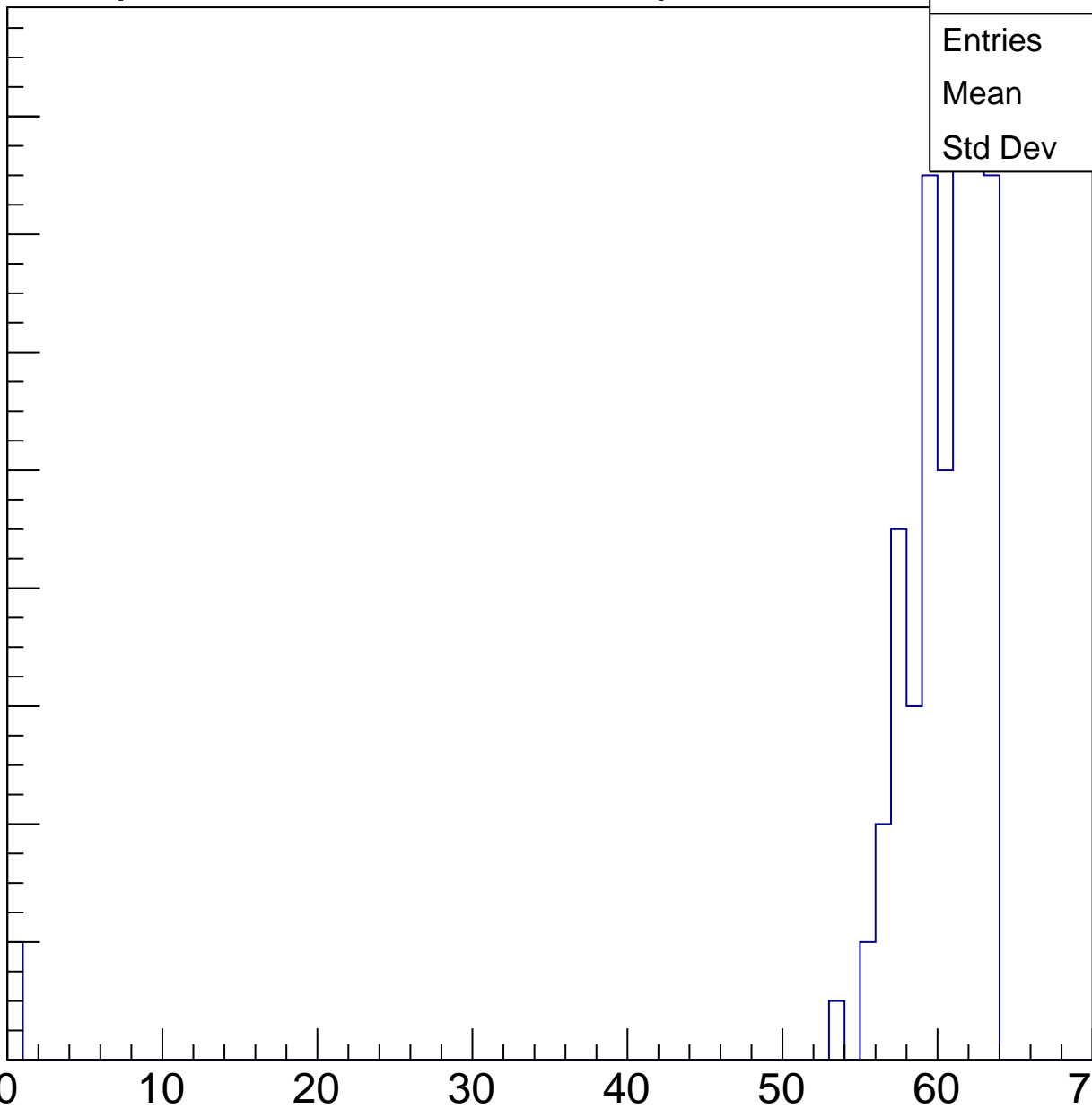
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.84
Std Dev	8.833

ampl



B1L001S, U25-ch84, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch84, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch85, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

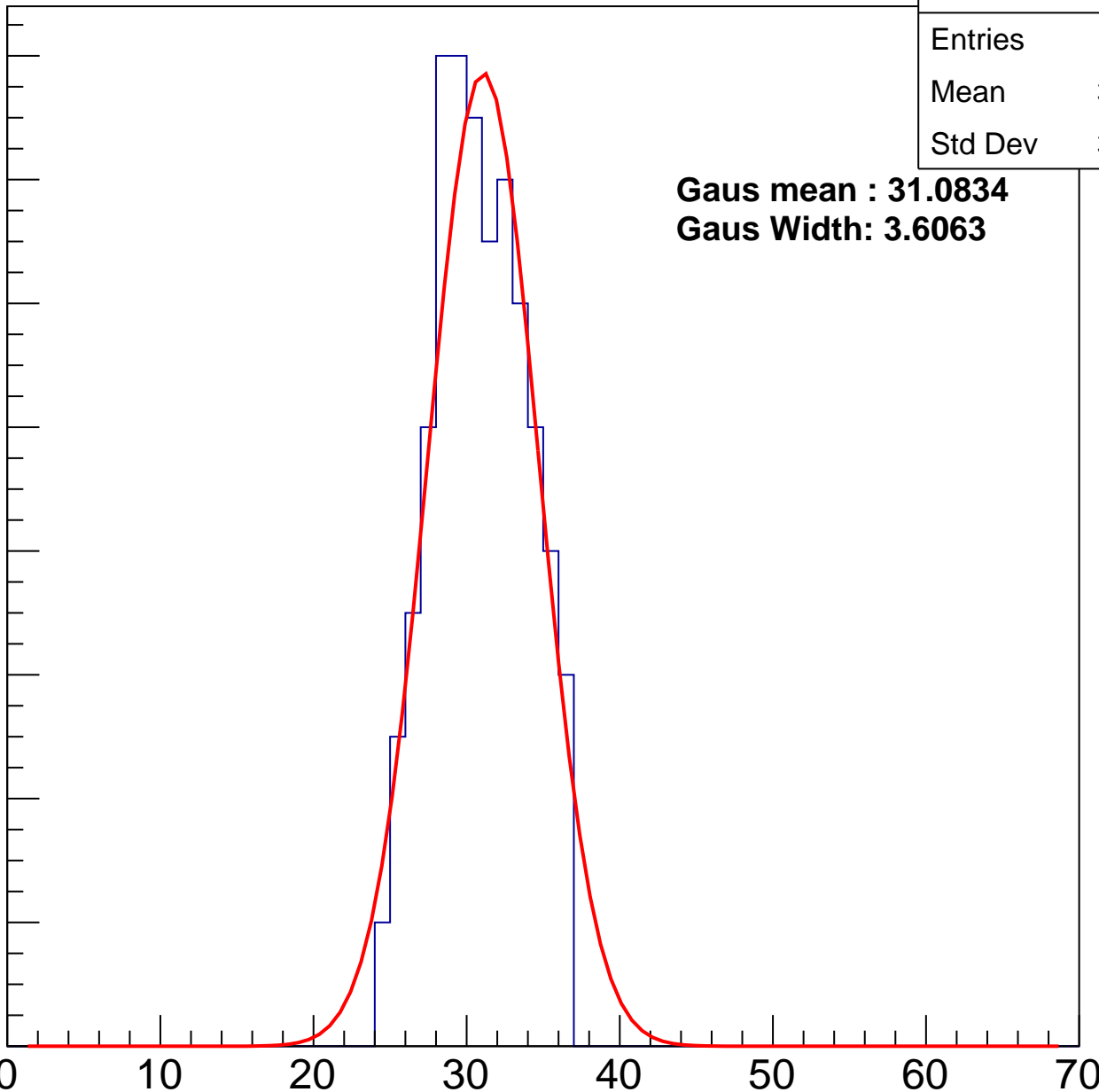
16
14
12
10
8
6
4
2
0

Entries	134
Mean	30.37
Std Dev	3.009

Gaus mean : 31.0834

Gaus Width: 3.6063

ampl



B1L001S, U25-ch85, adc1

calib_packv5_042523_0143.root, FC#2, port C2

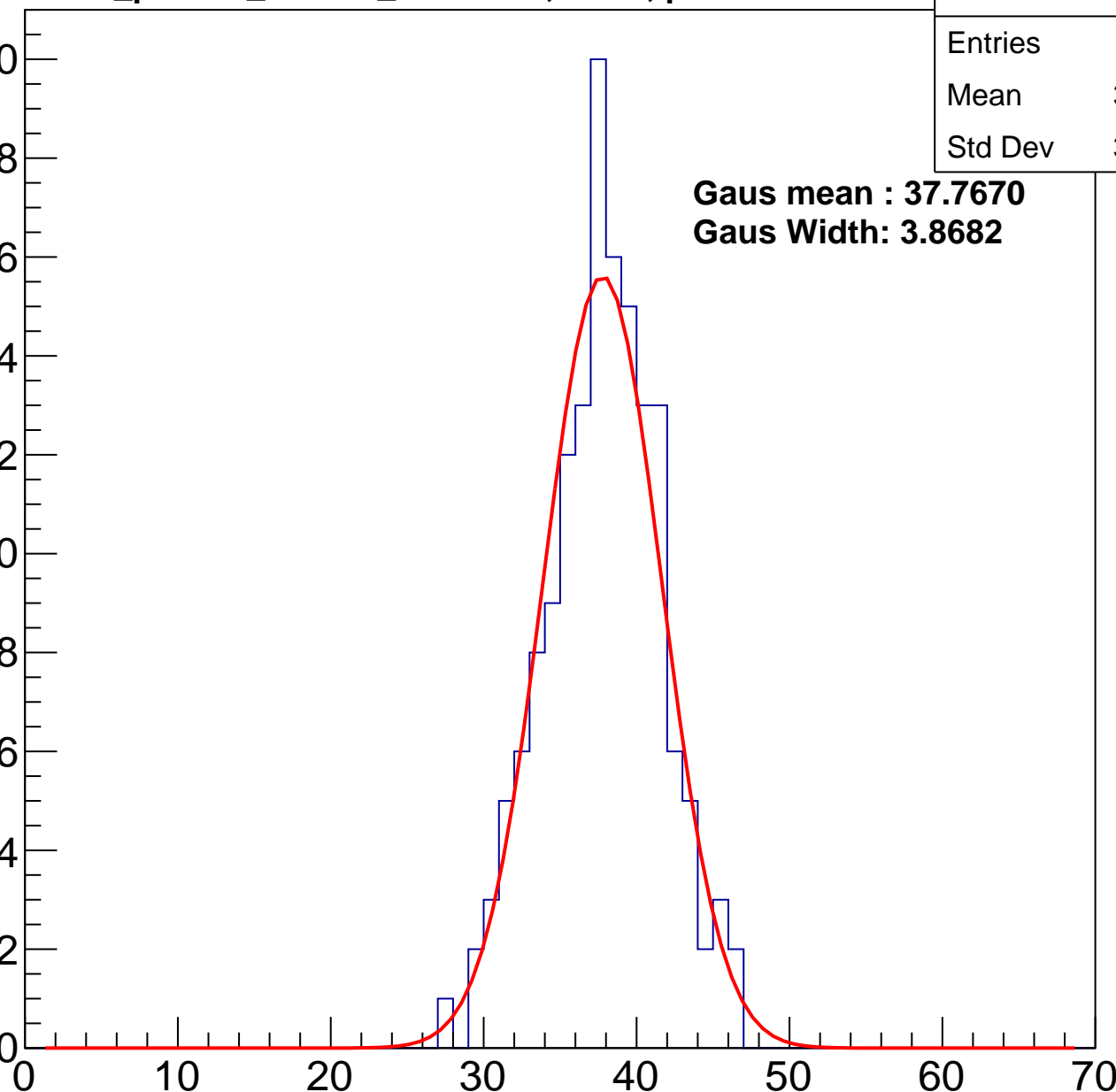
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	154
Mean	37.32
Std Dev	3.729

Gaus mean : 37.7670

Gaus Width: 3.8682



ampl

B1L001S, U25-ch85, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	44.52
Std Dev	3.351

Gaus mean : 44.9075

Gaus Width: 3.2787

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

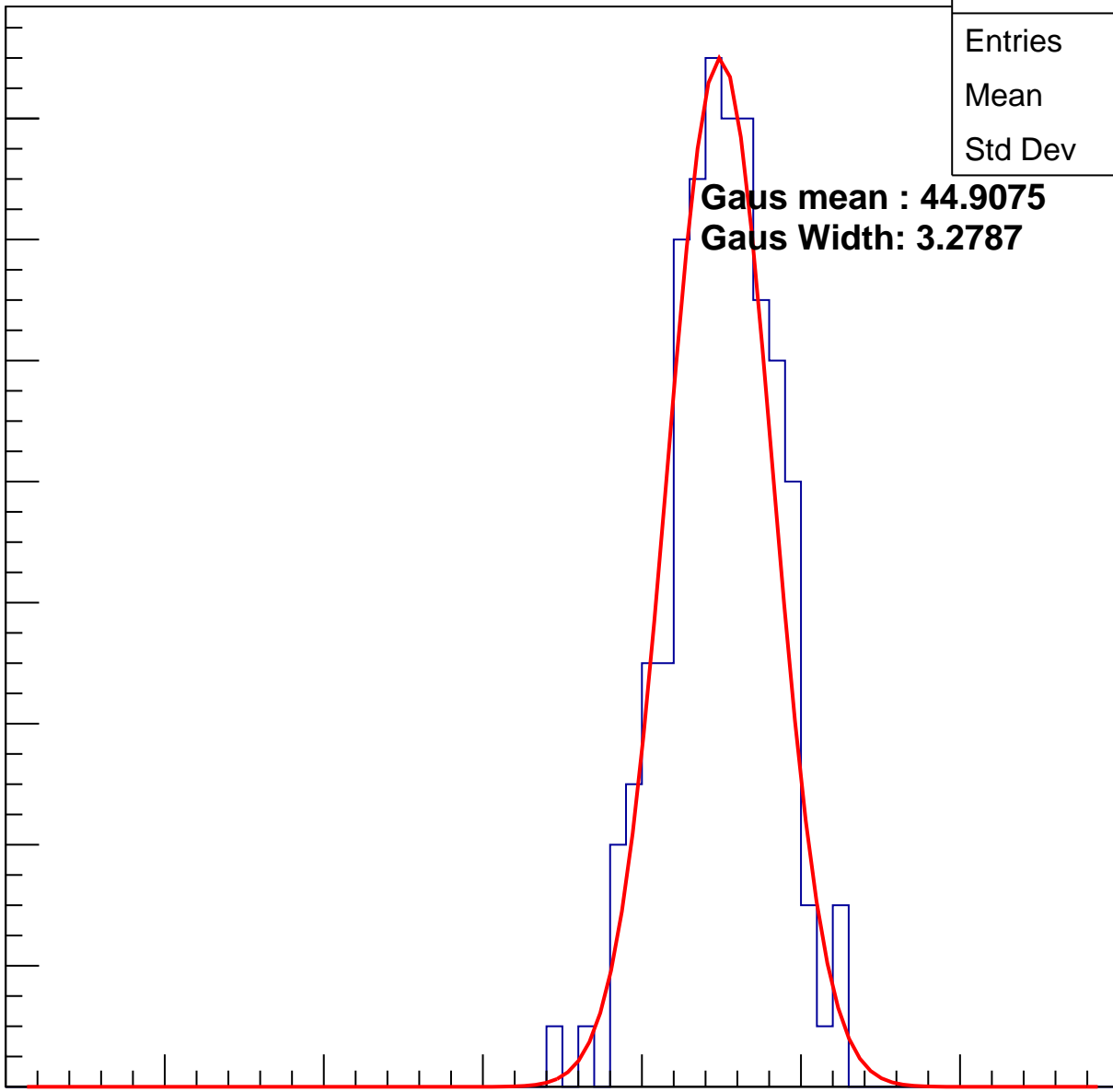
40

50

60

70

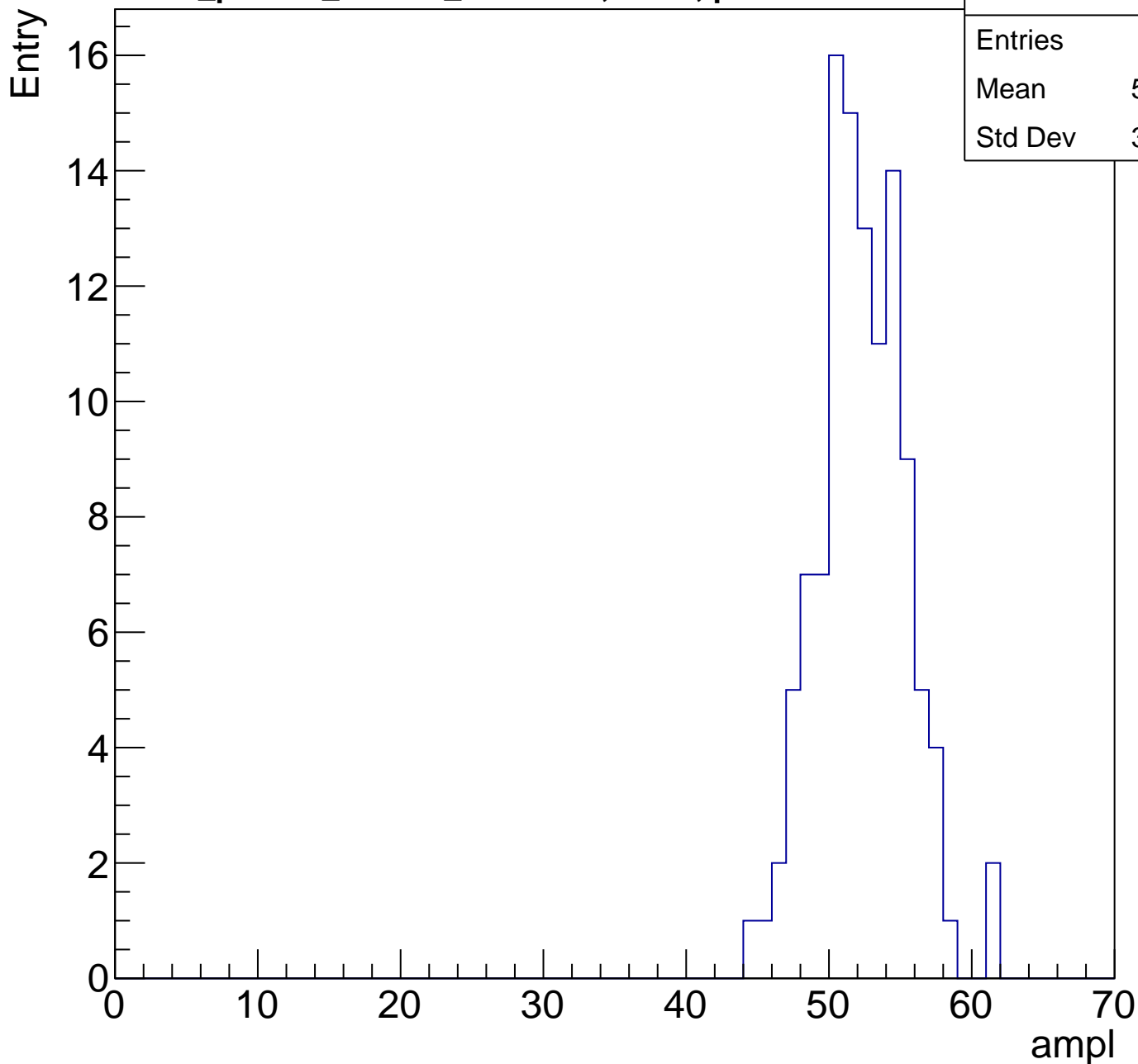
ampl



B1L001S, U25-ch85, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	51.84
Std Dev	3.102

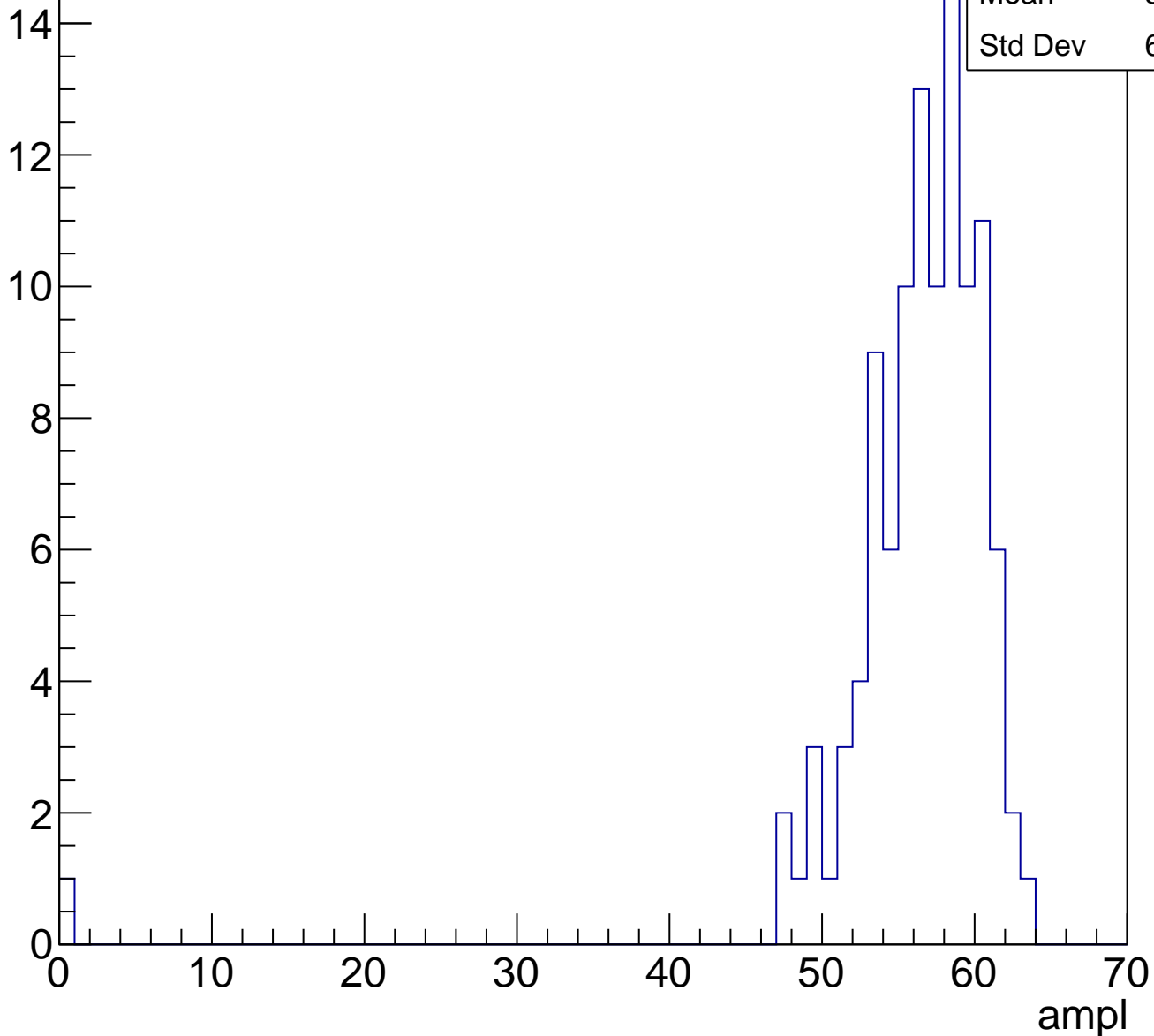


B1L001S, U25-ch85, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	55.76
Std Dev	6.383

Entry



B1L001S, U25-ch85, adc5

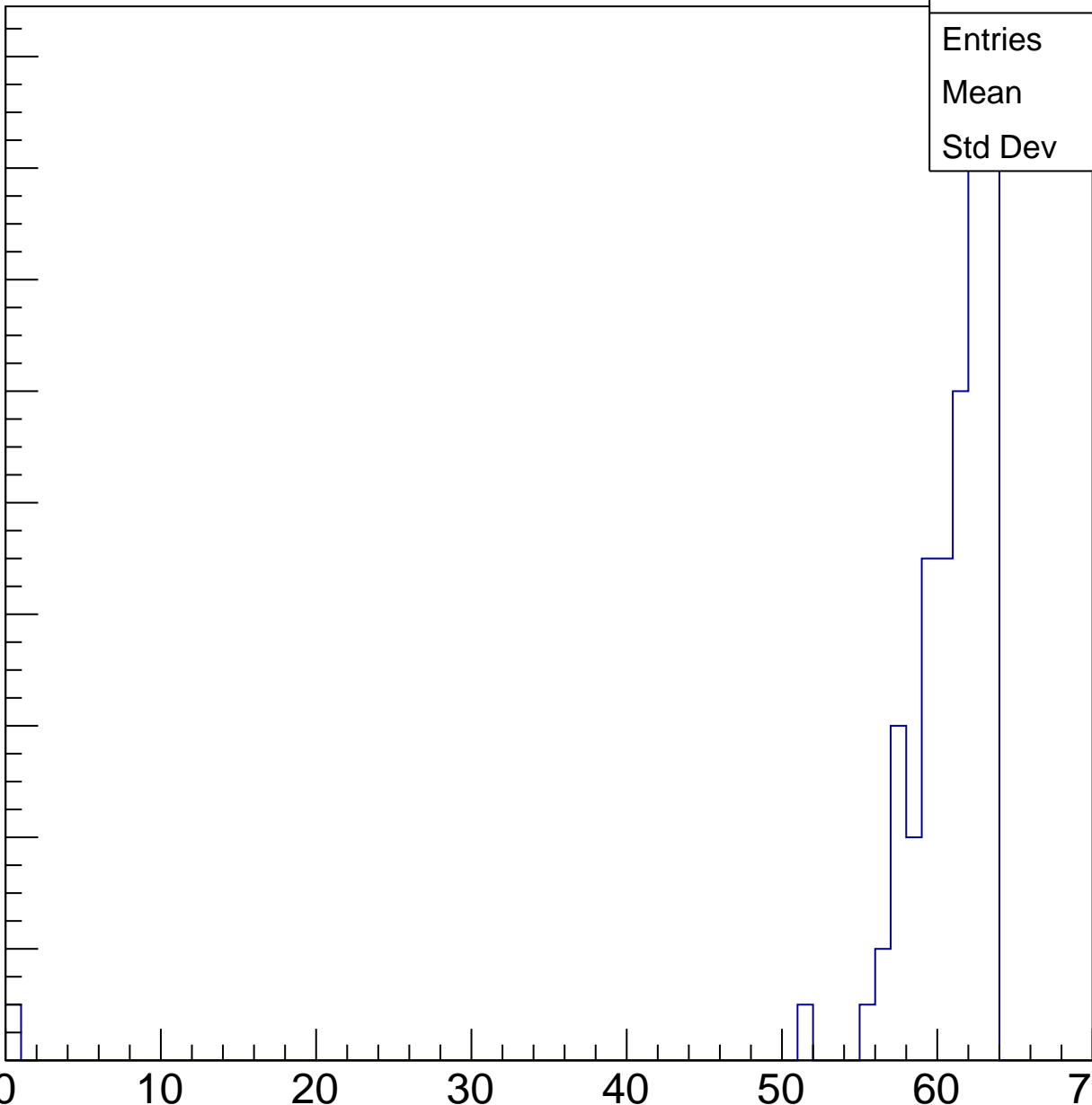
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	81
Mean	59.81
Std Dev	7.08

ampl



B1L001S, U25-ch85, adc6

calib_packv5_042523_0143.root, FC#2, port C2

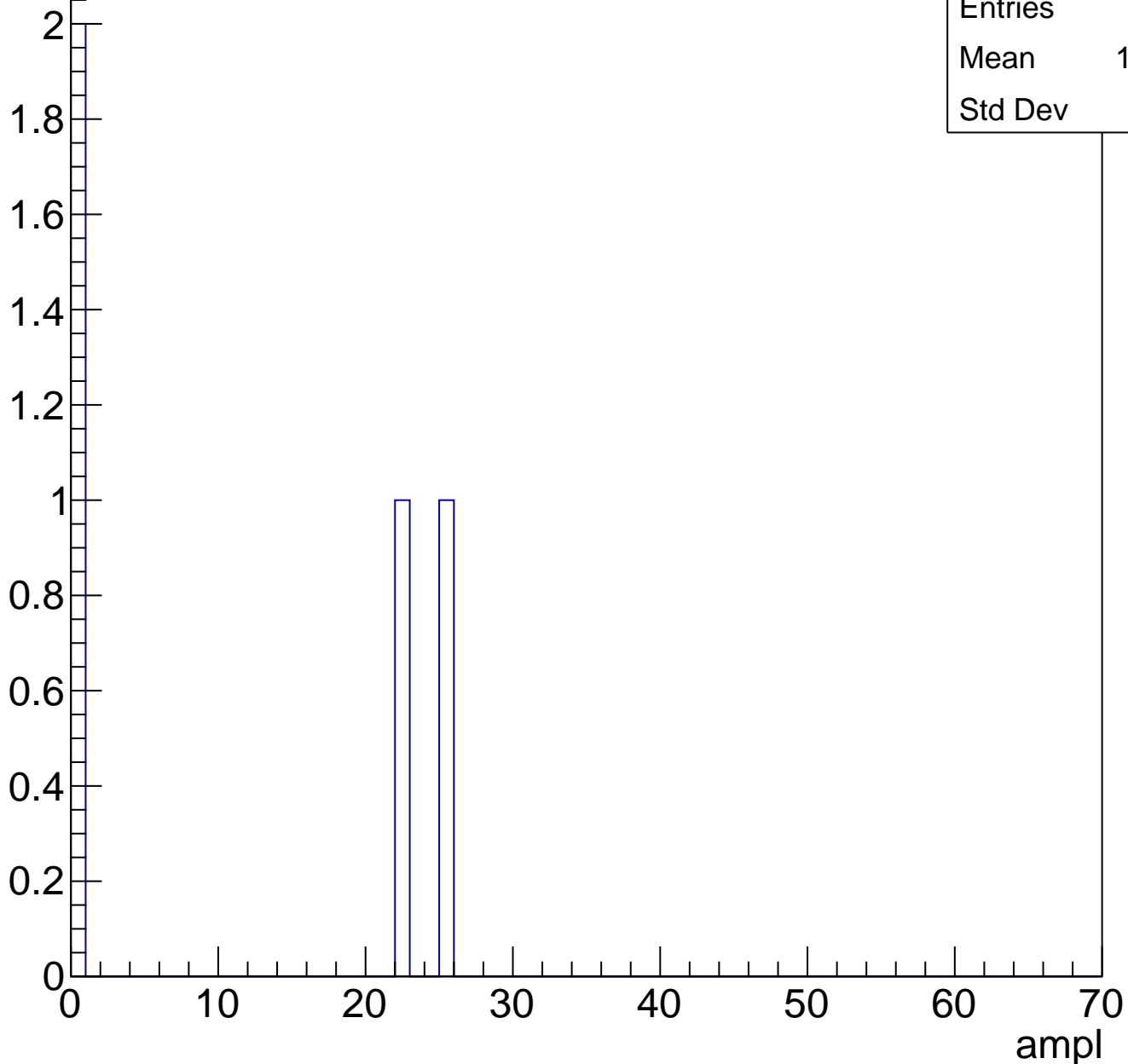
Entry



B1L001S, U25-ch85, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	11.75
Std Dev	11.8

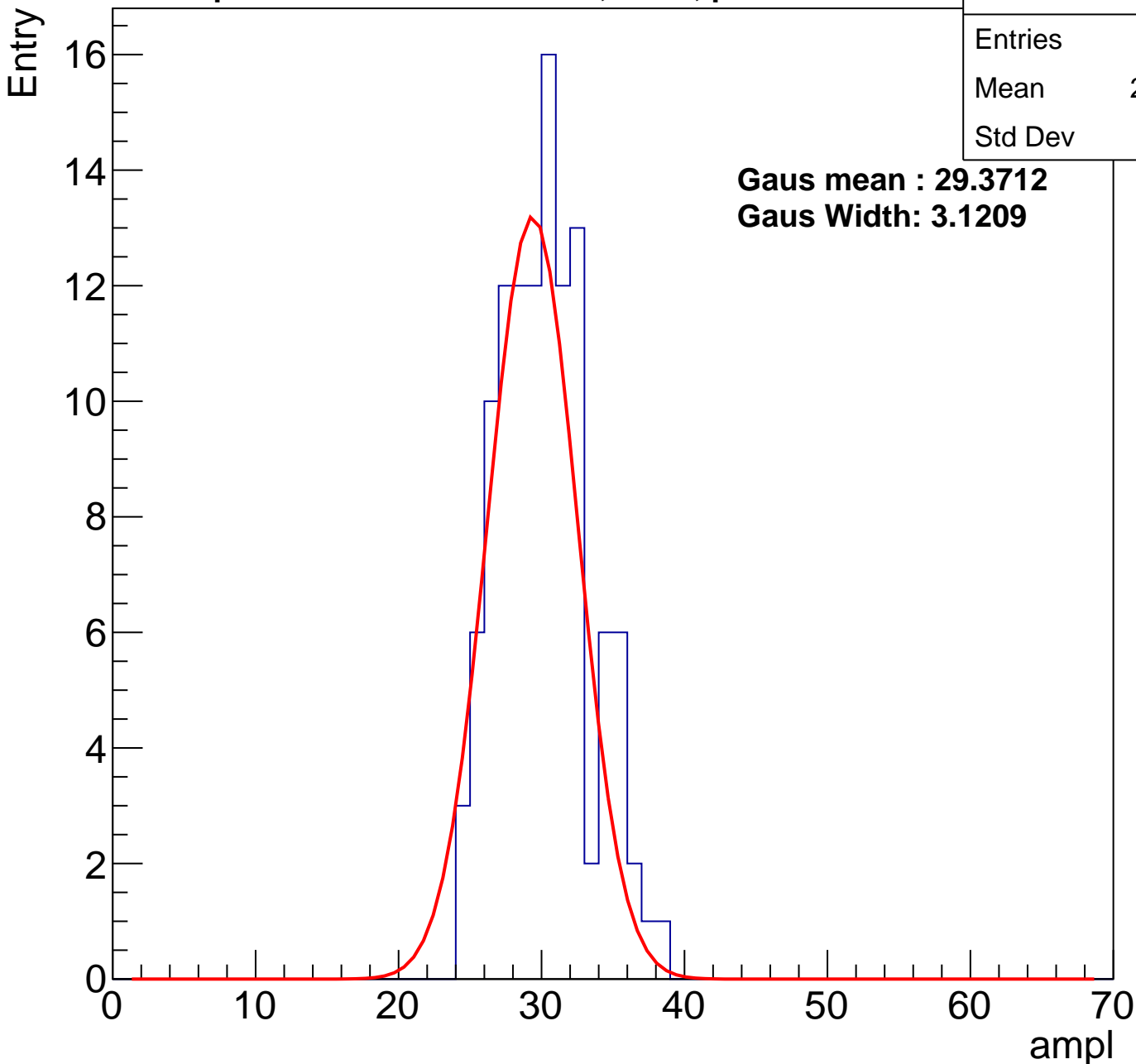
B1L001S, U25-ch86, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	29.69
Std Dev	3.09

Gaus mean : 29.3712

Gaus Width: 3.1209



B1L001S, U25-ch86, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

126

Mean

35.59

Std Dev

3.195

Gaus mean : 36.1077

Gaus Width: 3.3521

0

10

20

30

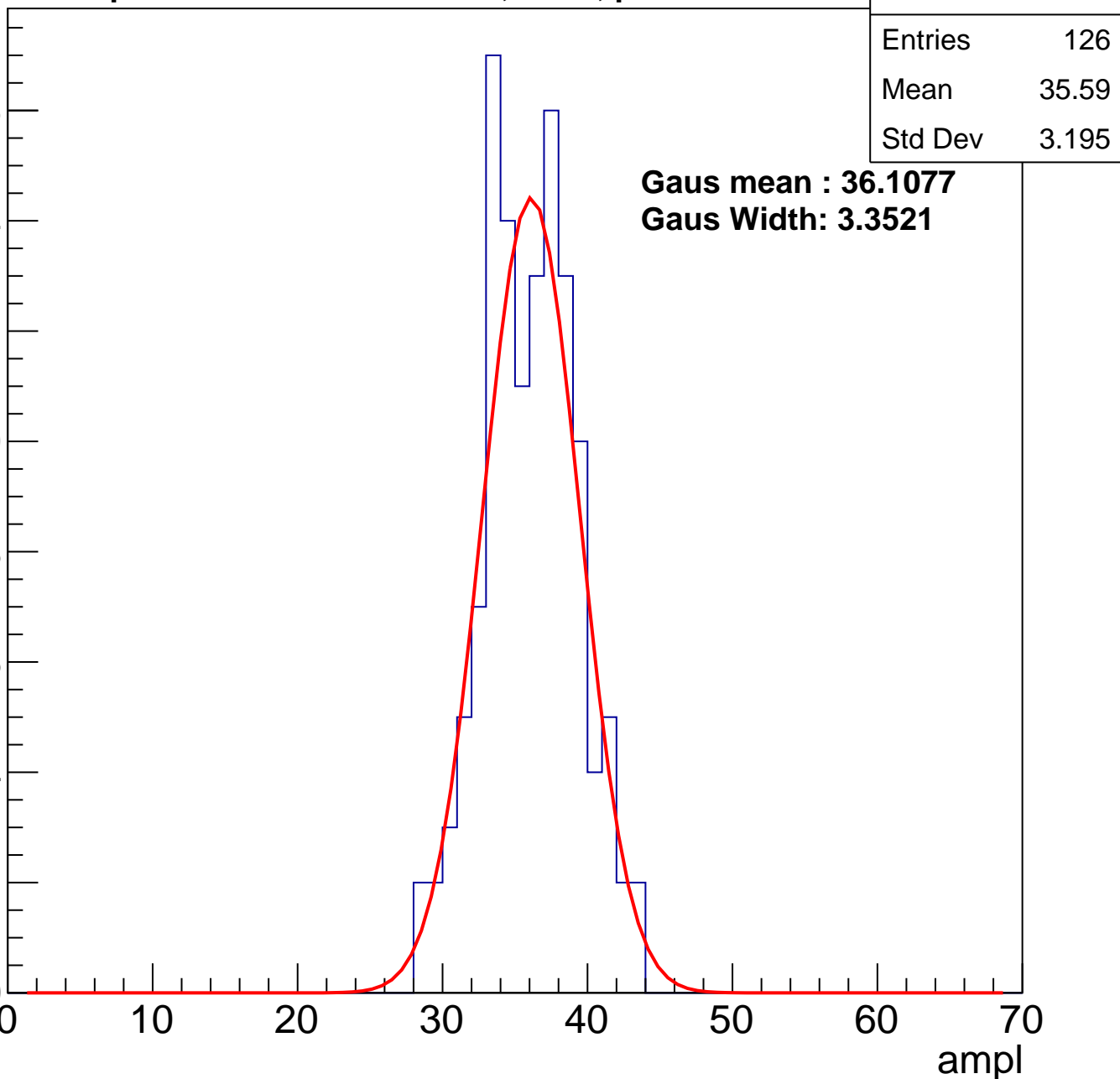
40

50

60

70

ampl



B1L001S, U25-ch86, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	41.85
Std Dev	3.307

Gaus mean : 42.4159

Gaus Width: 3.4930

0

10

20

30

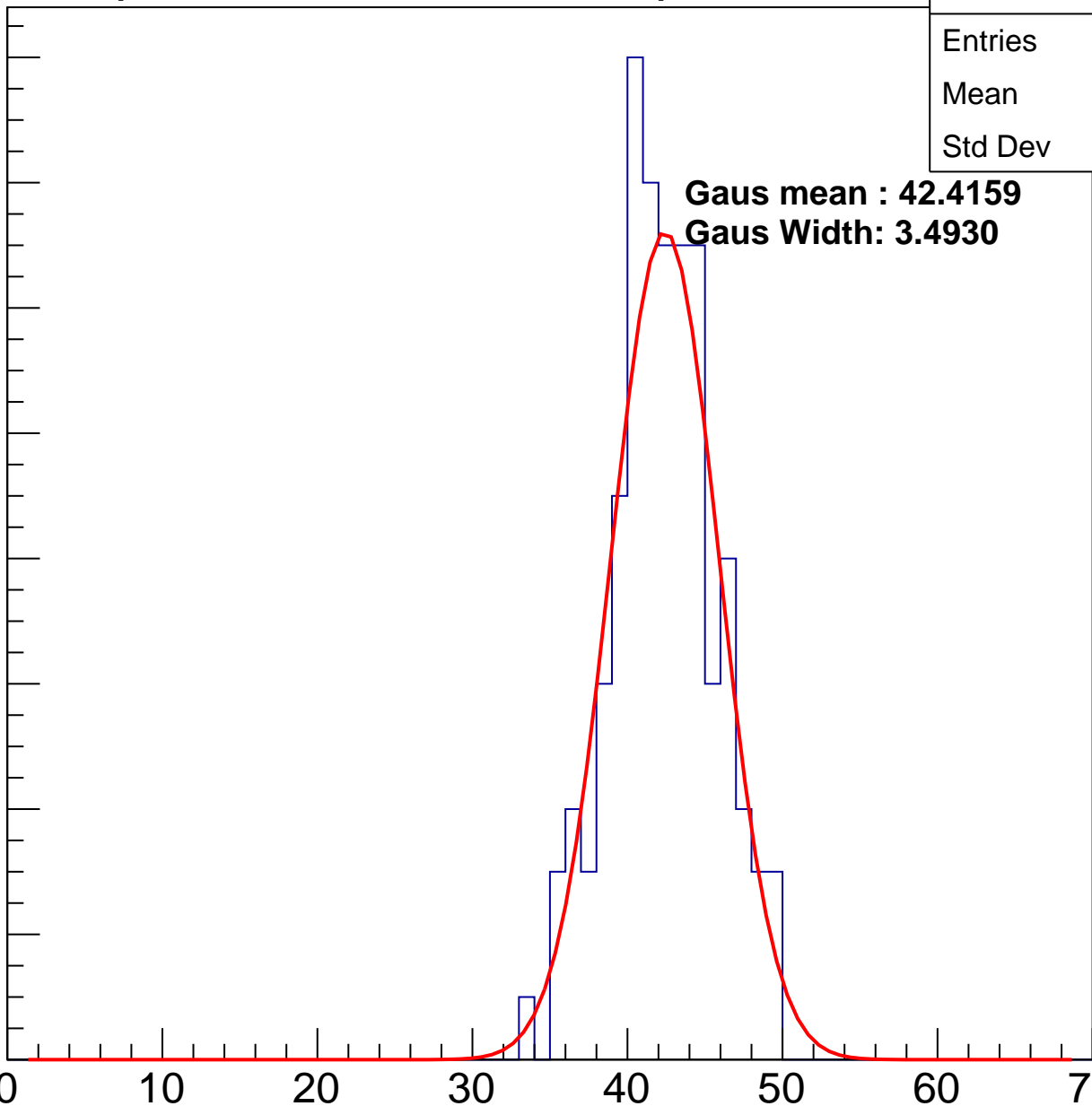
40

50

60

70

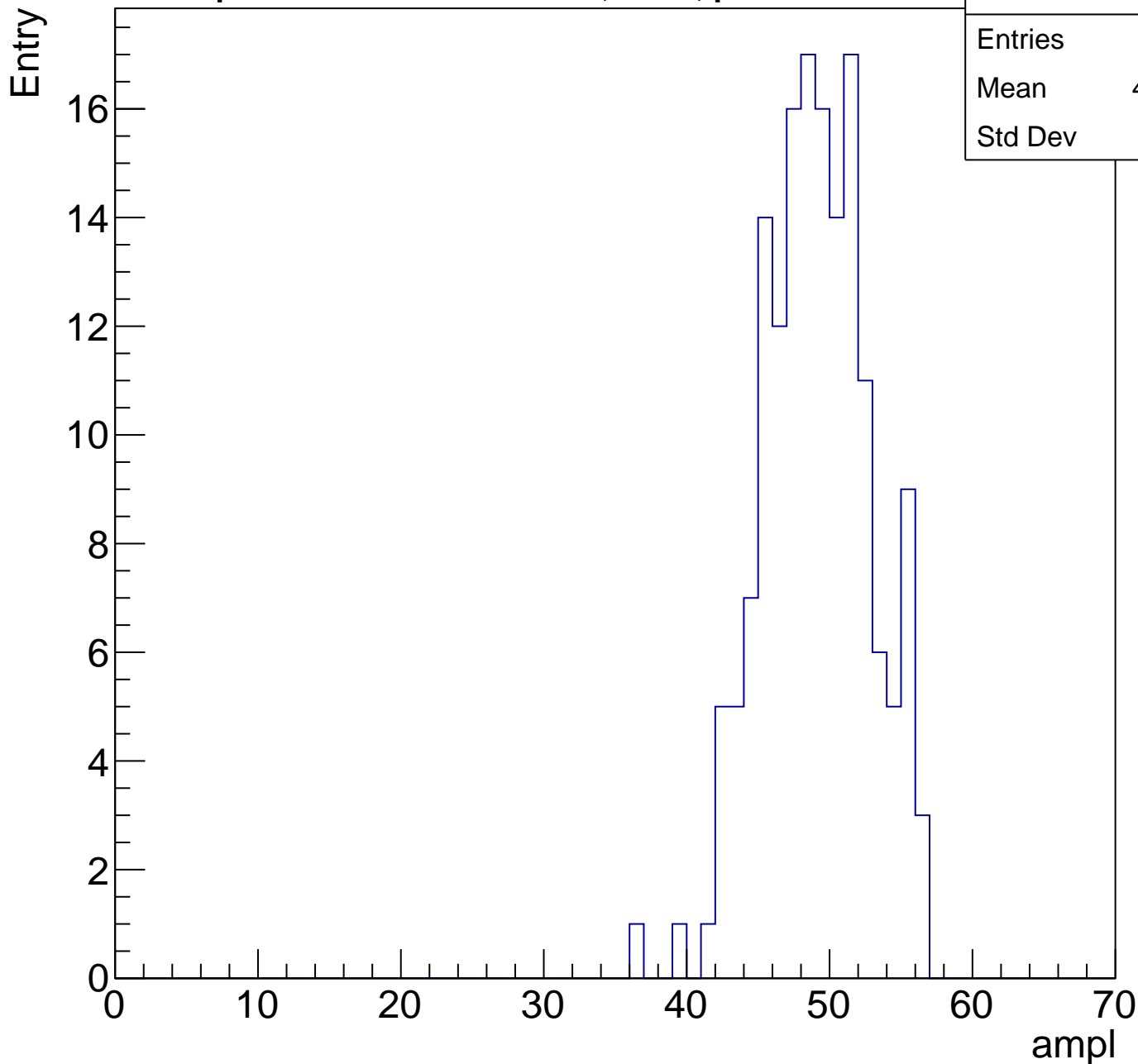
ampl



B1L001S, U25-ch86, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	48.58
Std Dev	3.7



B1L001S, U25-ch86, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

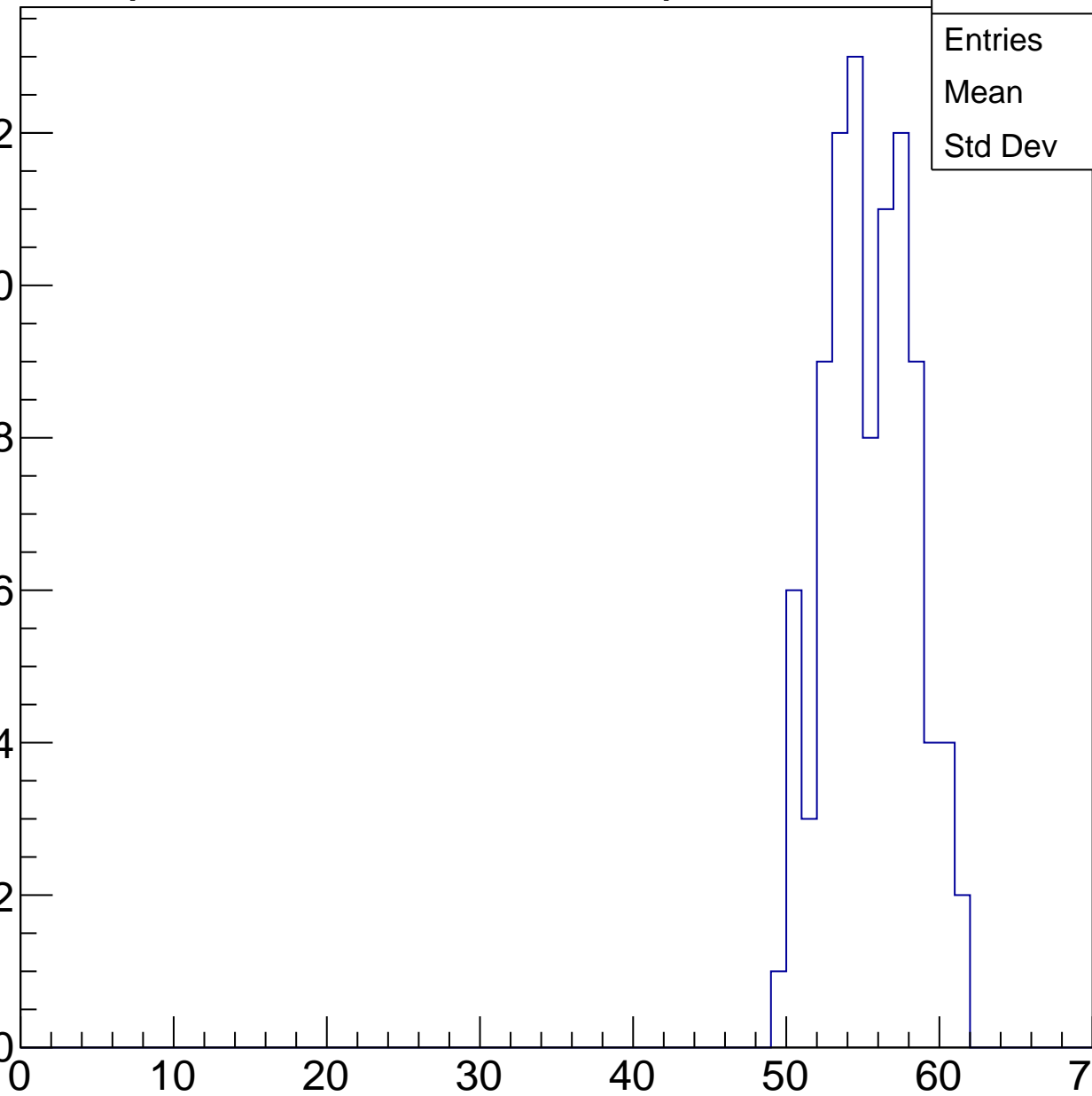
4

2

0

Entries	94
Mean	54.98
Std Dev	2.828

ampl



B1L001S, U25-ch86, adc5

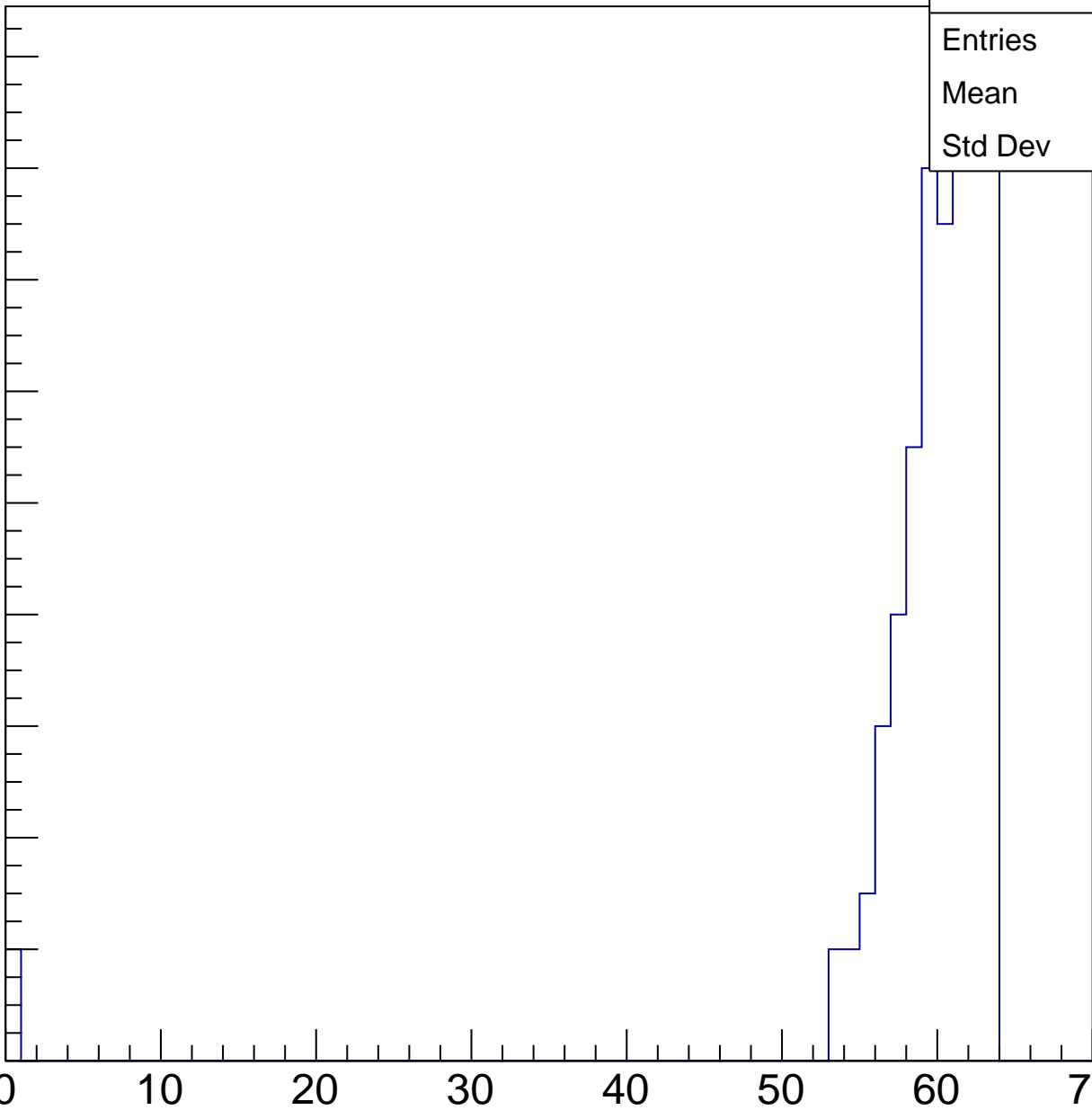
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	118
Mean	58.8
Std Dev	8.102

ampl



B1L001S, U25-ch86, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch86, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U25-ch87, adc0

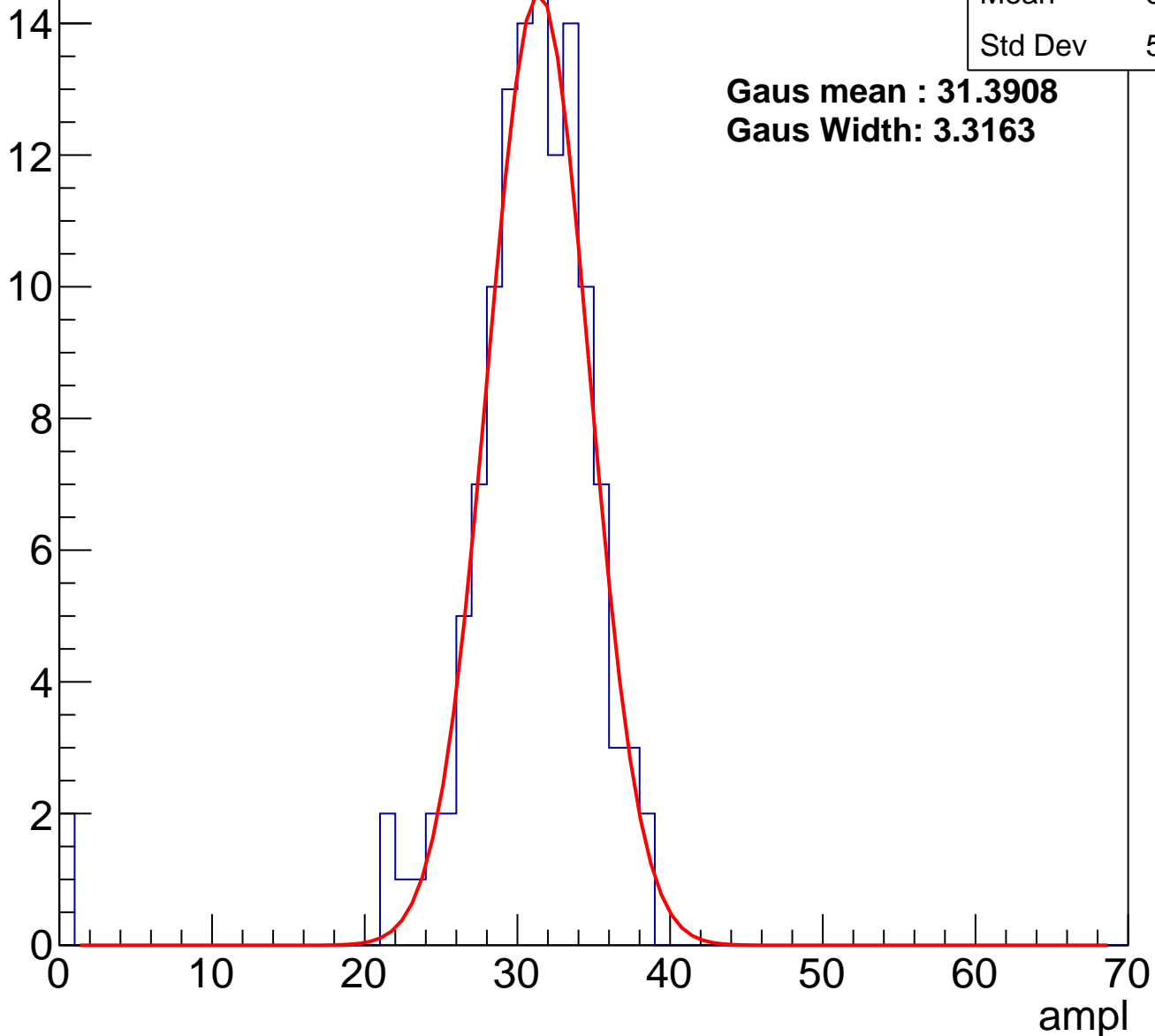
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	30.18
Std Dev	5.135

Gaus mean : 31.3908

Gaus Width: 3.3163

Entry



B1L001S, U25-ch87, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	37.71
Std Dev	3.695

Gaus mean : 38.1234

Gaus Width: 3.8684

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

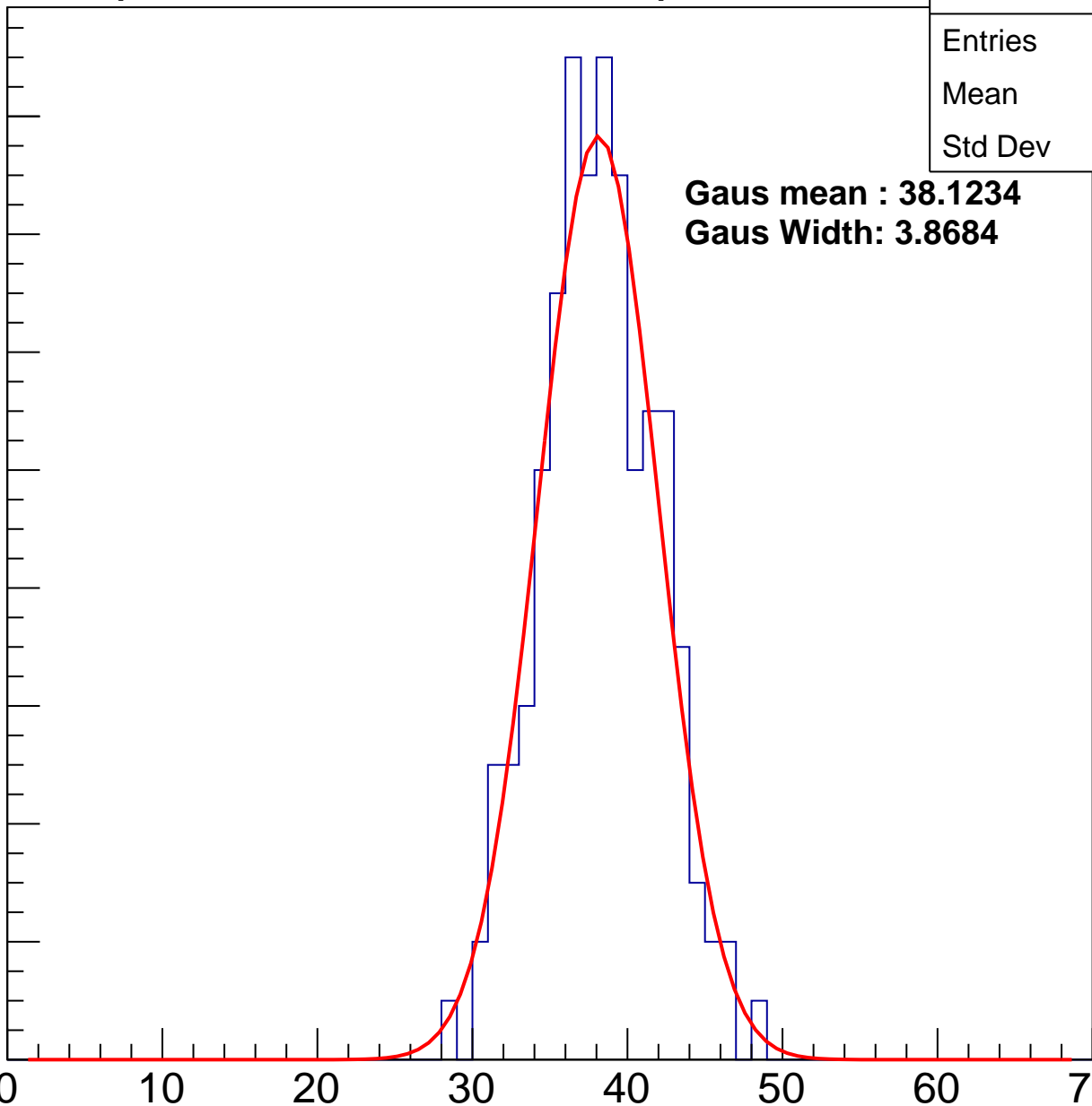
40

50

60

70

ampl



B1L001S, U25-ch87, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

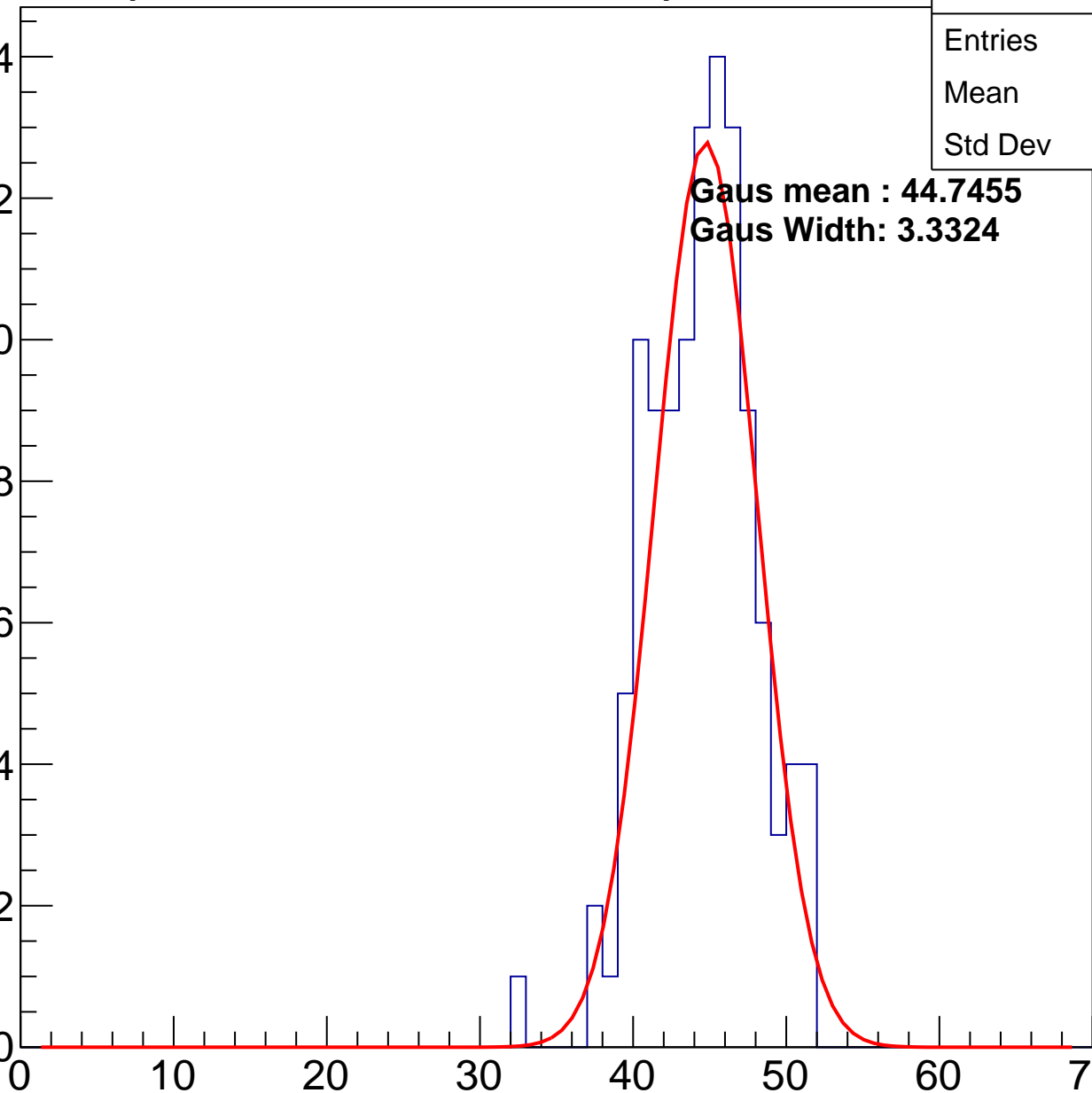
14
12
10
8
6
4
2
0

Entries	113
Mean	44.05
Std Dev	3.453

Gaus mean : 44.7455

Gaus Width: 3.3324

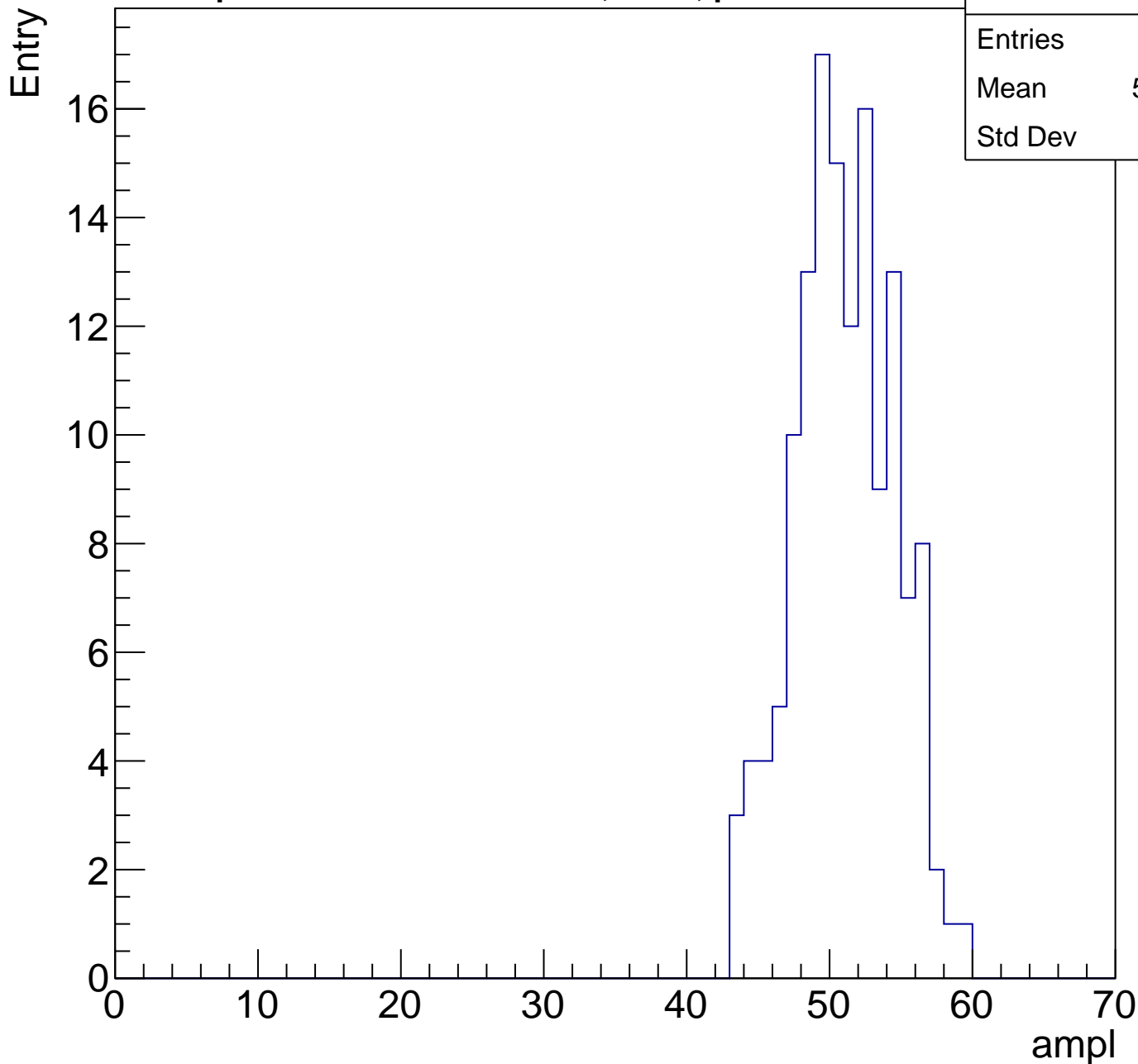
ampl



B1L001S, U25-ch87, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	50.56
Std Dev	3.44



B1L001S, U25-ch87, adc4

calib_packv5_042523_0143.root, FC#2, port C2

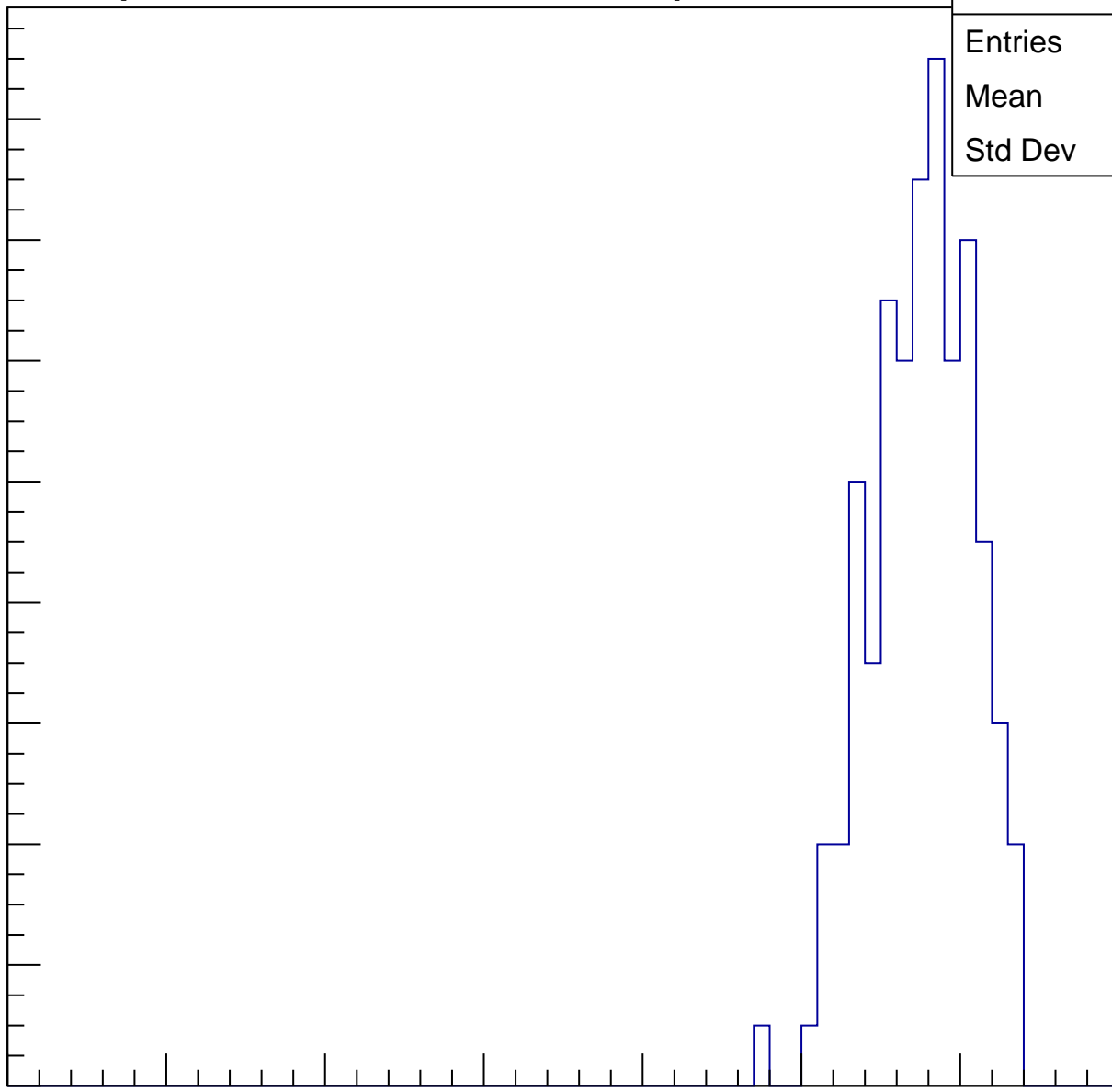
Entries	129
Mean	57.1
Std Dev	3.184

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch87, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

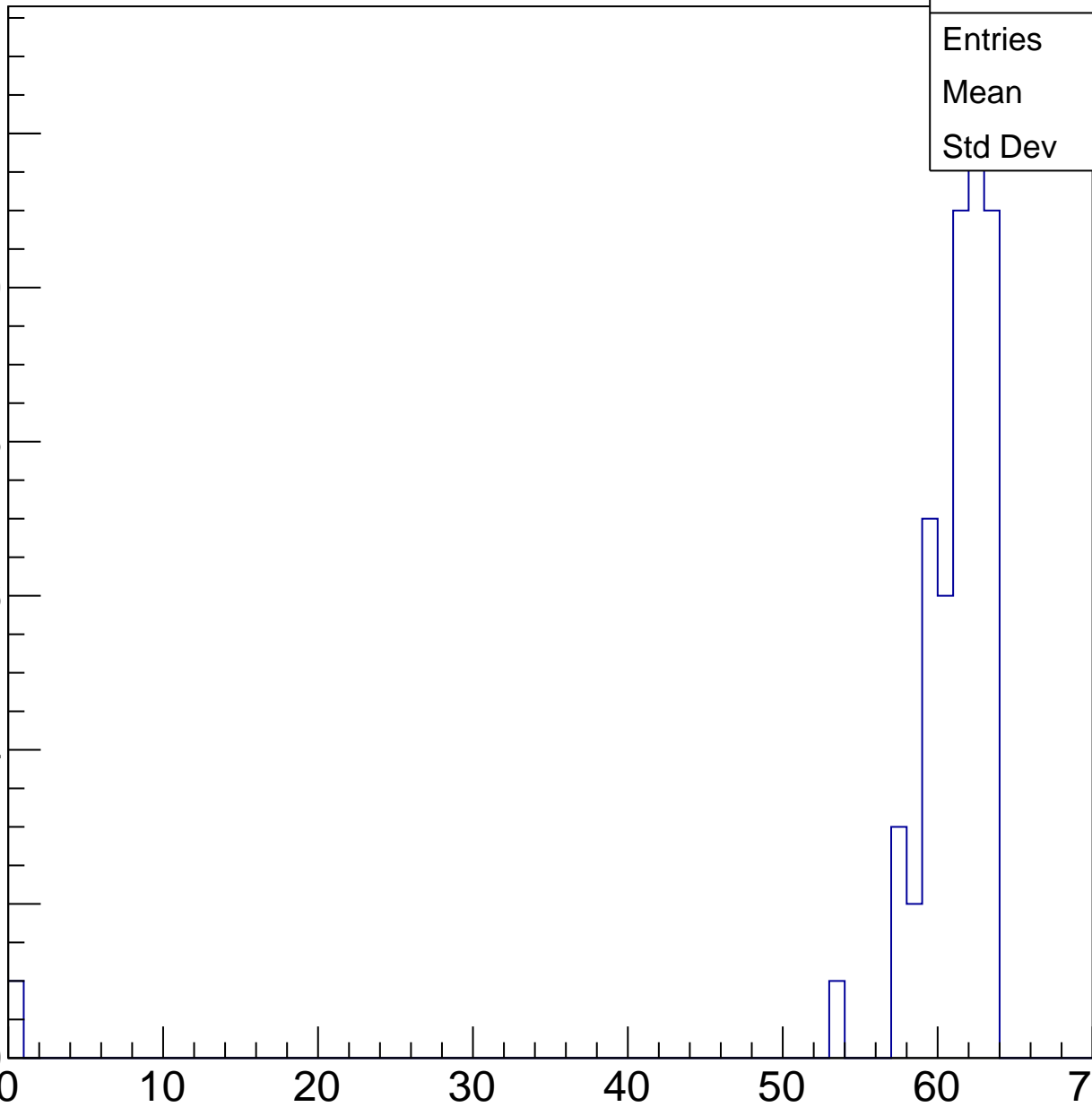
4

2

0

Entries	55
Mean	59.69
Std Dev	8.364

ampl

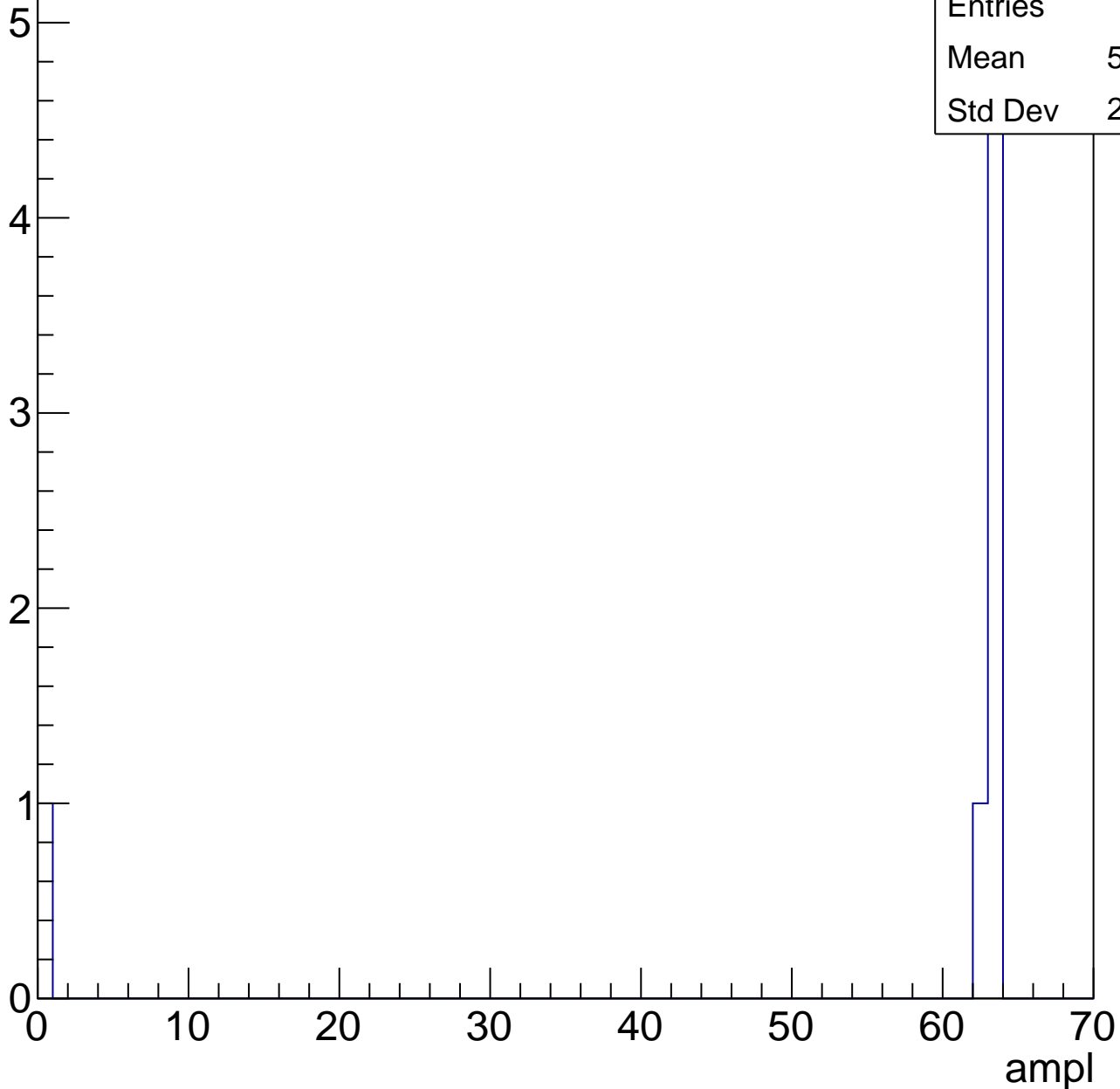


B1L001S, U25-ch87, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	53.86
Std Dev	21.99



B1L001S, U25-ch87, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch88, adc0

calib_packv5_042523_0143.root, FC#2, port C2

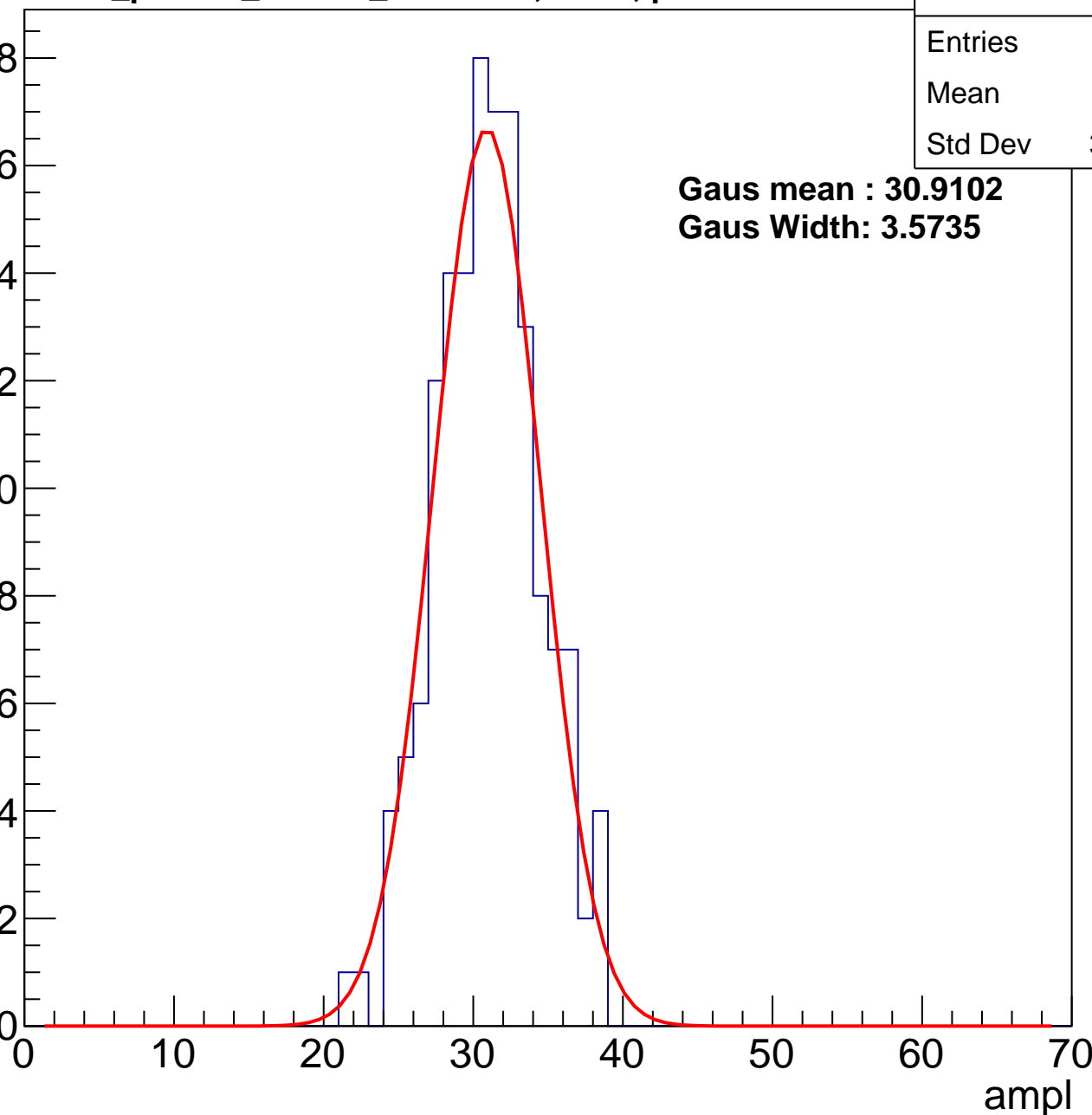
Entry

18
16
14
12
10
8
6
4
2
0

Entries	150
Mean	30.51
Std Dev	3.425

Gaus mean : 30.9102

Gaus Width: 3.5735



B1L001S, U25-ch88, adc1

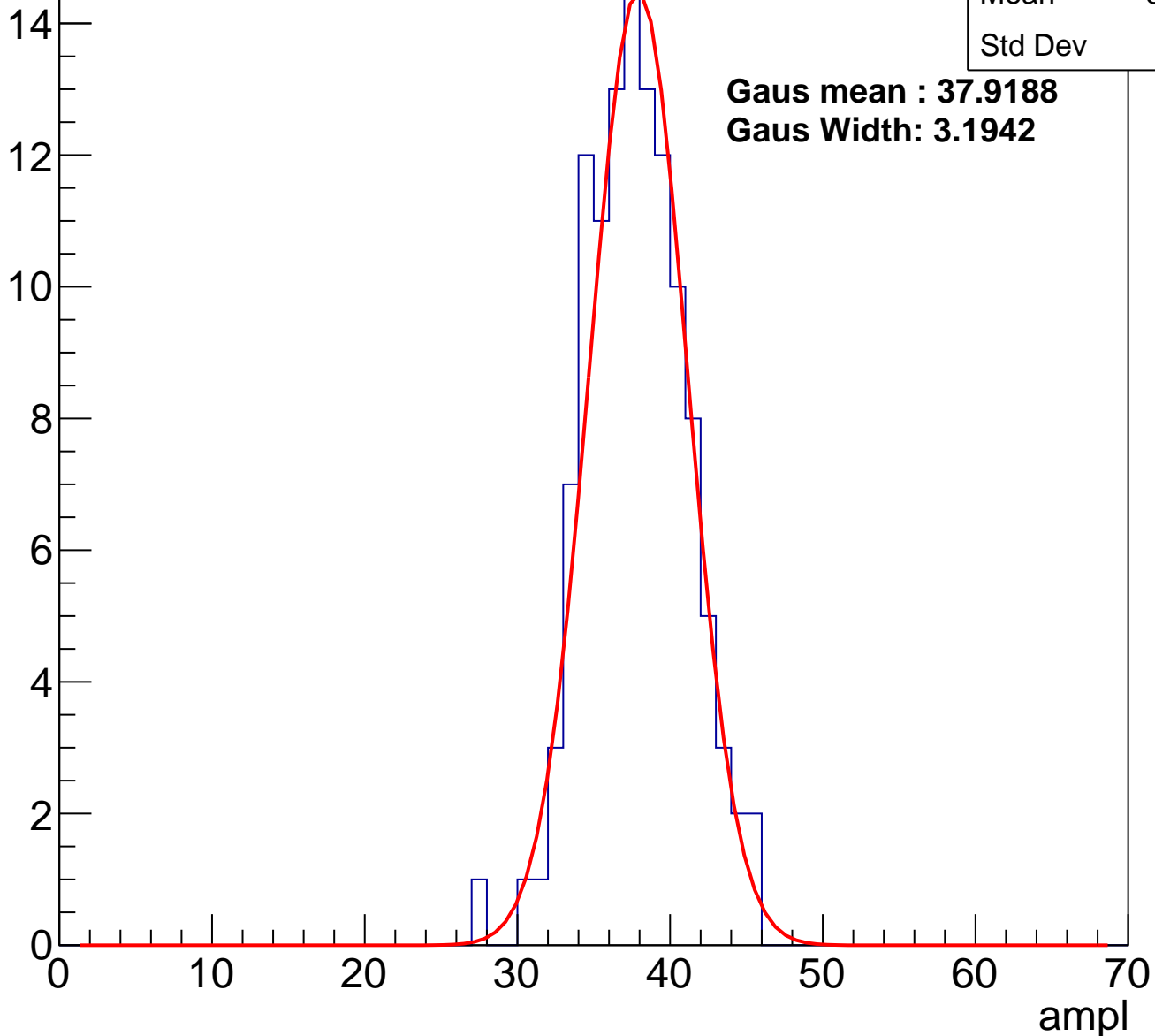
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	37.29
Std Dev	3.26

Gaus mean : 37.9188

Gaus Width: 3.1942

Entry



B1L001S, U25-ch88, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

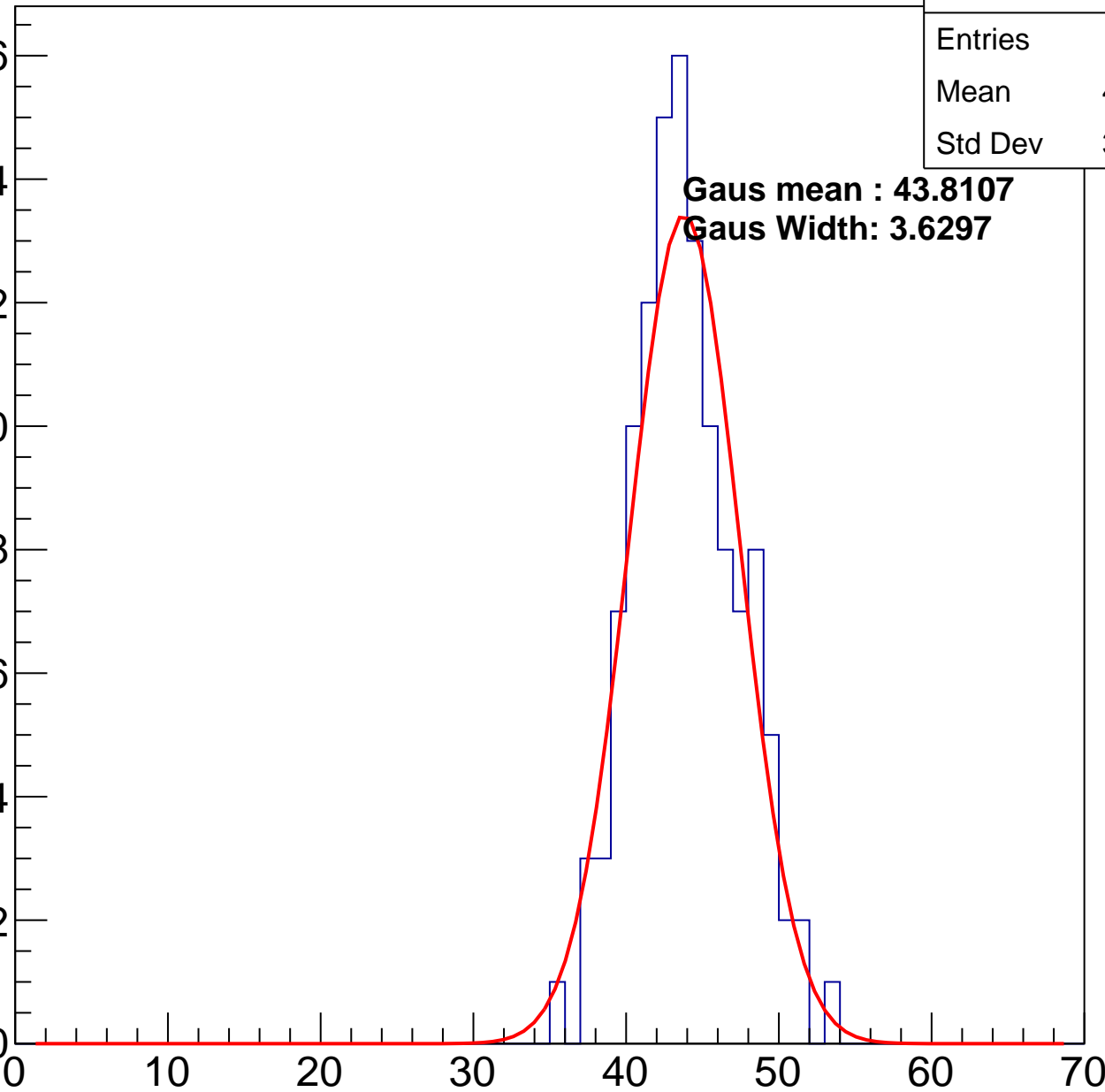
16
14
12
10
8
6
4
2
0

Entries	123
Mean	43.46
Std Dev	3.408

Gaus mean : 43.8107

Gaus Width: 3.6297

ampl



B1L001S, U25-ch88, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	49.82
Std Dev	3.498

Entry

14

12

10

8

6

4

2

0

0

10

20

30

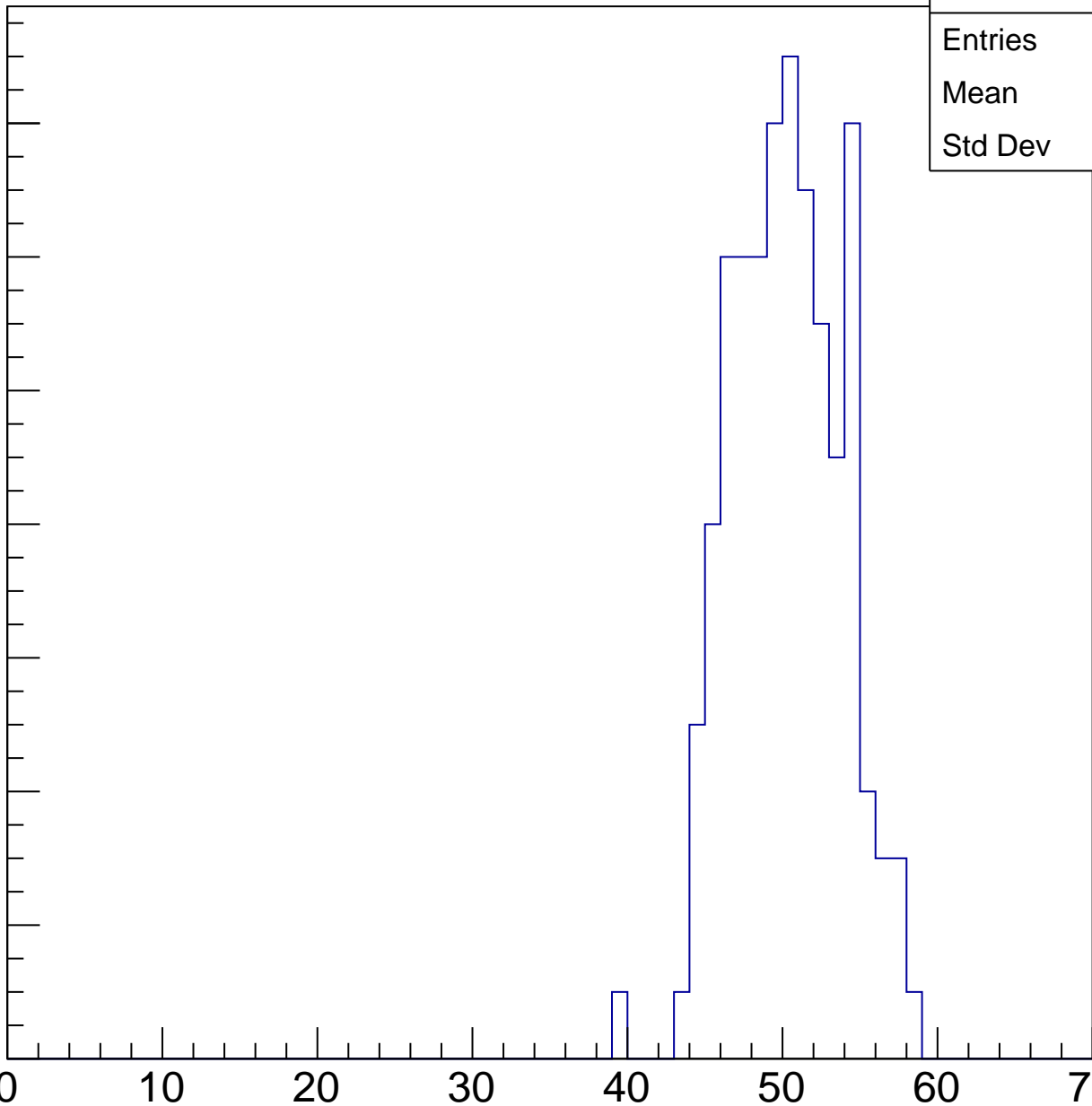
40

50

60

70

ampl



B1L001S, U25-ch88, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	55.81
Std Dev	3.134

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

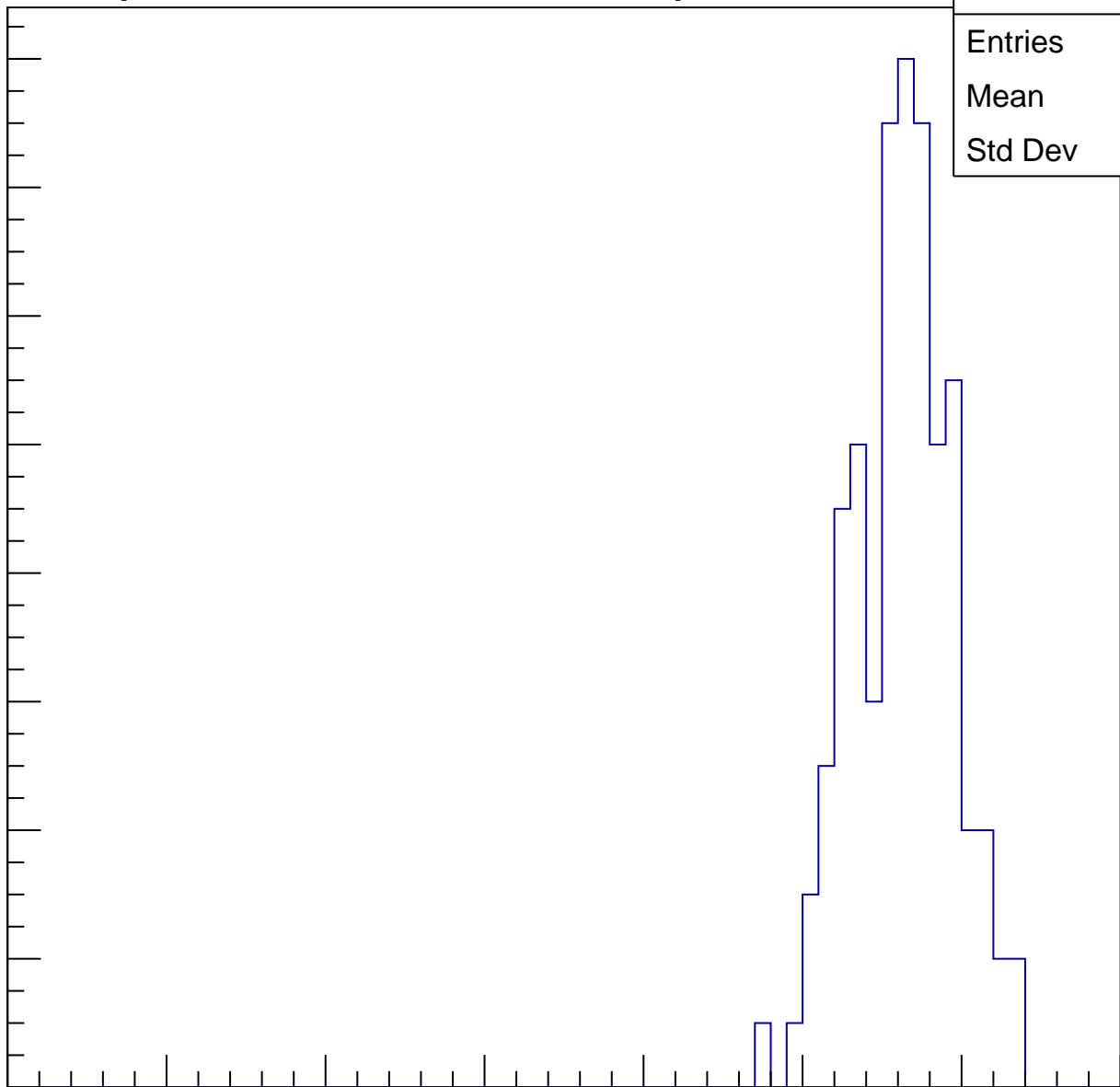
40

50

60

70

ampl



B1L001S, U25-ch88, adc5

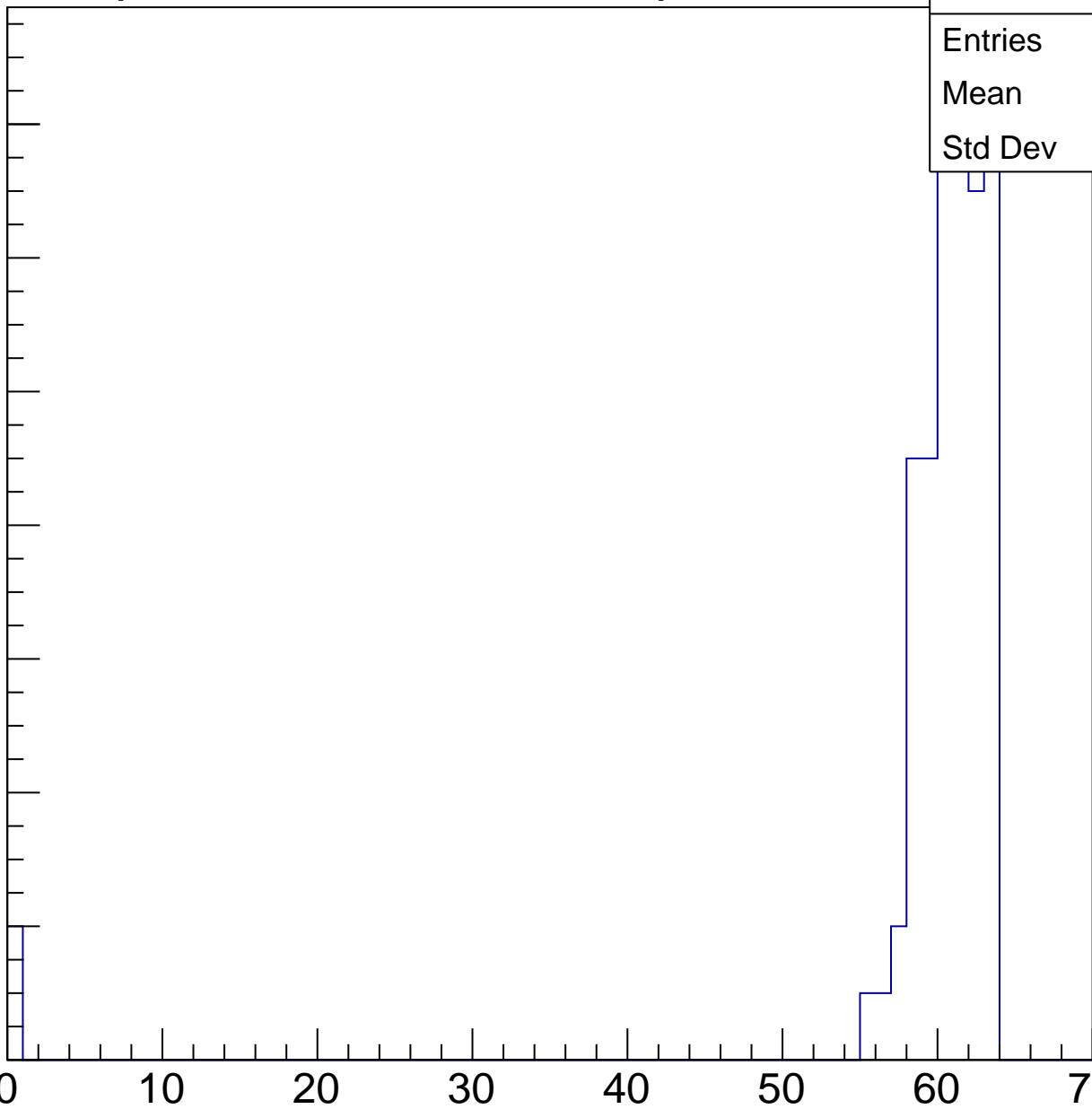
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	59.04
Std Dev	9.638

ampl

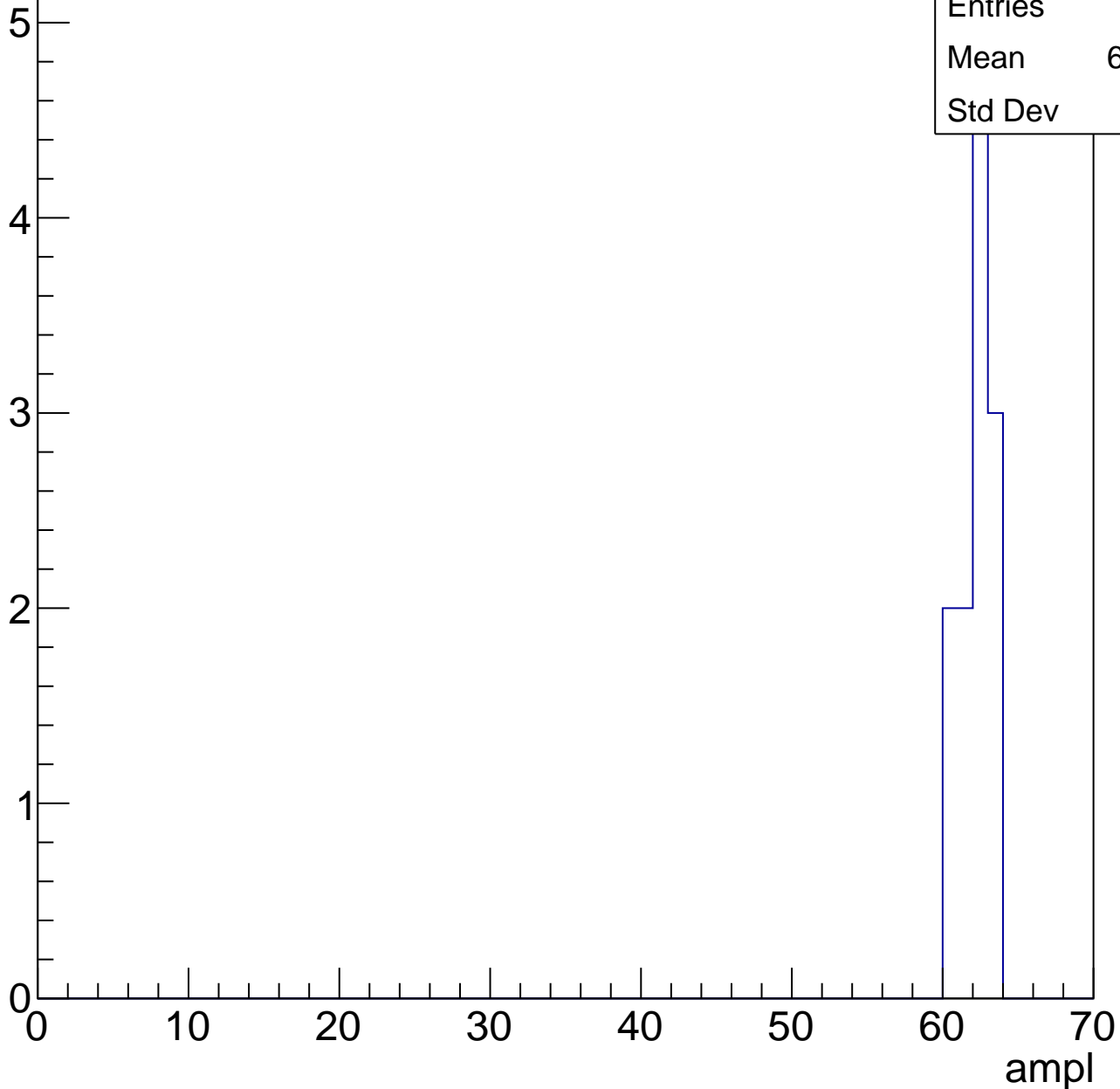


B1L001S, U25-ch88, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.75
Std Dev	1.01



B1L001S, U25-ch88, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

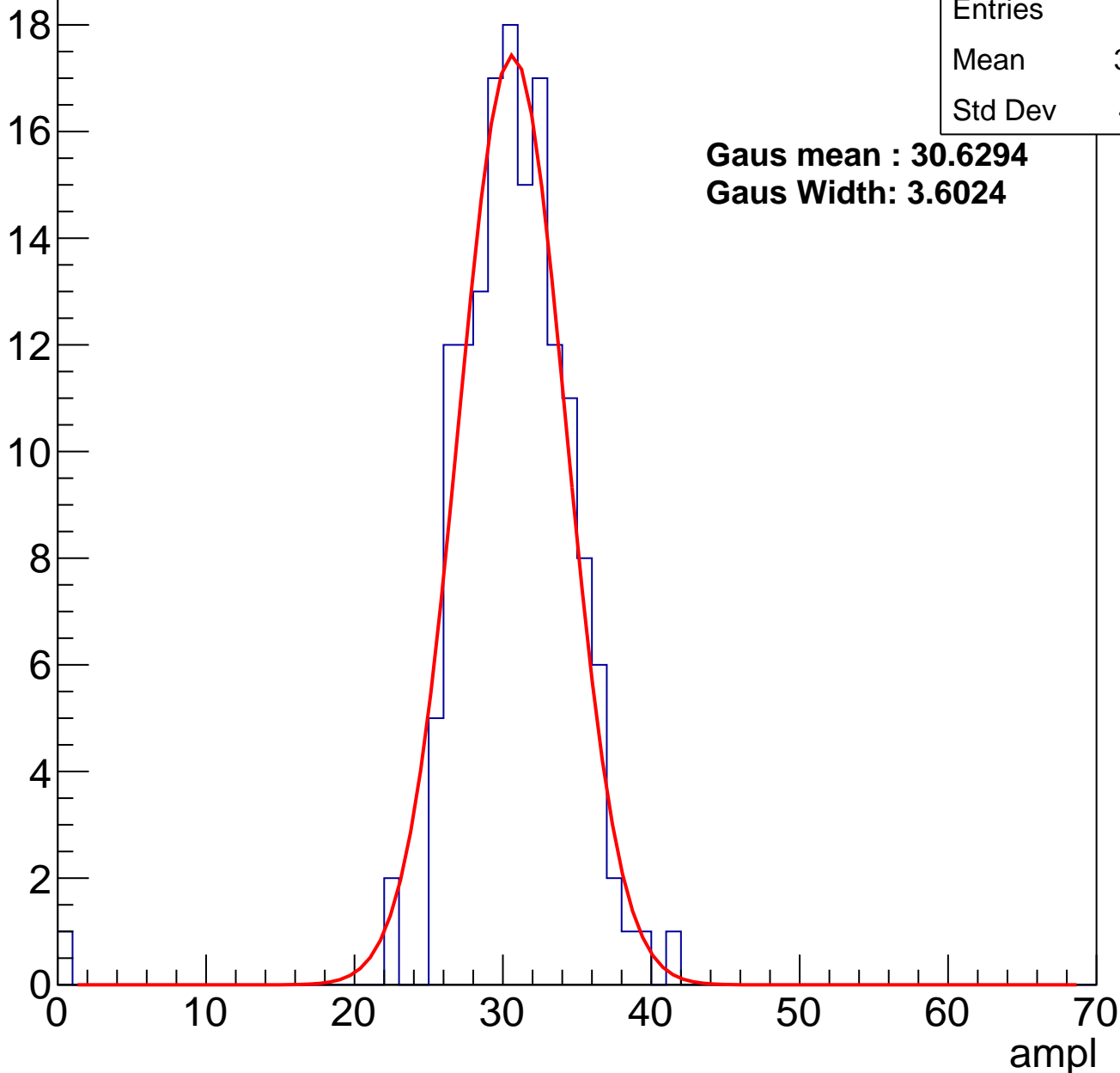


Entries	1
Mean	22
Std Dev	0

B1L001S, U25-ch89, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

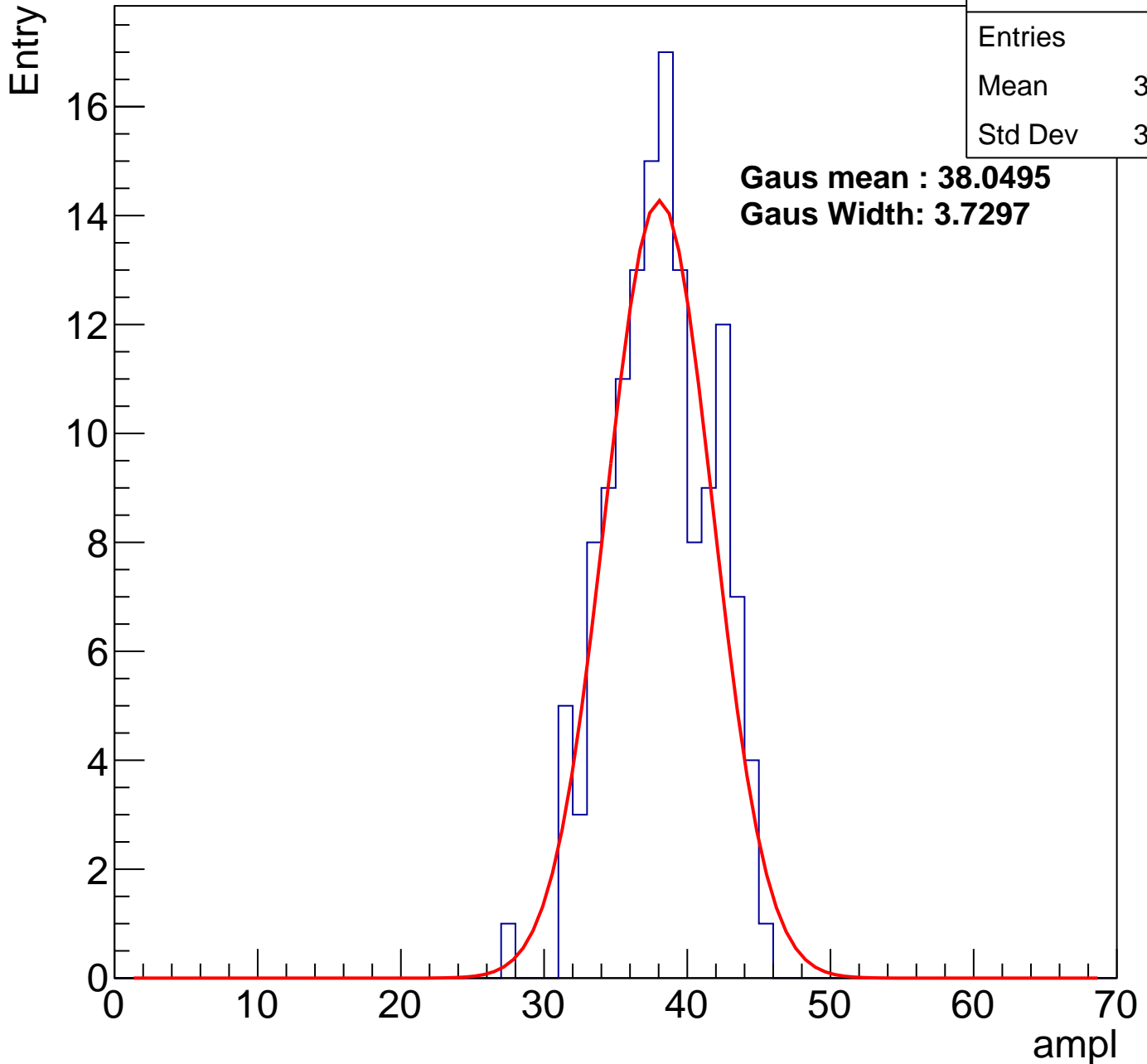


B1L001S, U25-ch89, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	37.68
Std Dev	3.489

Gaus mean : 38.0495
Gaus Width: 3.7297



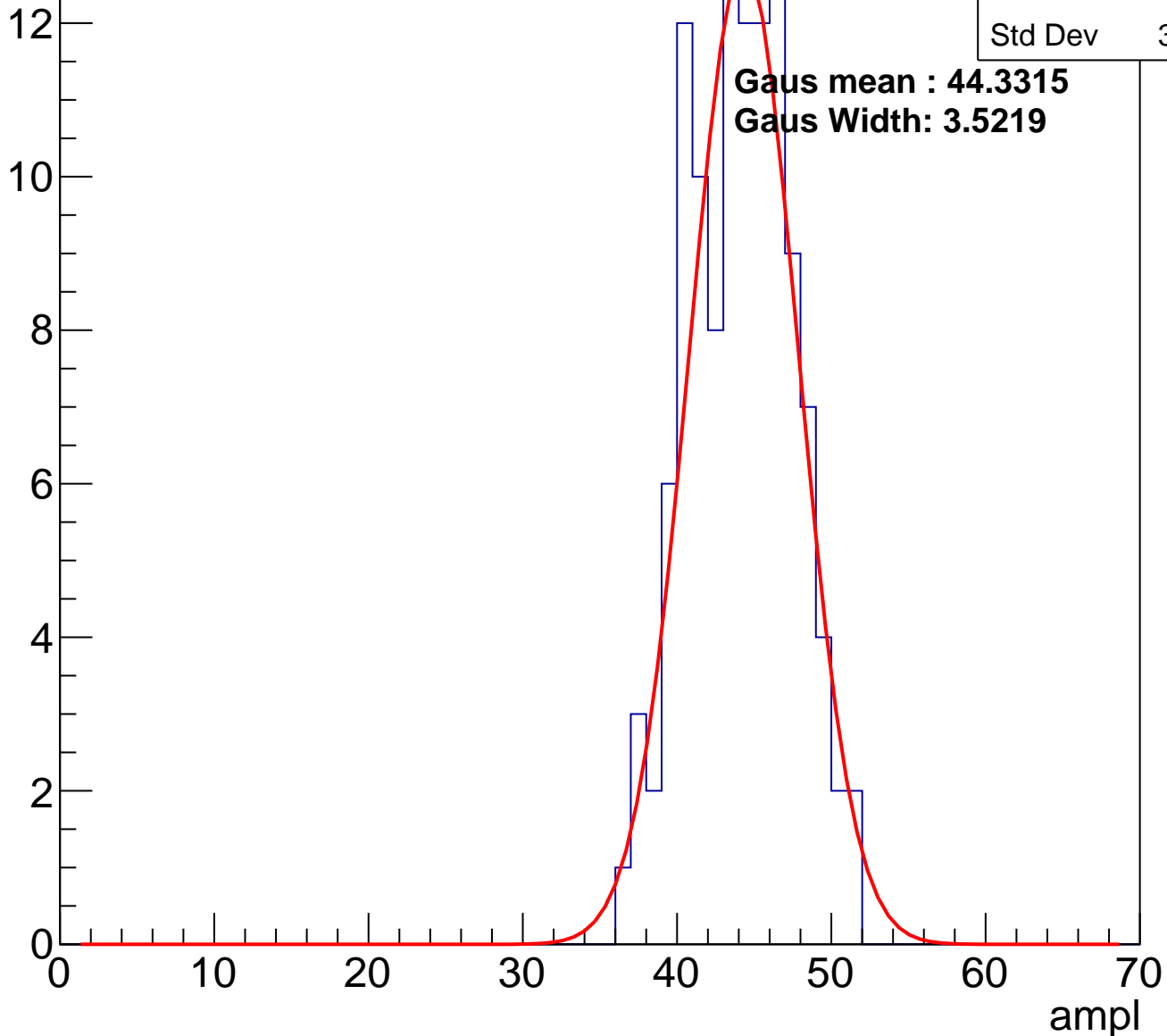
B1L001S, U25-ch89, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	43.66
Std Dev	3.314

Gaus mean : 44.3315
Gaus Width: 3.5219

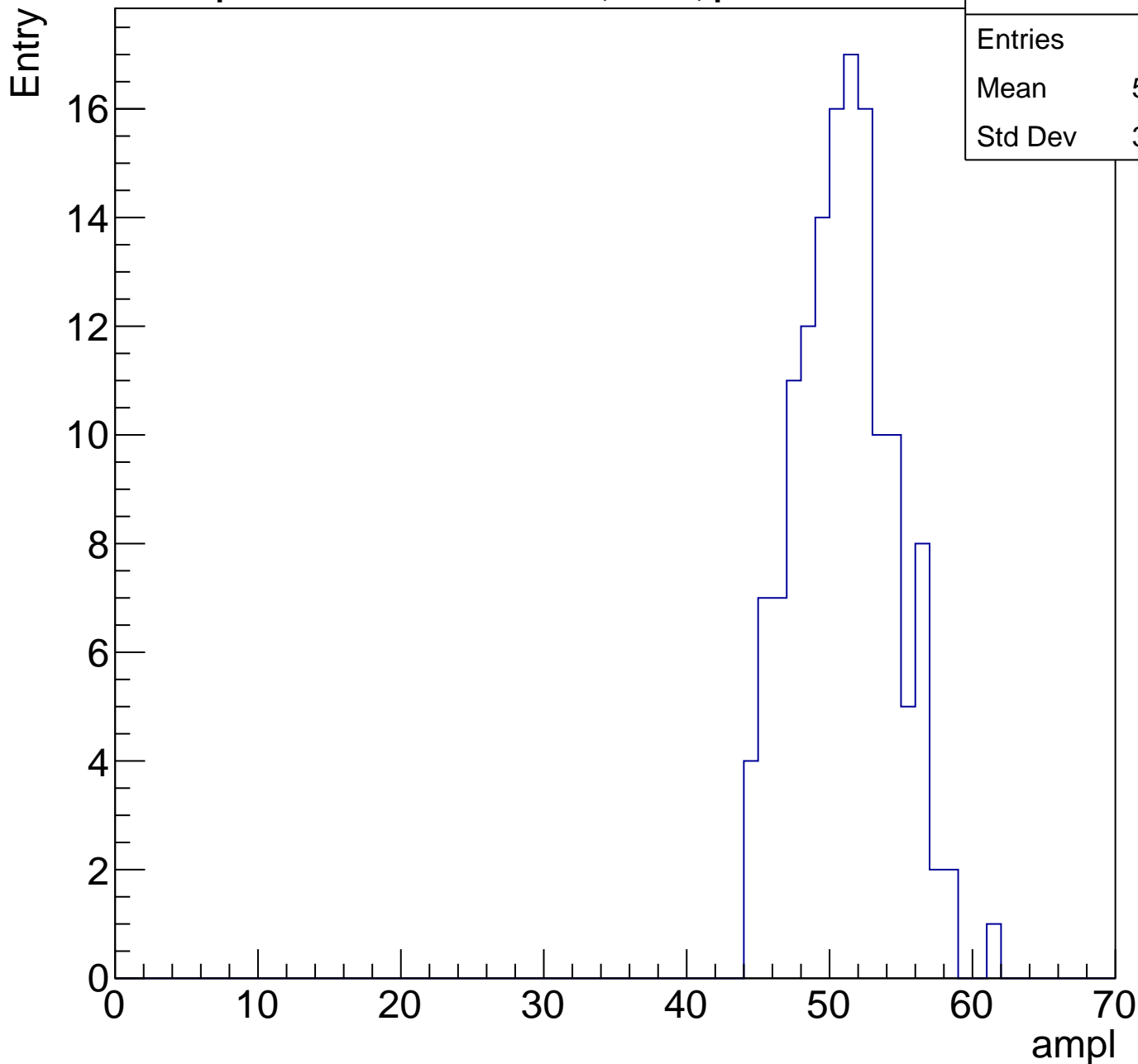
Entry



B1L001S, U25-ch89, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	50.53
Std Dev	3.402



B1L001S, U25-ch89, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

144

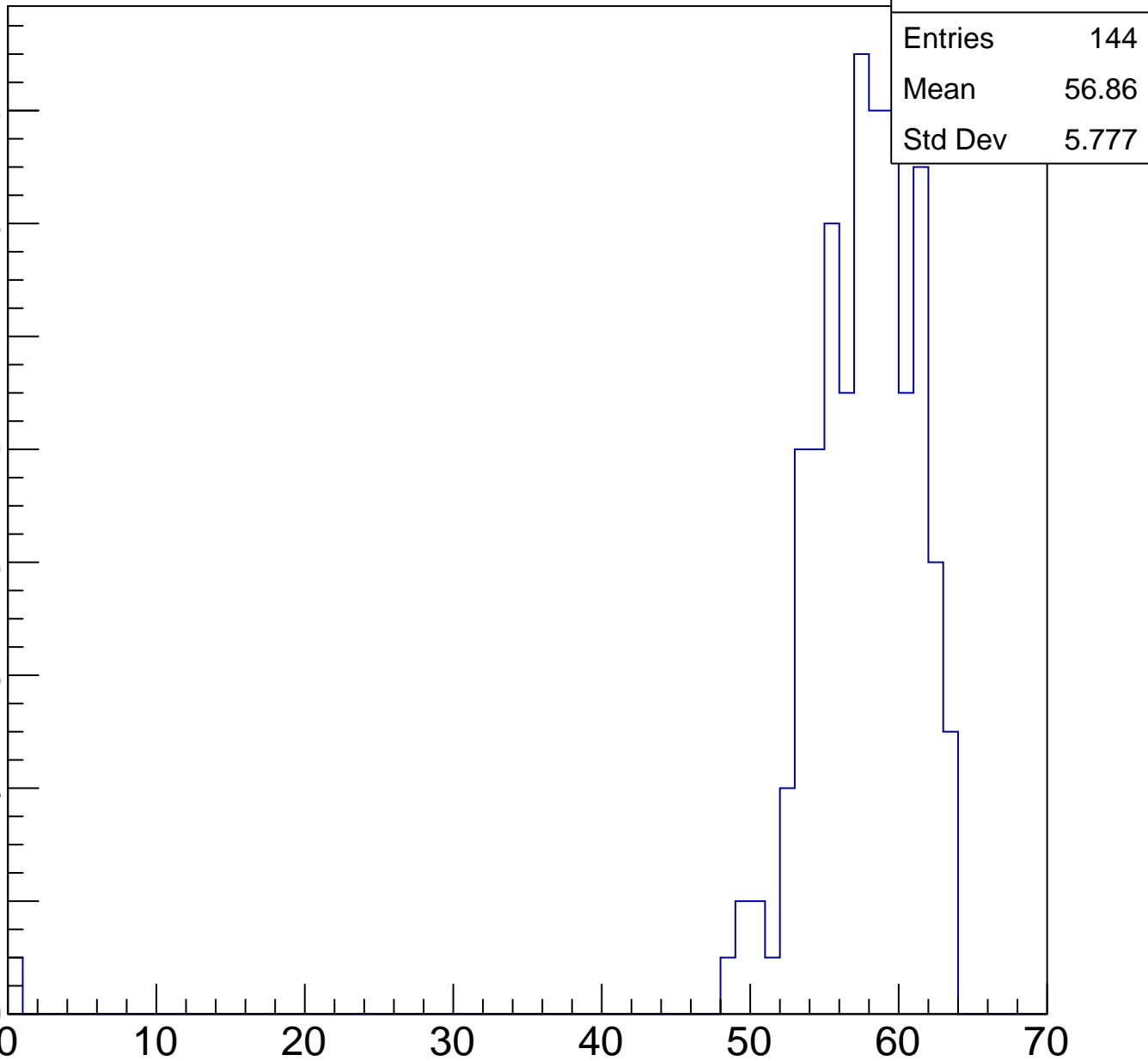
Mean

56.86

Std Dev

5.777

ampl



B1L001S, U25-ch89, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	47
Mean	59.66
Std Dev	9.011

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch89, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch89, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch90, adc0

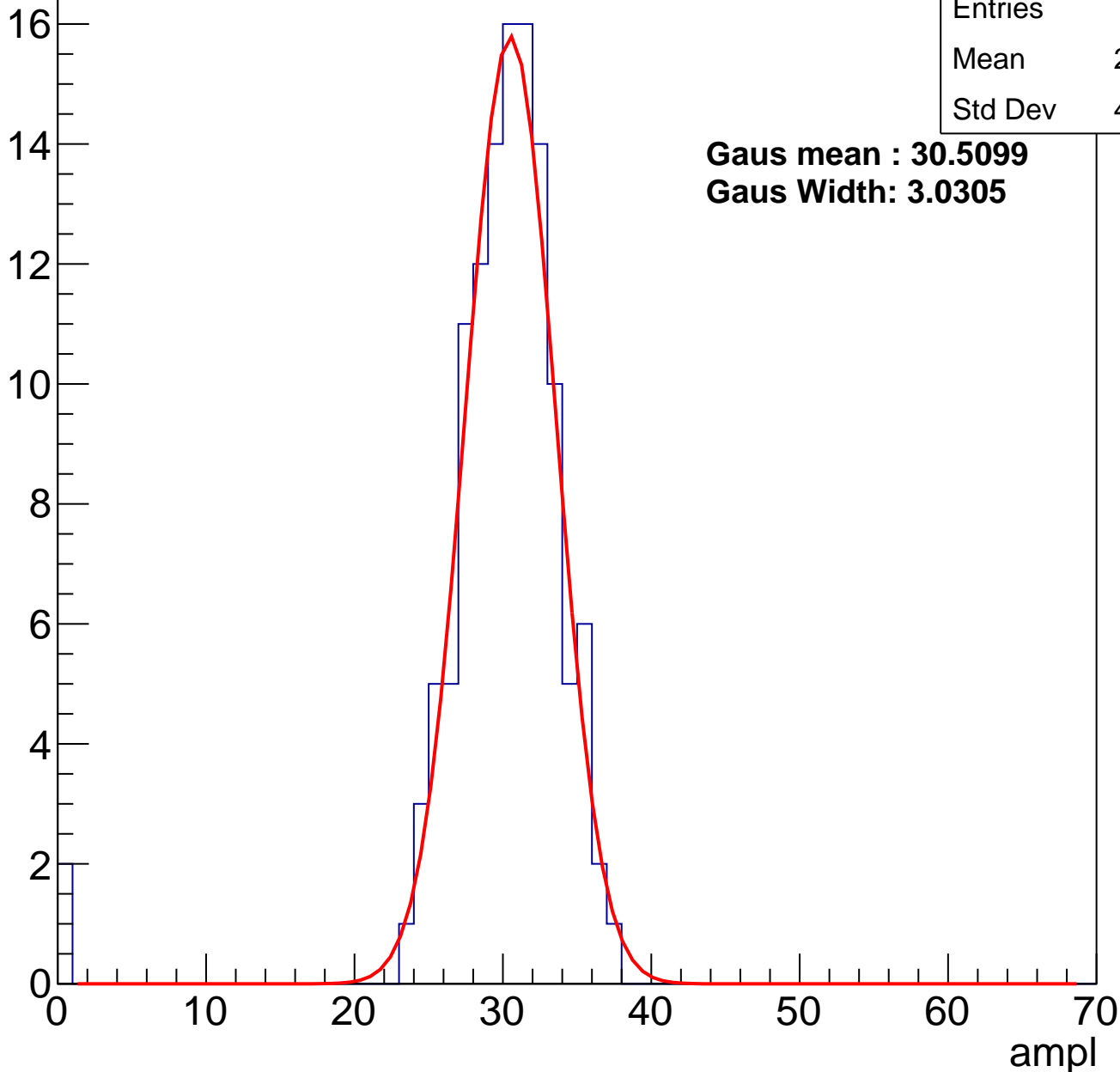
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	29.53
Std Dev	4.779

Gaus mean : 30.5099

Gaus Width: 3.0305

Entry



B1L001S, U25-ch90, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	37.02
Std Dev	3.35

Gaus mean : 37.5392

Gaus Width: 3.3560

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

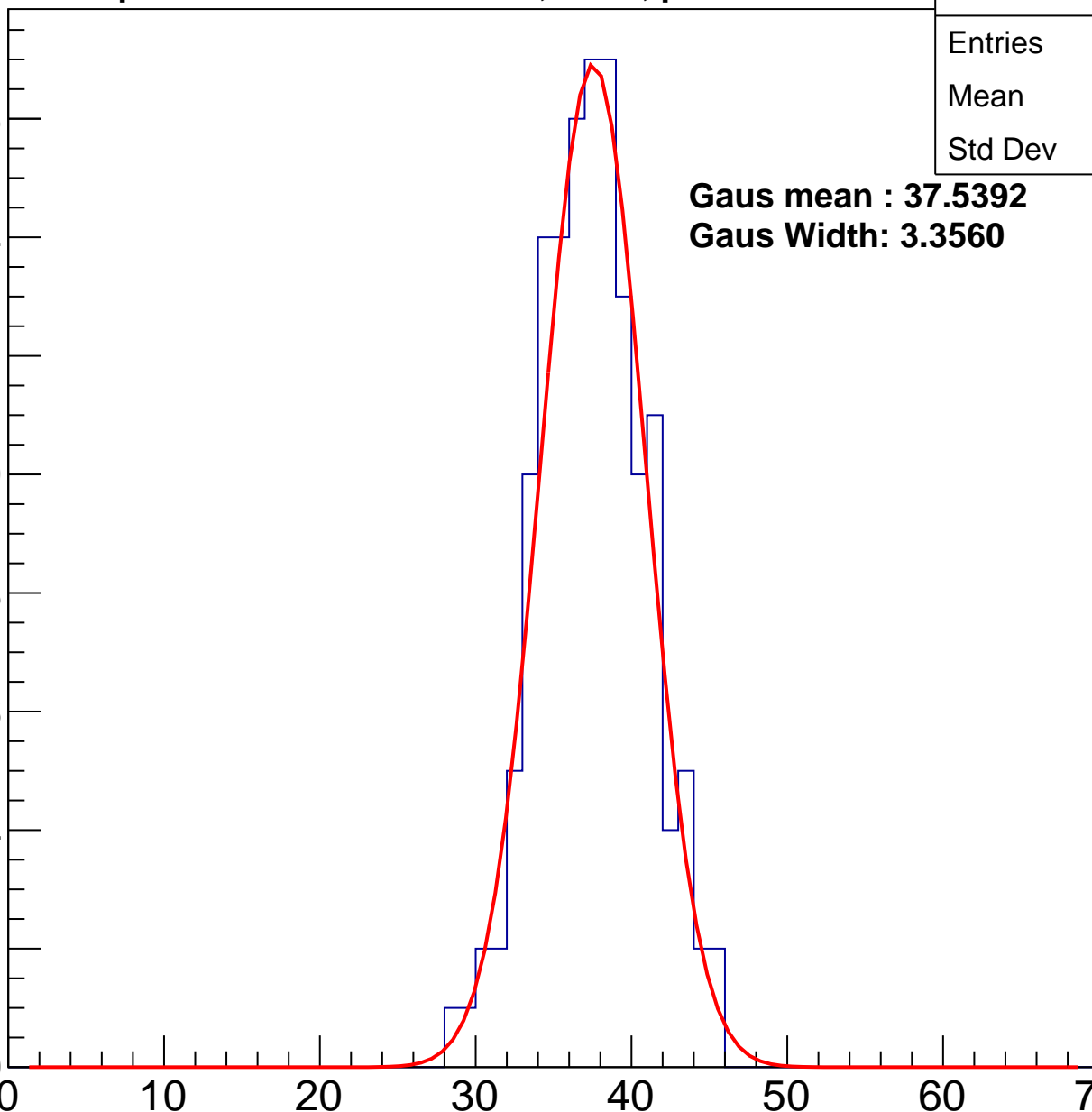
40

50

60

70

ampl



B1L001S, U25-ch90, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

108

Mean

43.14

Std Dev

3.084

Gaus mean : 43.4943

Gaus Width: 3.0284

0

10

20

30

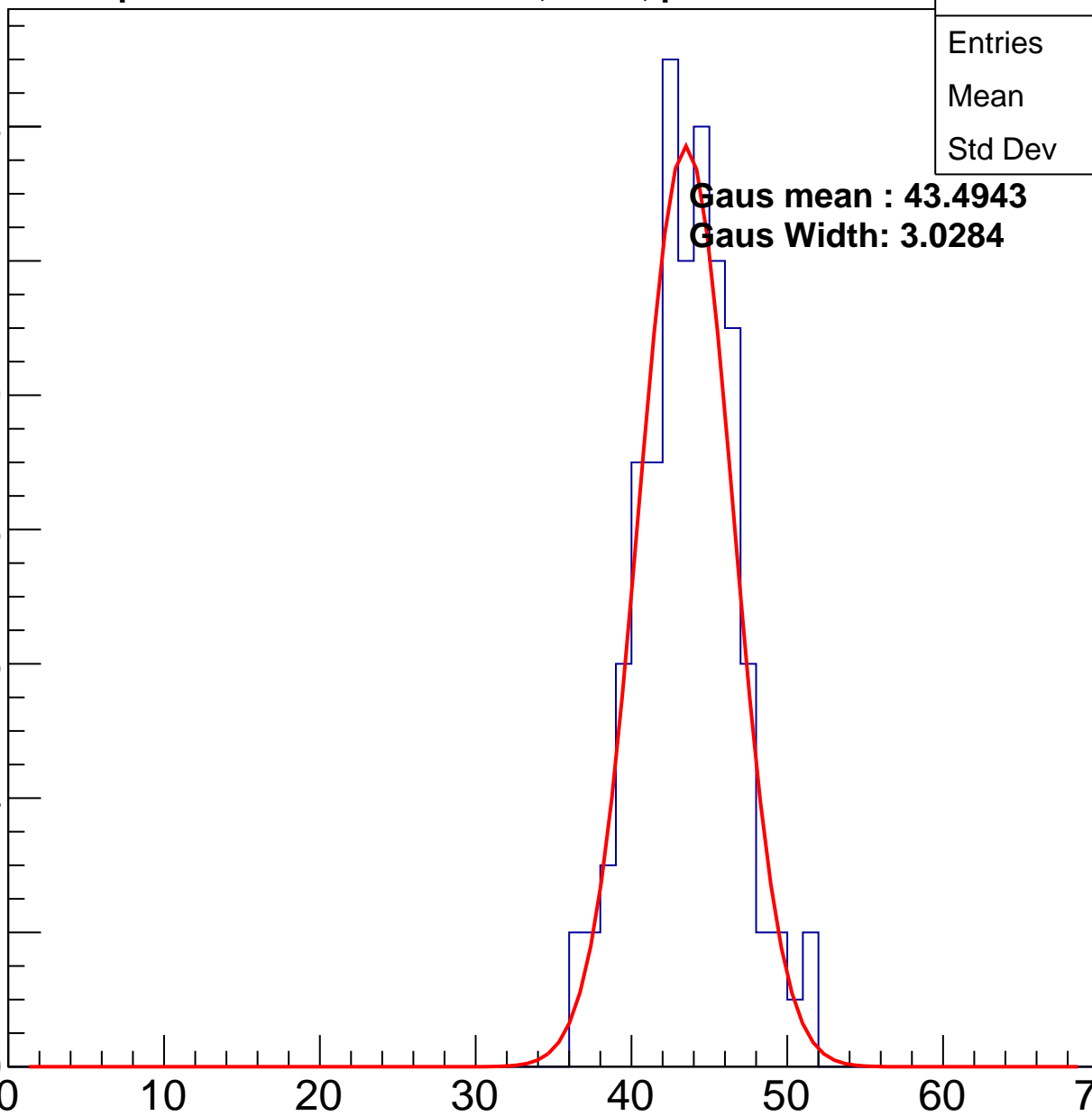
40

50

60

70

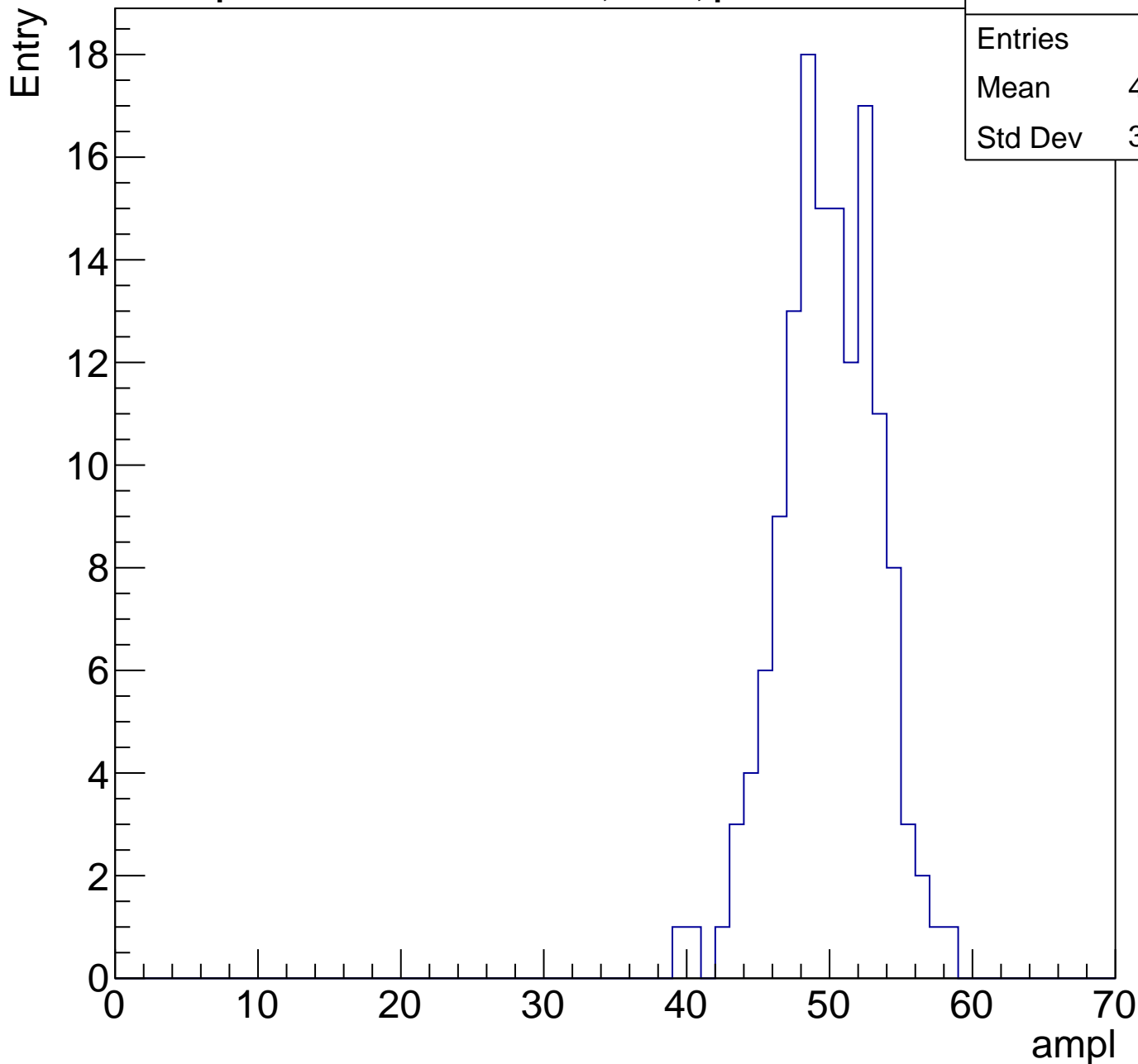
ampl



B1L001S, U25-ch90, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	49.45
Std Dev	3.368



B1L001S, U25-ch90, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	56.25
Std Dev	3.357

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

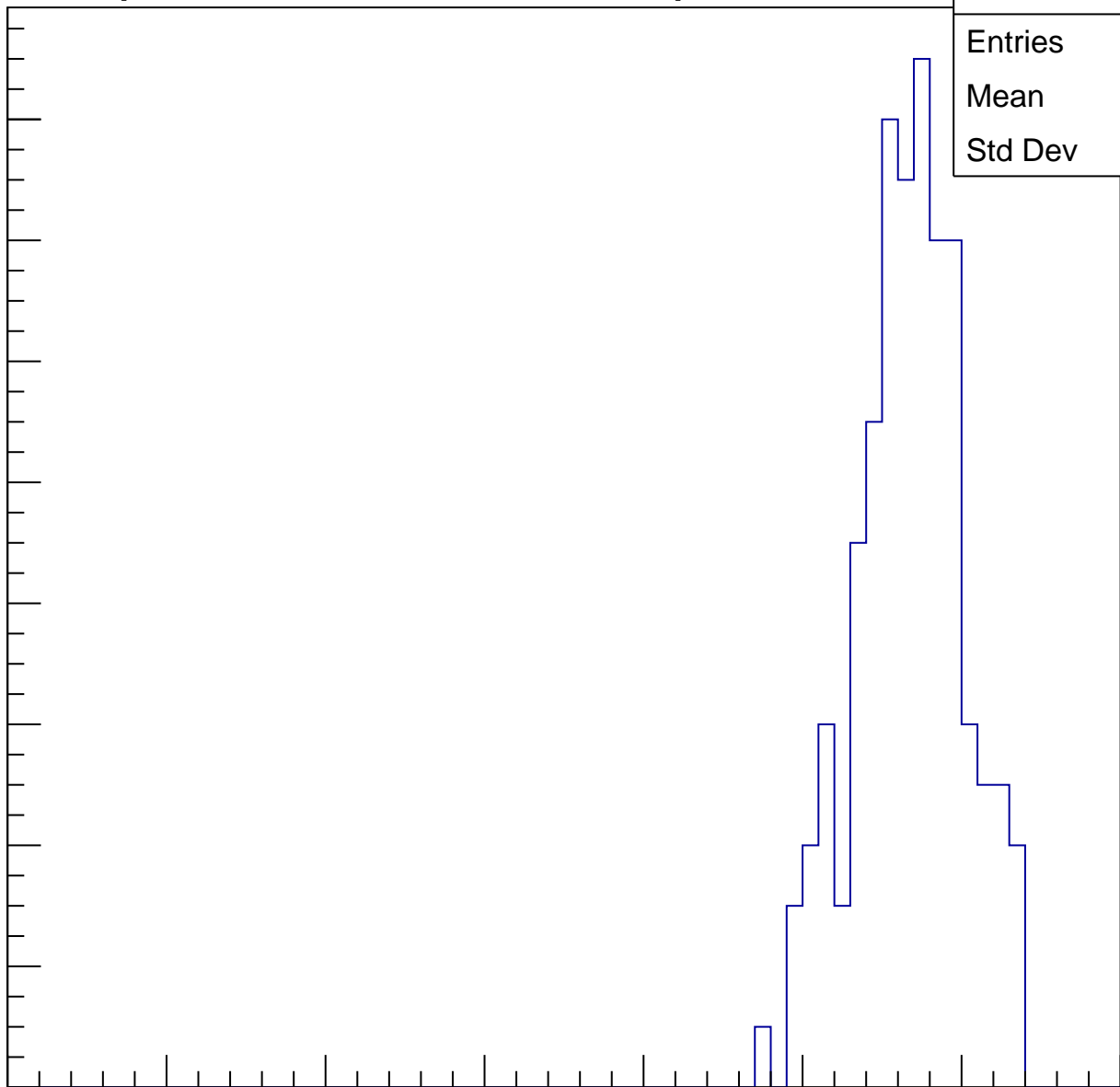
40

50

60

70

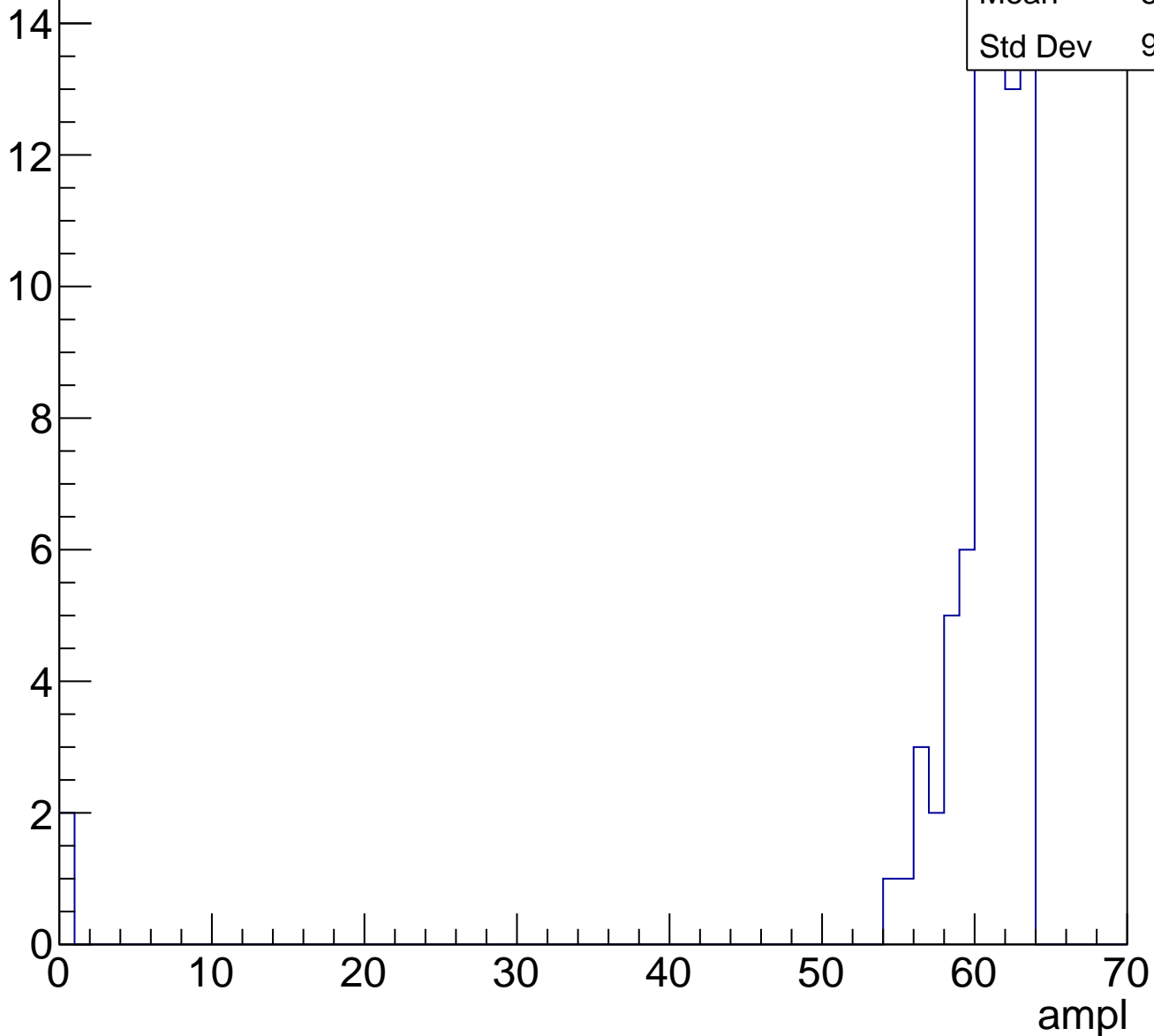
ampl



B1L001S, U25-ch90, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch90, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch90, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch91, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	31.04
Std Dev	5.145

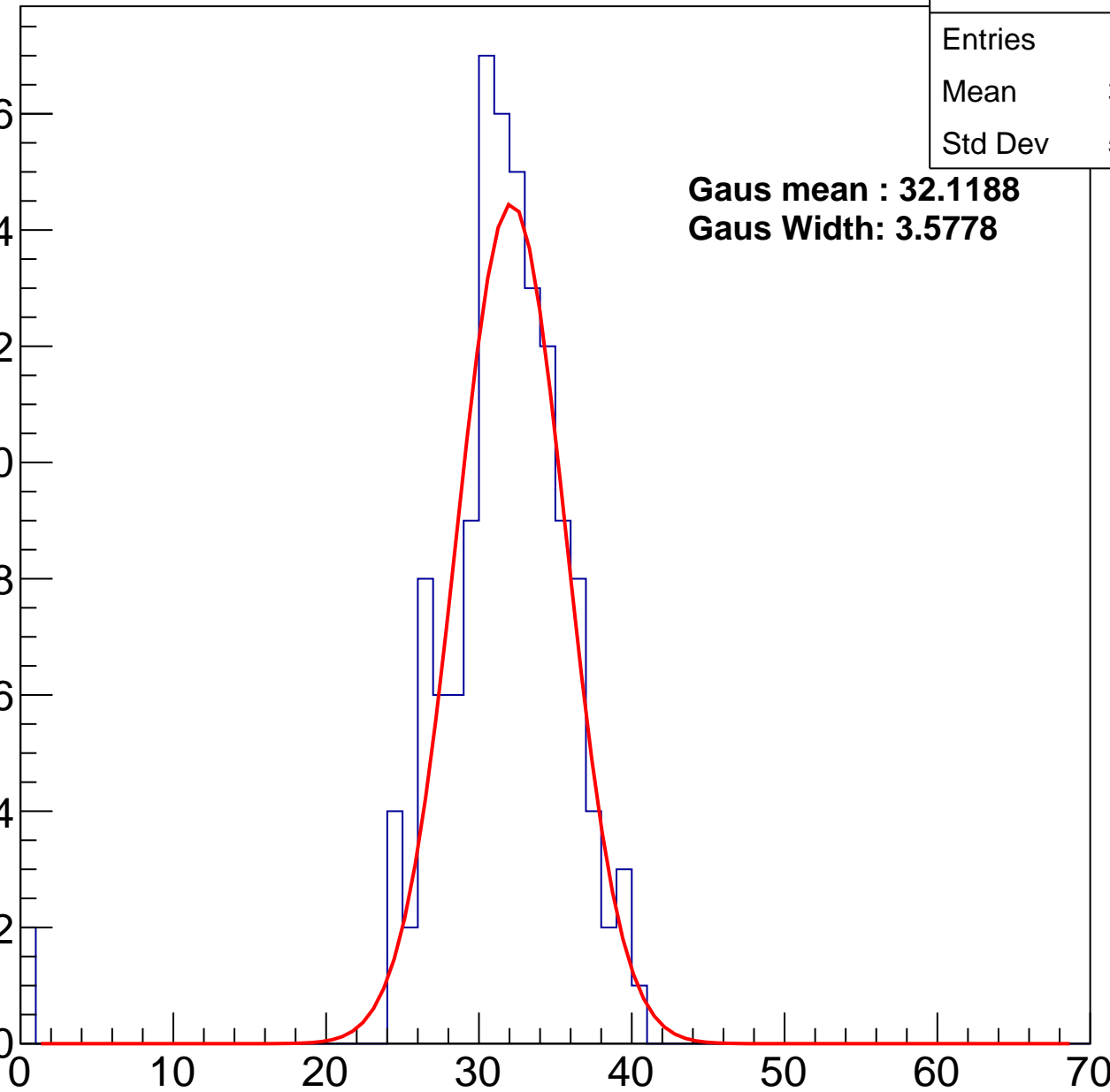
Gaus mean : 32.1188

Gaus Width: 3.5778

Entry

16
14
12
10
8
6
4
2
0

ampl

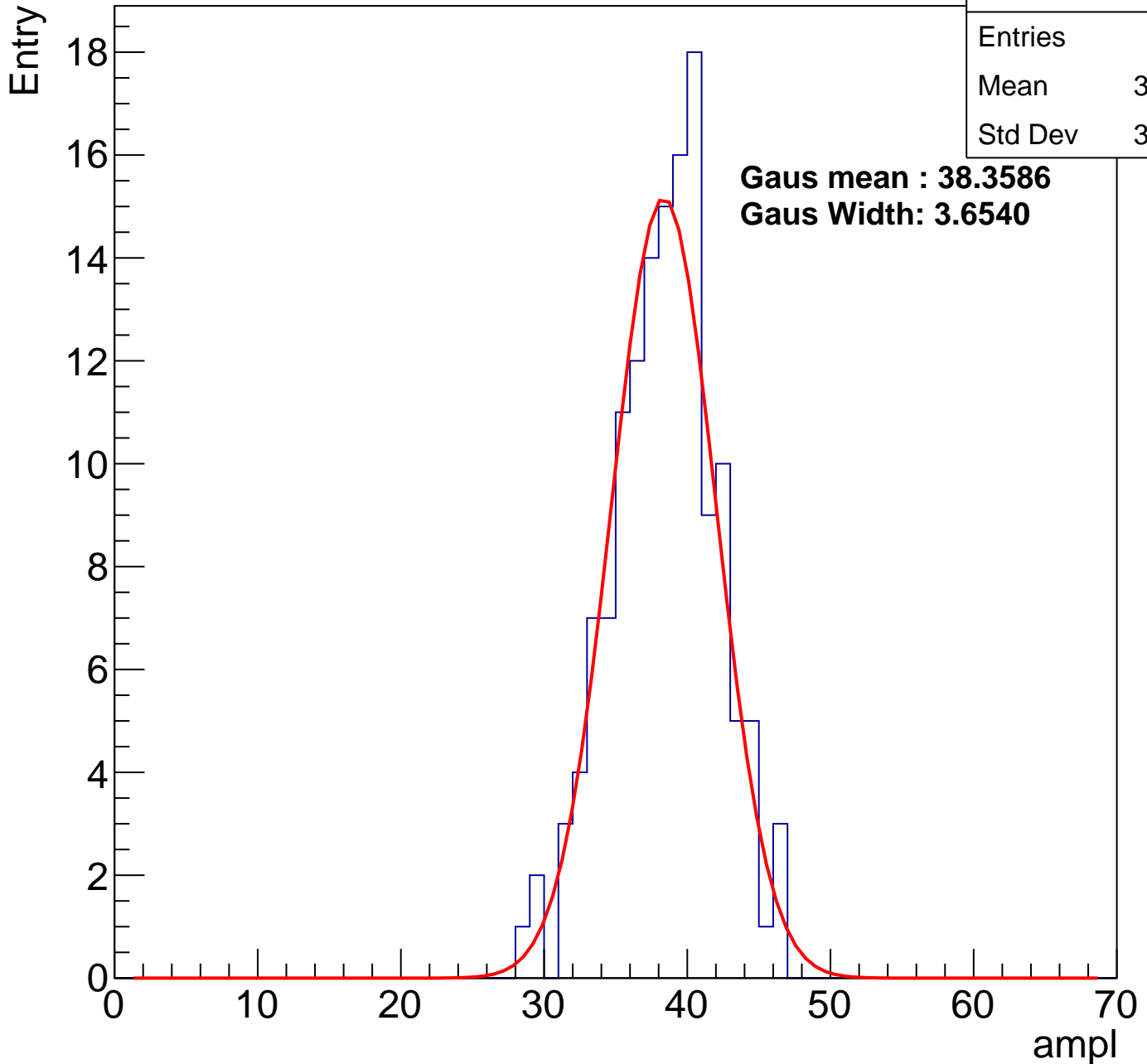


B1L001S, U25-ch91, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	37.99
Std Dev	3.622

Gaus mean : 38.3586
Gaus Width: 3.6540



B1L001S, U25-ch91, adc2

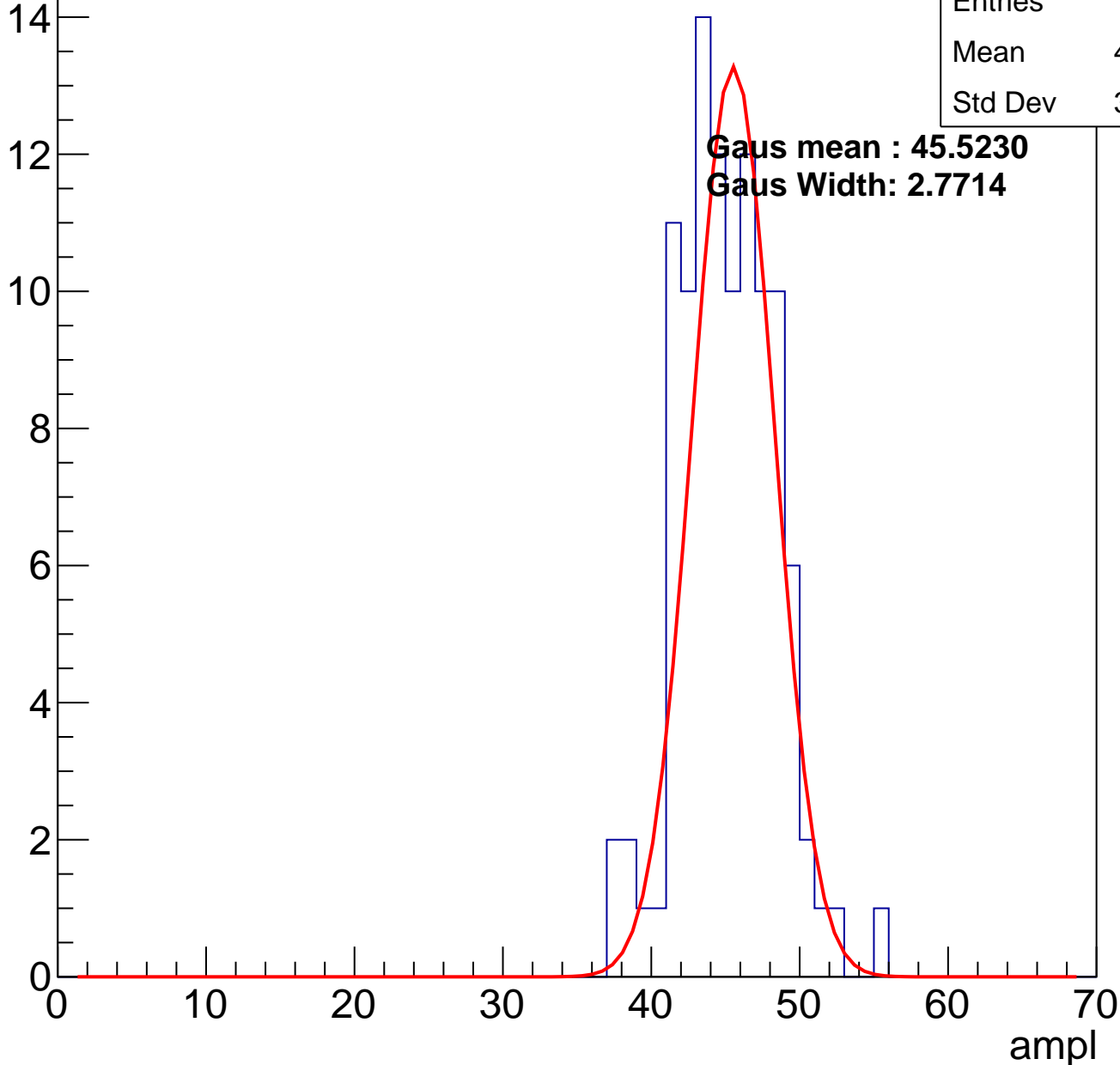
calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	44.66
Std Dev	3.195

Gaus mean : 45.5230

Gaus Width: 2.7714

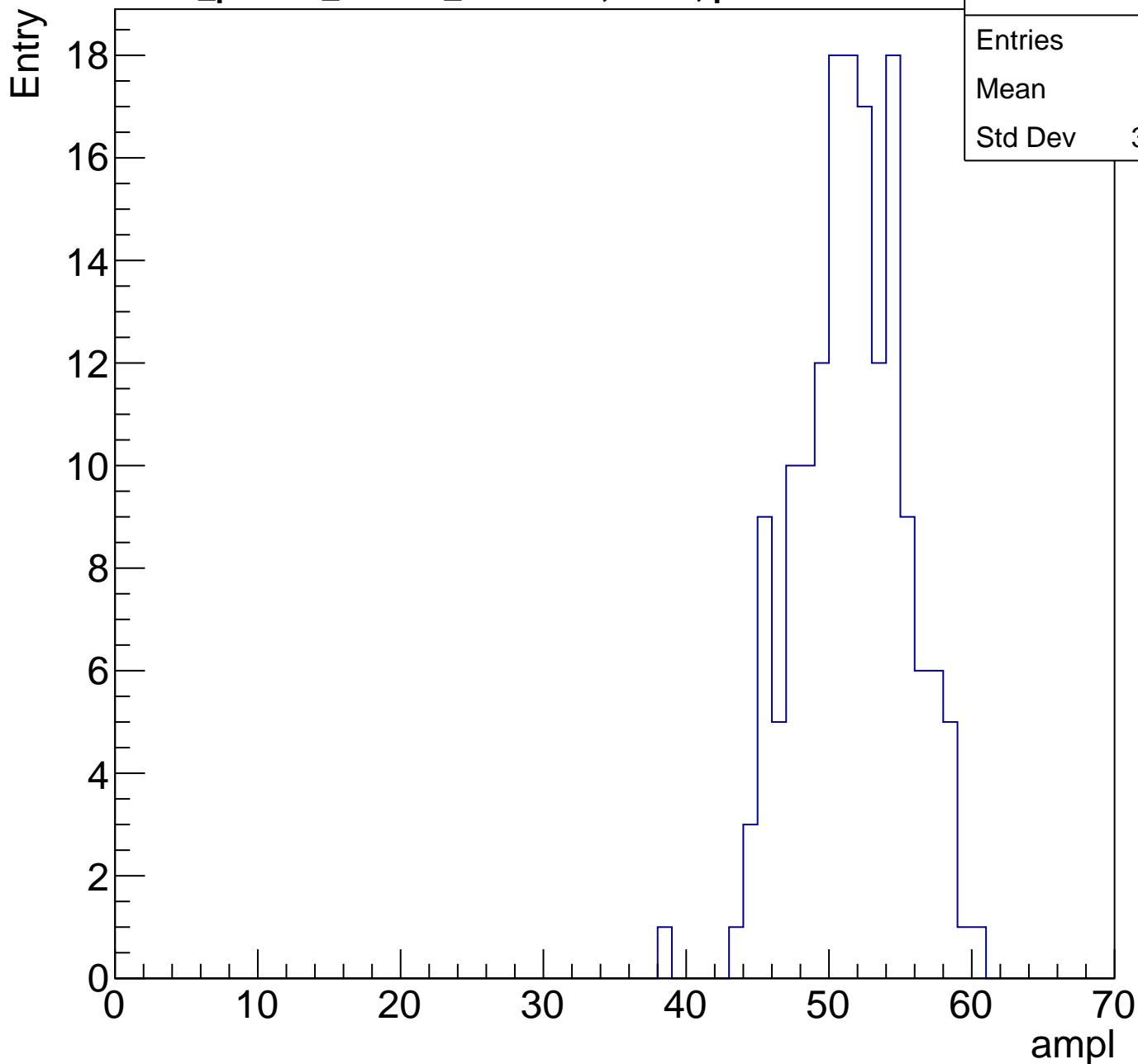
Entry



B1L001S, U25-ch91, adc3

calib_packv5_042523_0143.root, FC#2, port C2

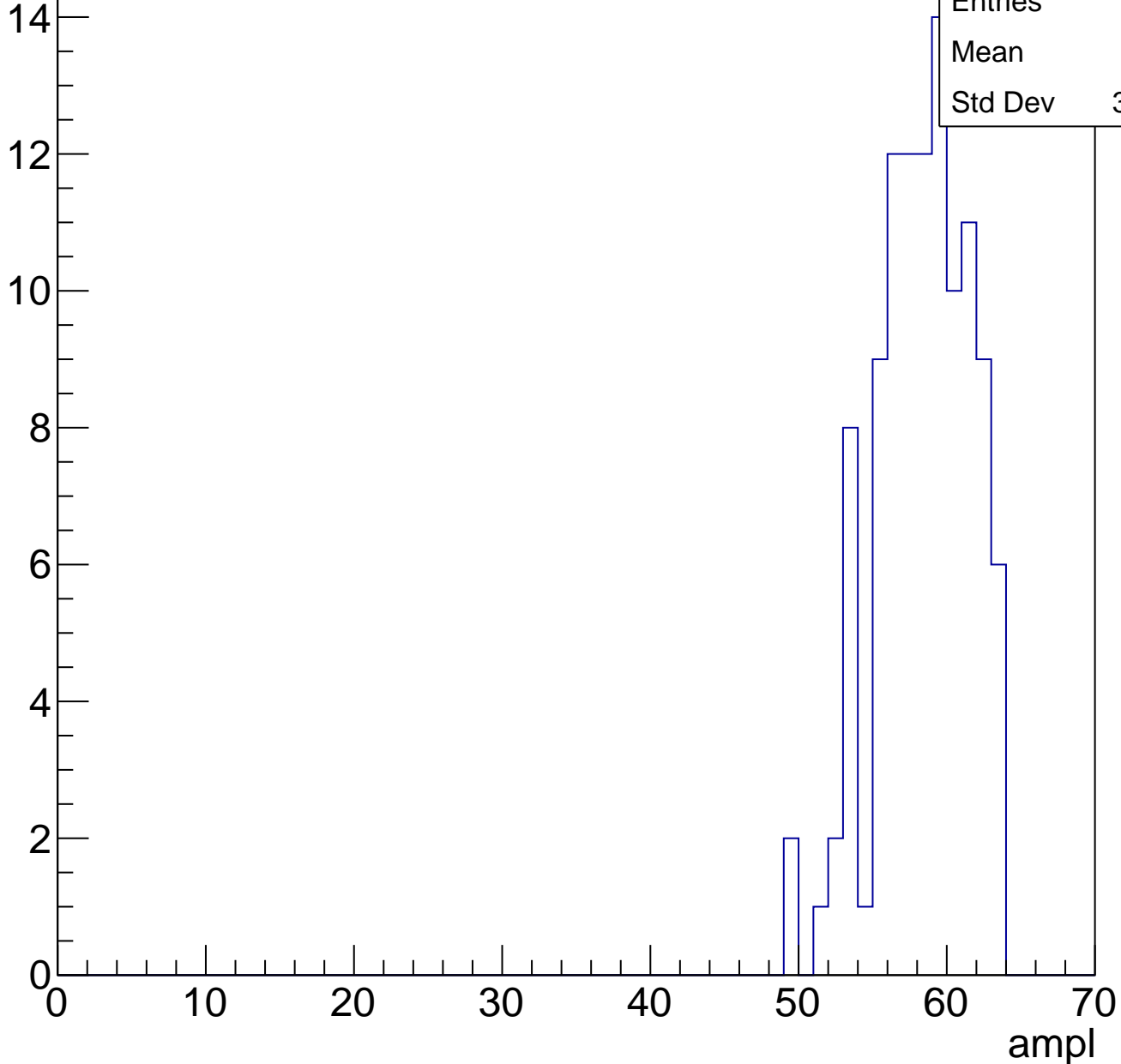
Entries	162
Mean	51.1
Std Dev	3.744



B1L001S, U25-ch91, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch91, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

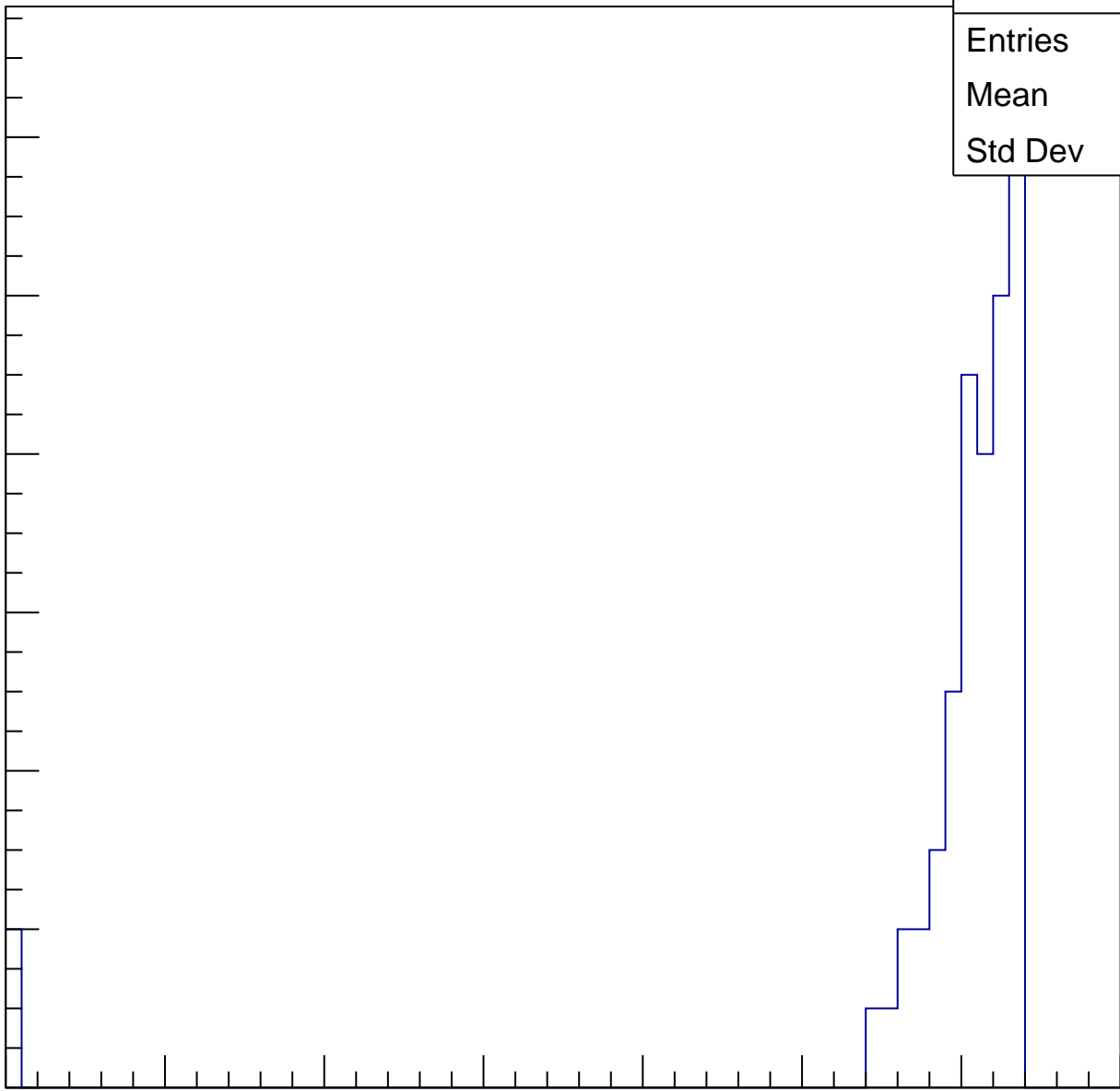
2

0

Entries	56
Mean	58.41
Std Dev	11.46

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch91, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch91, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch92, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	29.83
Std Dev	3.337

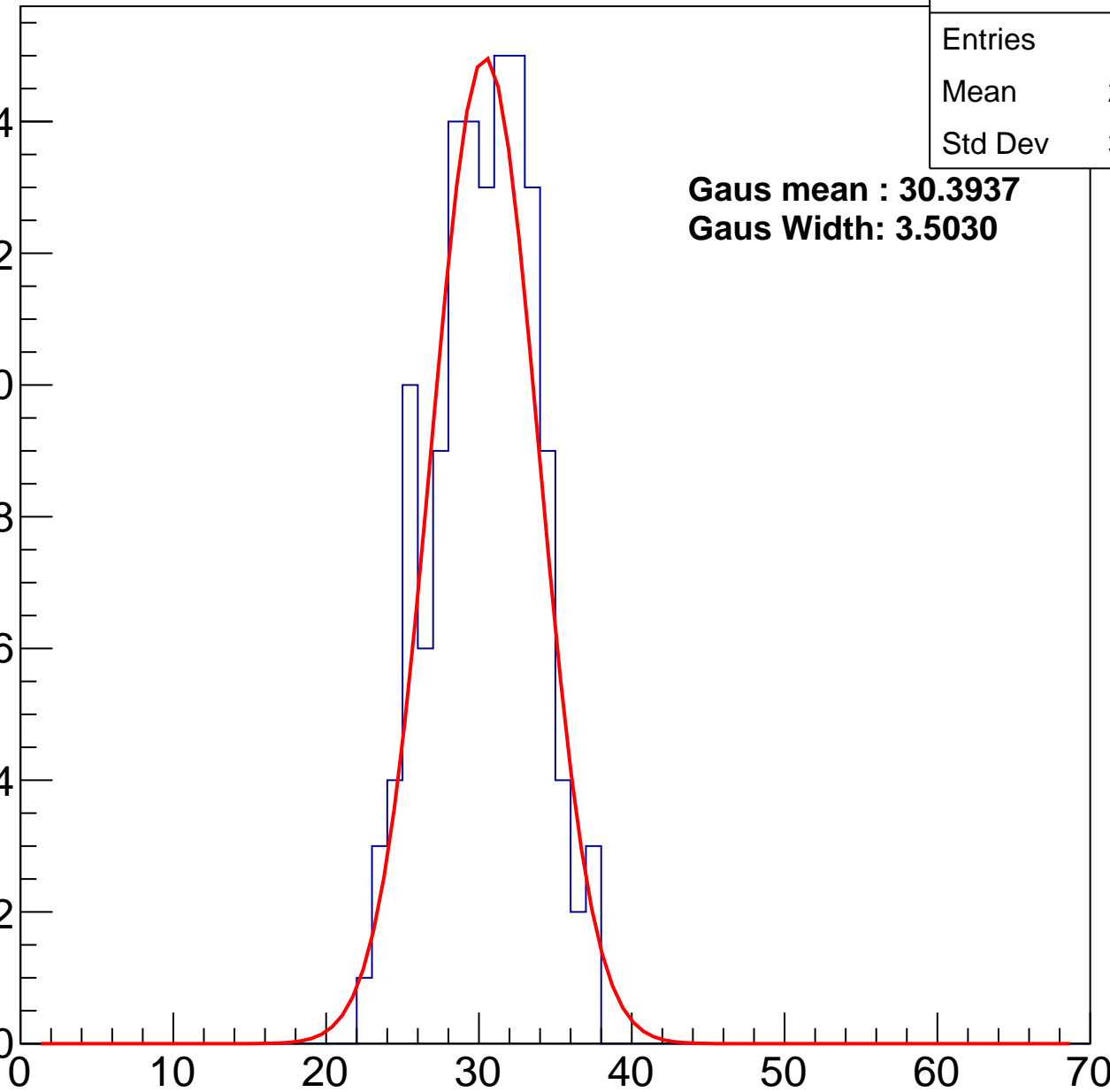
Gaus mean : 30.3937

Gaus Width: 3.5030

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch92, adc1

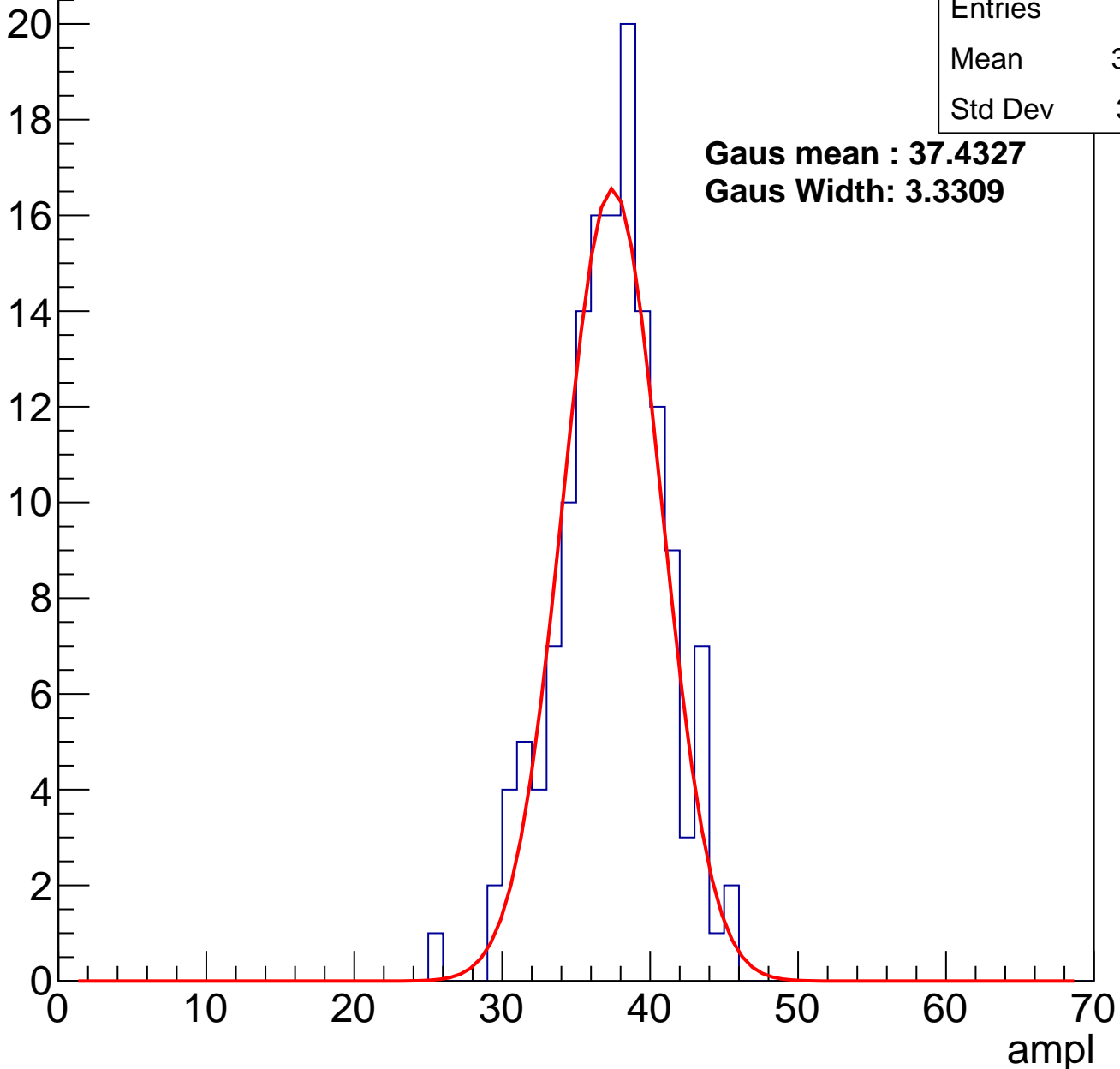
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	36.95
Std Dev	3.551

Gaus mean : 37.4327

Gaus Width: 3.3309

Entry



B1L001S, U25-ch92, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

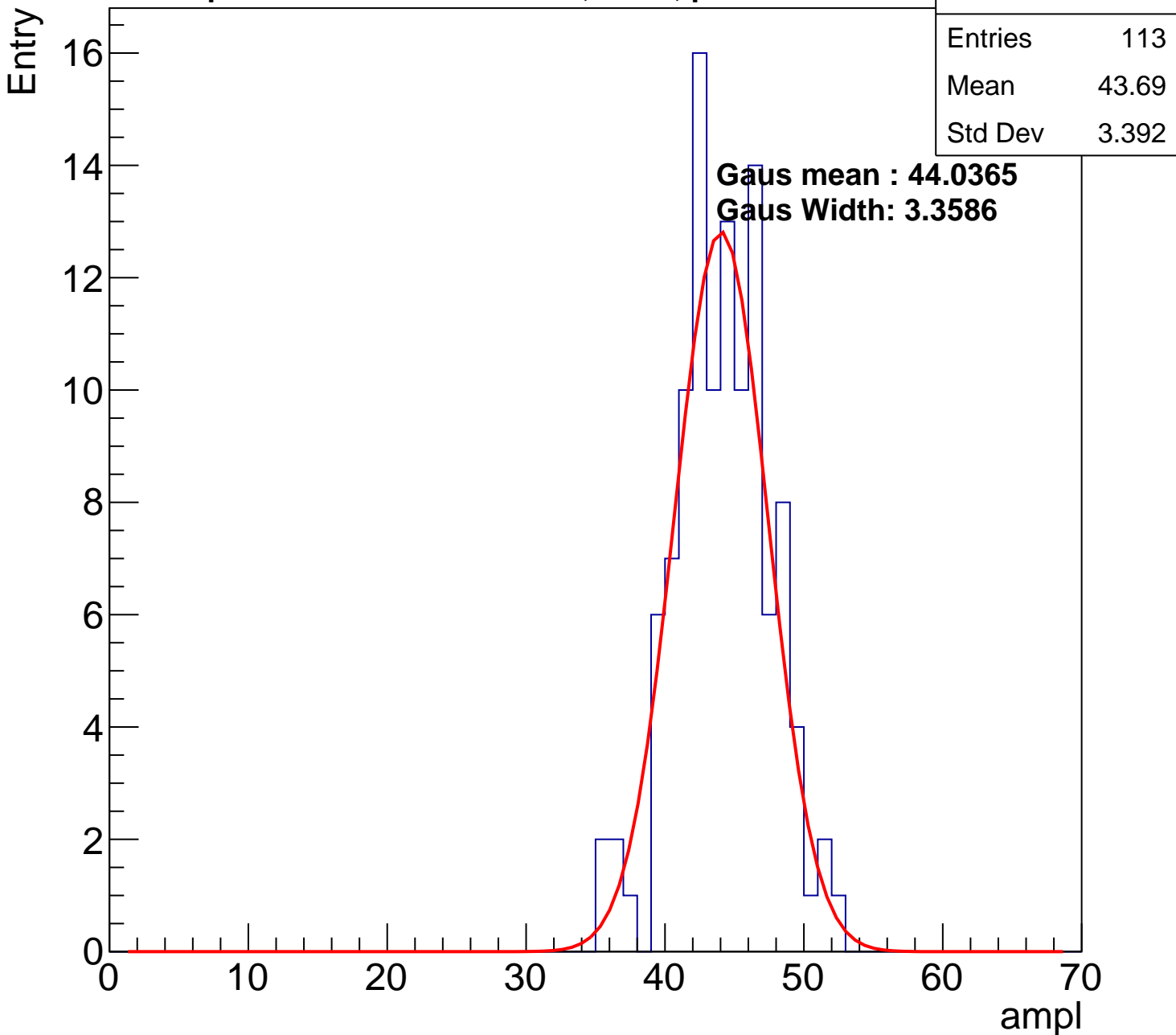
Entries	113
Mean	43.69
Std Dev	3.392

Gaus mean : 44.0365

Gaus Width: 3.3586

0 10 20 30 40 50 60 70

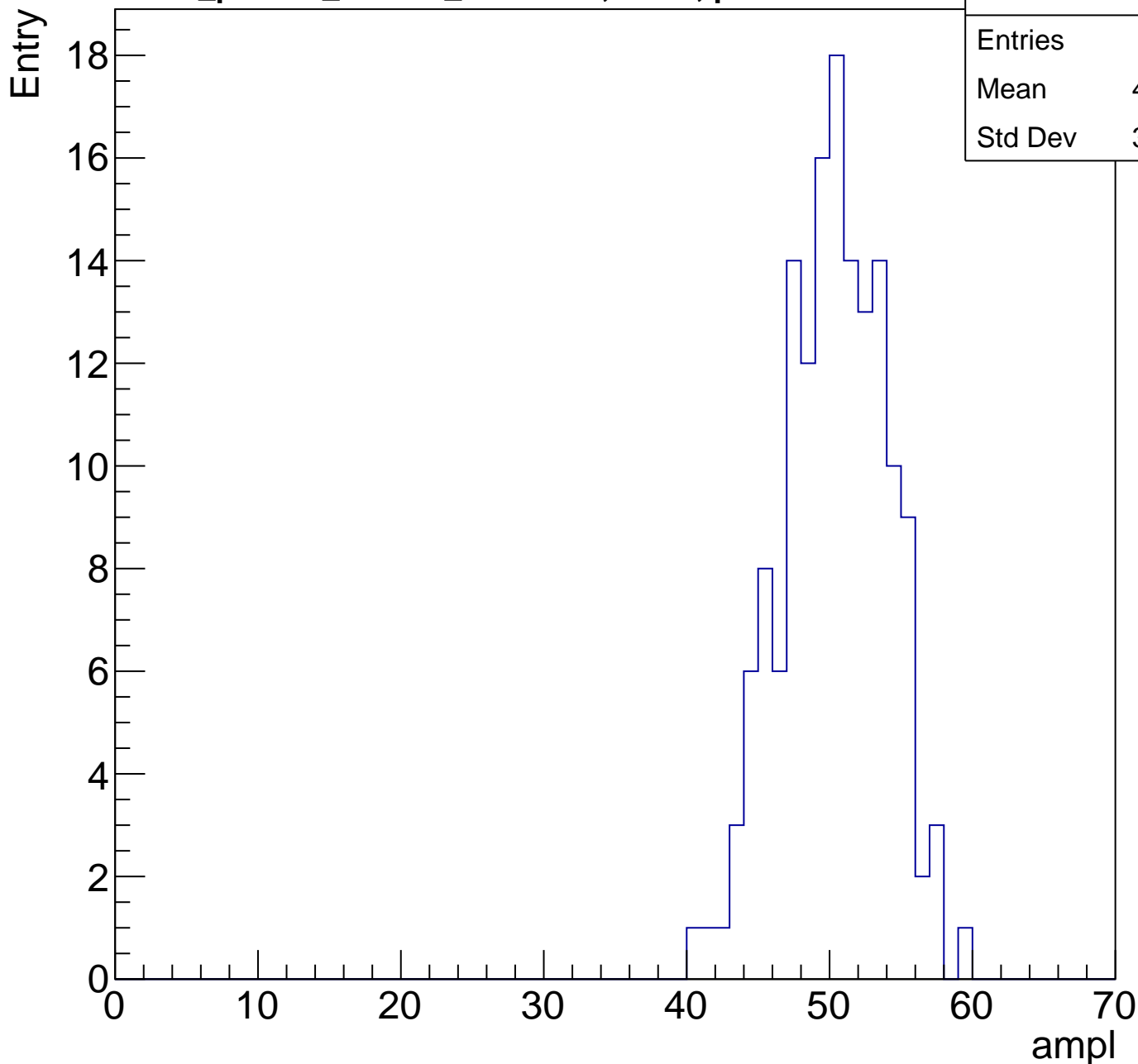
ampl



B1L001S, U25-ch92, adc3

calib_packv5_042523_0143.root, FC#2, port C2

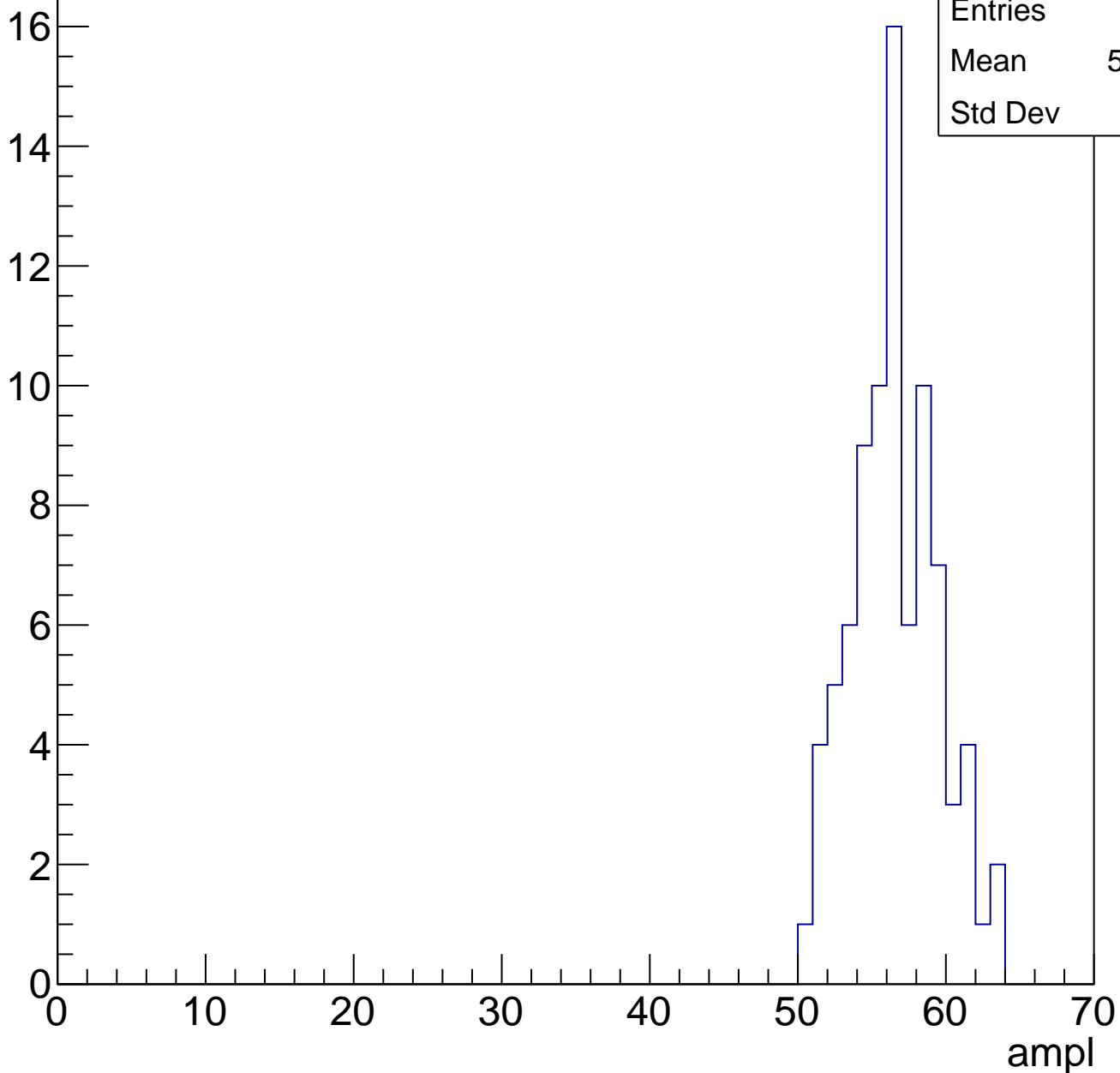
Entries	152
Mean	49.86
Std Dev	3.598



B1L001S, U25-ch92, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	84
Mean	56.08
Std Dev	2.9

B1L001S, U25-ch92, adc5

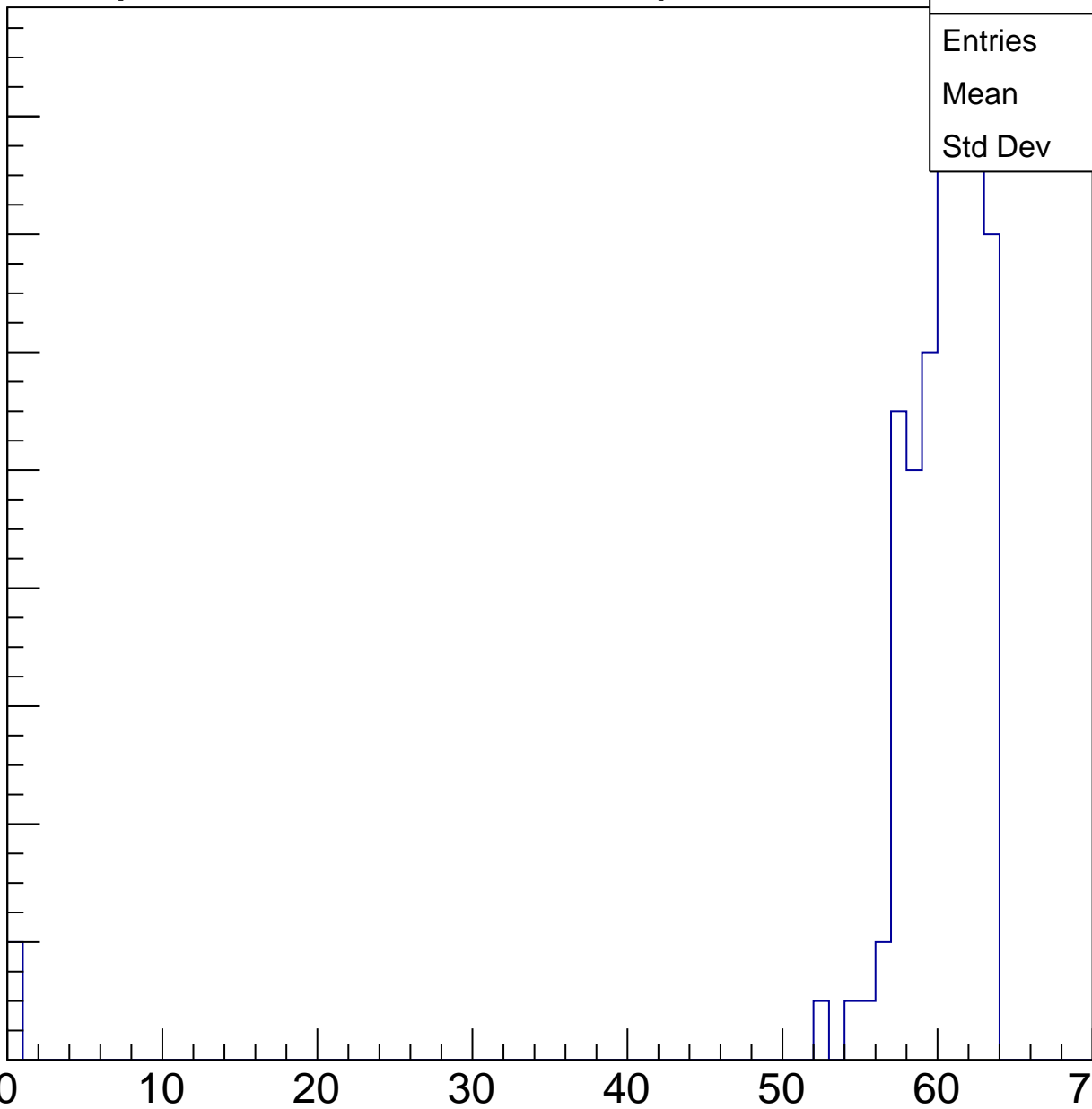
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	58.85
Std Dev	8.537

ampl



B1L001S, U25-ch92, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch92, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch93, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

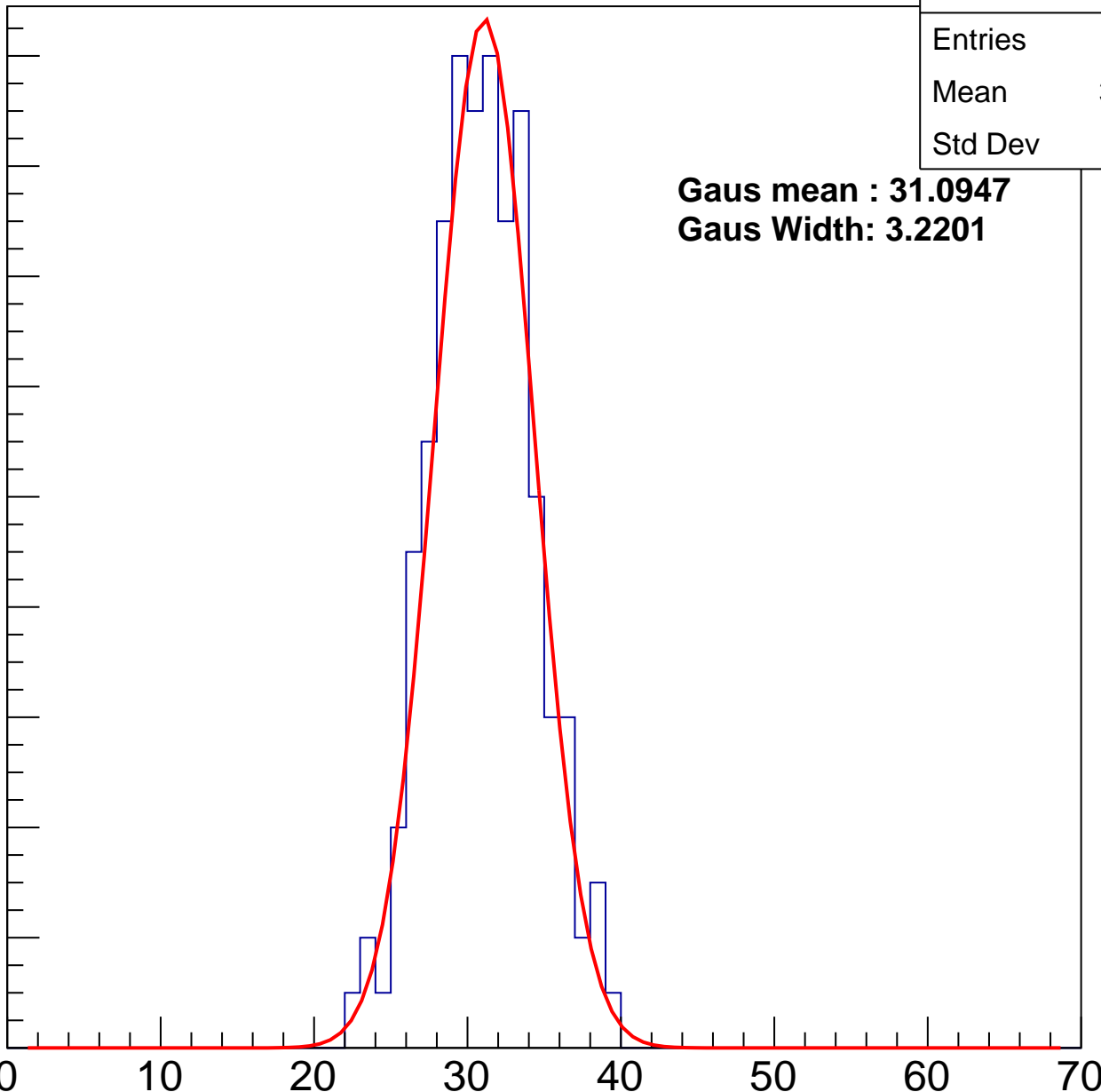
18
16
14
12
10
8
6
4
2
0

Entries	156
Mean	30.56
Std Dev	3.3

Gaus mean : 31.0947

Gaus Width: 3.2201

ampl



B1L001S, U25-ch93, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	37.96
Std Dev	3.532

Gaus mean : 38.5620

Gaus Width: 3.3424

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

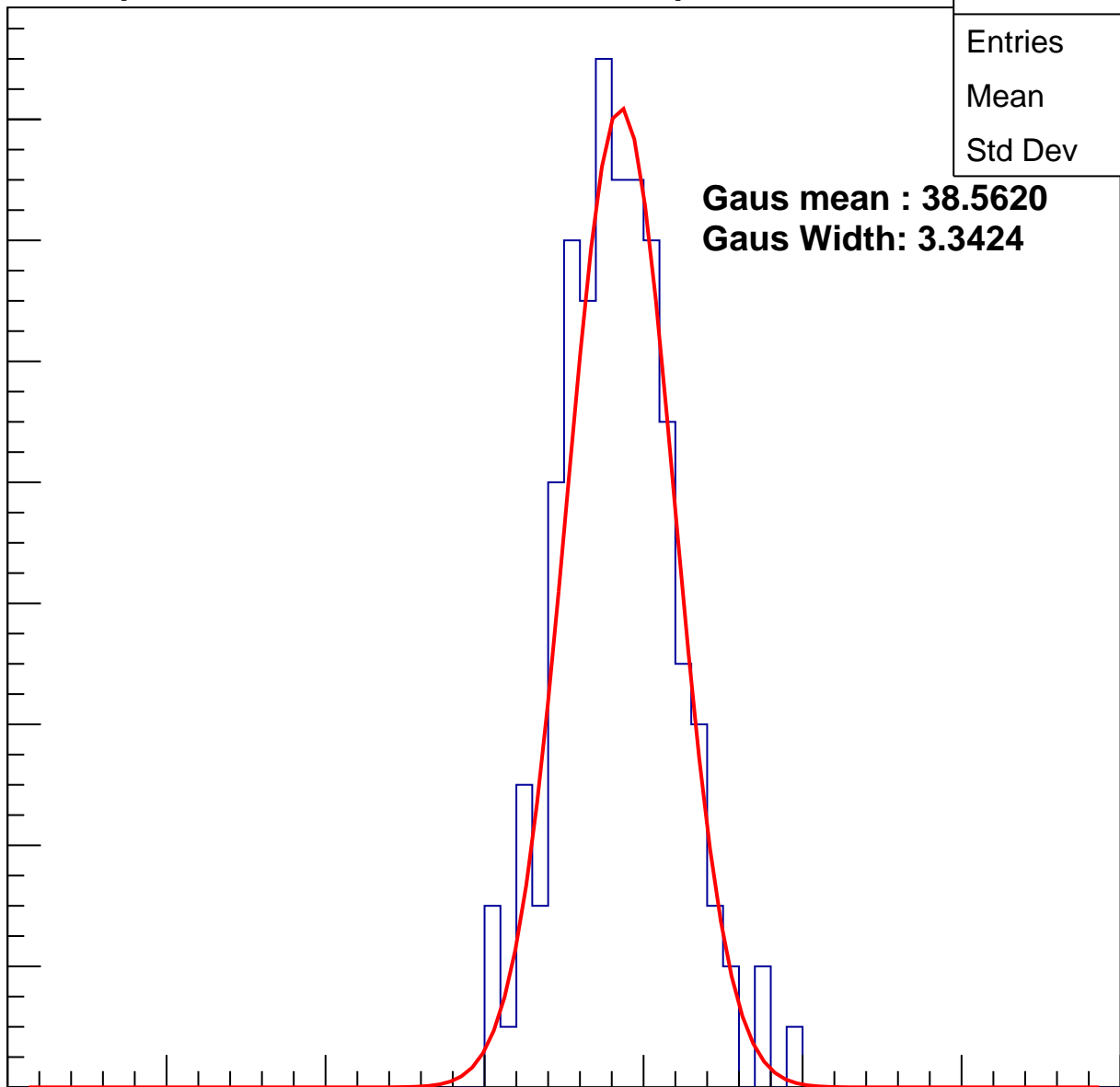
40

50

60

70

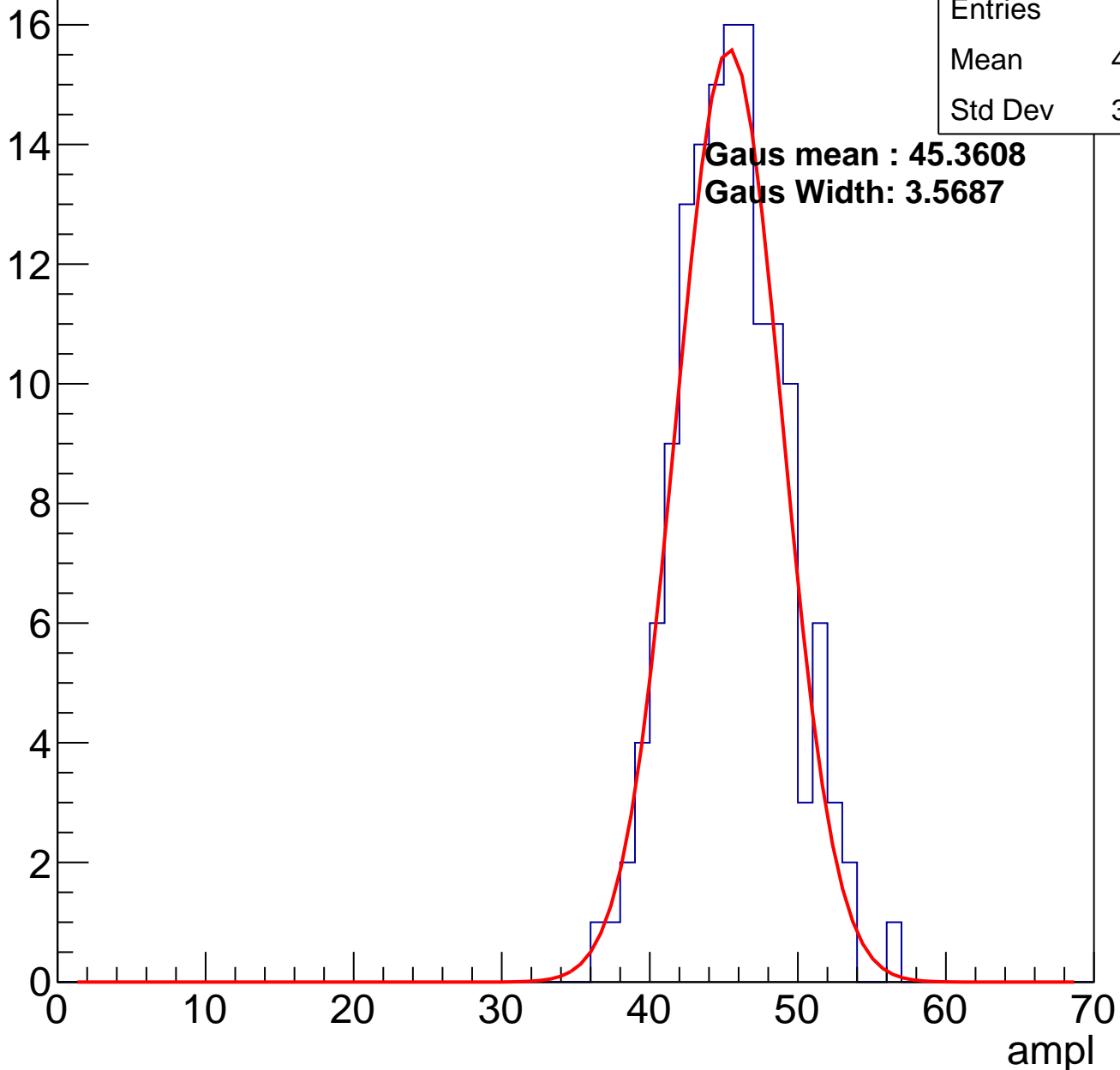
ampl



B1L001S, U25-ch93, adc2

calib_packv5_042523_0143.root, FC#2, port C2

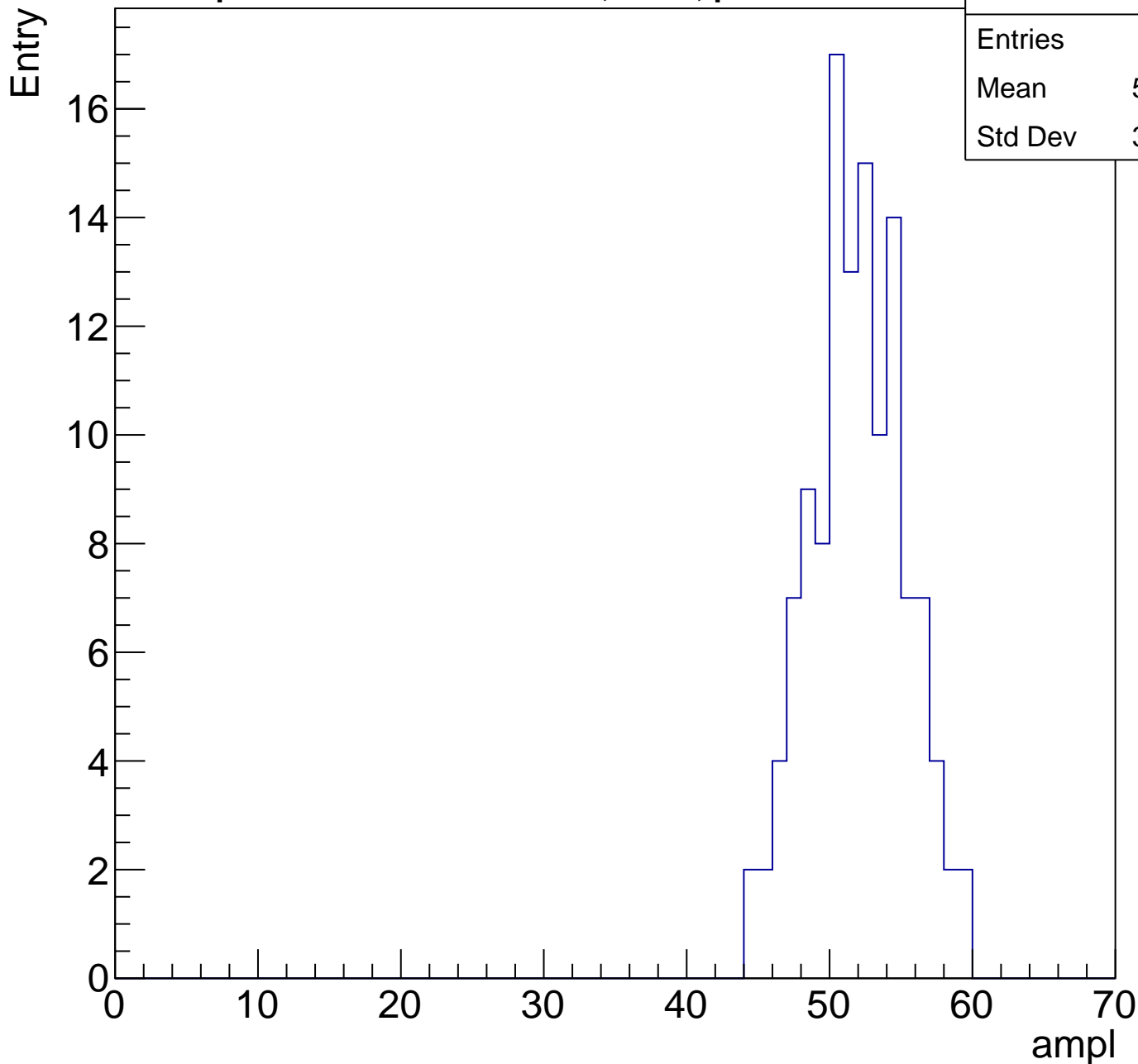
Entry



B1L001S, U25-ch93, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	51.49
Std Dev	3.277



B1L001S, U25-ch93, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	57.08
Std Dev	6.183

Entry

12

10

8

6

4

2

0

0

10

20

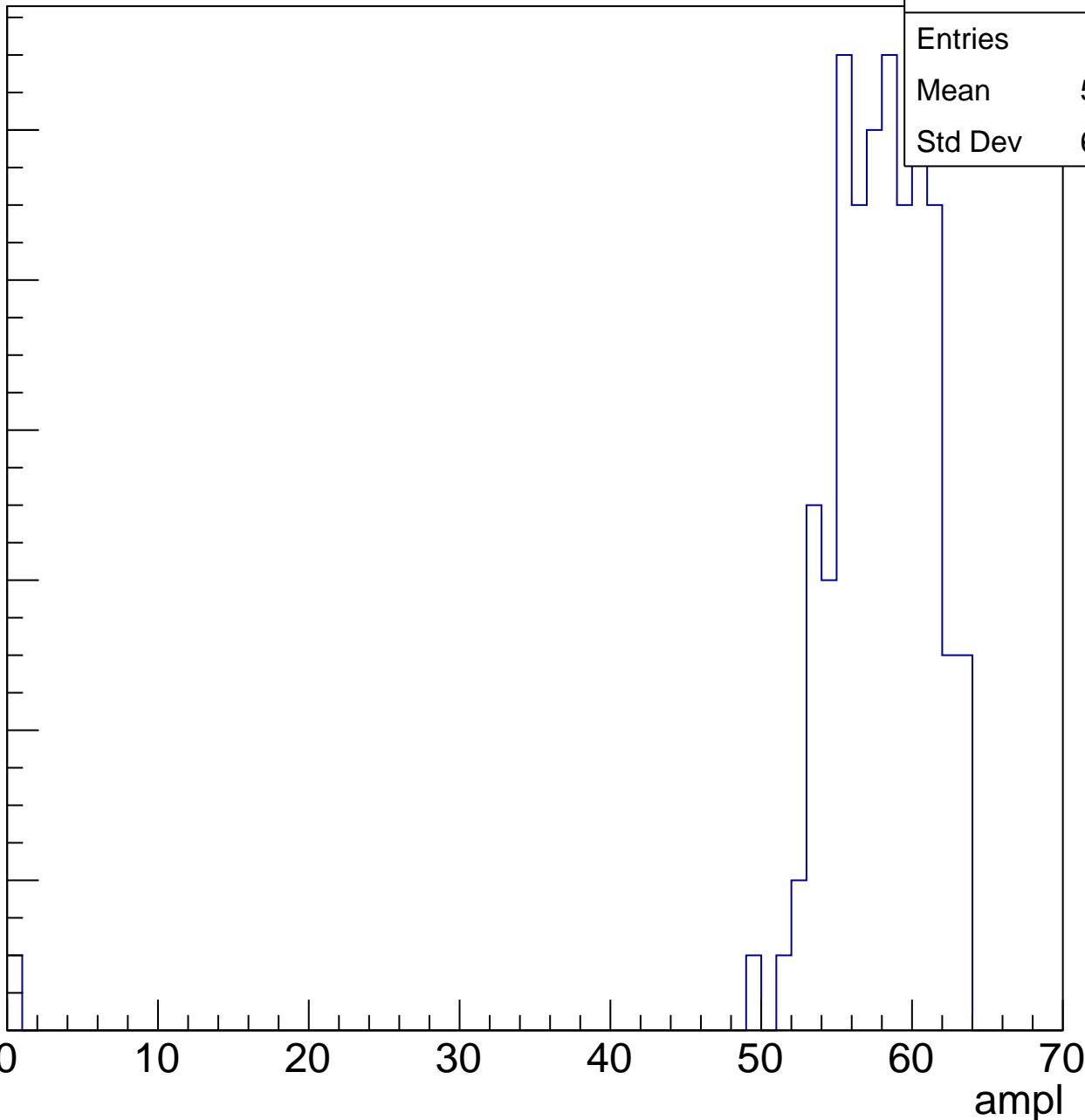
30

40

50

60

ampl



B1L001S, U25-ch93, adc5

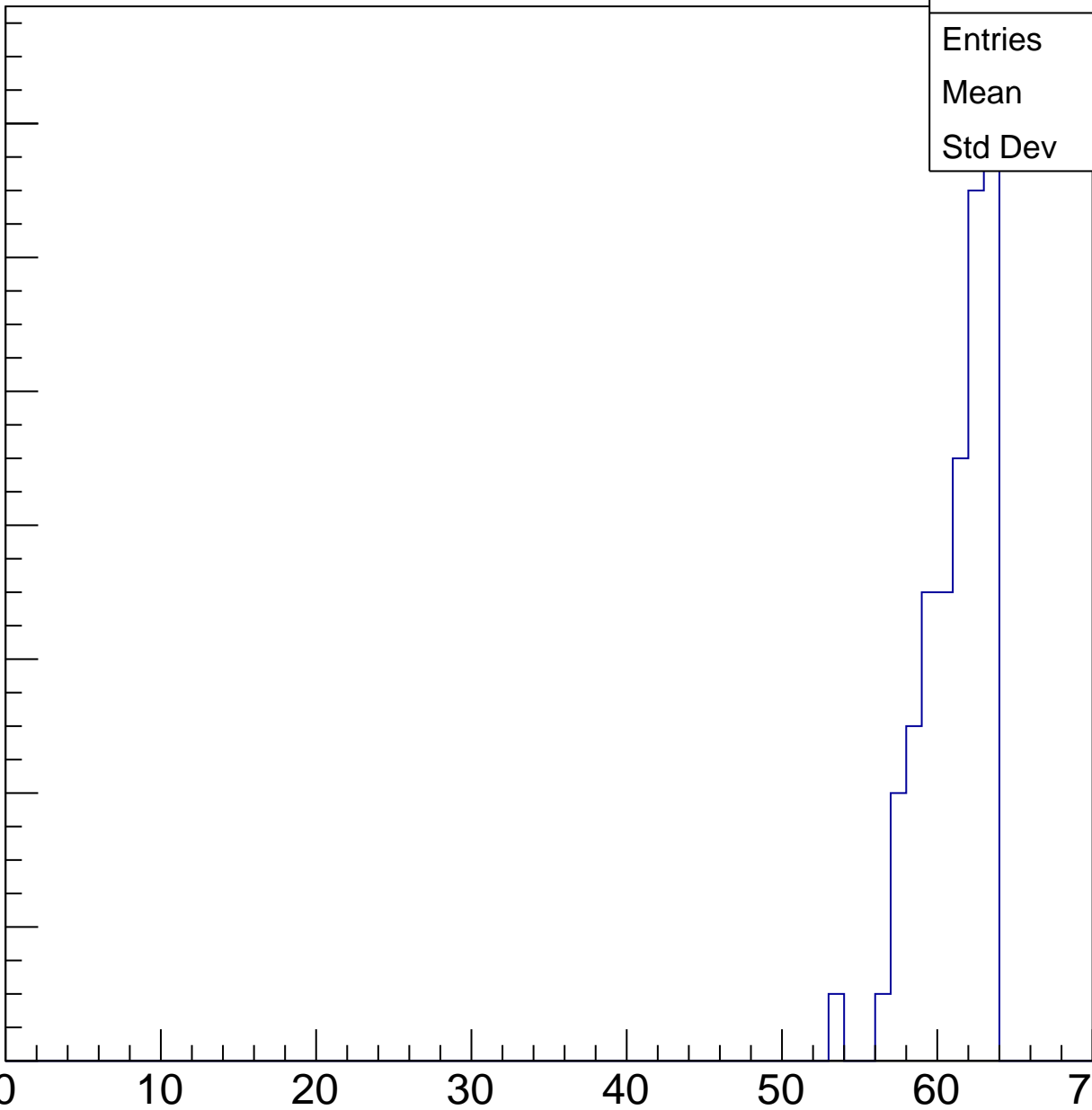
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	62
Mean	60.65
Std Dev	2.193

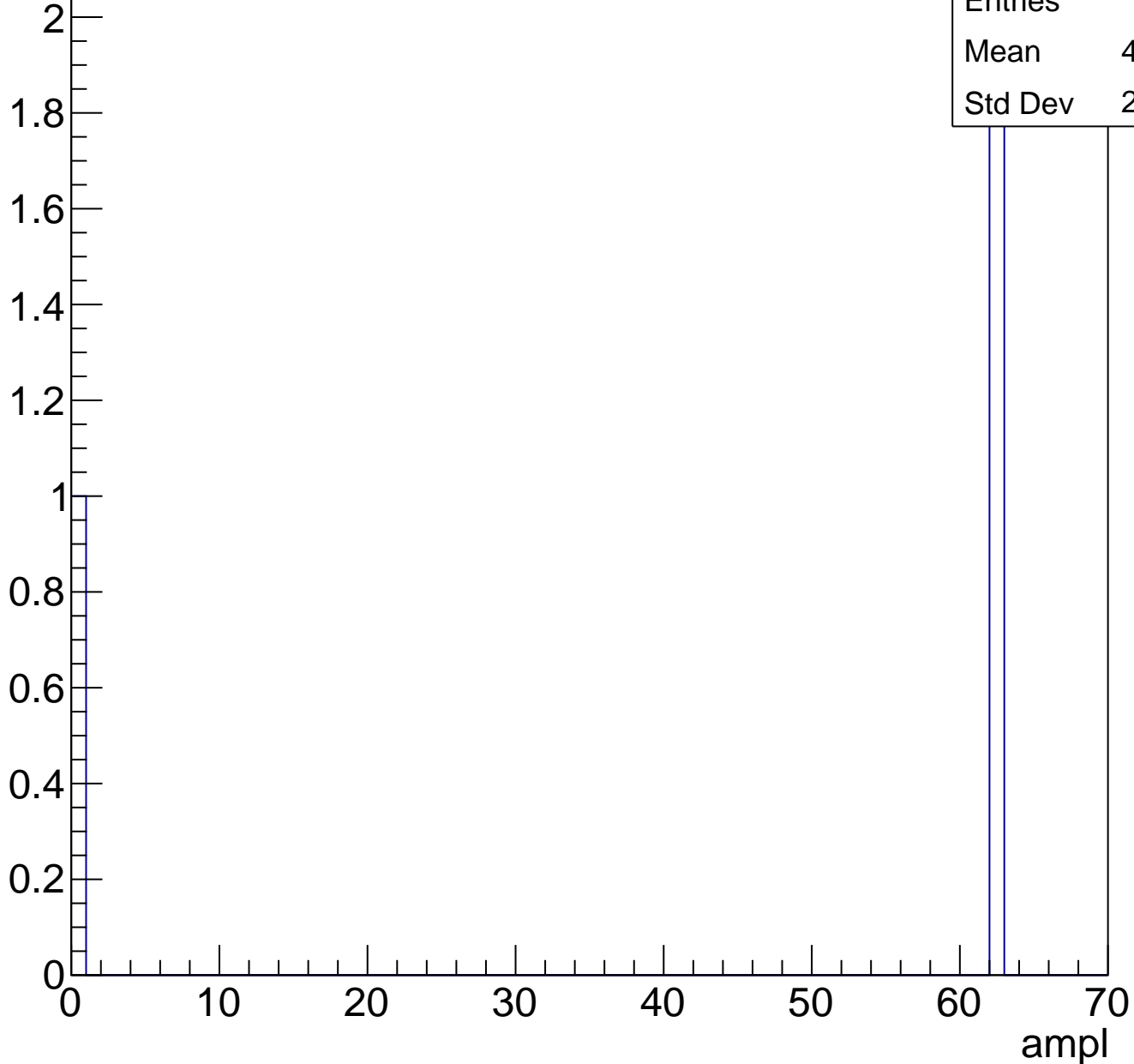
ampl



B1L001S, U25-ch93, adc6

calib_packv5_042523_0143.root, FC#2, port C2

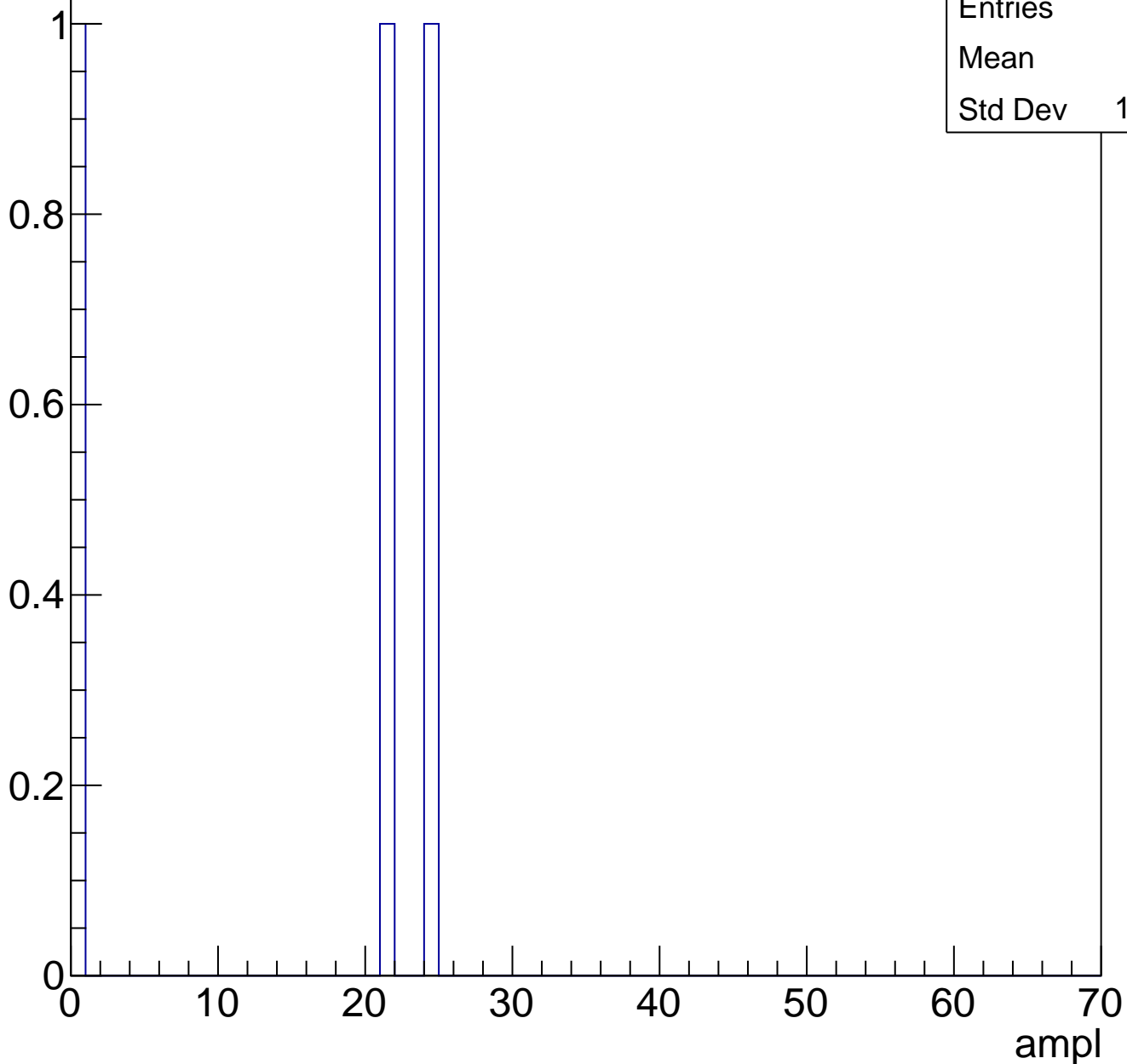
Entry



B1L001S, U25-ch93, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch94, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	86
Mean	28.63
Std Dev	3.376

Gaus mean : 29.1244

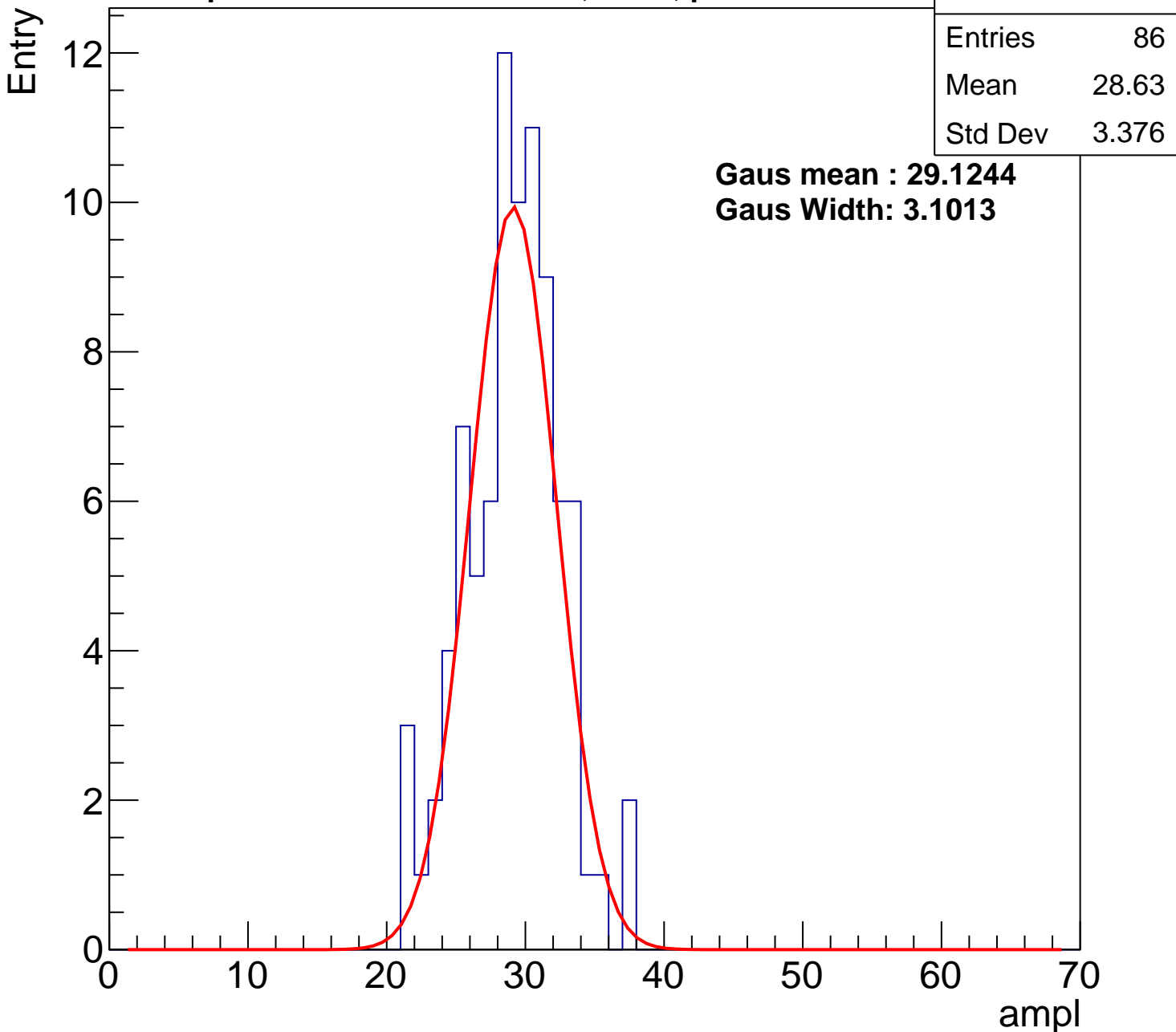
Gaus Width: 3.1013

Entry

12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch94, adc1

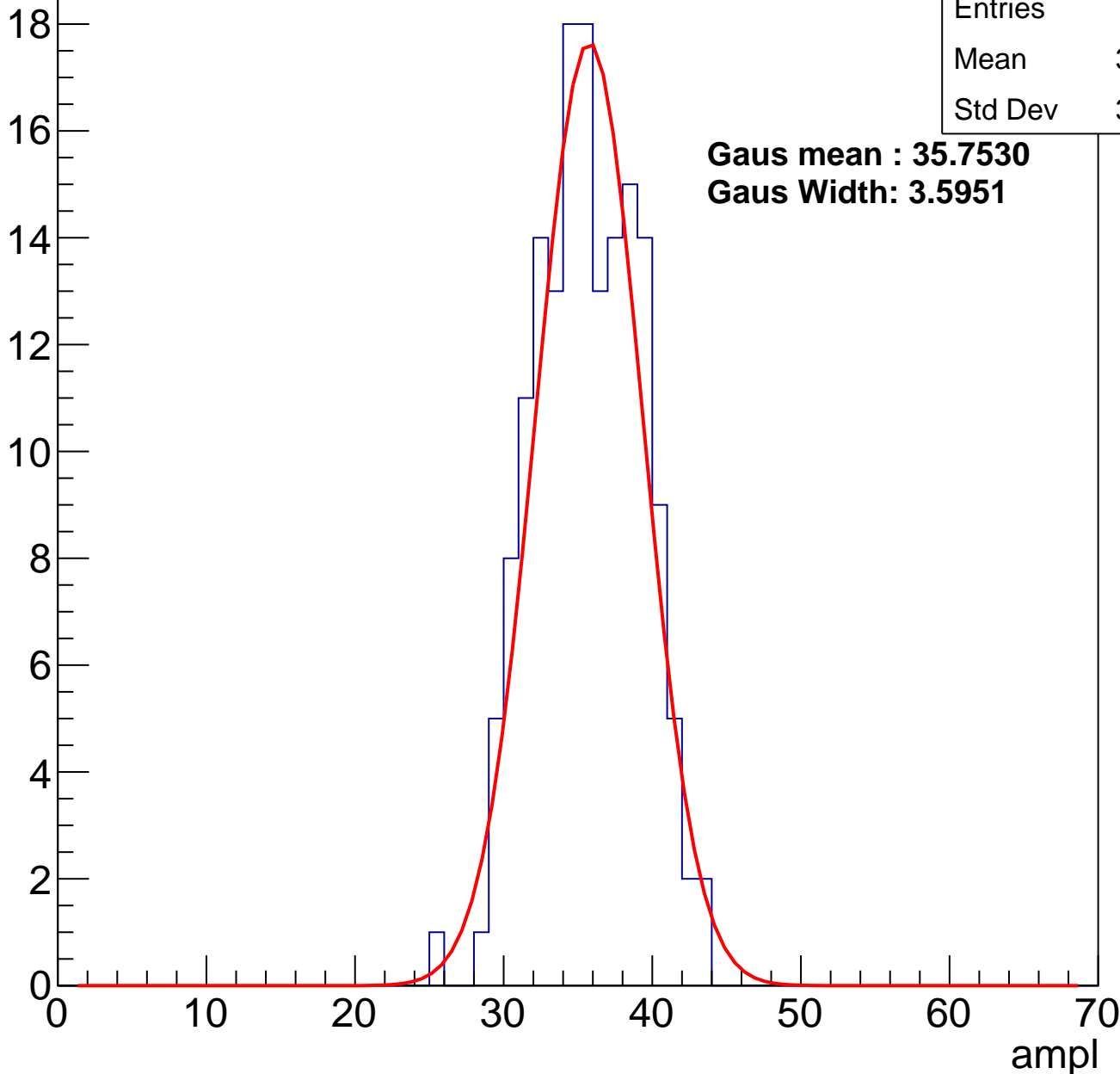
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	35.18
Std Dev	3.463

Gaus mean : 35.7530

Gaus Width: 3.5951

Entry



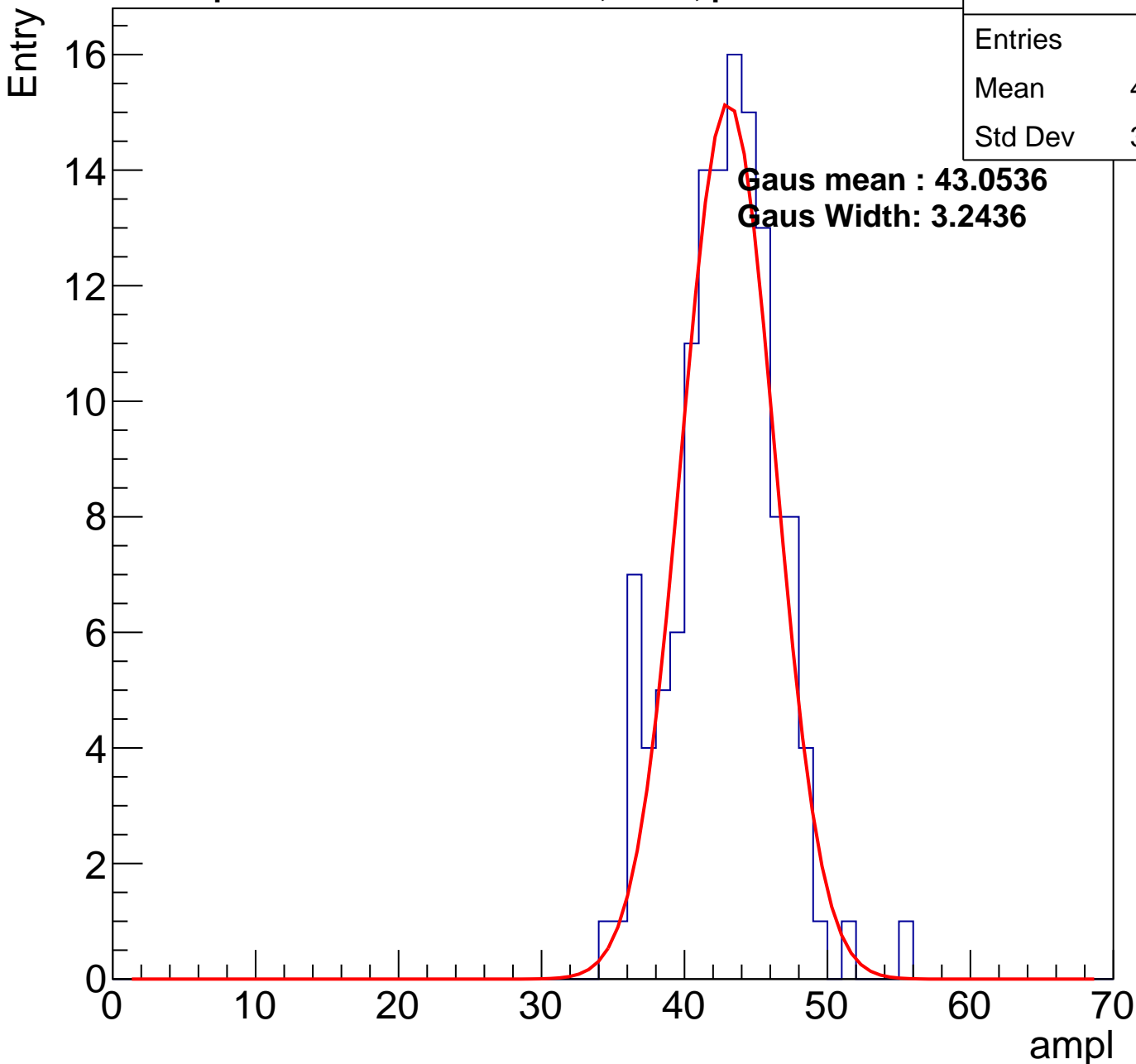
B1L001S, U25-ch94, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	42.45
Std Dev	3.502

Gaus mean : 43.0536

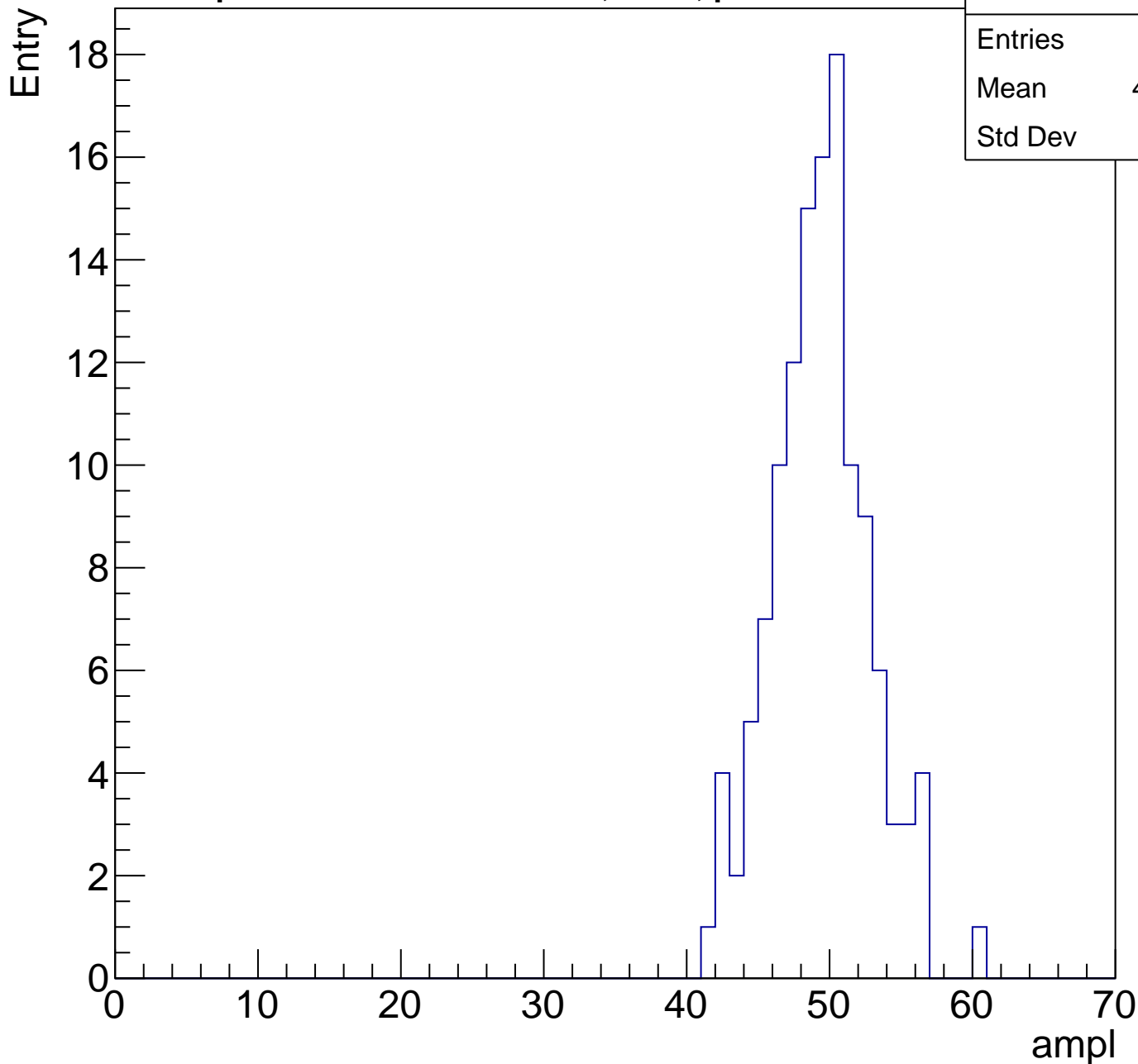
Gaus Width: 3.2436



B1L001S, U25-ch94, adc3

calib_packv5_042523_0143.root, FC#2, port C2

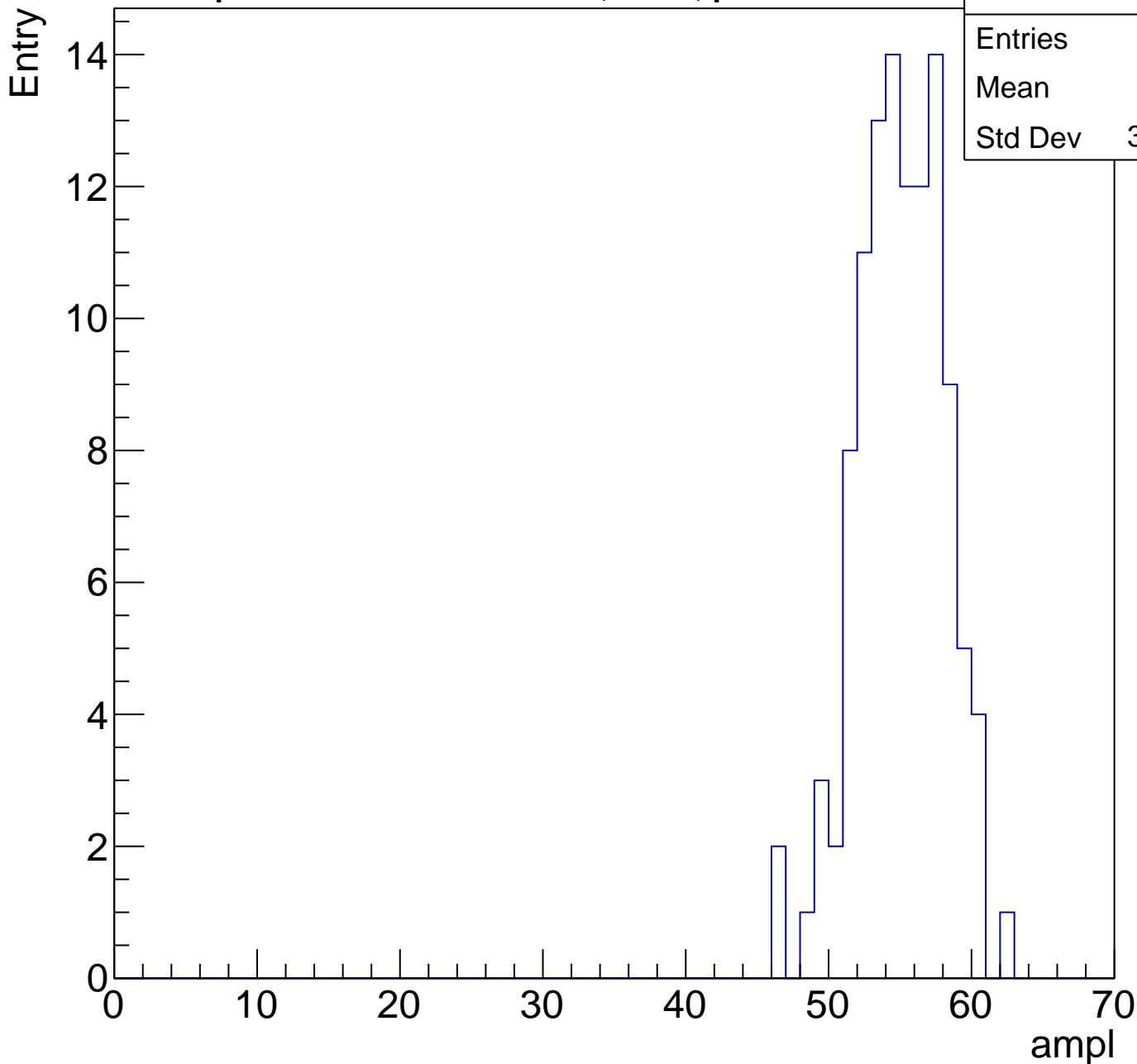
Entries	126
Mean	48.93
Std Dev	3.4



B1L001S, U25-ch94, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	54.6
Std Dev	3.035



B1L001S, U25-ch94, adc5

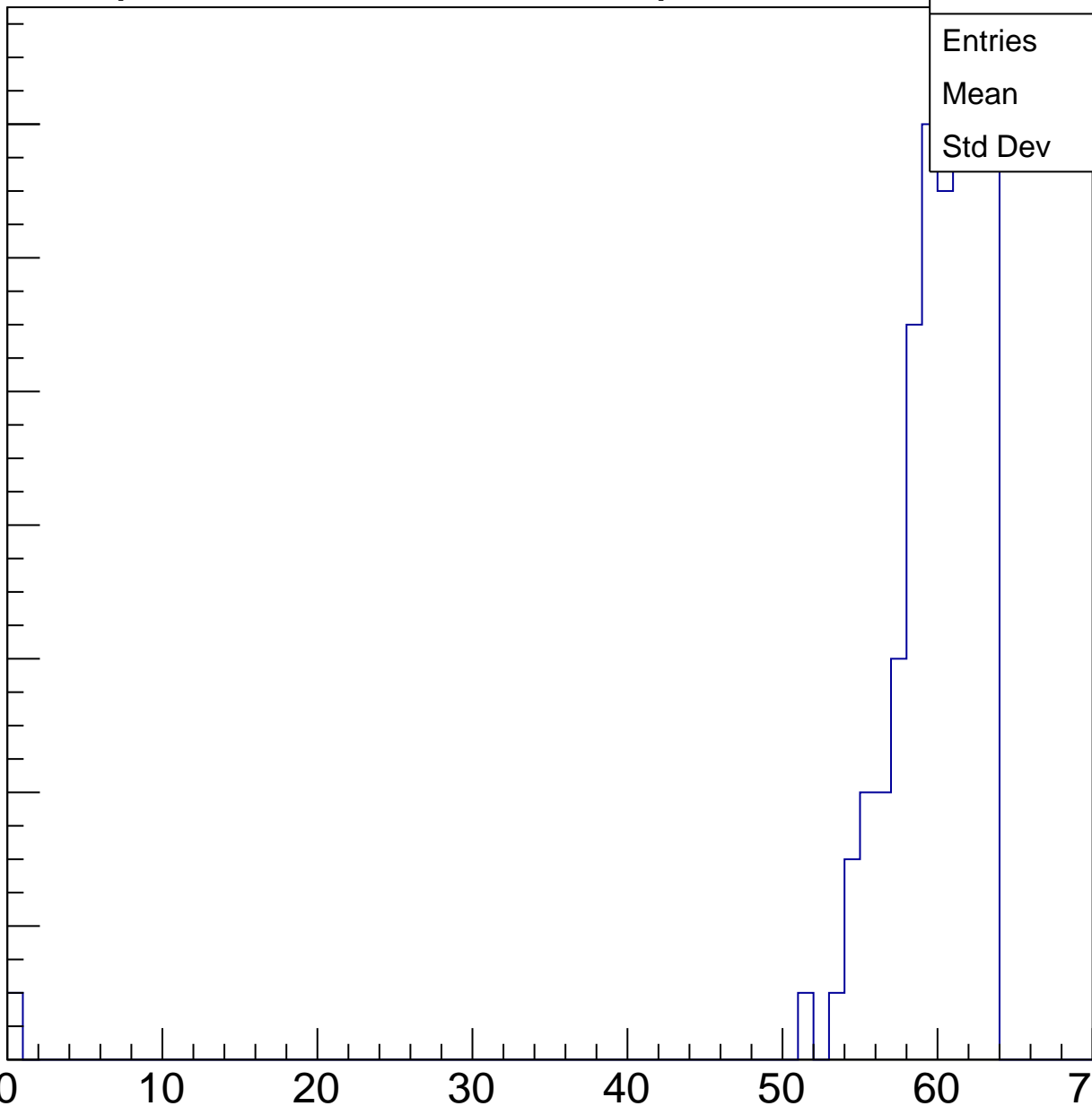
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	102
Mean	59.06
Std Dev	6.439

ampl

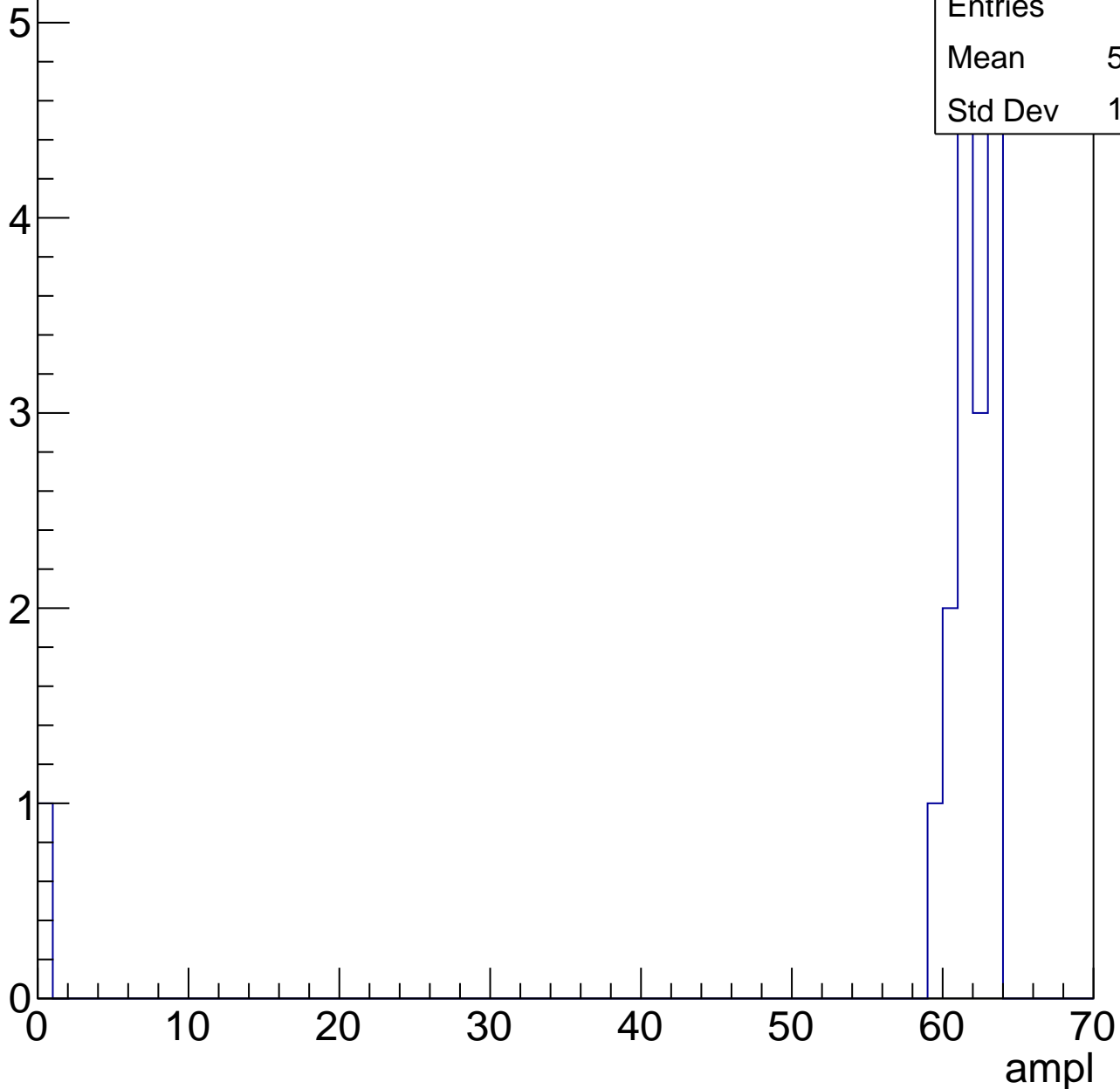


B1L001S, U25-ch94, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

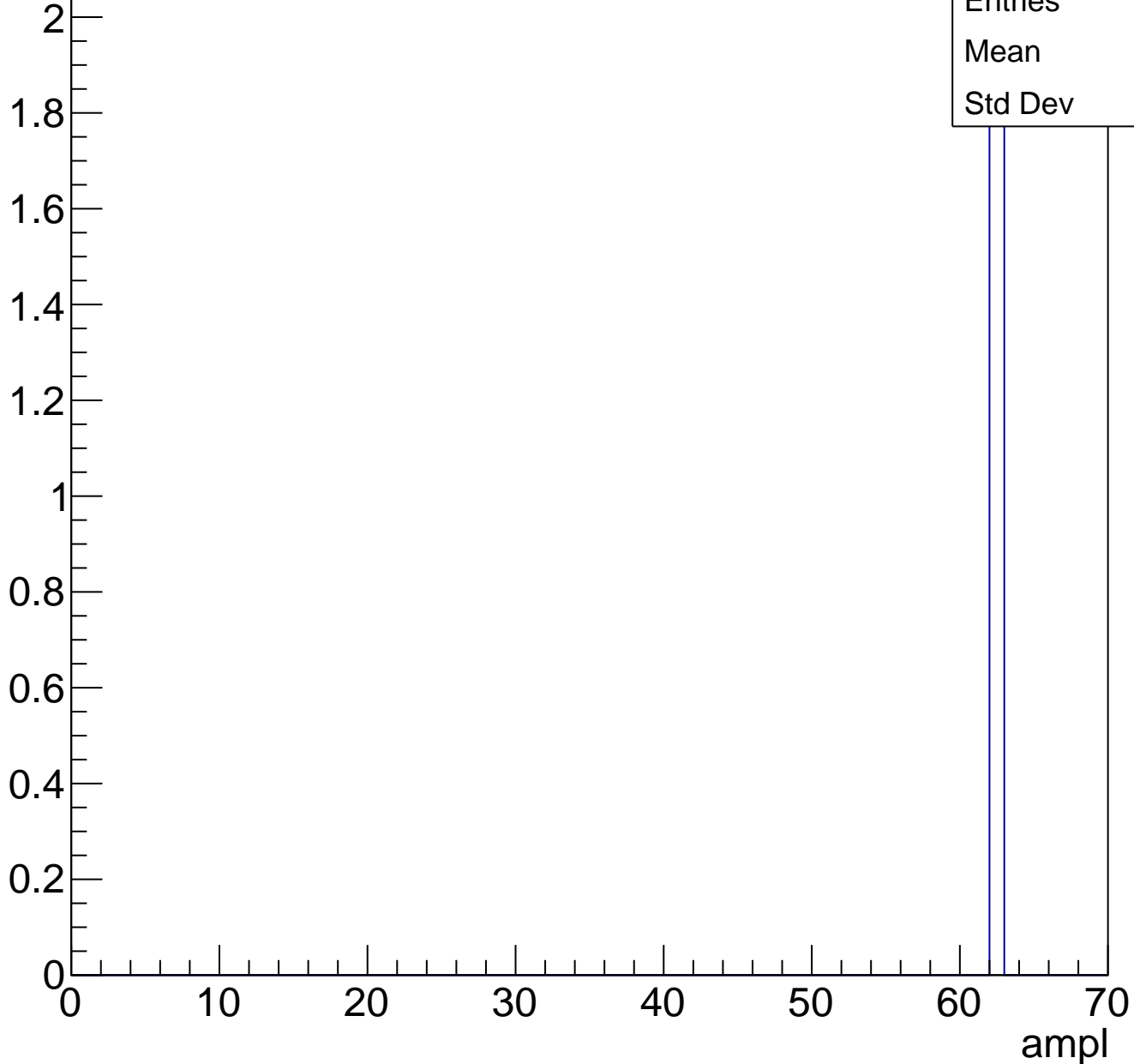
Entries	17
Mean	57.94
Std Dev	14.53



B1L001S, U25-ch94, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



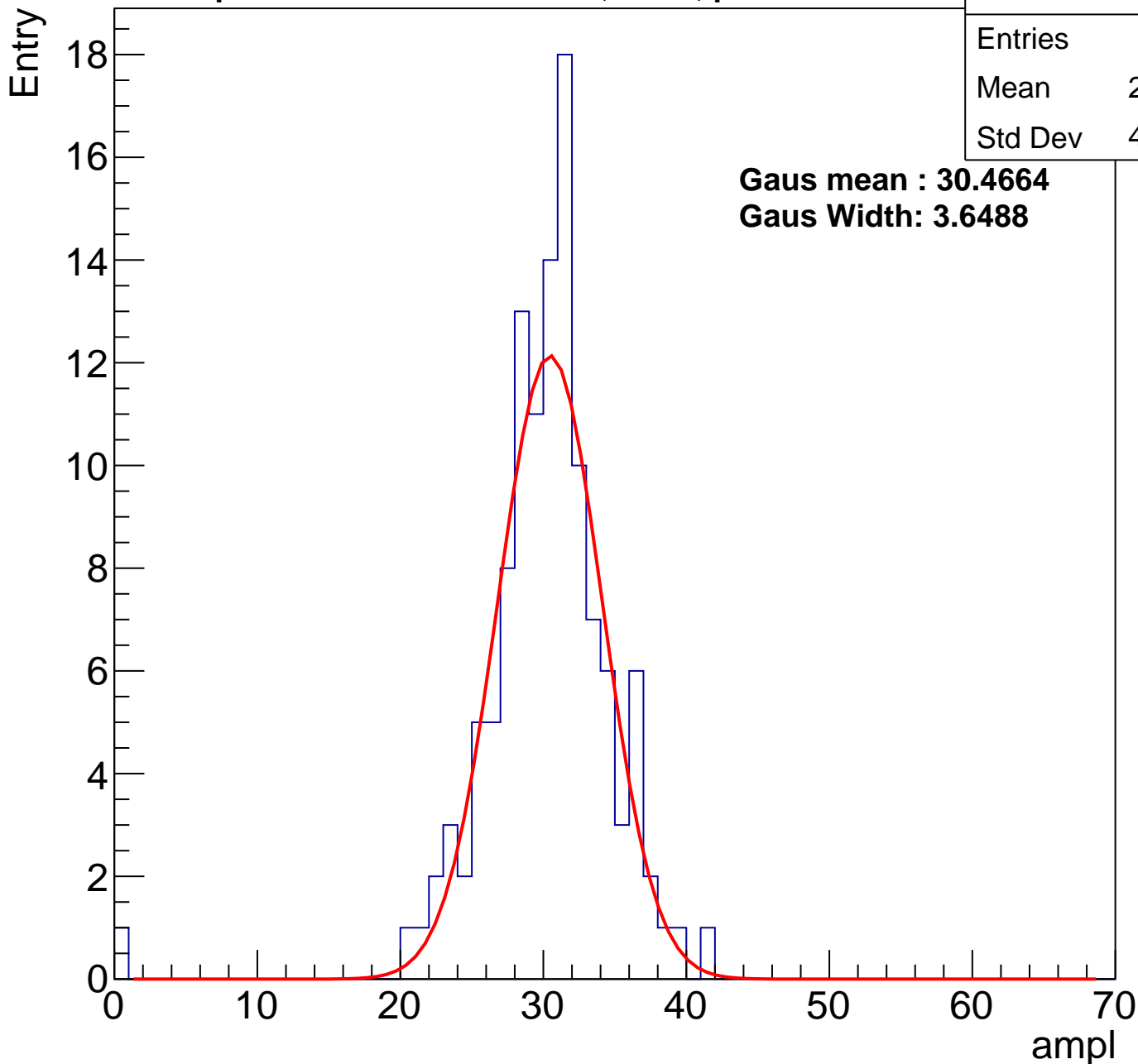
B1L001S, U25-ch95, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	29.77
Std Dev	4.665

Gaus mean : 30.4664

Gaus Width: 3.6488



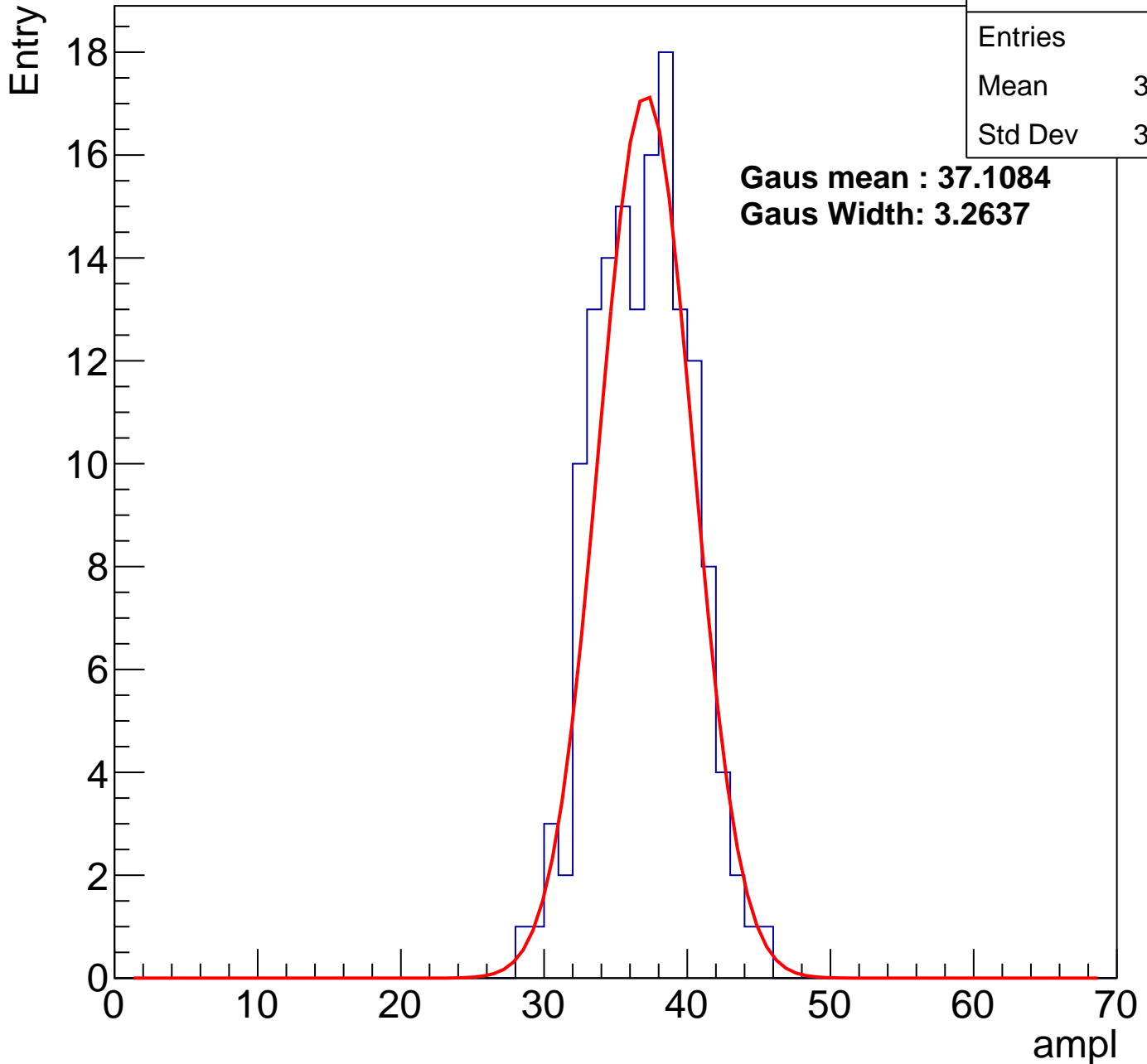
B1L001S, U25-ch95, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	36.47
Std Dev	3.269

Gaus mean : 37.1084

Gaus Width: 3.2637



B1L001S, U25-ch95, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

129

Mean

43.51

Std Dev

3.098

Gaus mean : 44.2095

Gaus Width: 2.8535

0

10

20

30

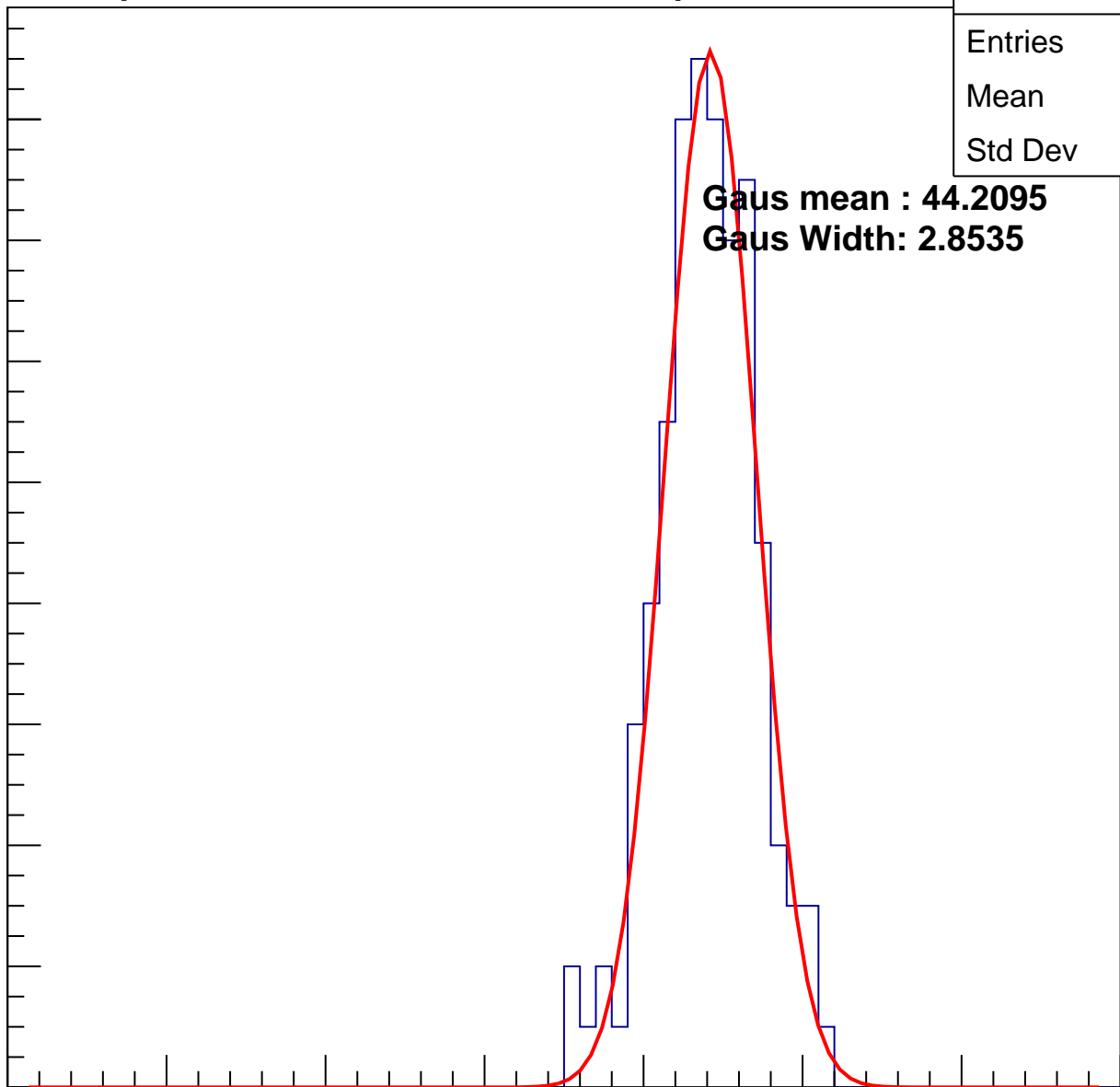
40

50

60

70

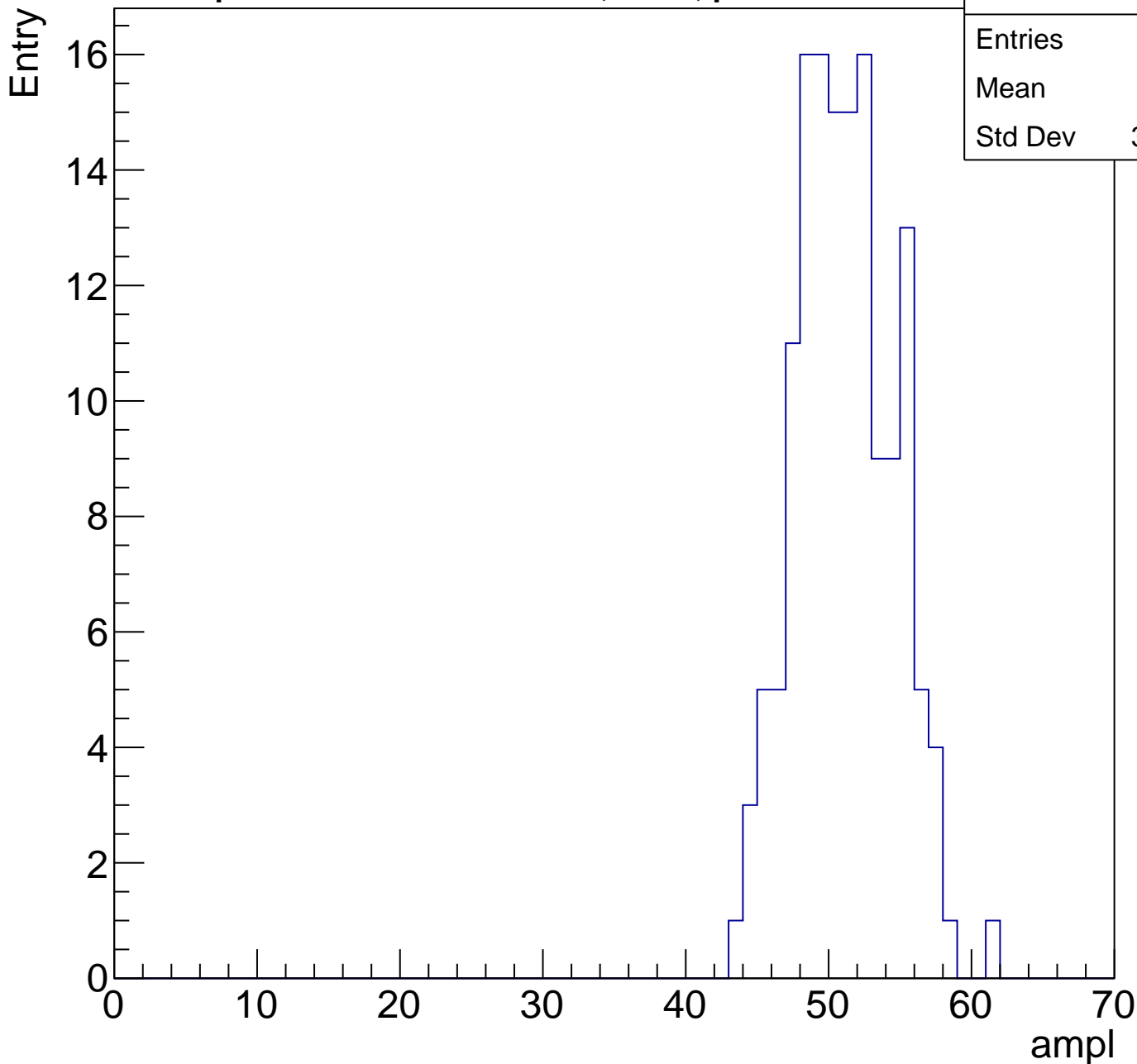
ampl



B1L001S, U25-ch95, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	50.7
Std Dev	3.384

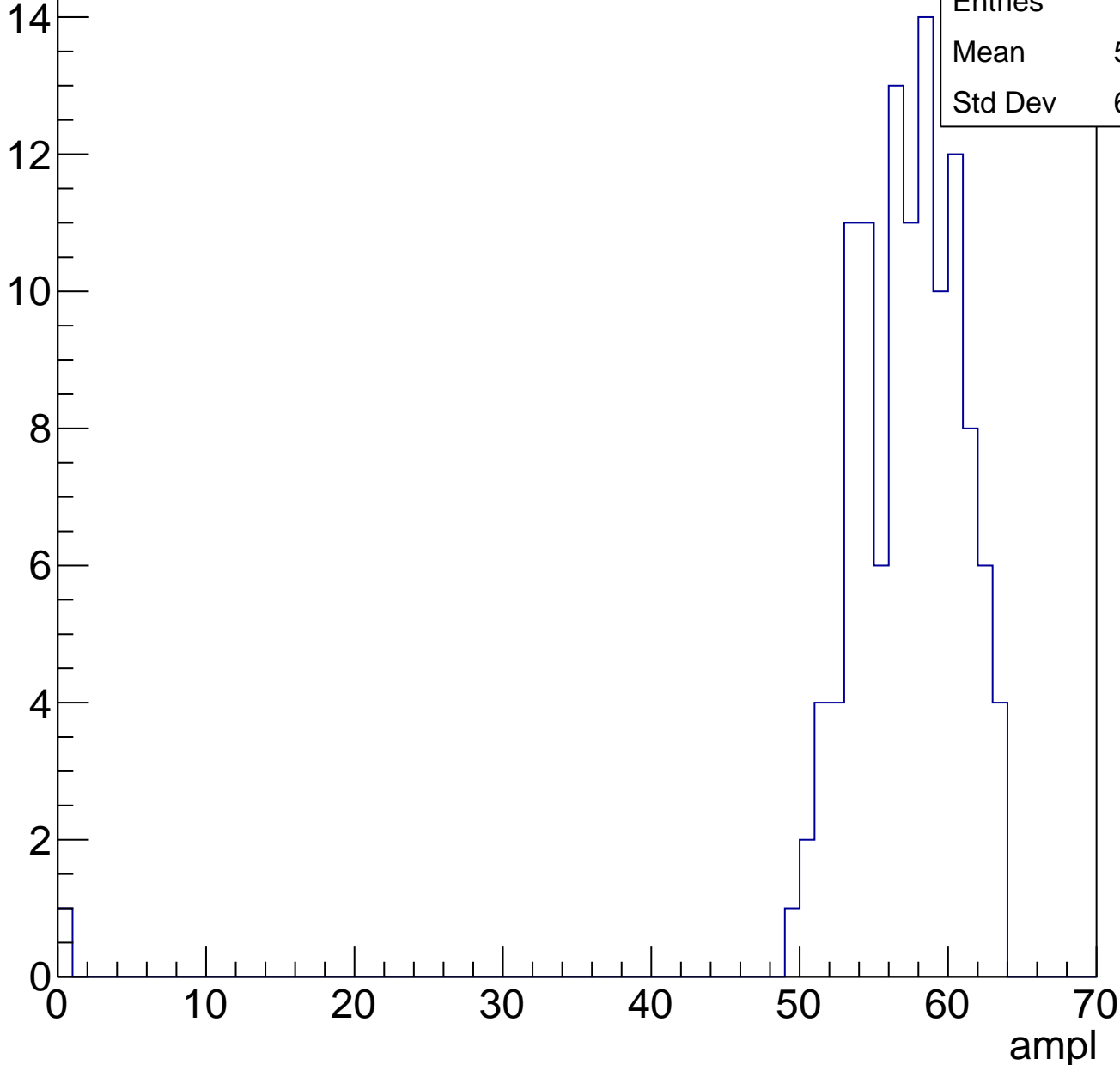


B1L001S, U25-ch95, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	56.42
Std Dev	6.183

Entry



B1L001S, U25-ch95, adc5

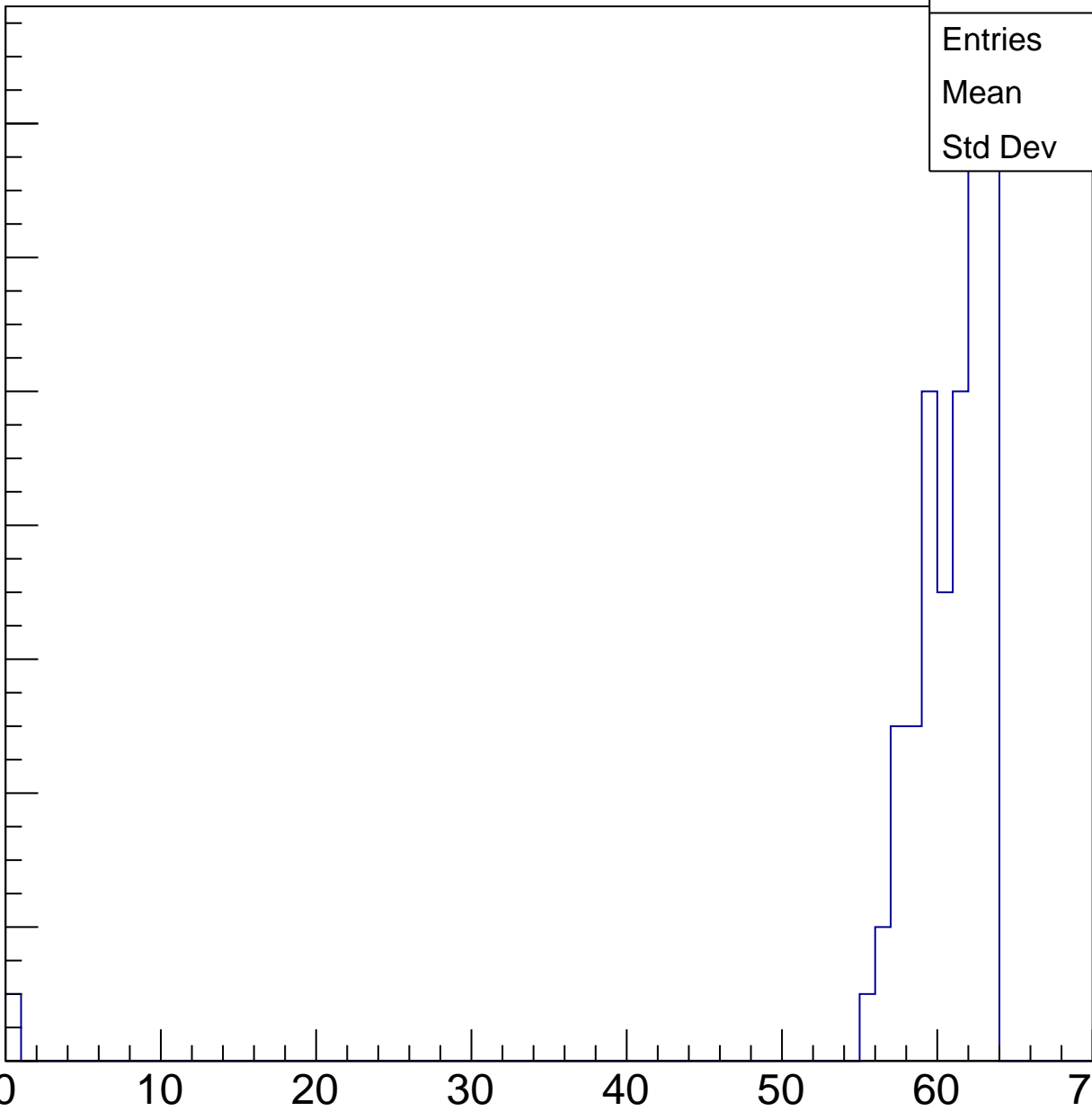
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	70
Mean	59.64
Std Dev	7.487

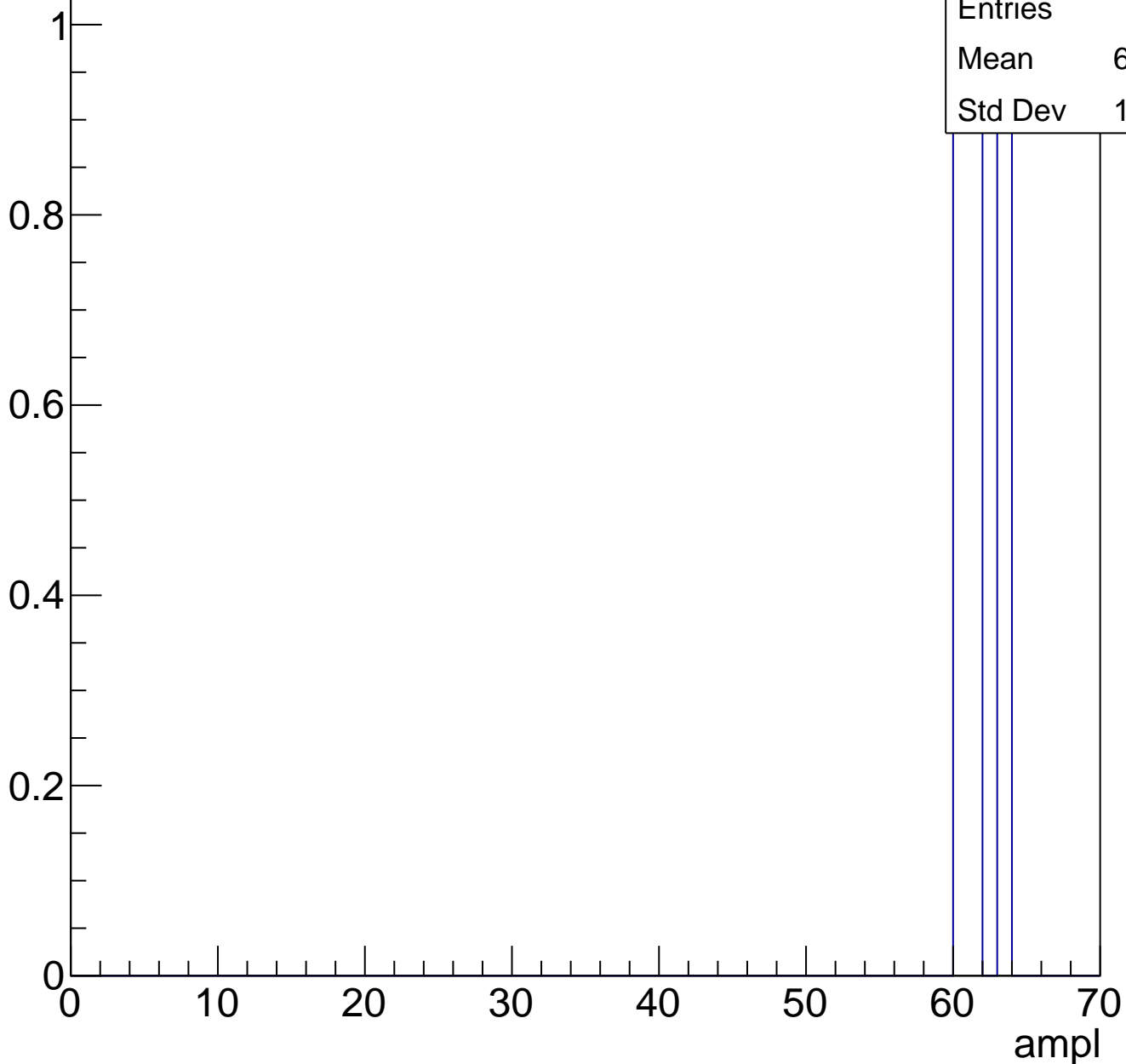
ampl



B1L001S, U25-ch95, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch95, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch96, adc0

calib_packv5_042523_0143.root, FC#2, port C2

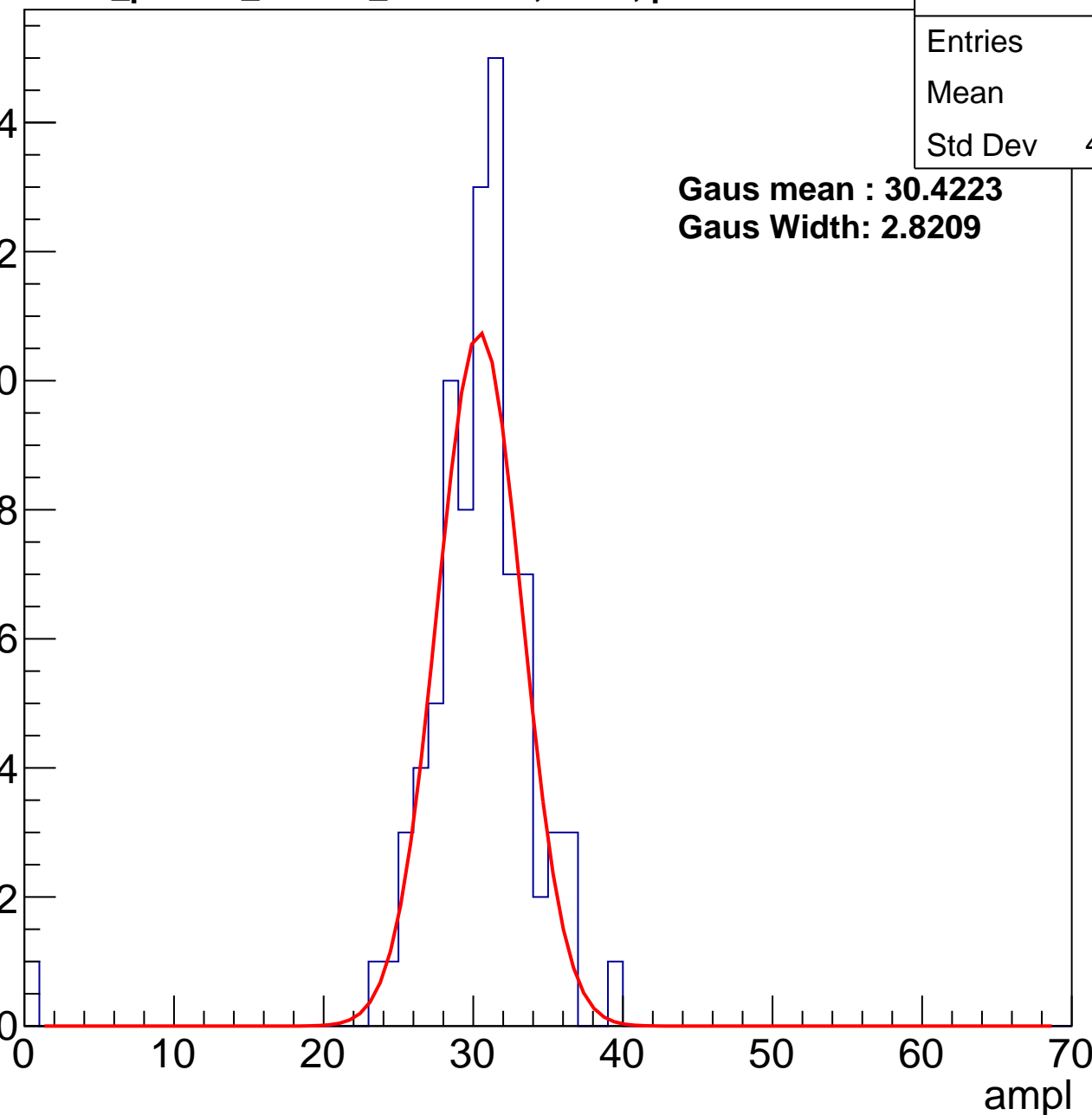
Entry

14
12
10
8
6
4
2
0

Entries	84
Mean	29.8
Std Dev	4.386

Gaus mean : 30.4223

Gaus Width: 2.8209



B1L001S, U25-ch96, adc1

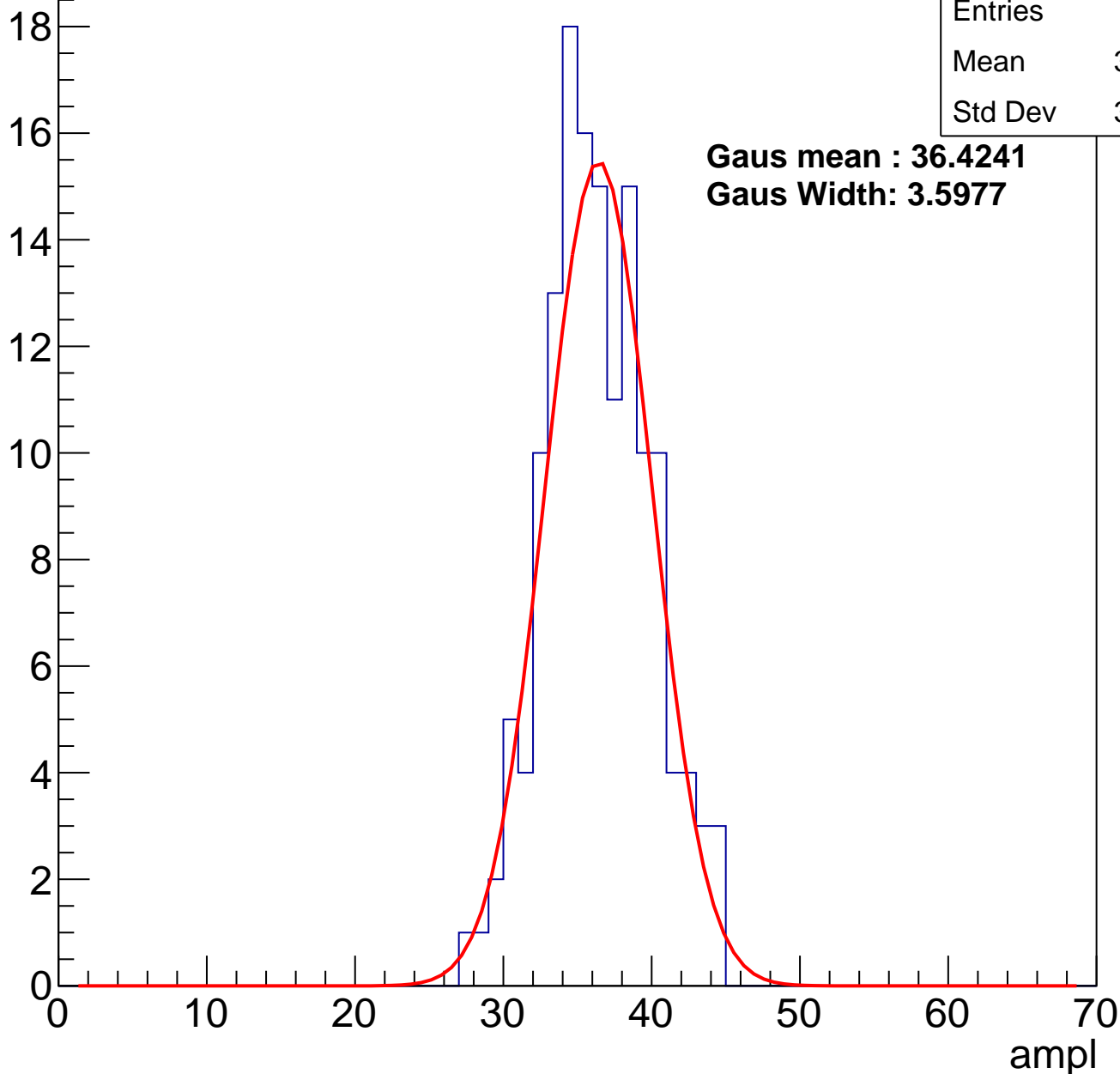
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	35.92
Std Dev	3.515

Gaus mean : 36.4241

Gaus Width: 3.5977

Entry



B1L001S, U25-ch96, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

122

Mean

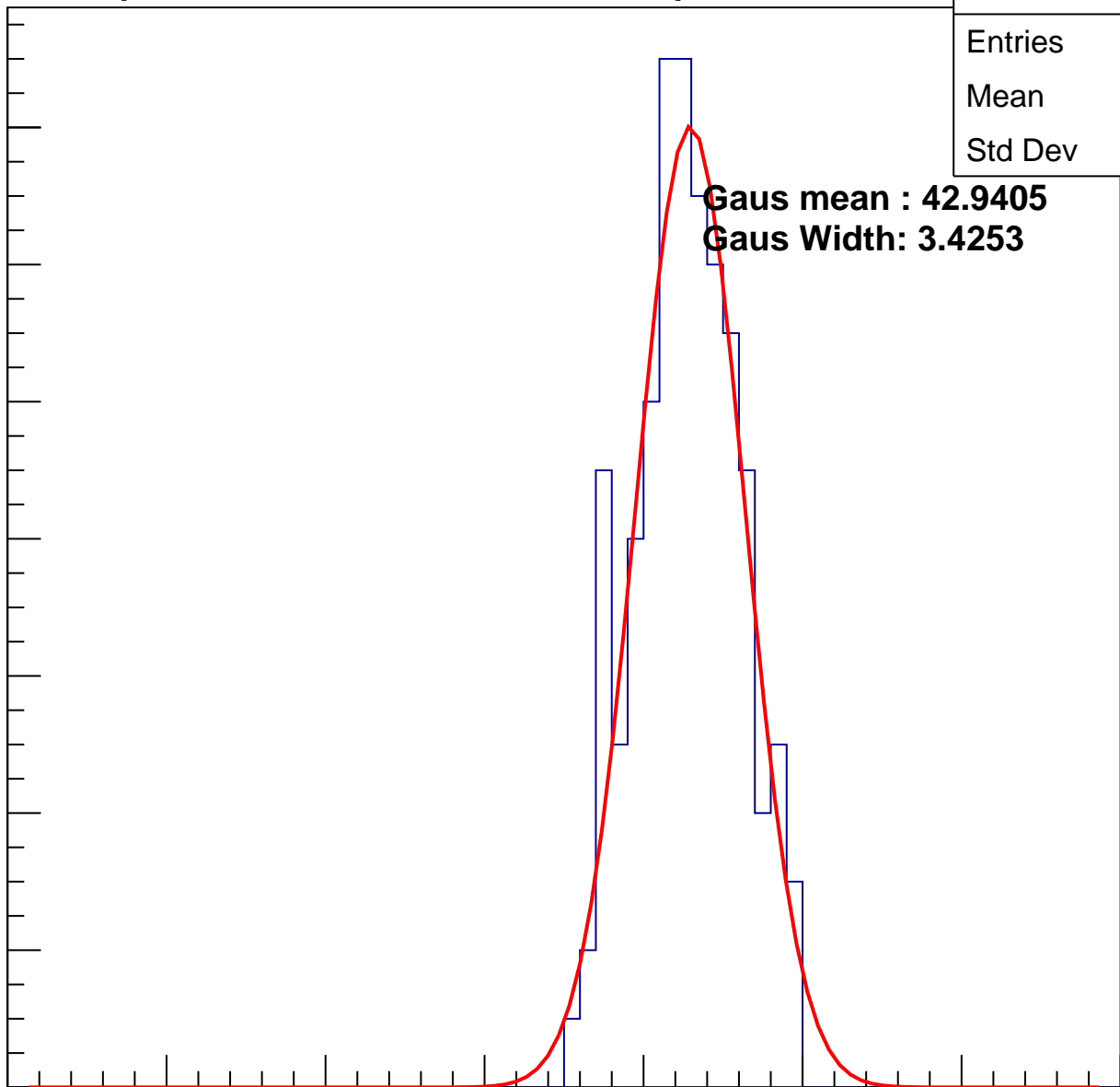
42.28

Std Dev

3.232

Gaus mean : 42.9405

Gaus Width: 3.4253



B1L001S, U25-ch96, adc3

calib_packv5_042523_0143.root, FC#2, port C2

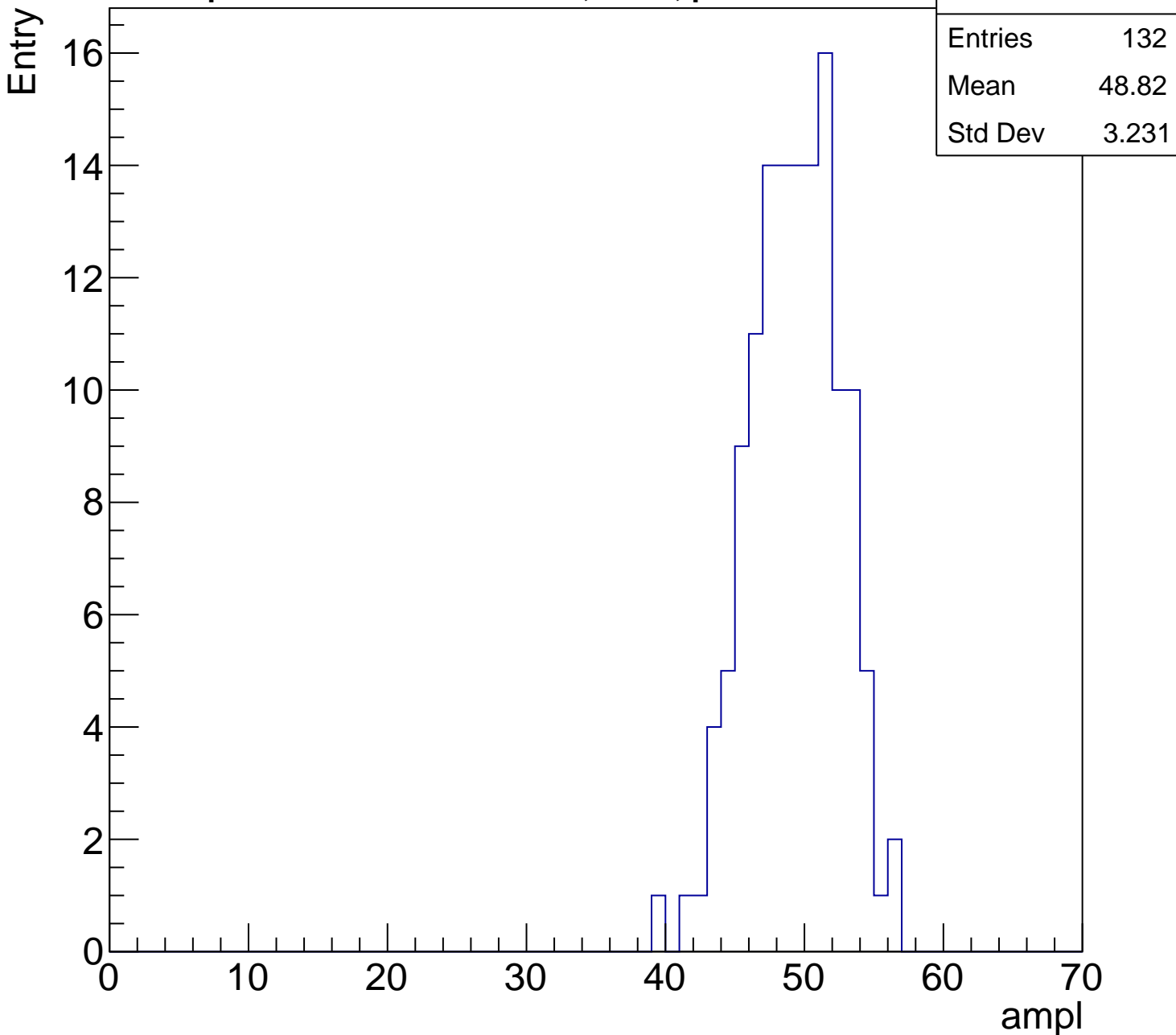
Entries	132
Mean	48.82
Std Dev	3.231

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch96, adc4

calib_packv5_042523_0143.root, FC#2, port C2

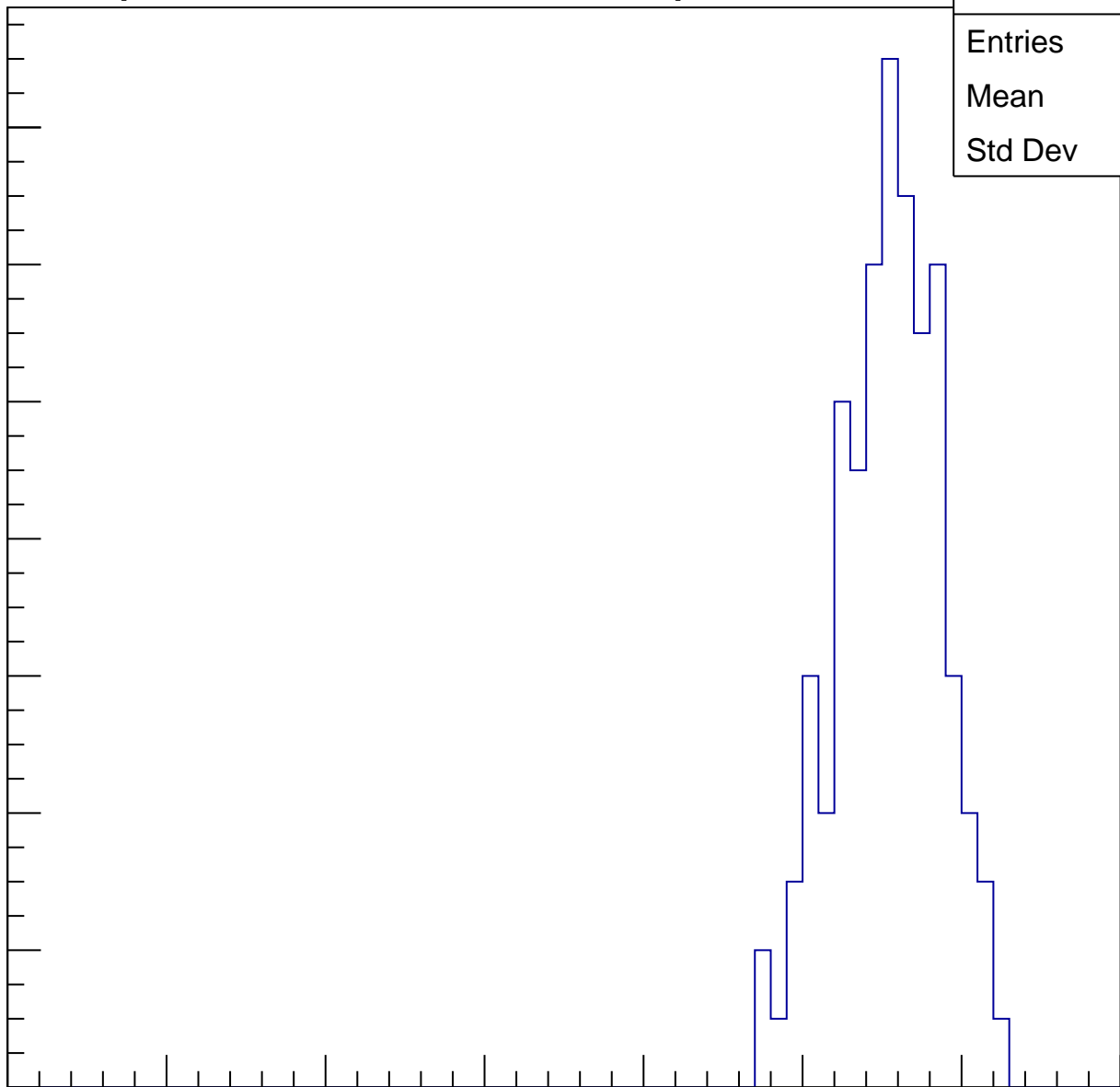
Entries	112
Mean	54.94
Std Dev	3.211

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

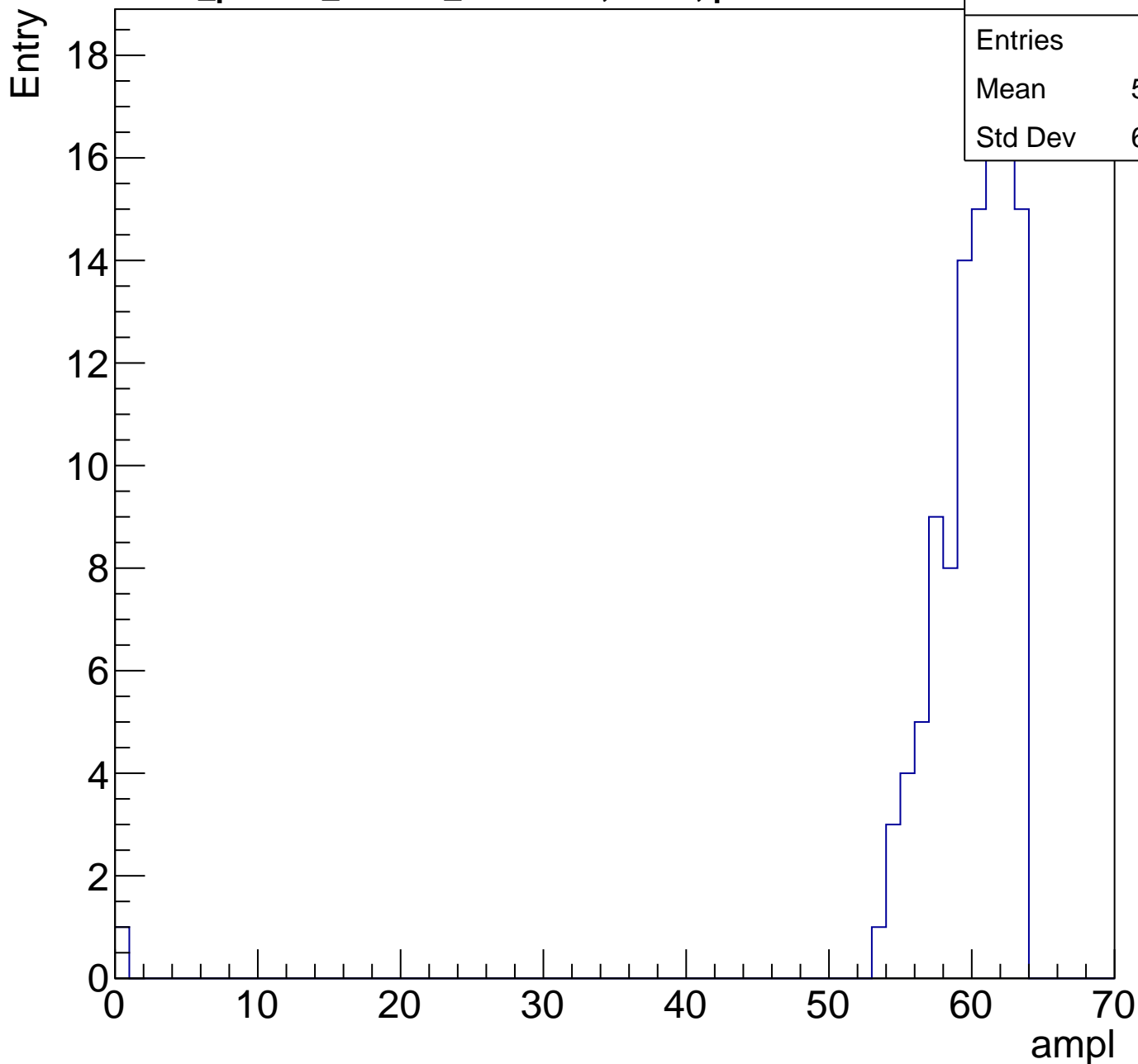
ampl



B1L001S, U25-ch96, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	59.24
Std Dev	6.197

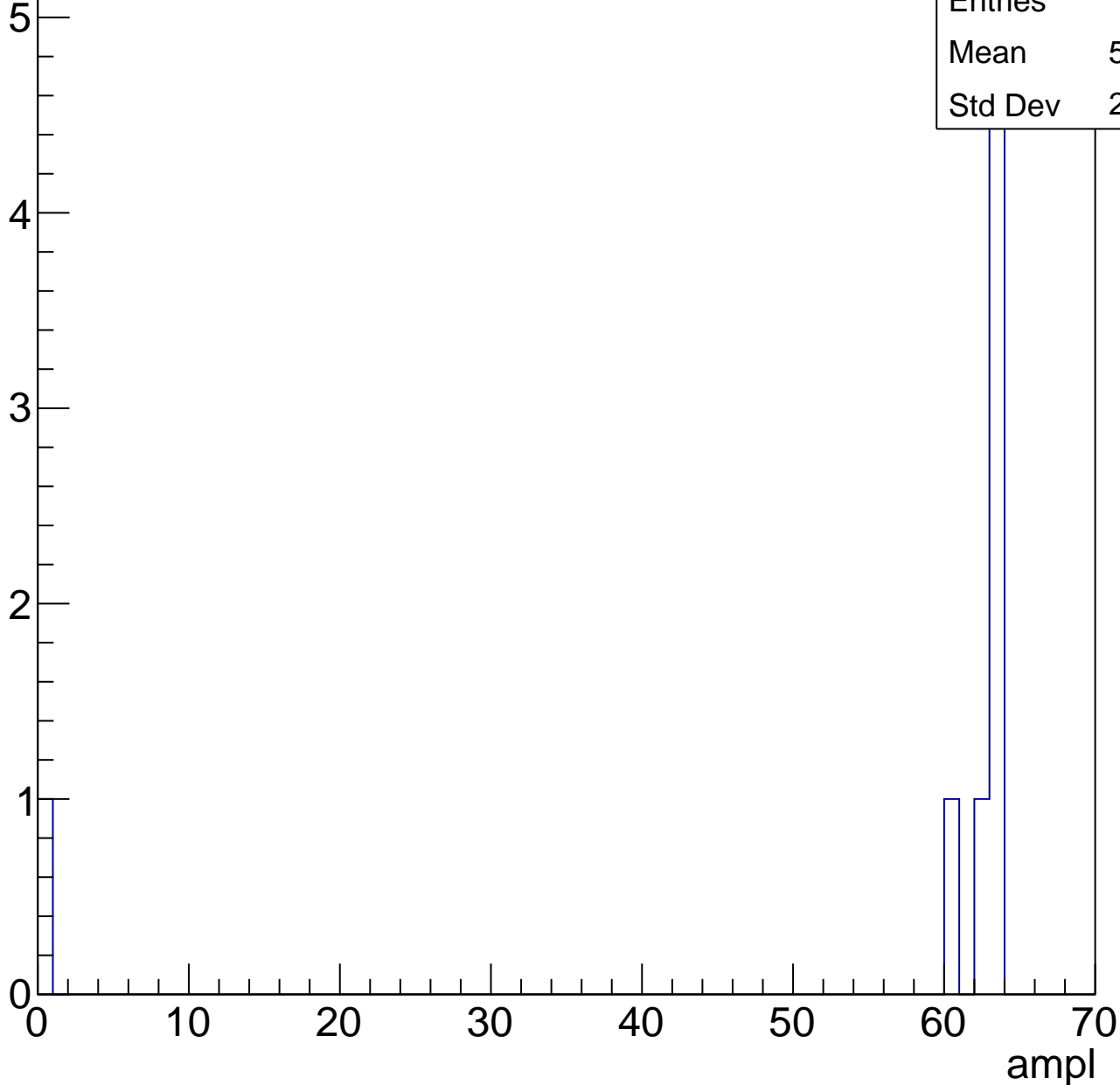


B1L001S, U25-ch96, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	54.62
Std Dev	20.67



B1L001S, U25-ch96, adc7

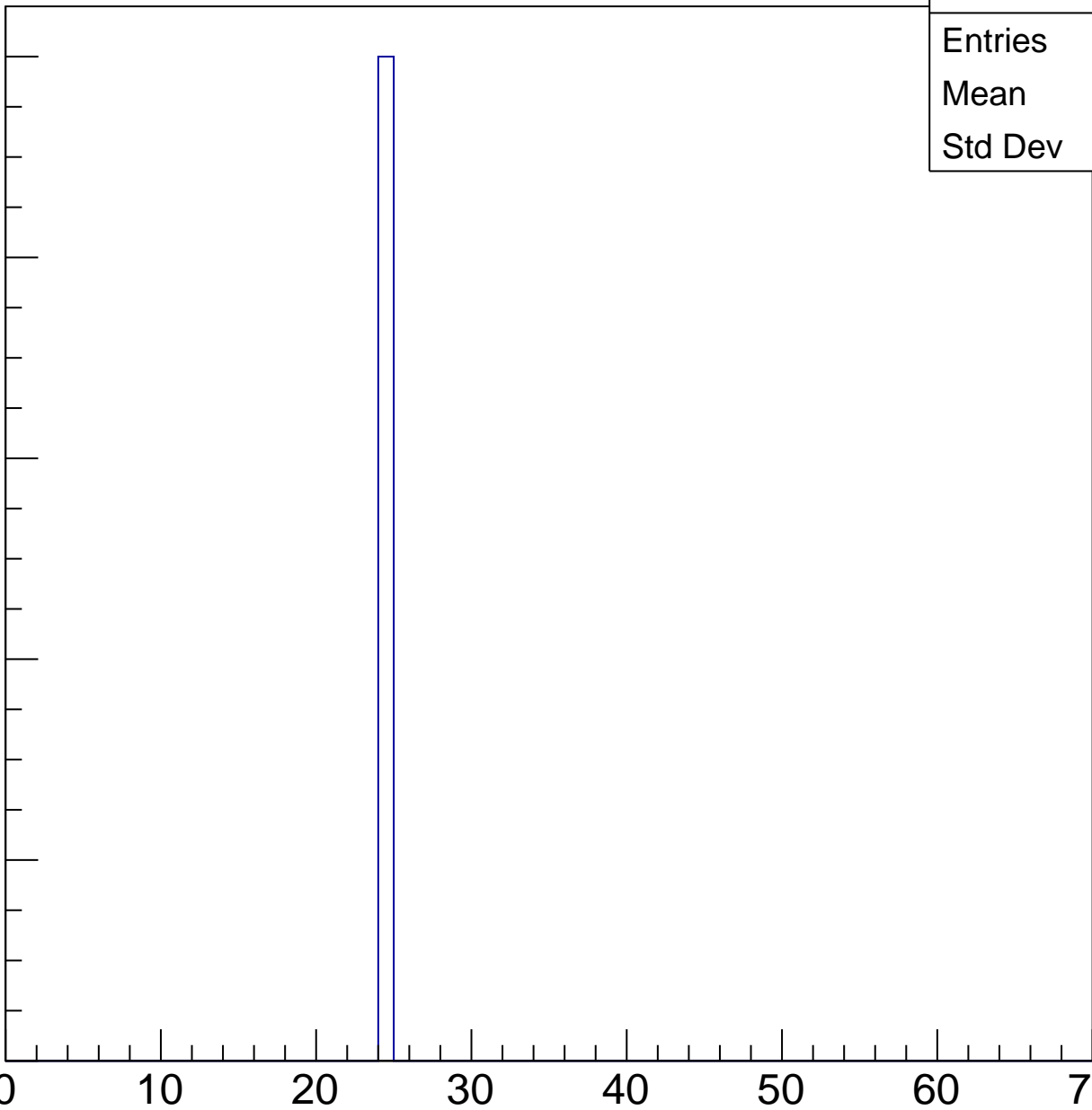
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



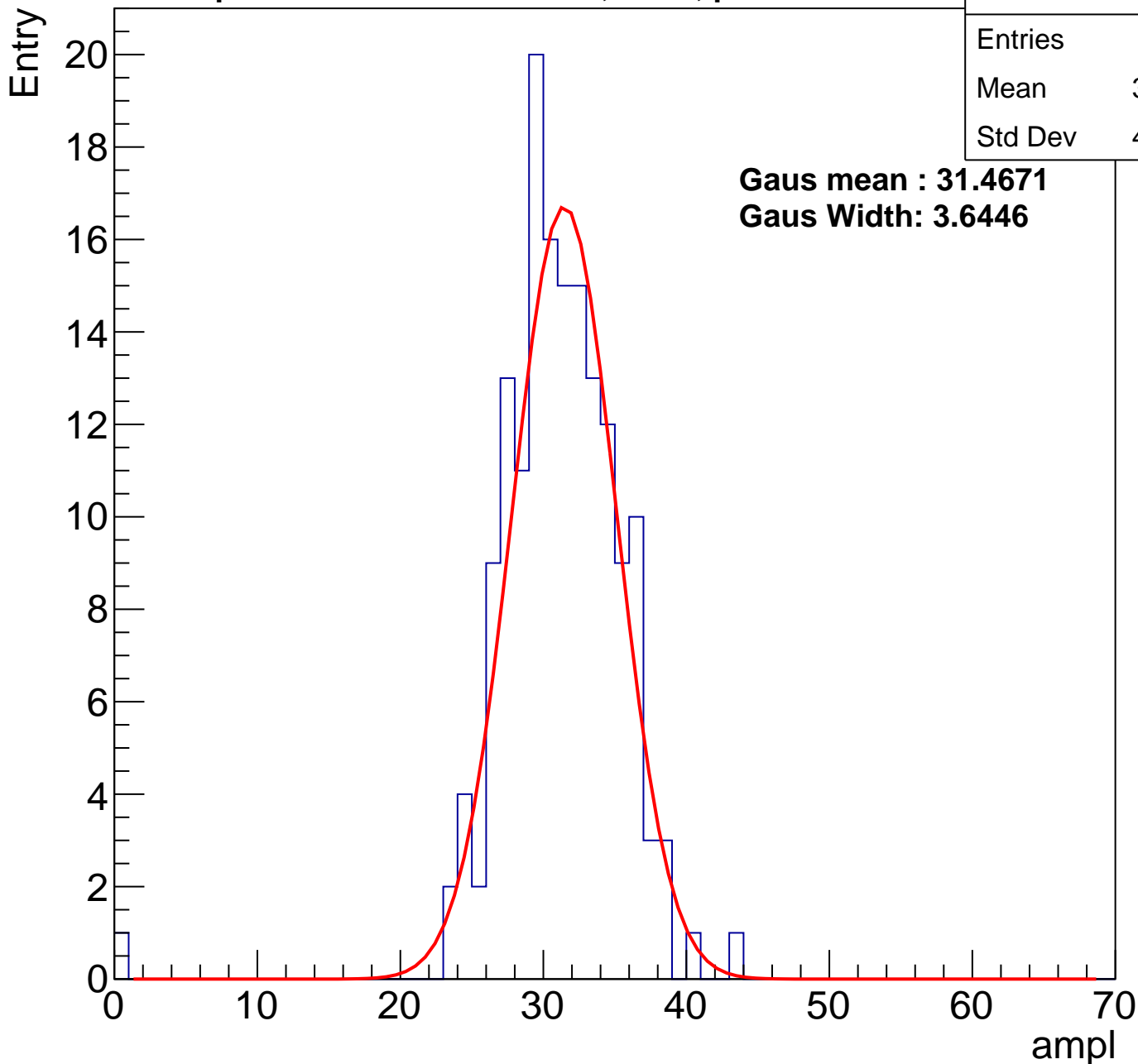
B1L001S, U25-ch97, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	30.69
Std Dev	4.343

Gaus mean : 31.4671

Gaus Width: 3.6446



B1L001S, U25-ch97, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	38.42
Std Dev	3.487

Gaus mean : 38.8991

Gaus Width: 3.9194

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

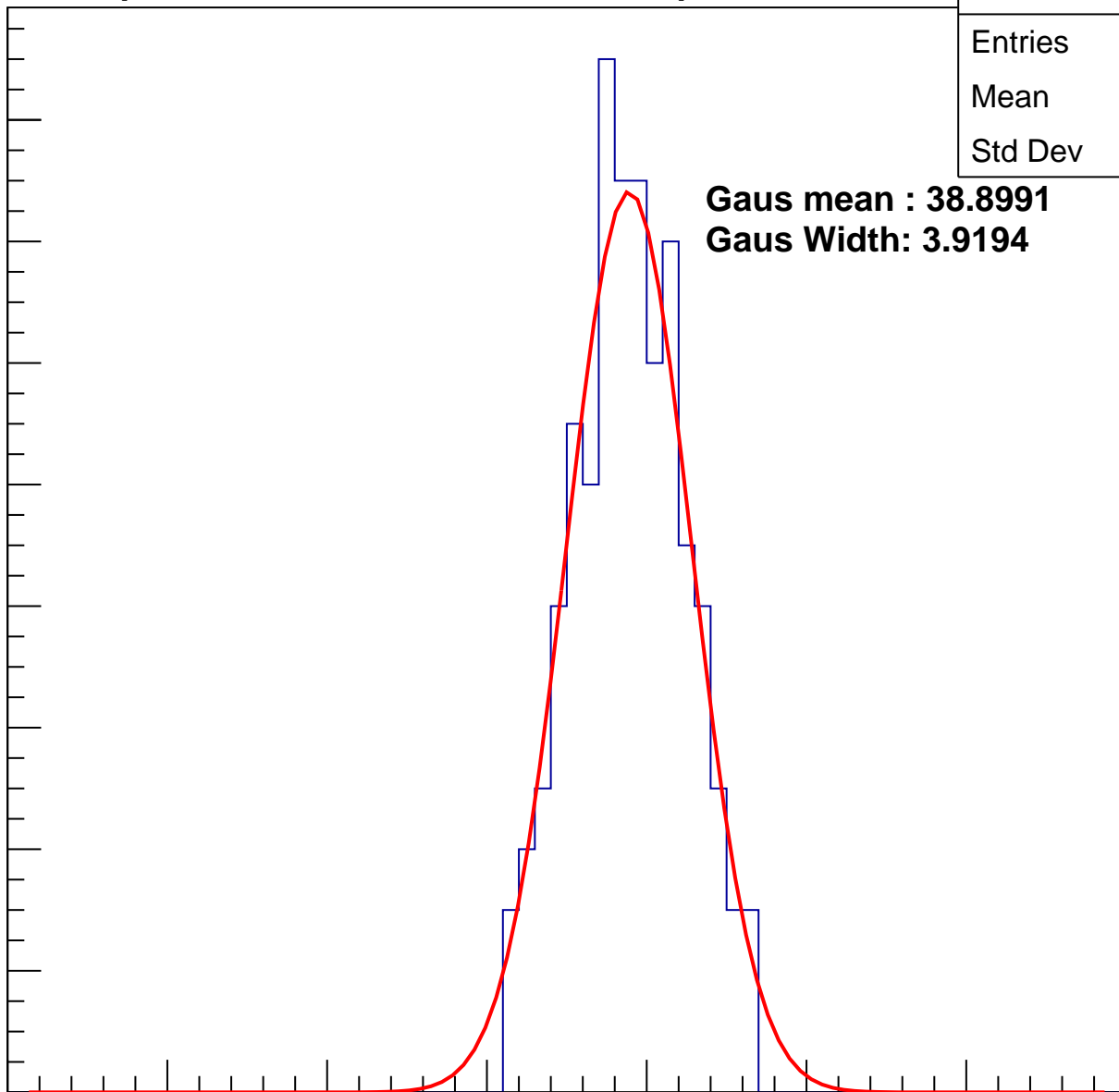
40

50

60

70

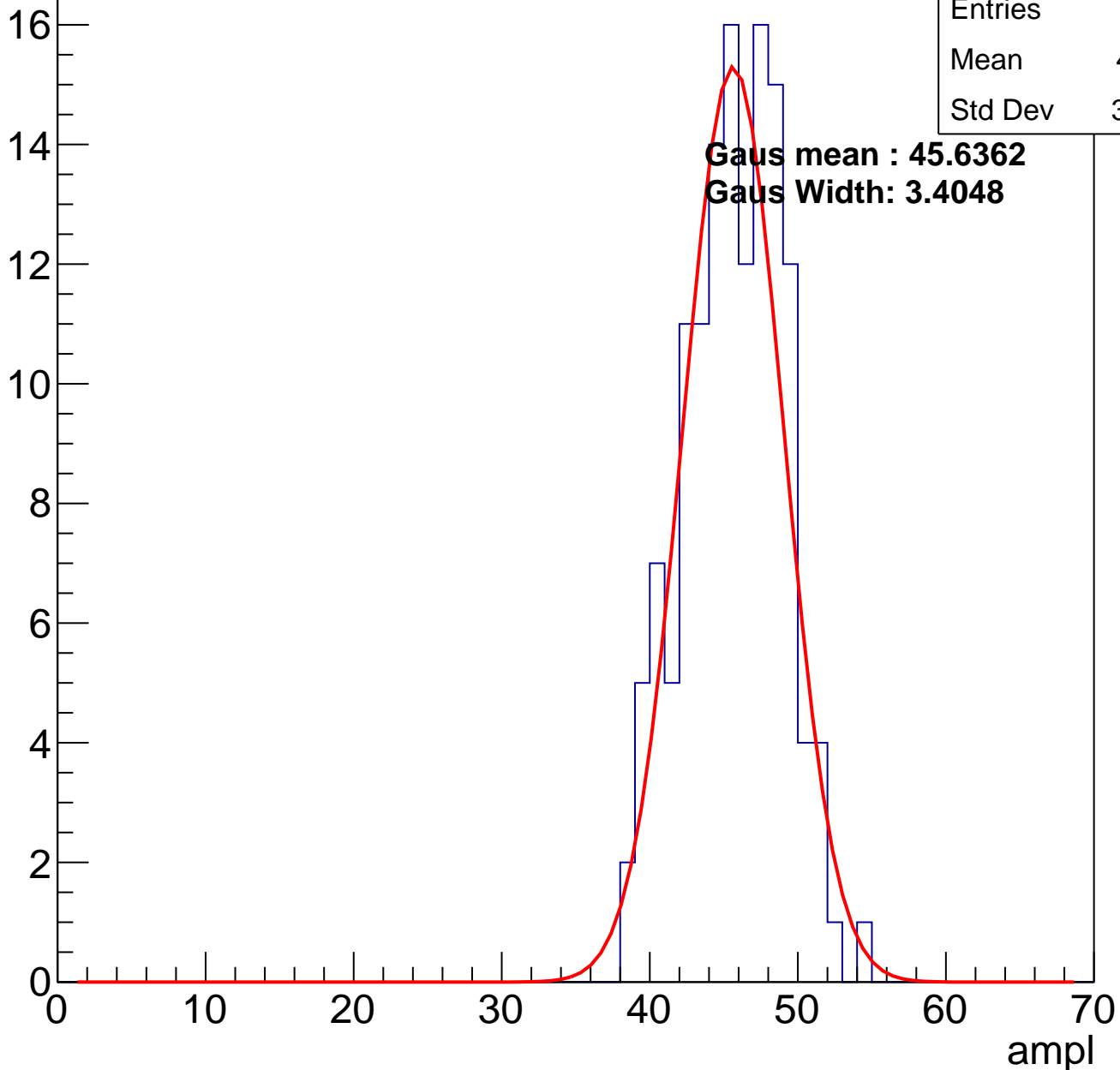
ampl



B1L001S, U25-ch97, adc2

calib_packv5_042523_0143.root, FC#2, port C2

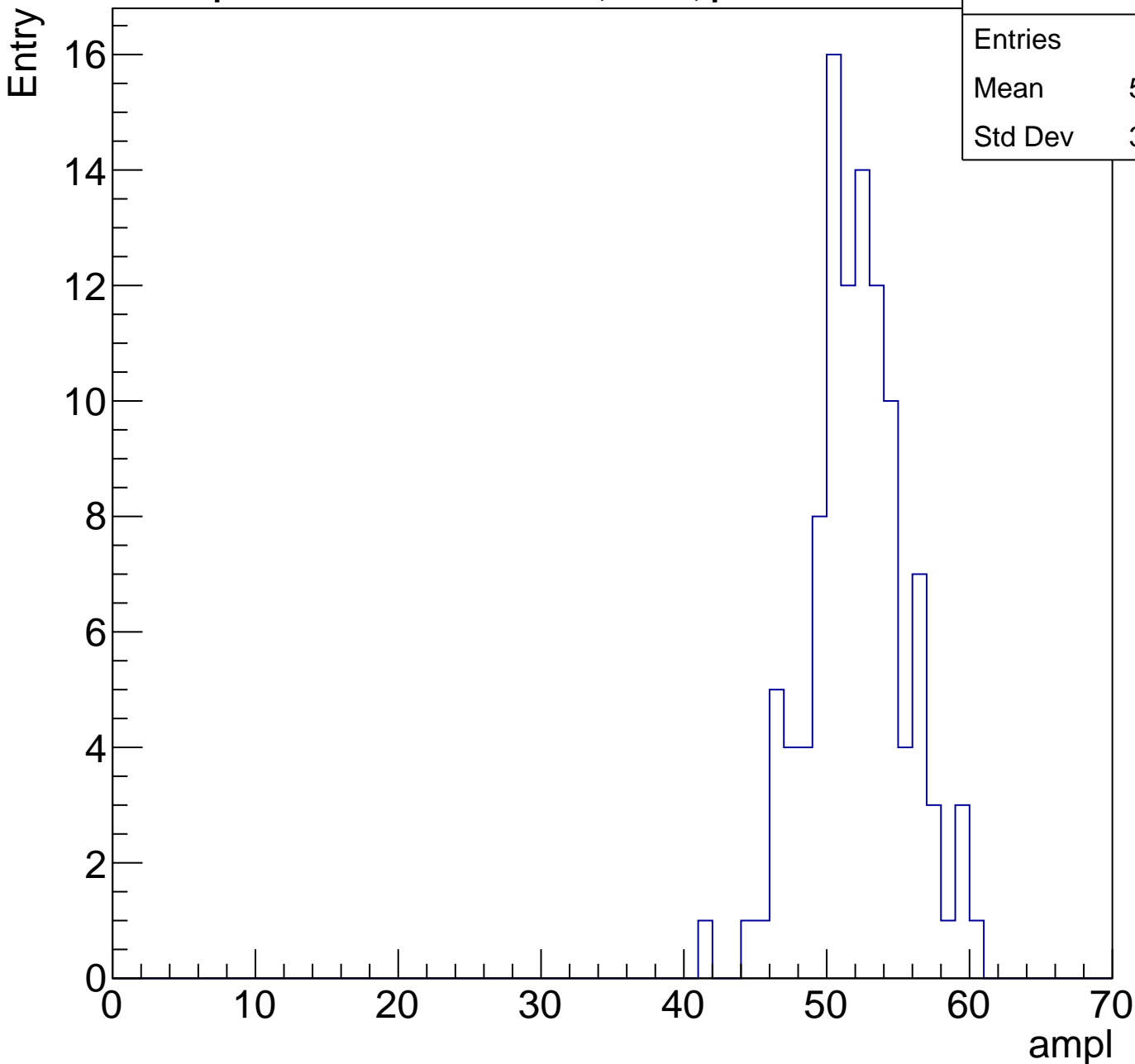
Entry



B1L001S, U25-ch97, adc3

calib_packv5_042523_0143.root, FC#2, port C2

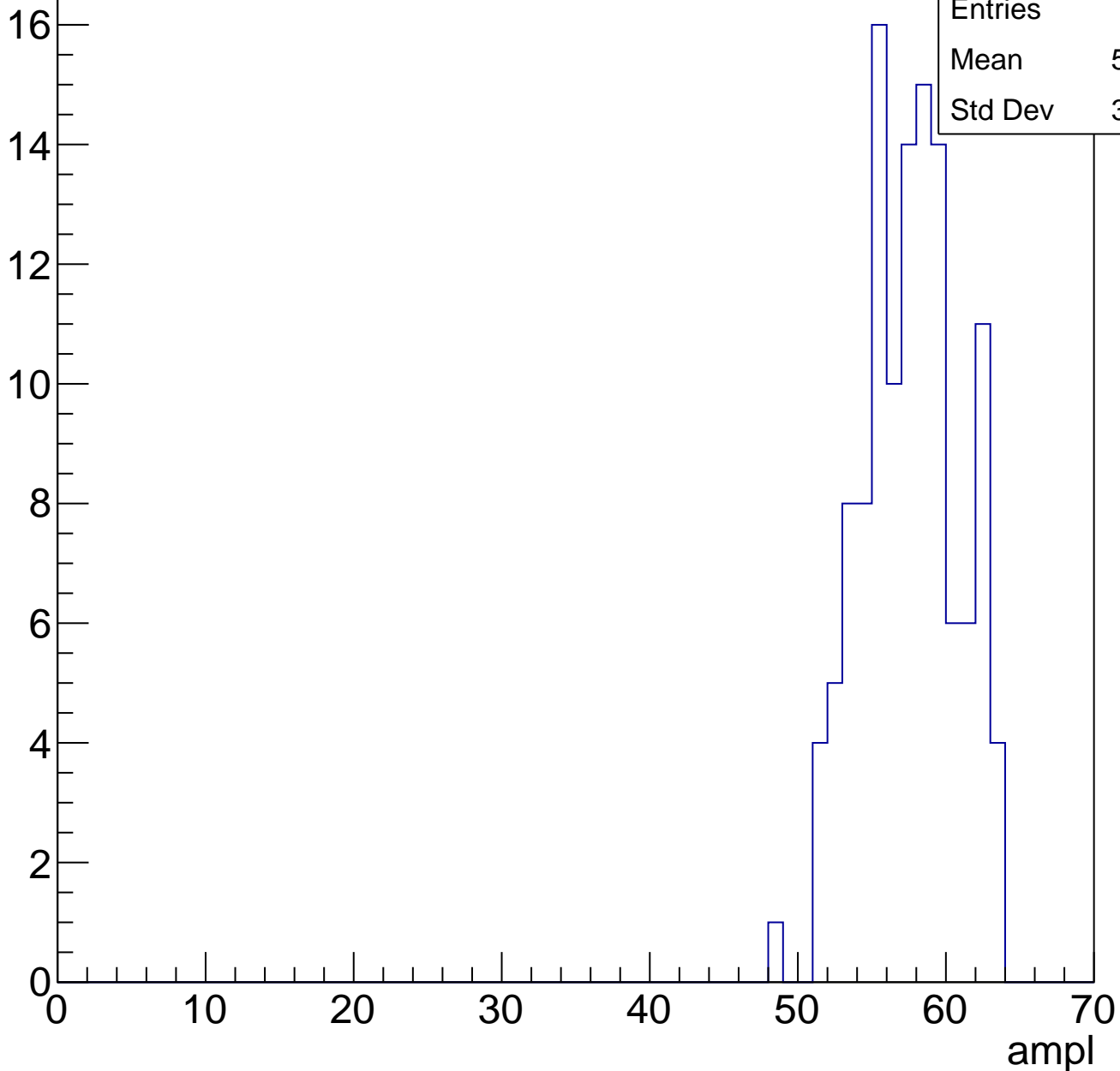
Entries	107
Mean	51.64
Std Dev	3.397



B1L001S, U25-ch97, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch97, adc5

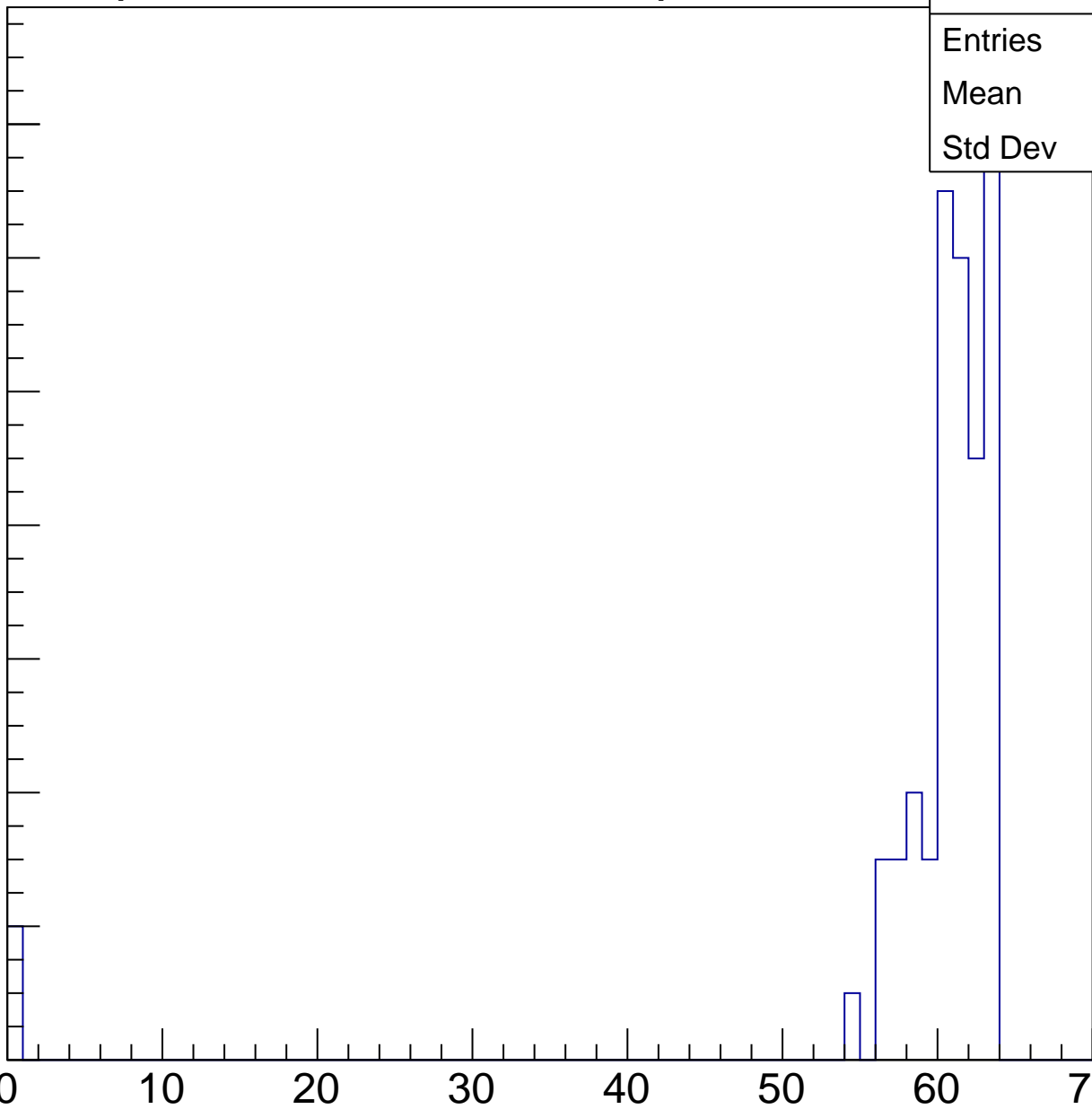
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	58.72
Std Dev	10.67

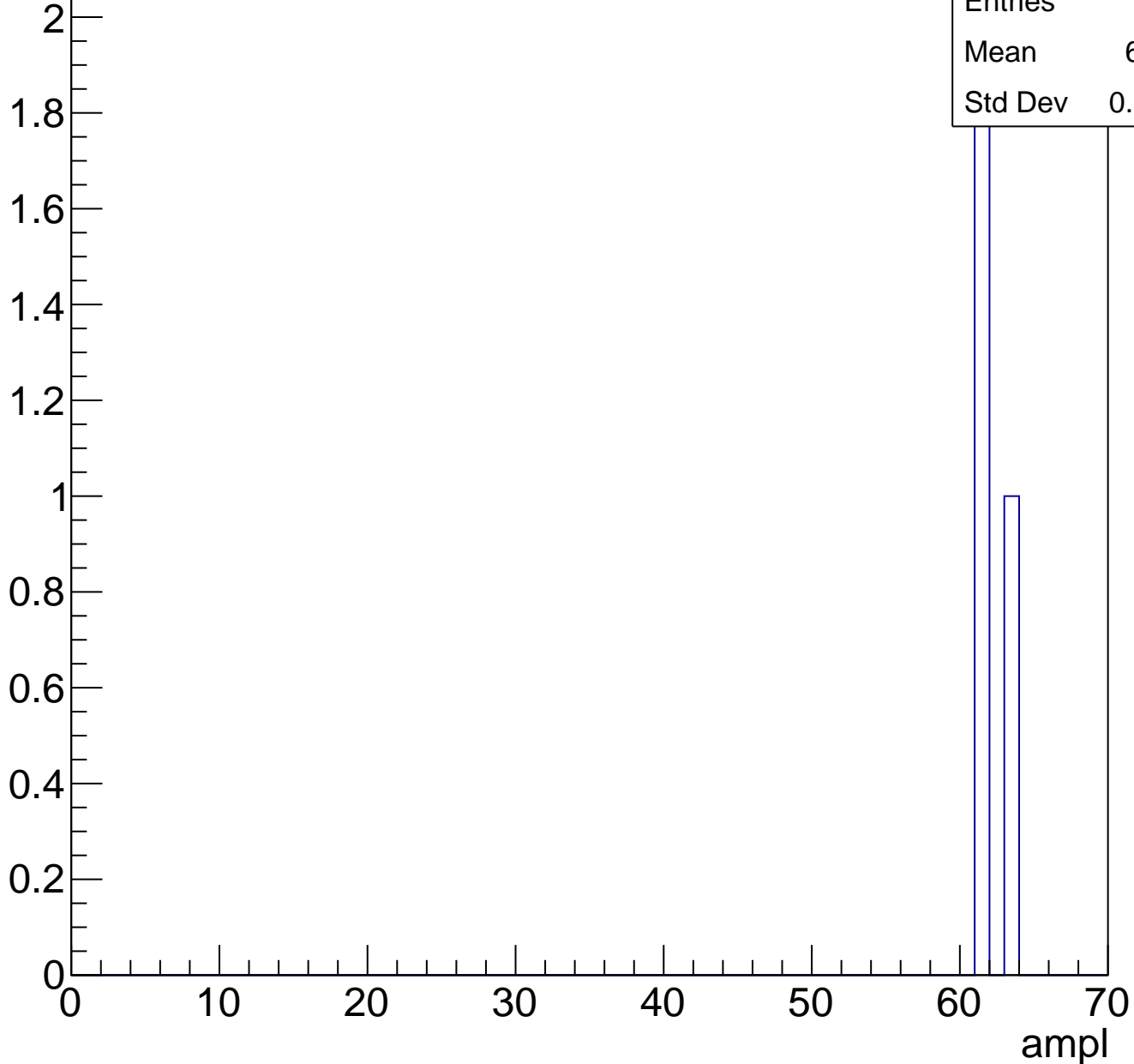
ampl



B1L001S, U25-ch97, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch97, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U25-ch98, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

120

Mean

29.1

Std Dev

4.061

Gaus mean : 29.5836

Gaus Width: 2.8730

0

10

20

30

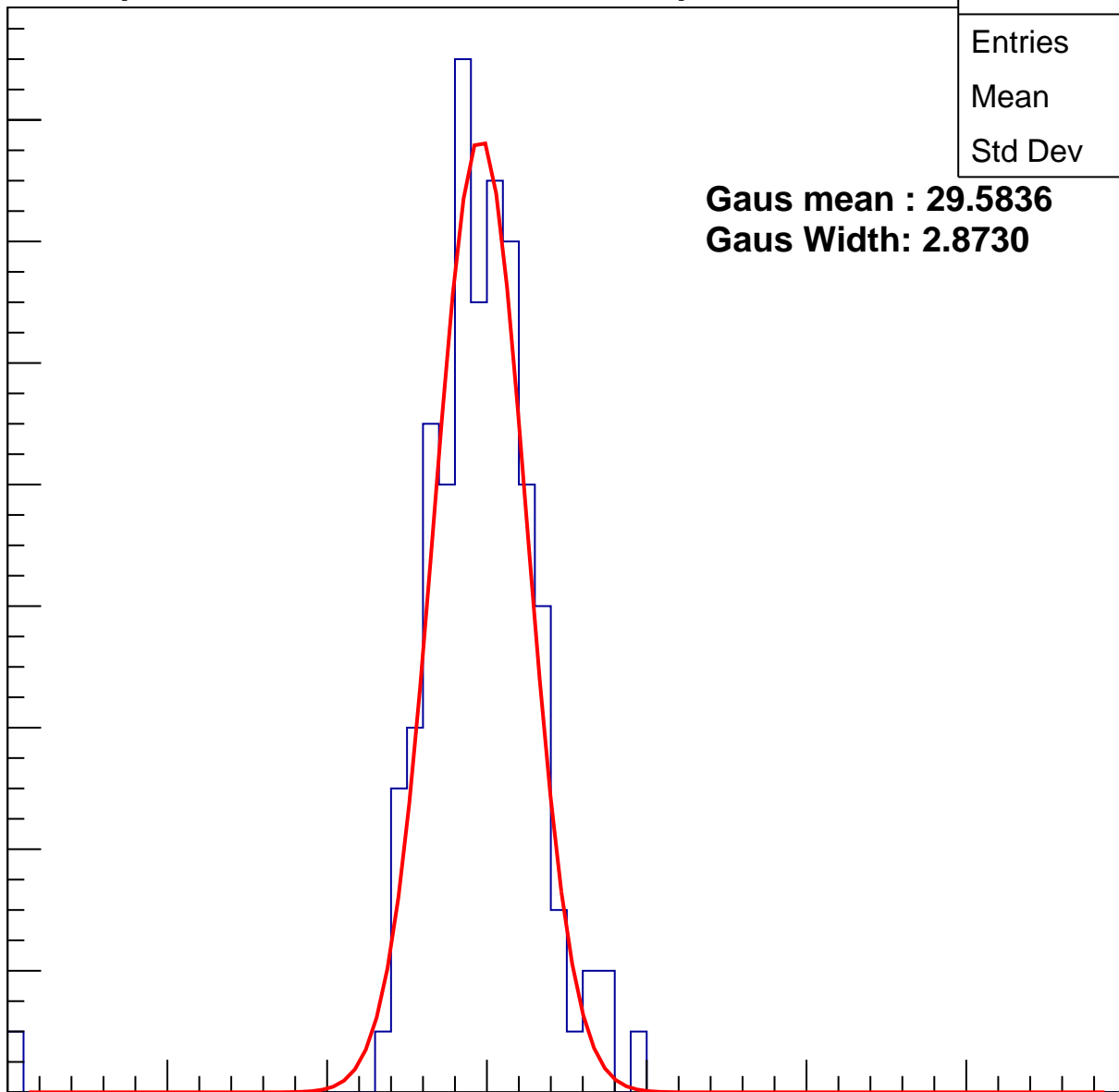
40

50

60

70

ampl



B1L001S, U25-ch98, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	36.05
Std Dev	3.672

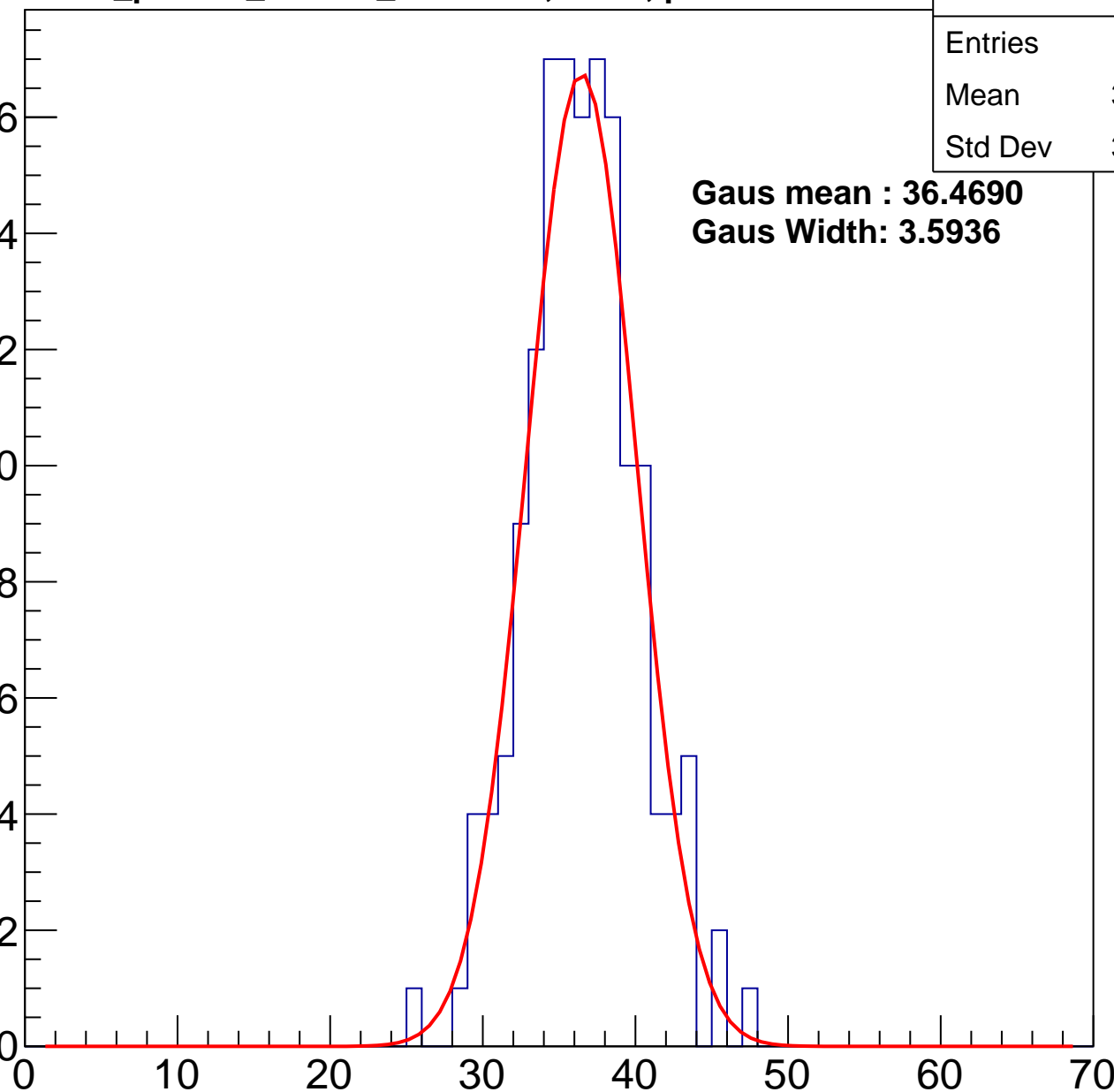
Gaus mean : 36.4690

Gaus Width: 3.5936

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch98, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

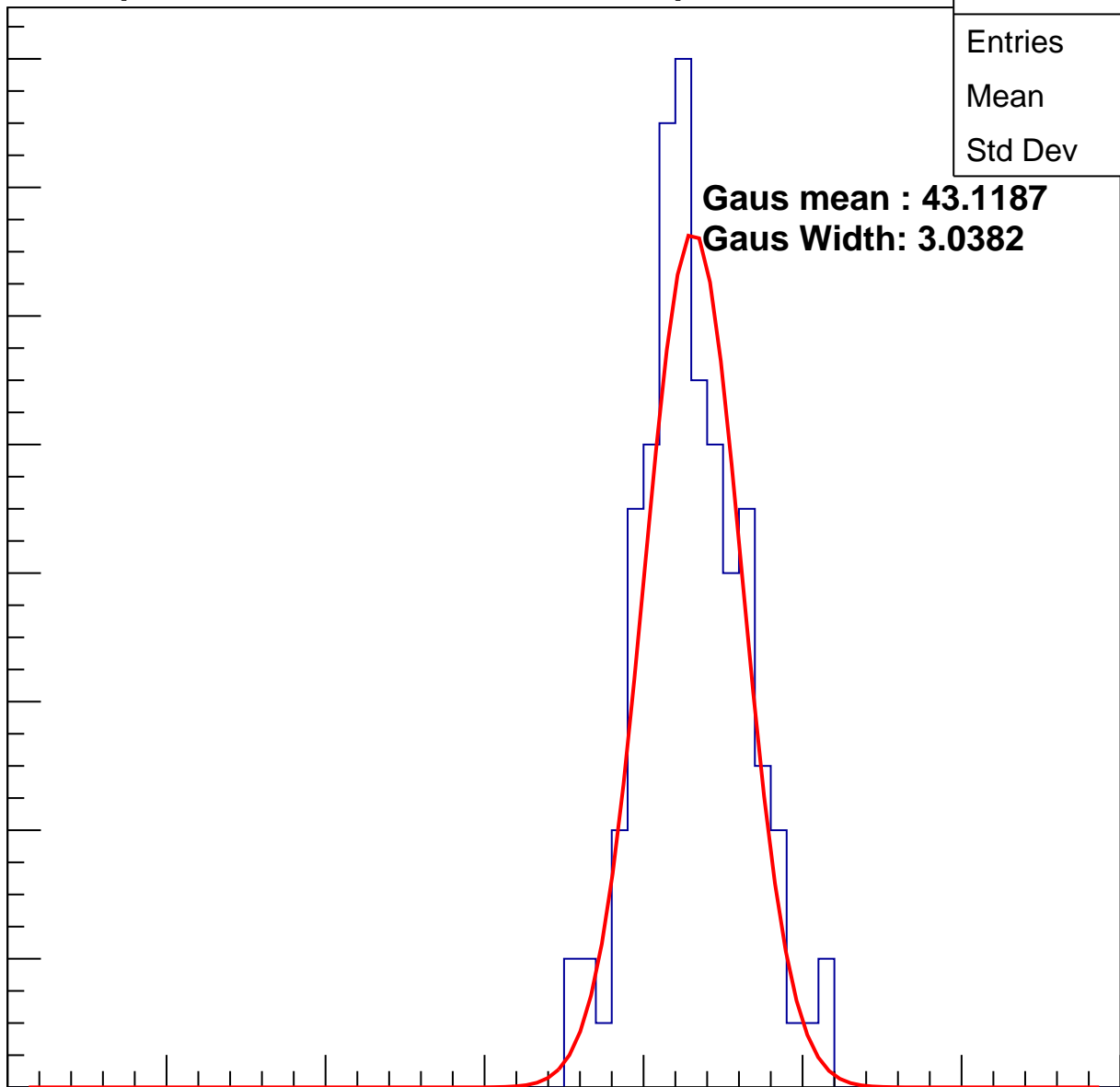
Entries	110
Mean	42.58
Std Dev	3.26

Gaus mean : 43.1187

Gaus Width: 3.0382

0 10 20 30 40 50 60 70

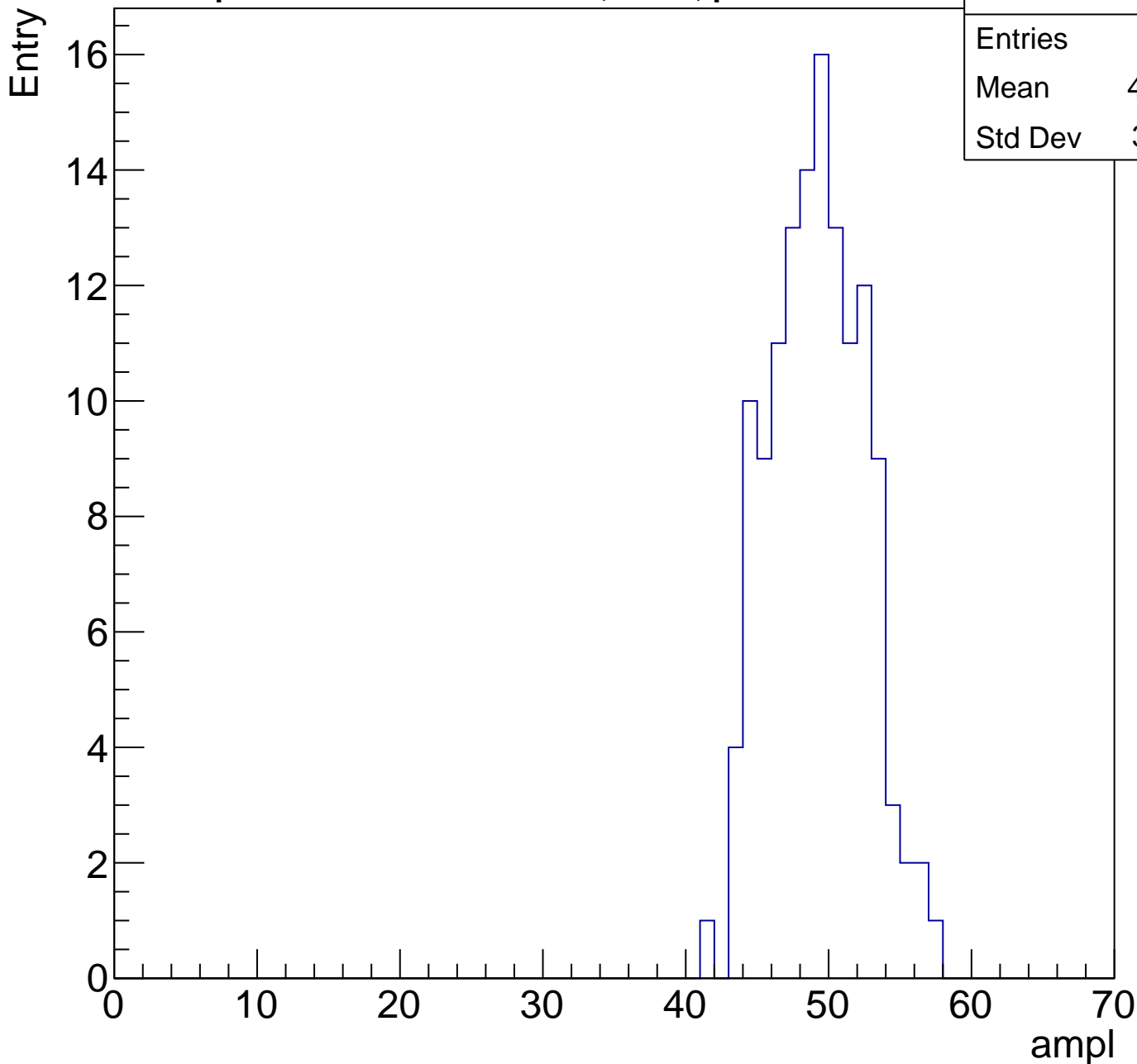
ampl



B1L001S, U25-ch98, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	48.73
Std Dev	3.241



B1L001S, U25-ch98, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	54.81
Std Dev	3.317

Entry

16

14

12

10

8

6

4

2

0

0

10

20

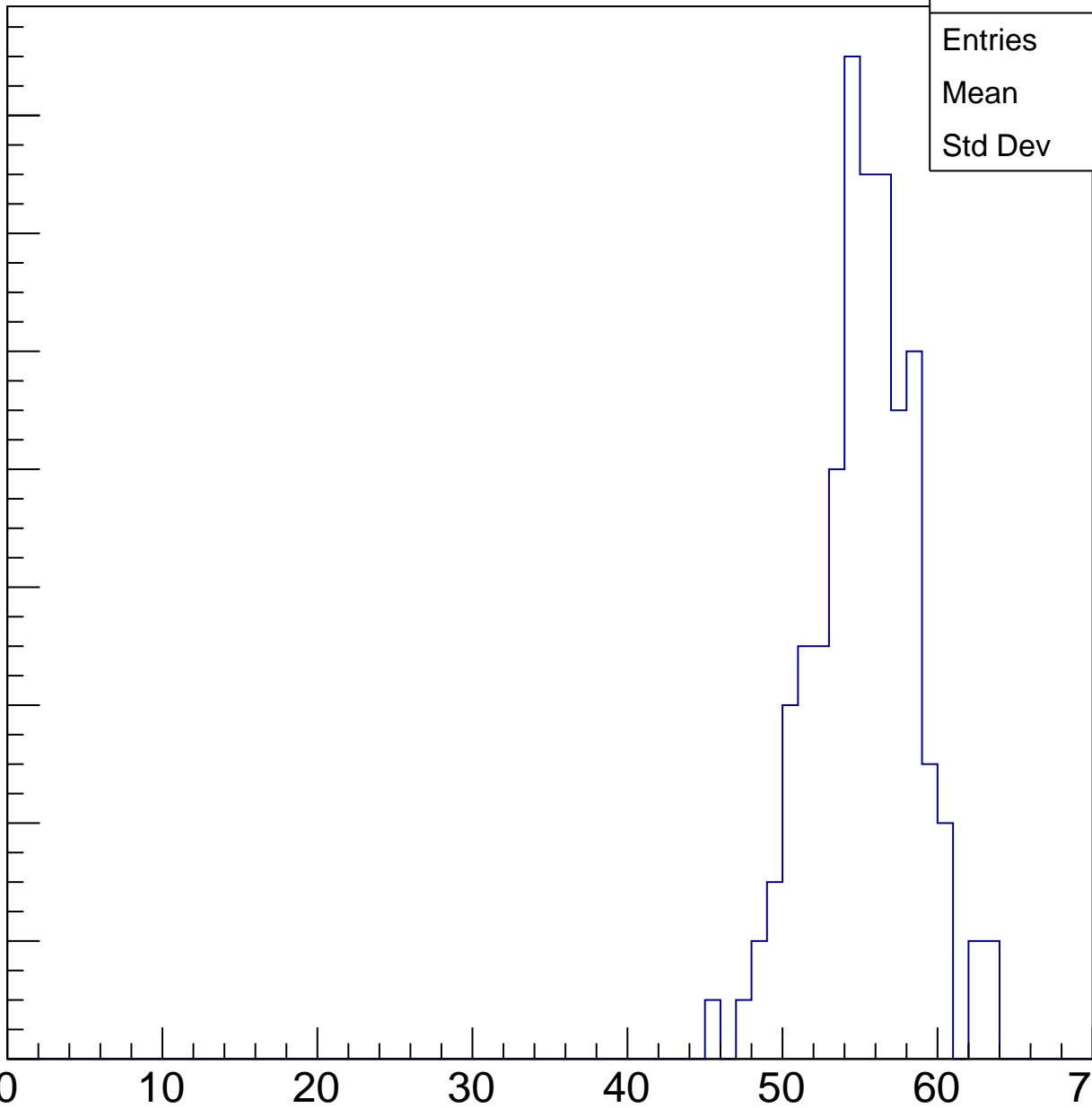
30

40

50

60

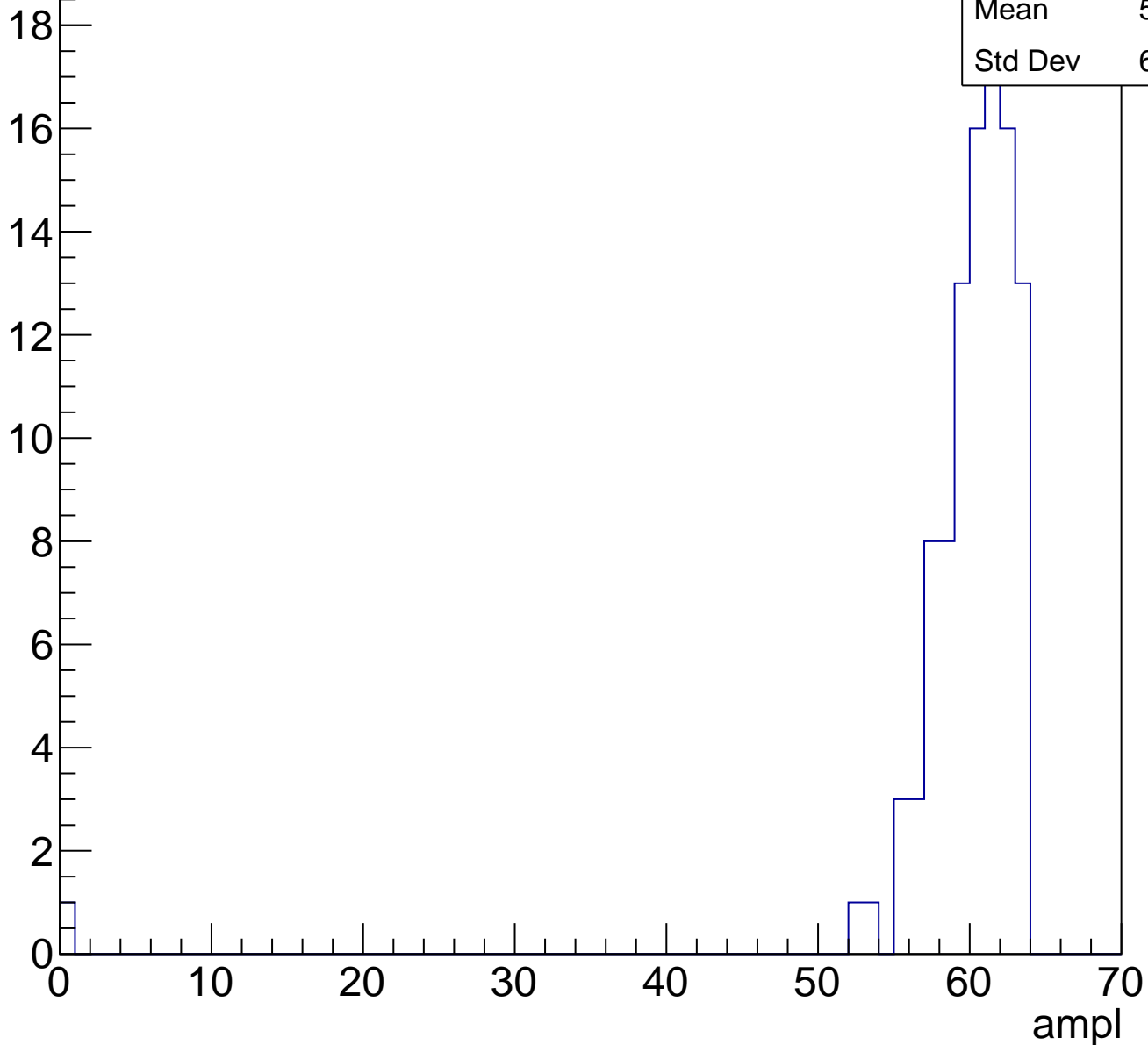
ampl



B1L001S, U25-ch98, adc5

calib_packv5_042523_0143.root, FC#2, port C2

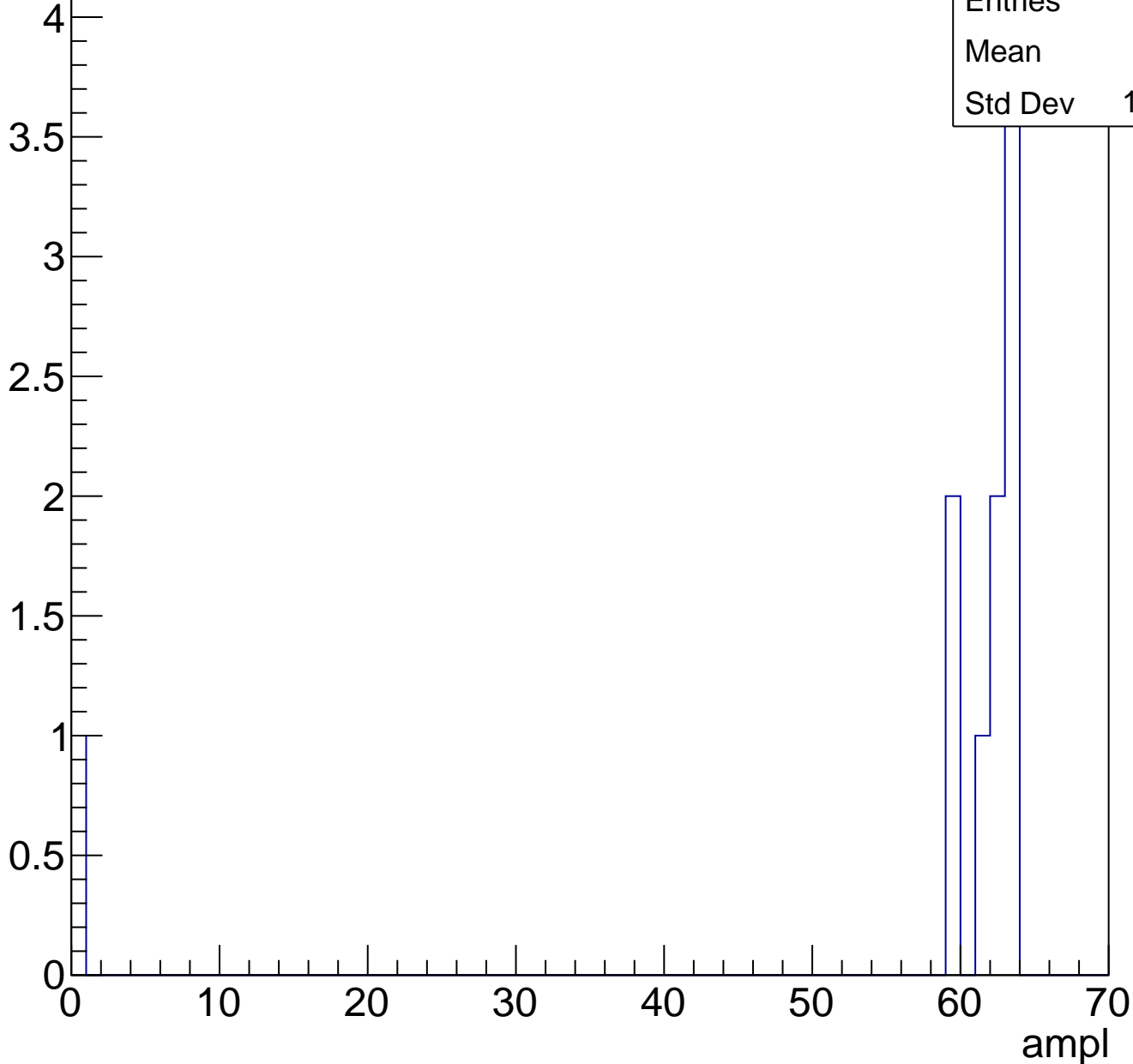
Entry



B1L001S, U25-ch98, adc6

calib_packv5_042523_0143.root, FC#2, port C2

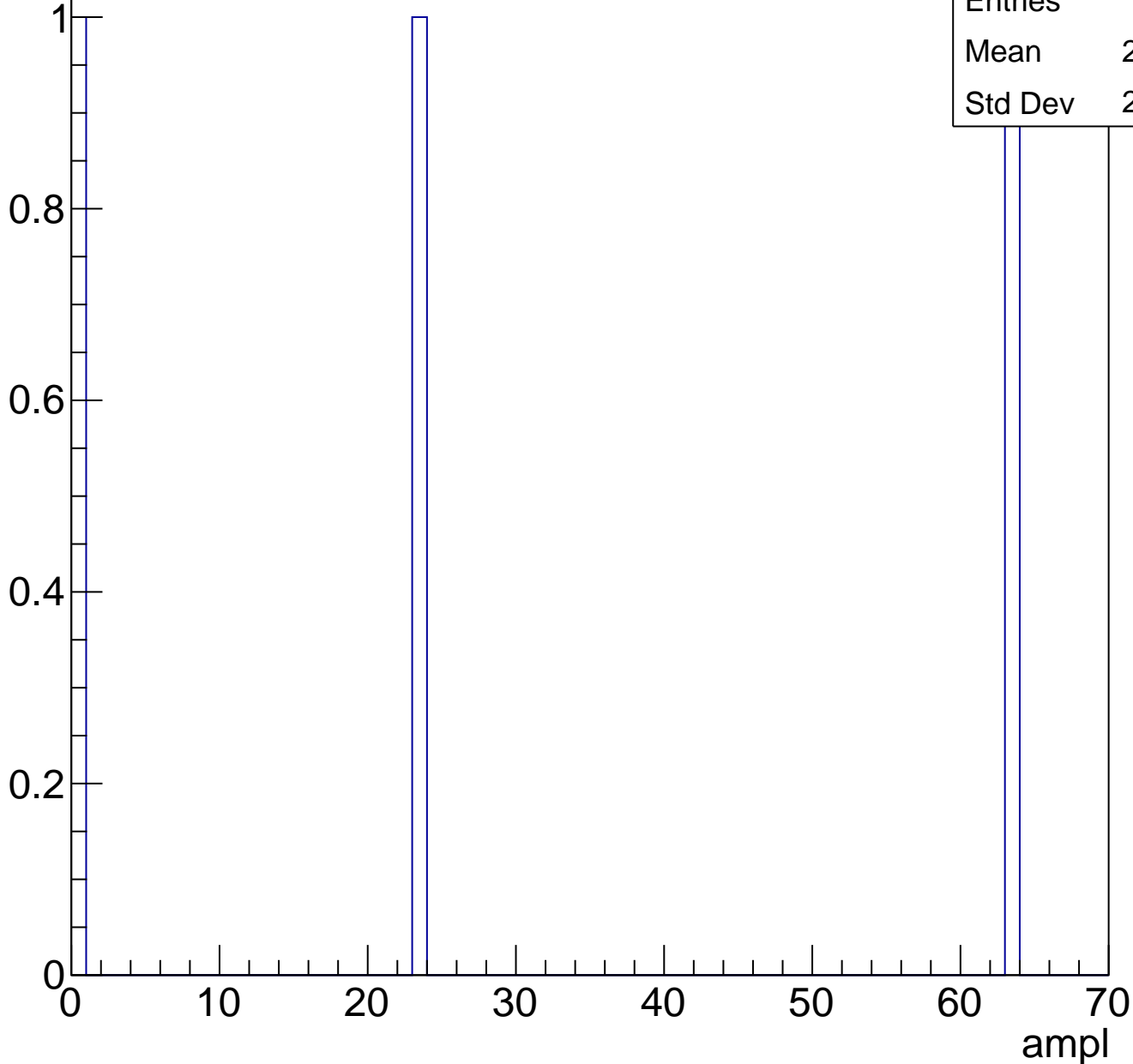
Entry



B1L001S, U25-ch98, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

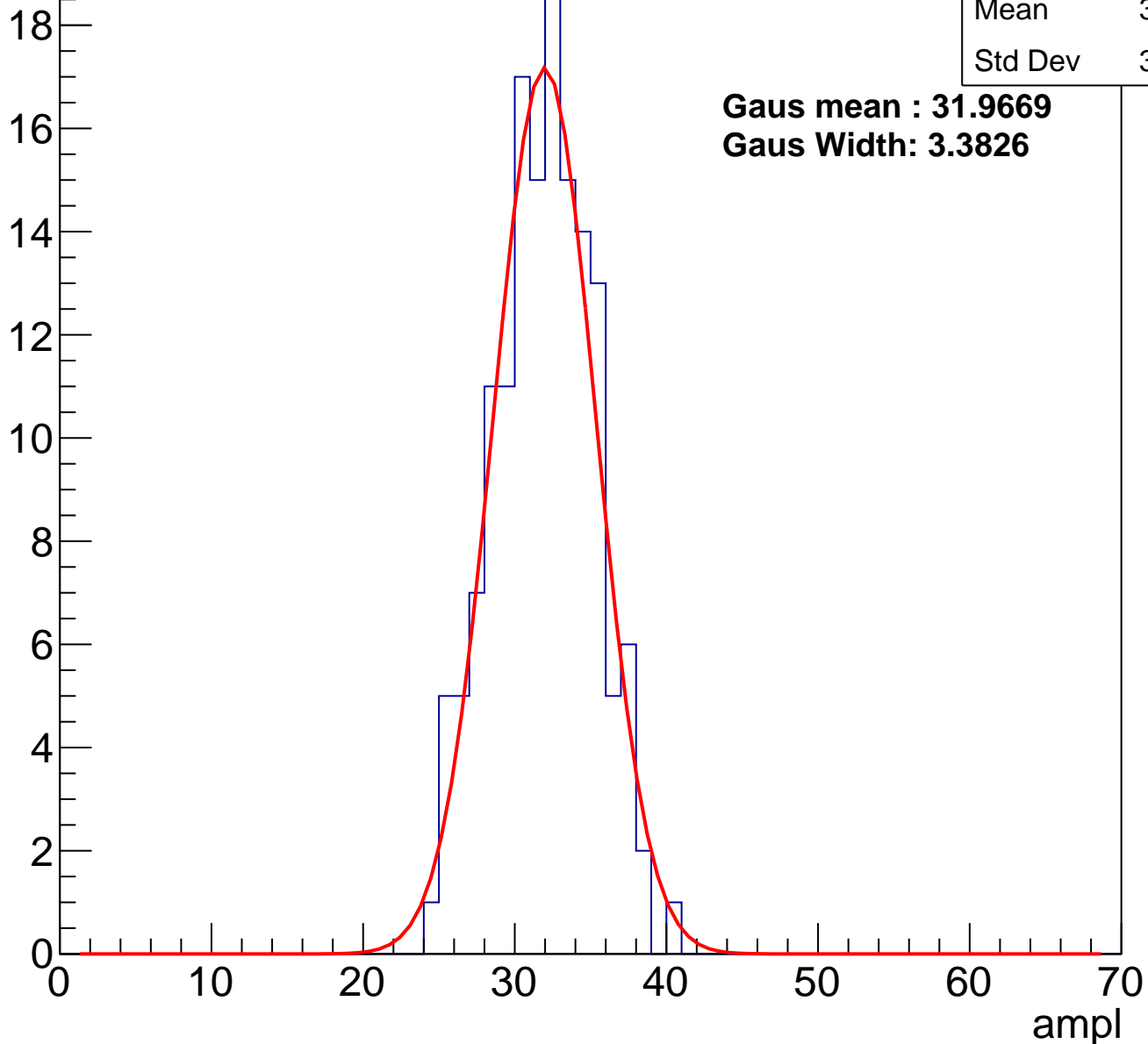


Entries	3
Mean	28.67
Std Dev	26.03

B1L001S, U25-ch99, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	147
Mean	31.44
Std Dev	3.226

Gaus mean : 31.9669

Gaus Width: 3.3826

B1L001S, U25-ch99, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	38.38
Std Dev	2.87

Gaus mean : 38.9624

Gaus Width: 3.0534

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

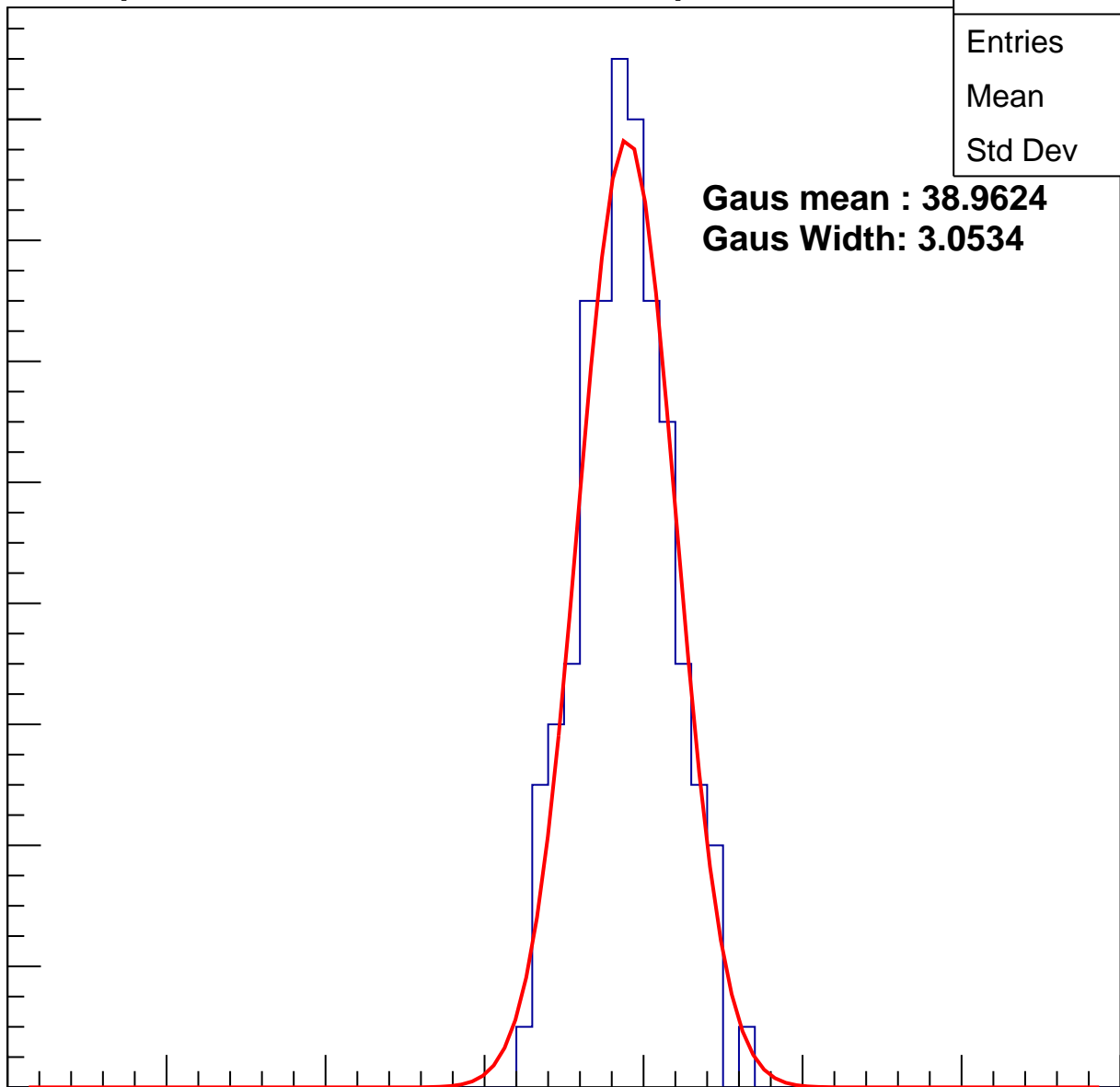
40

50

60

70

ampl



B1L001S, U25-ch99, adc2

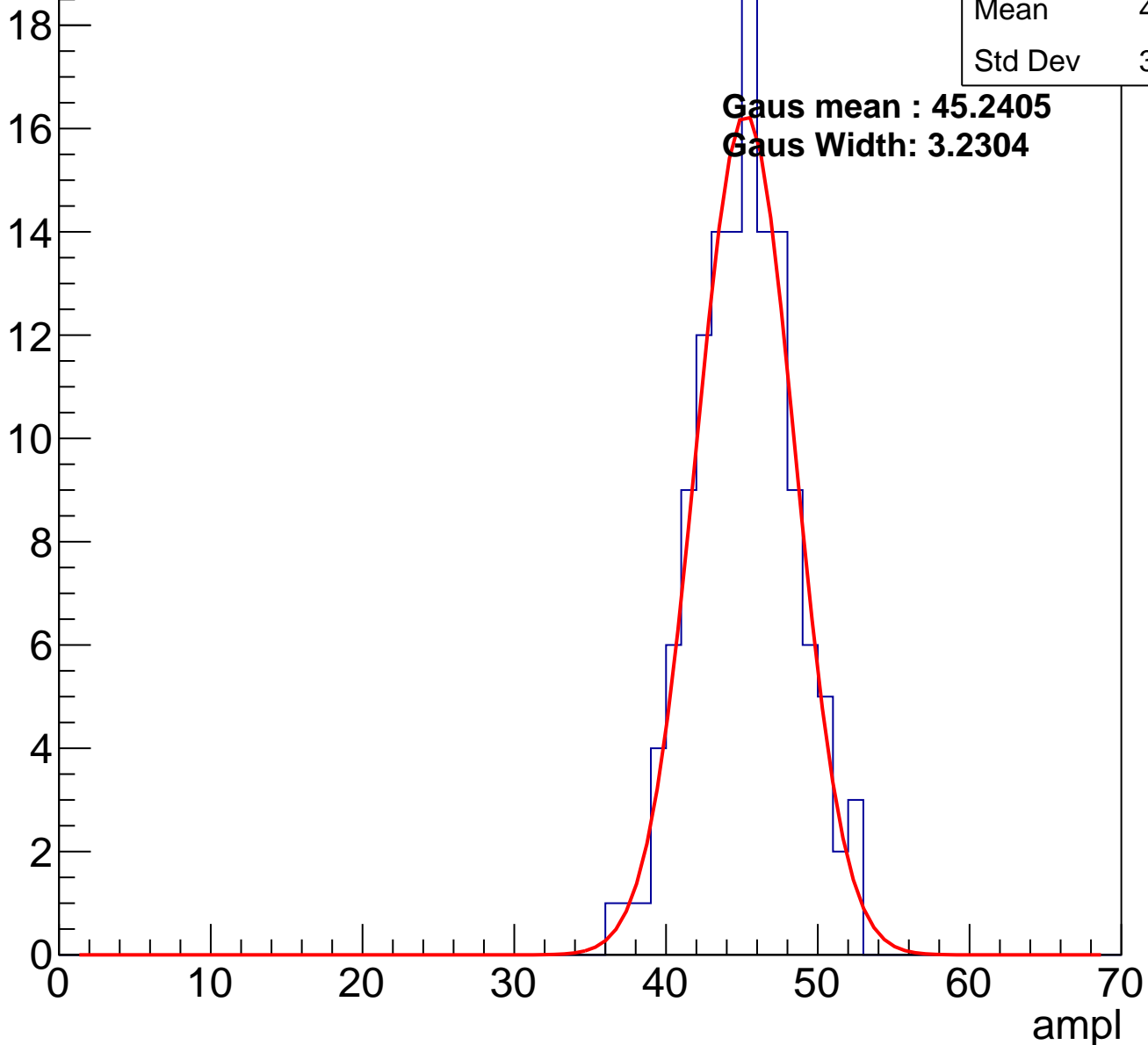
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	44.69
Std Dev	3.219

Gaus mean : 45.2405

Gaus Width: 3.2304

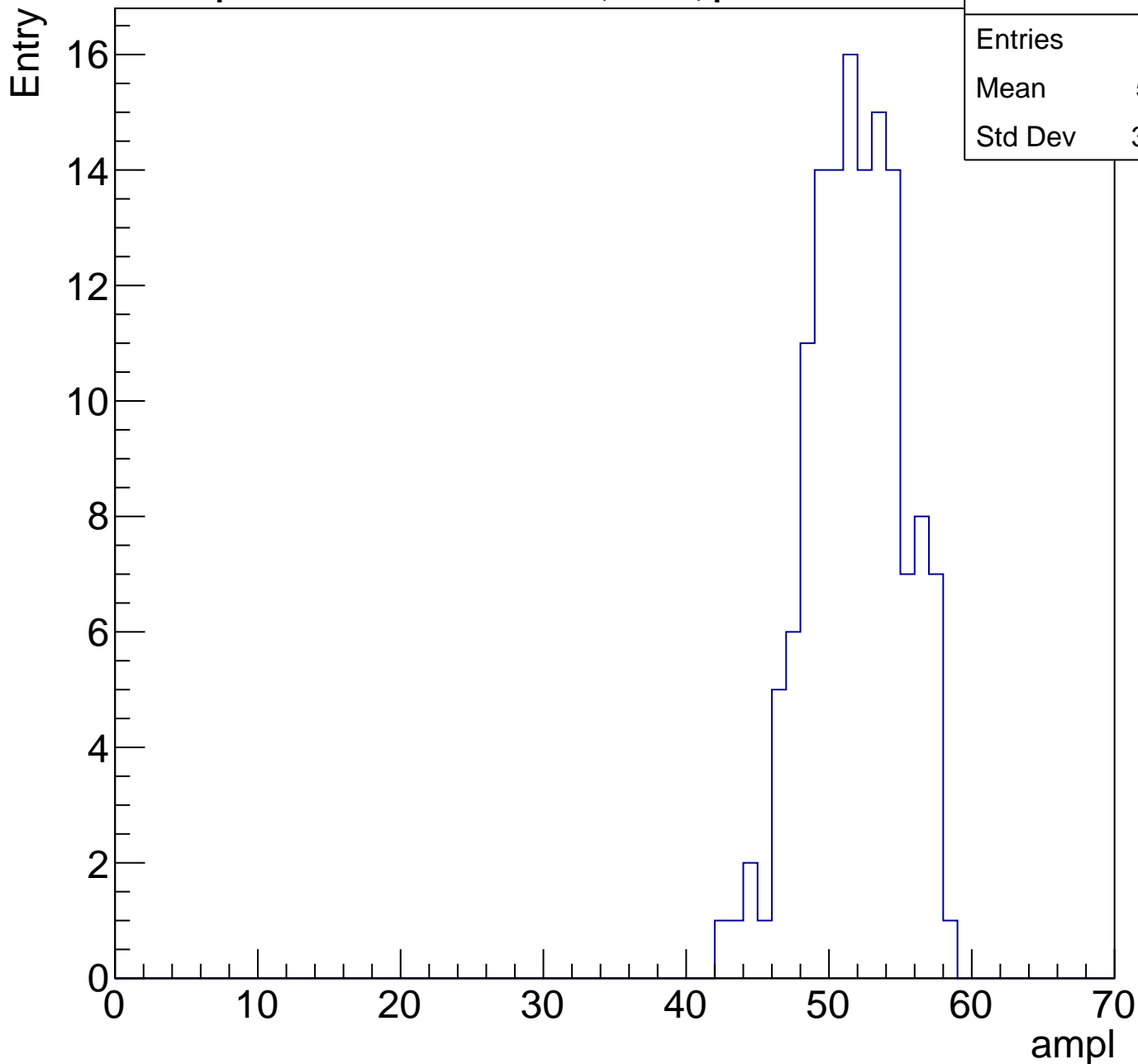
Entry



B1L001S, U25-ch99, adc3

calib_packv5_042523_0143.root, FC#2, port C2

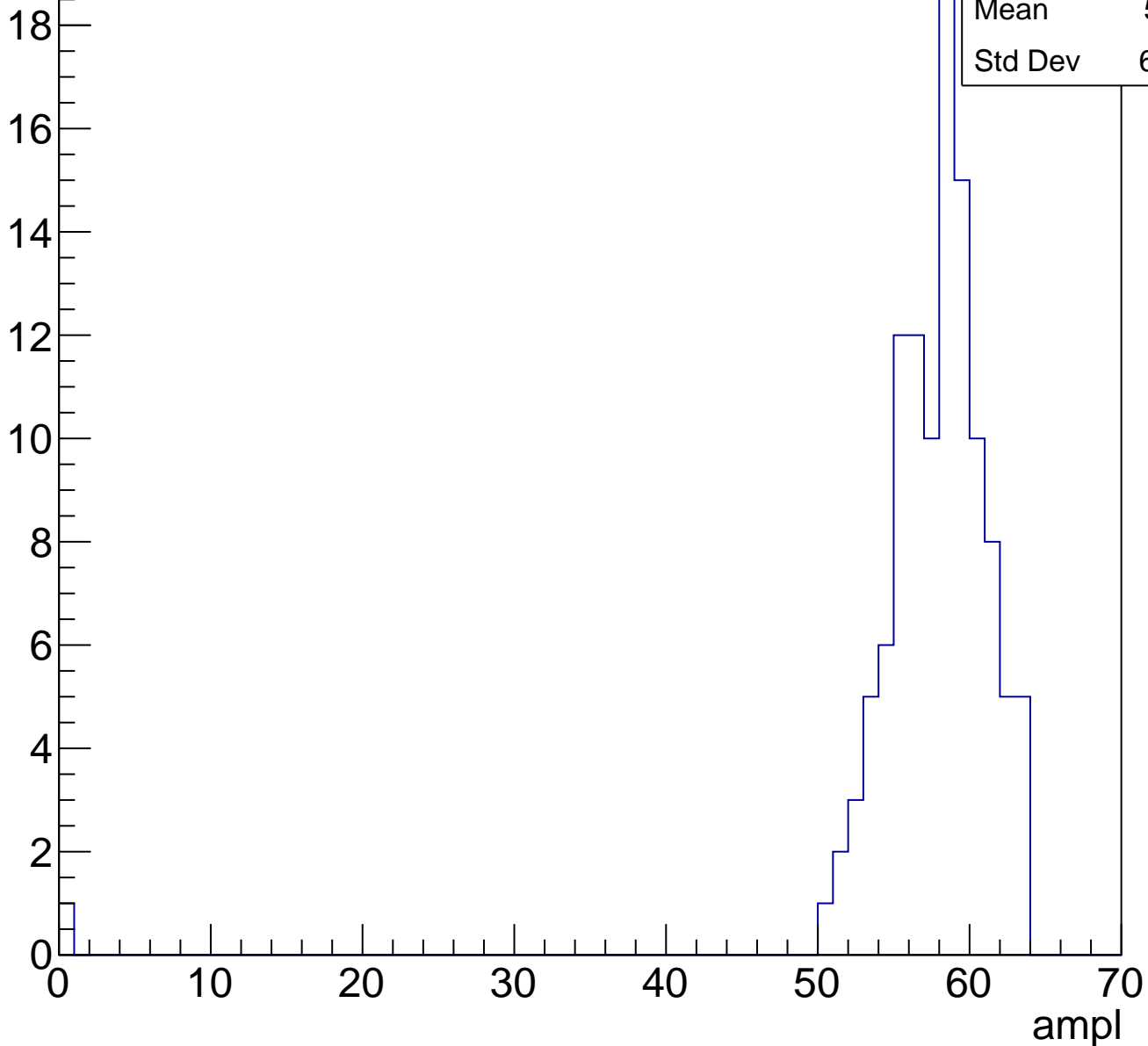
Entries	137
Mean	51.31
Std Dev	3.275



B1L001S, U25-ch99, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch99, adc5

calib_packv5_042523_0143.root, FC#2, port C2

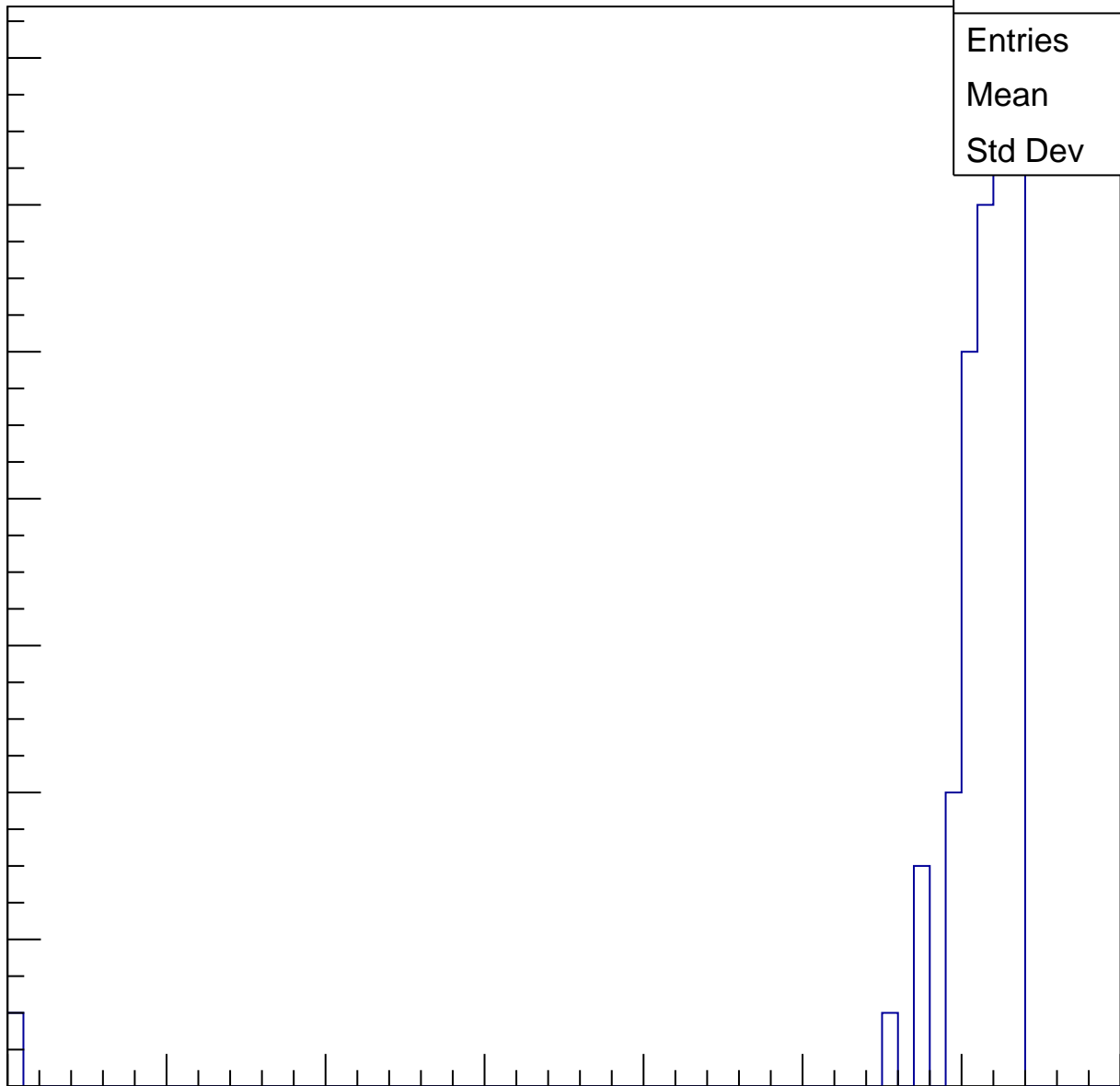
Entry

14
12
10
8
6
4
2
0

Entries	58
Mean	60.02
Std Dev	8.138

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch99, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch99, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch100, adc0

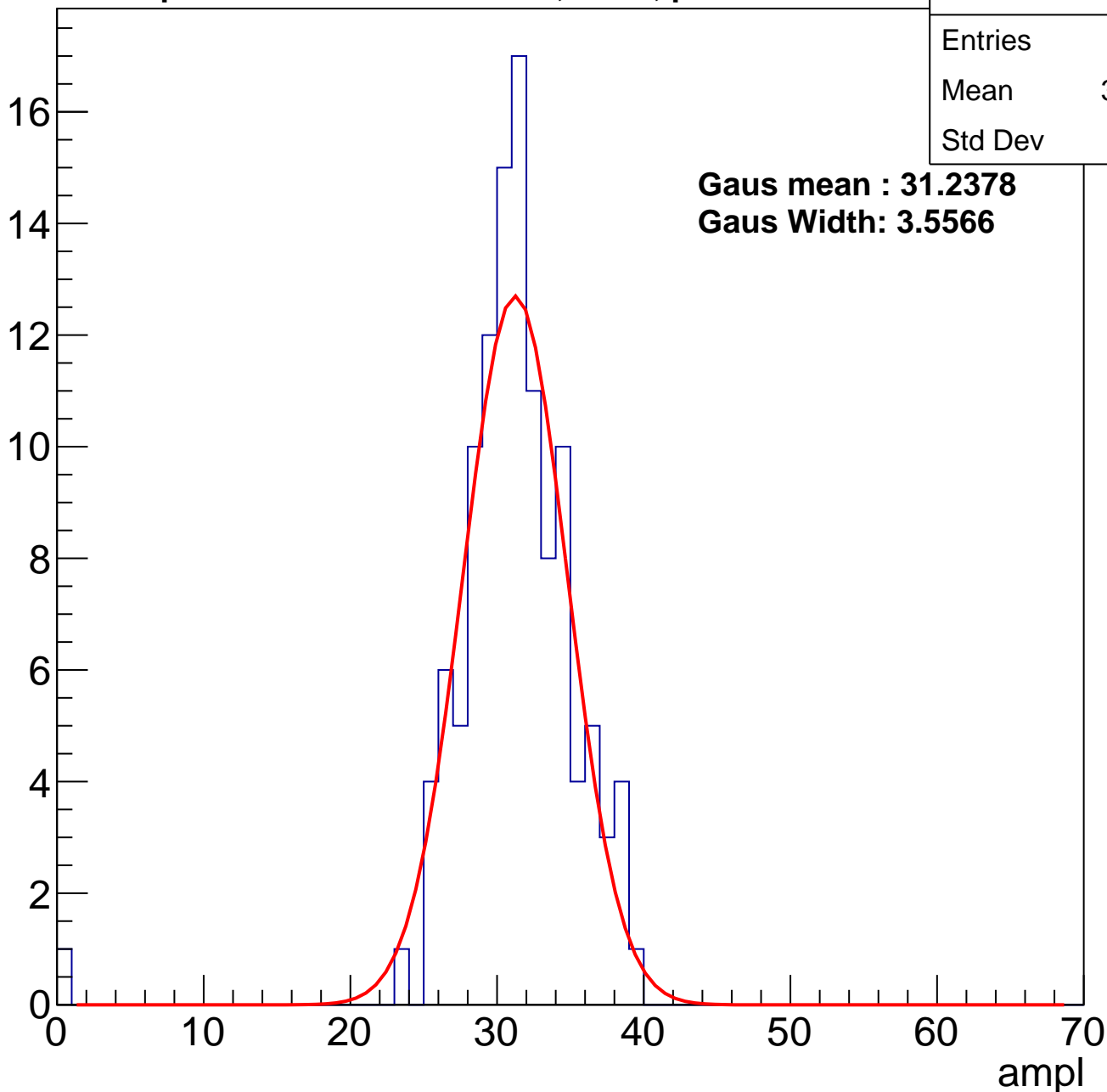
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	30.74
Std Dev	4.37

Gaus mean : 31.2378

Gaus Width: 3.5566

Entry



B1L001S, U25-ch100, adc1

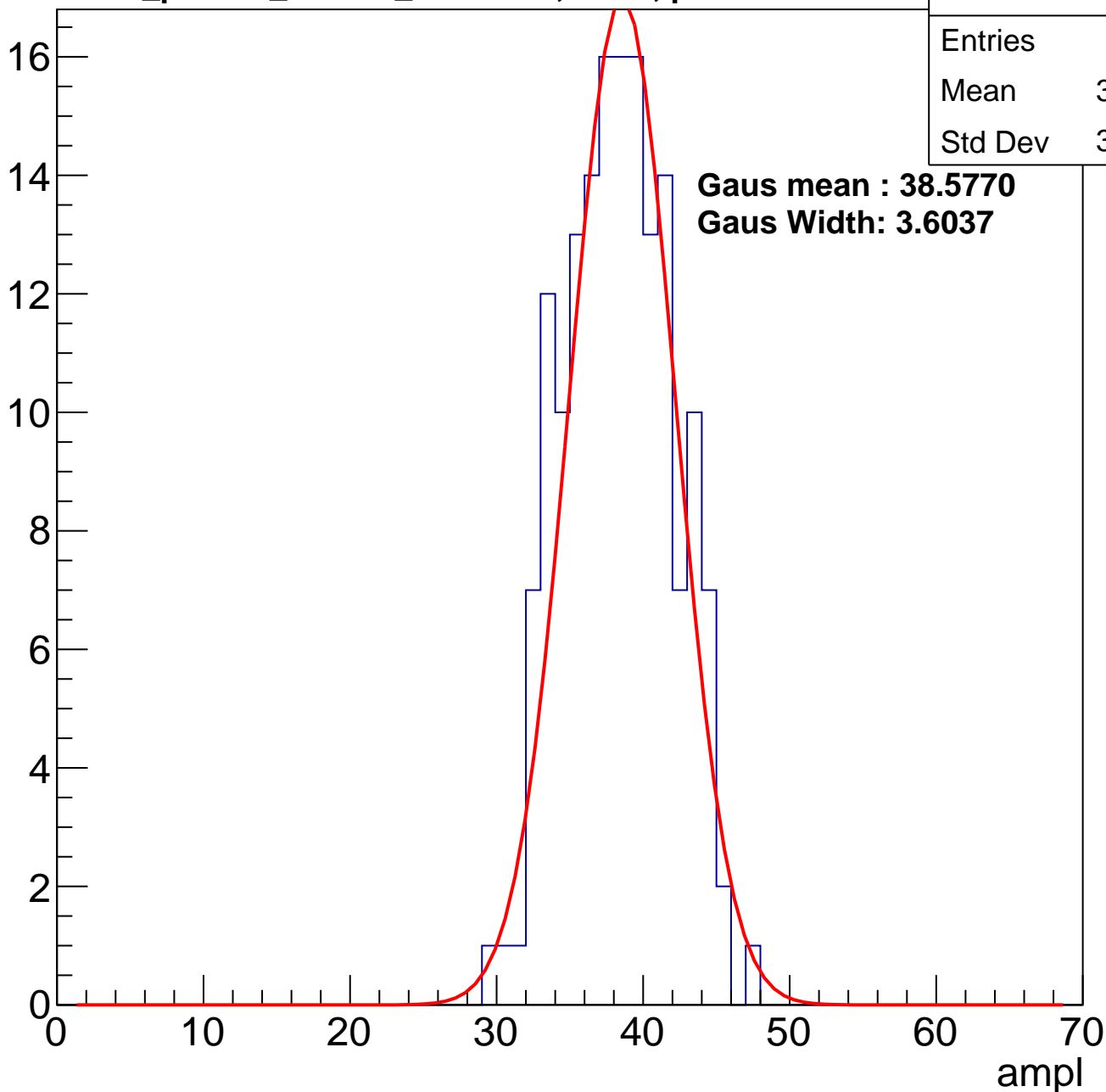
calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	37.86
Std Dev	3.593

Gaus mean : 38.5770

Gaus Width: 3.6037

Entry



B1L001S, U25-ch100, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries 111

Mean 44.36

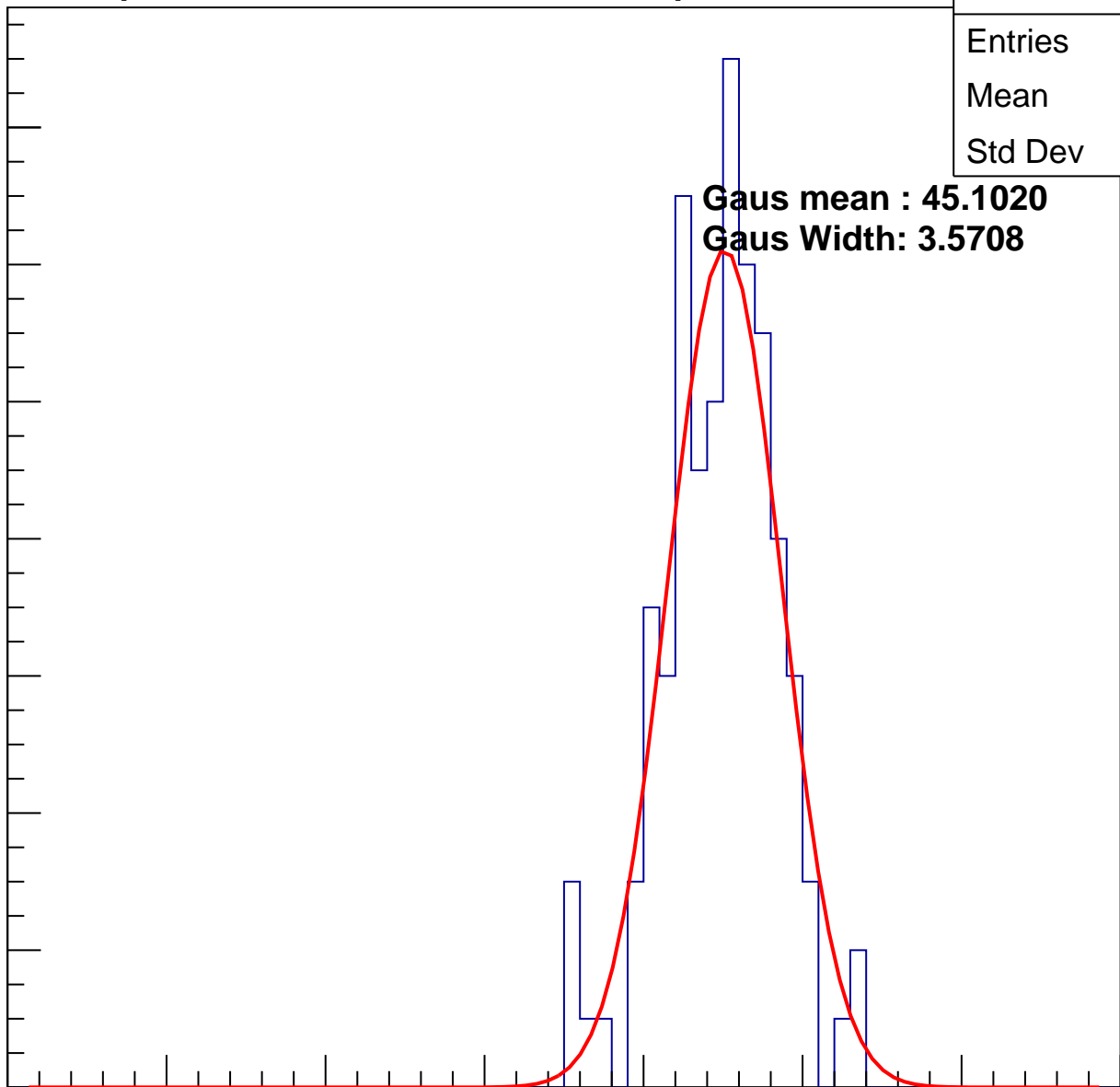
Std Dev 3.574

Gaus mean : 45.1020

Gaus Width: 3.5708

0 10 20 30 40 50 60 70

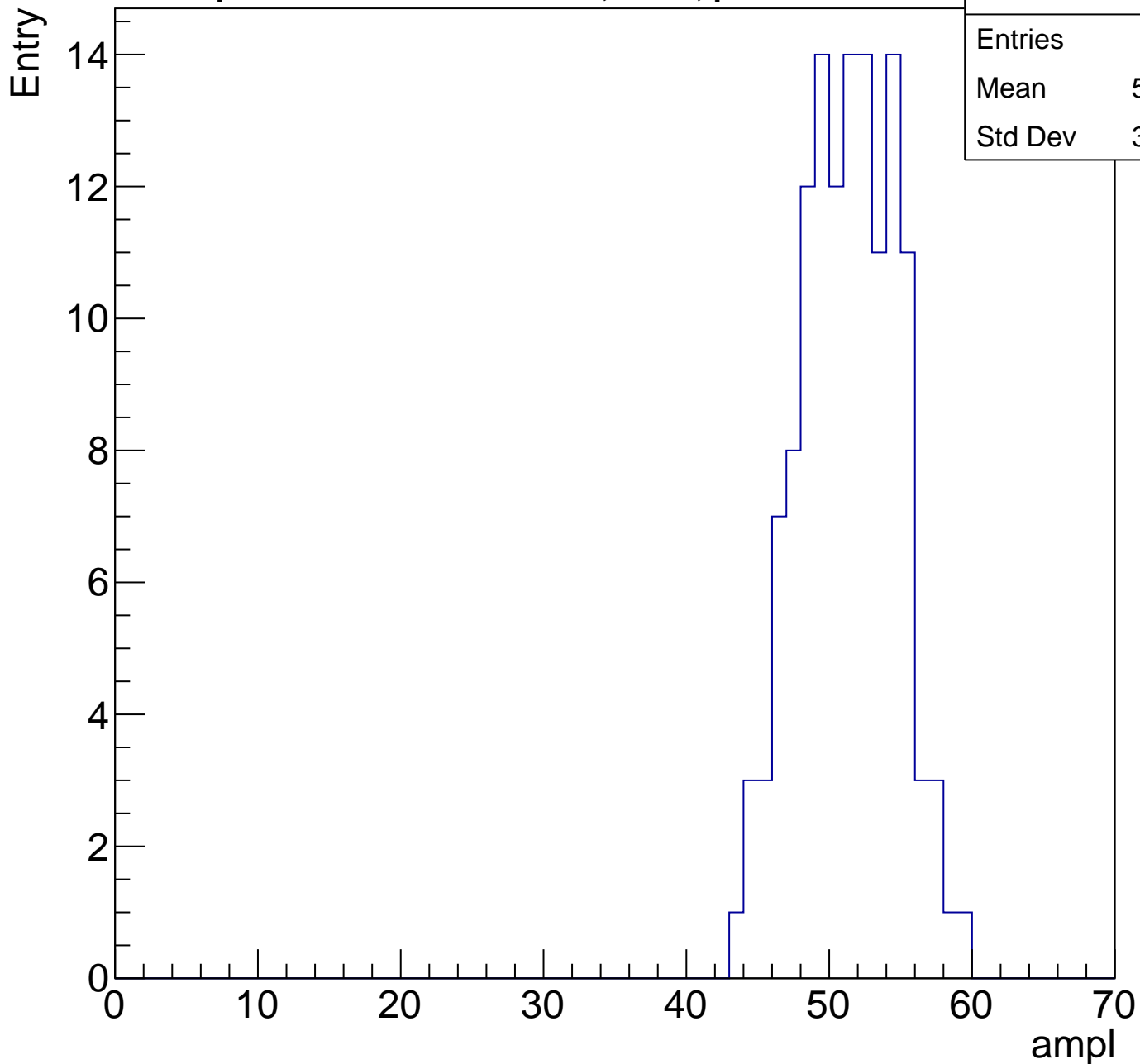
ampl



B1L001S, U25-ch100, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	50.85
Std Dev	3.315



B1L001S, U25-ch100, adc4

calib_packv5_042523_0143.root, FC#2, port C2

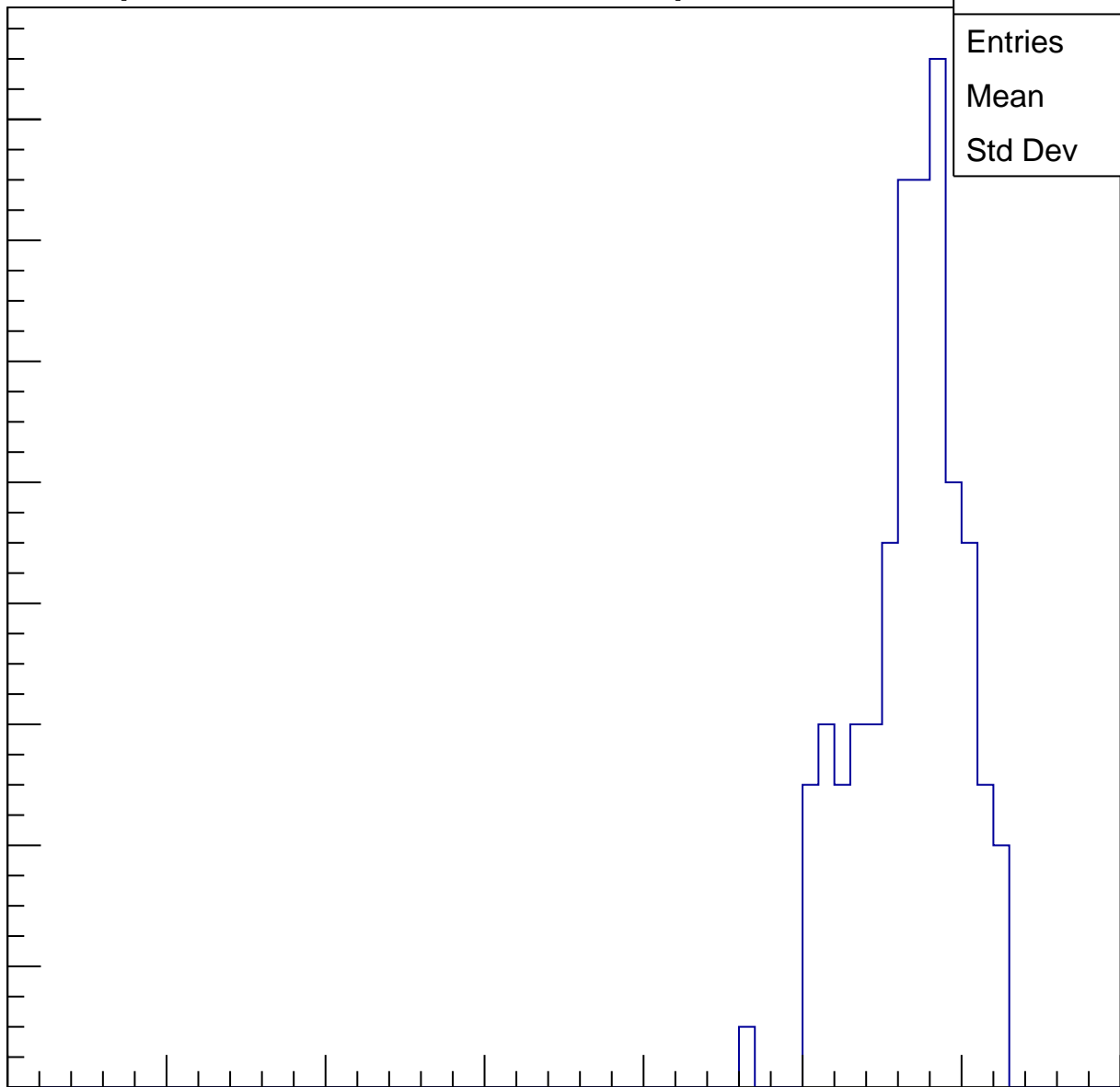
Entries	113
Mean	56.31
Std Dev	3.221

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch100, adc5

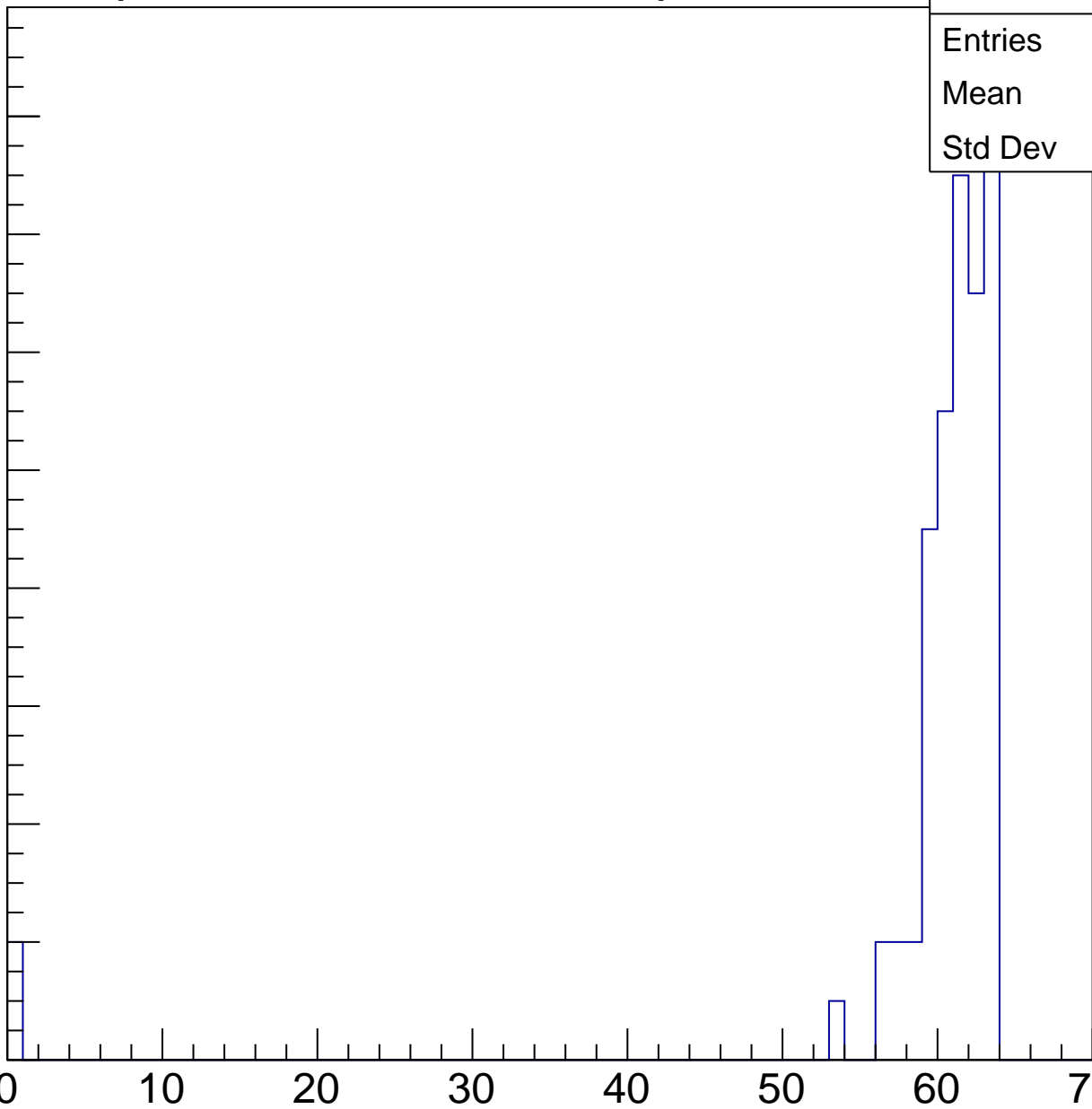
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.16
Std Dev	10.06

ampl



B1L001S, U25-ch100, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch100, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch101, adc0

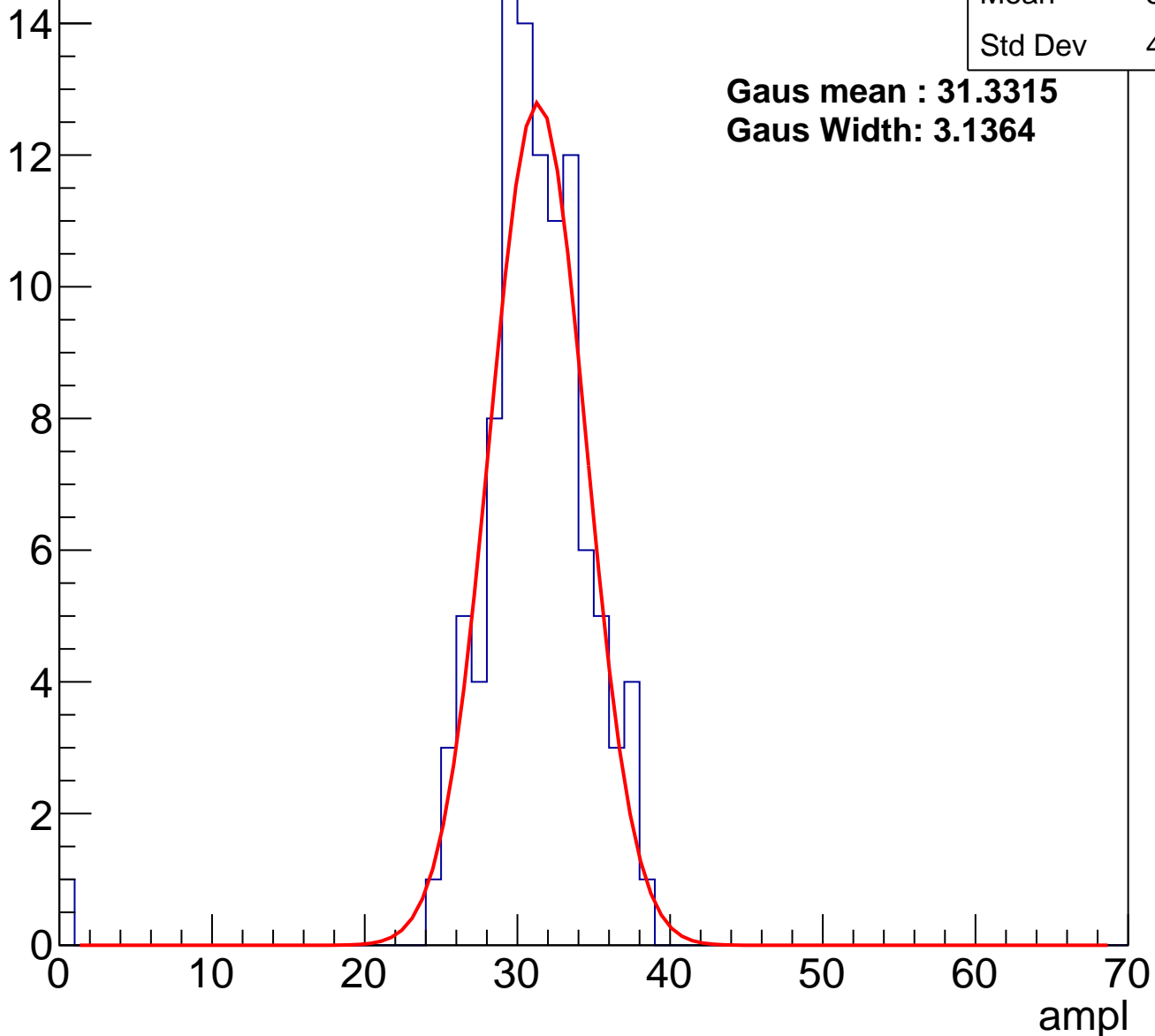
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	30.56
Std Dev	4.247

Gaus mean : 31.3315

Gaus Width: 3.1364

Entry



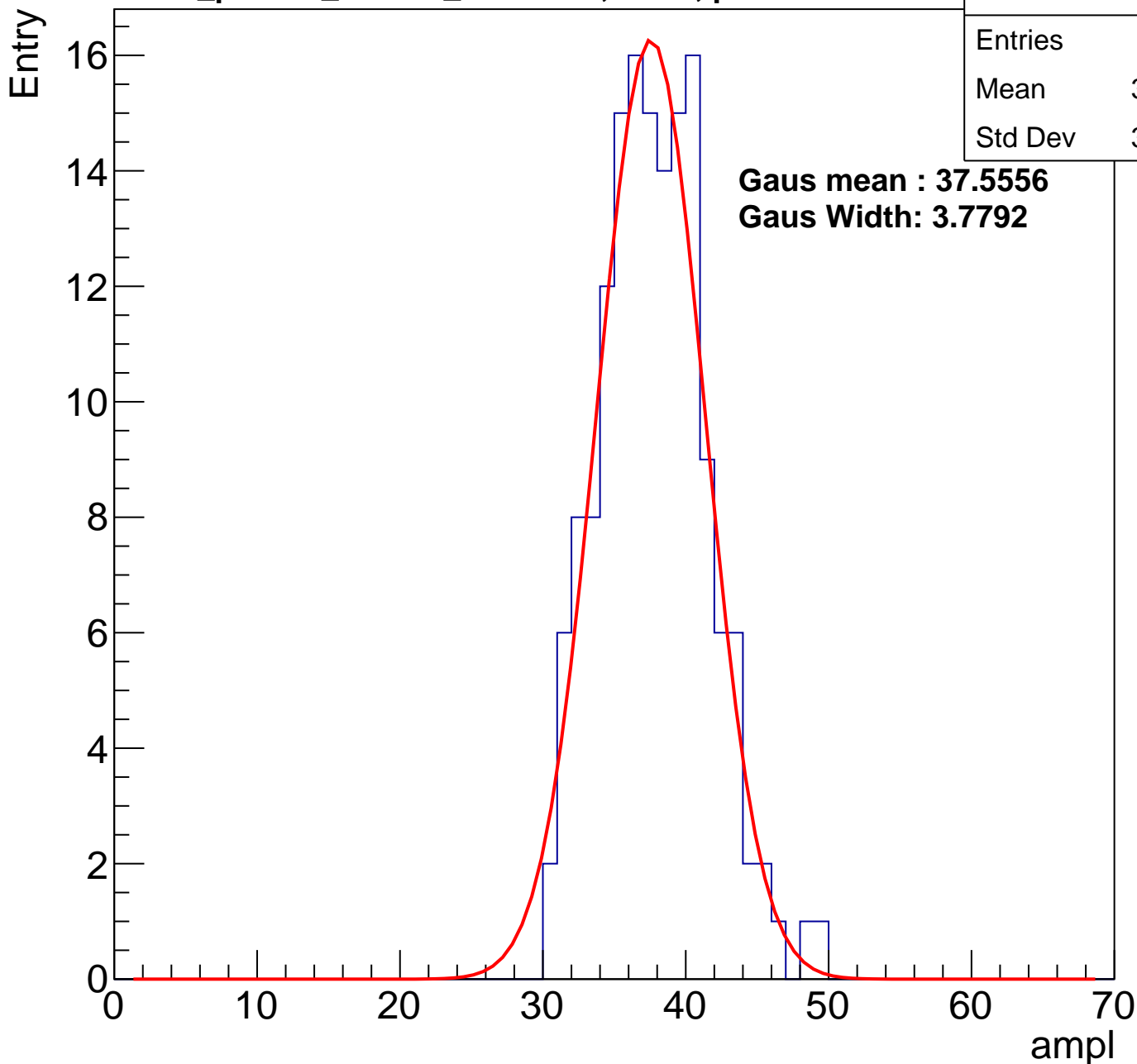
B1L001S, U25-ch101, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	37.34
Std Dev	3.688

Gaus mean : 37.5556

Gaus Width: 3.7792



B1L001S, U25-ch101, adc2

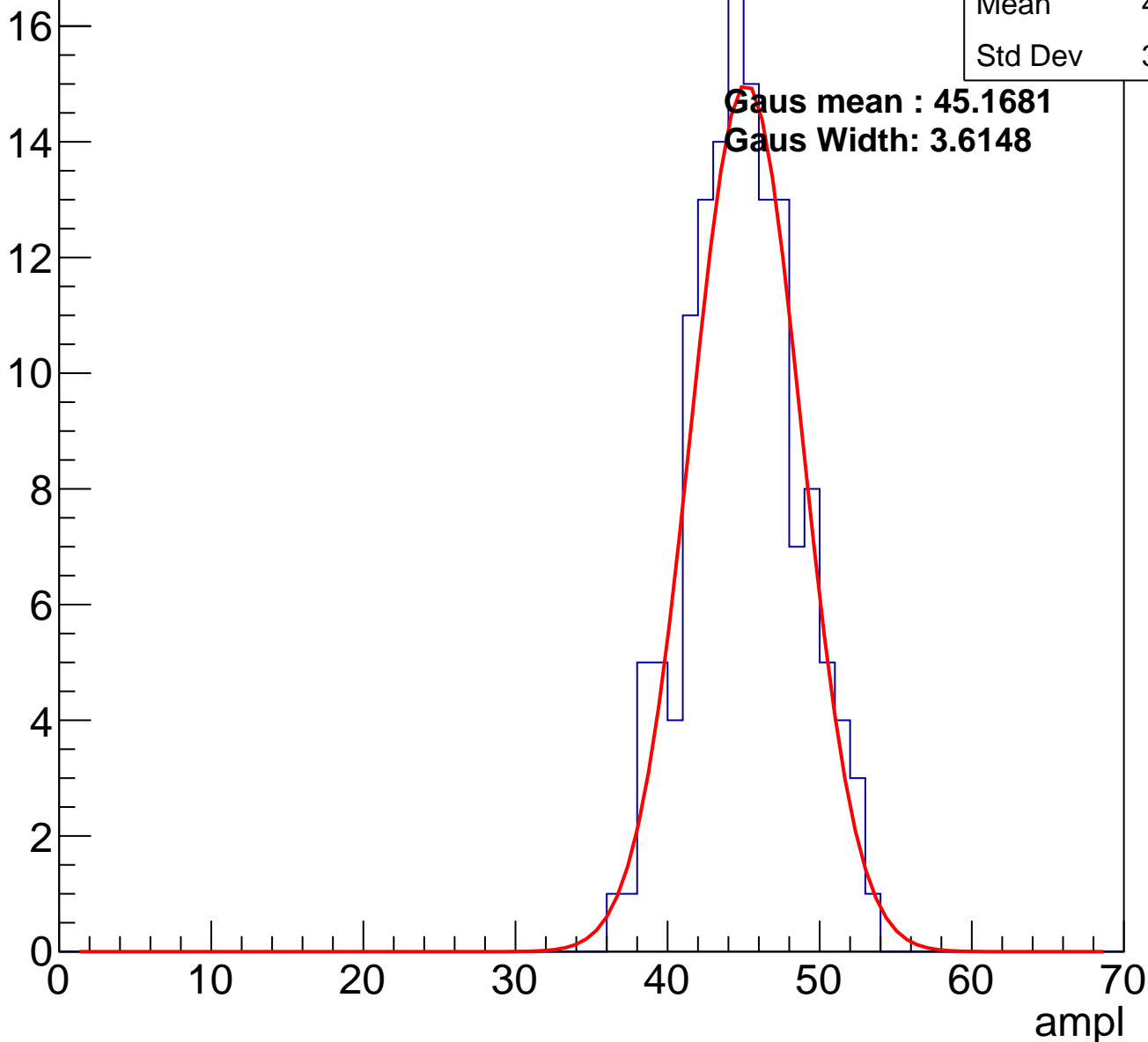
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	140
Mean	44.57
Std Dev	3.532

Gaus mean : 45.1681

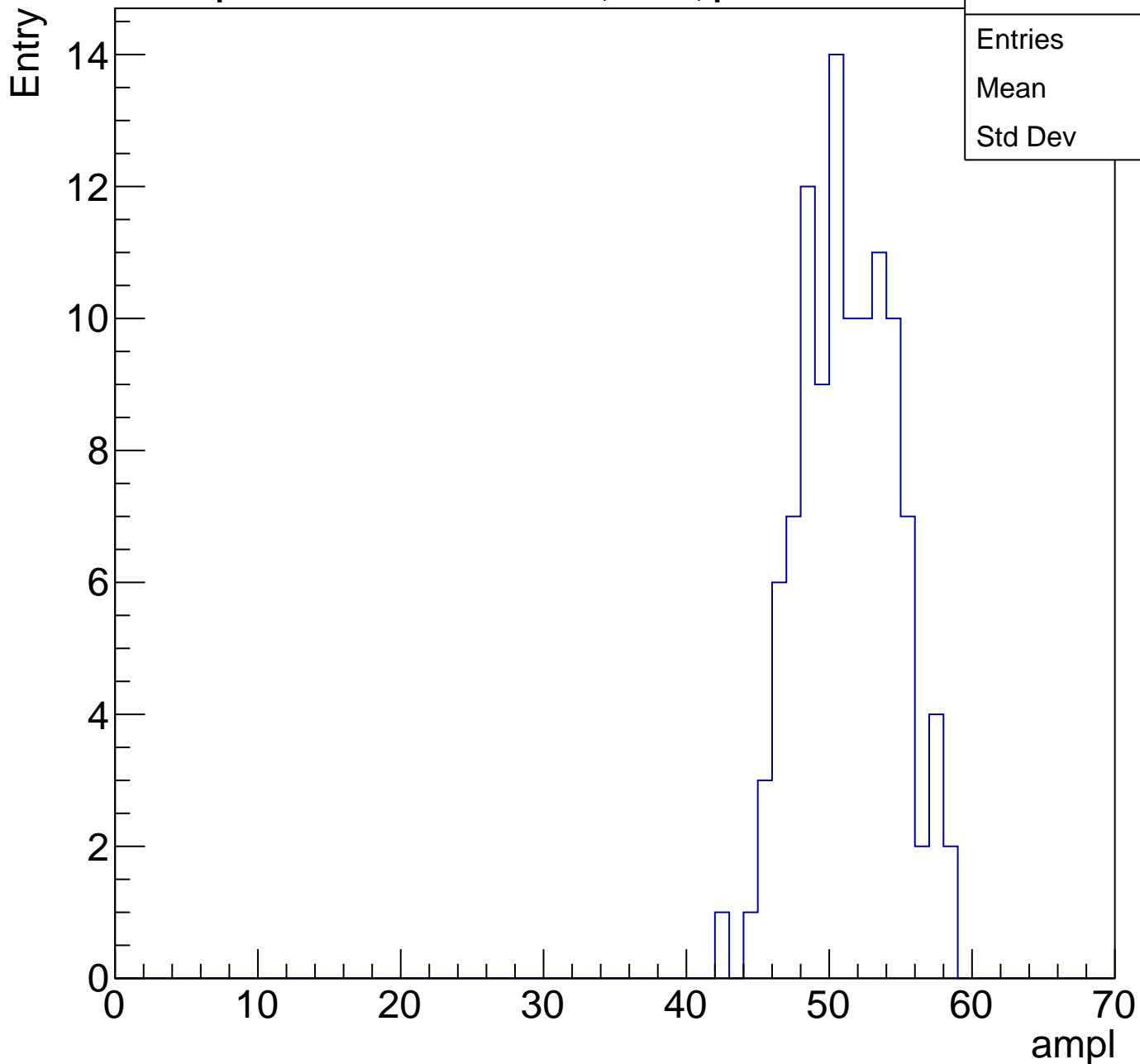
Gaus Width: 3.6148



B1L001S, U25-ch101, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	50.8
Std Dev	3.32



B1L001S, U25-ch101, adc4

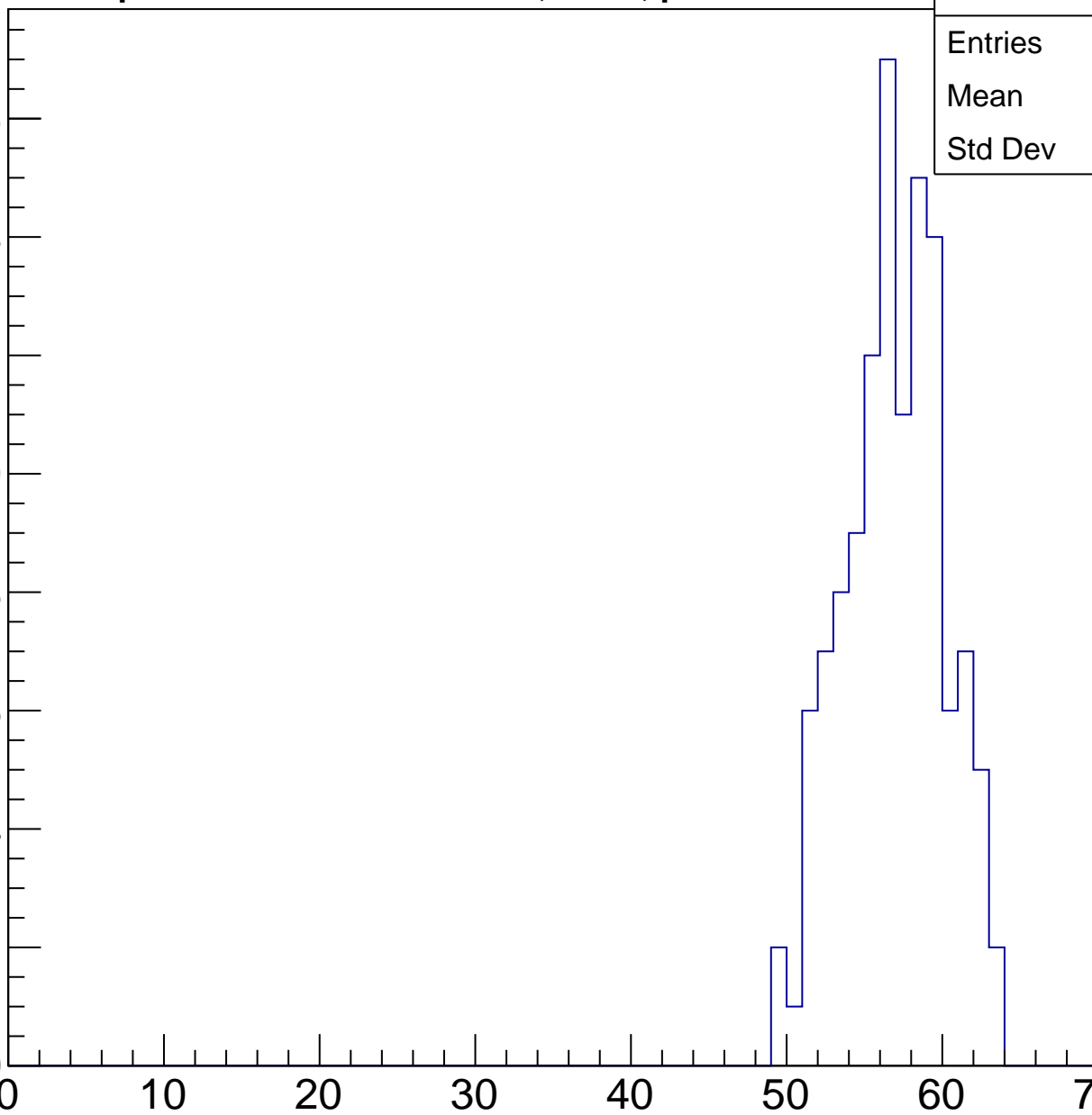
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	56.44
Std Dev	3.186

ampl



B1L001S, U25-ch101, adc5

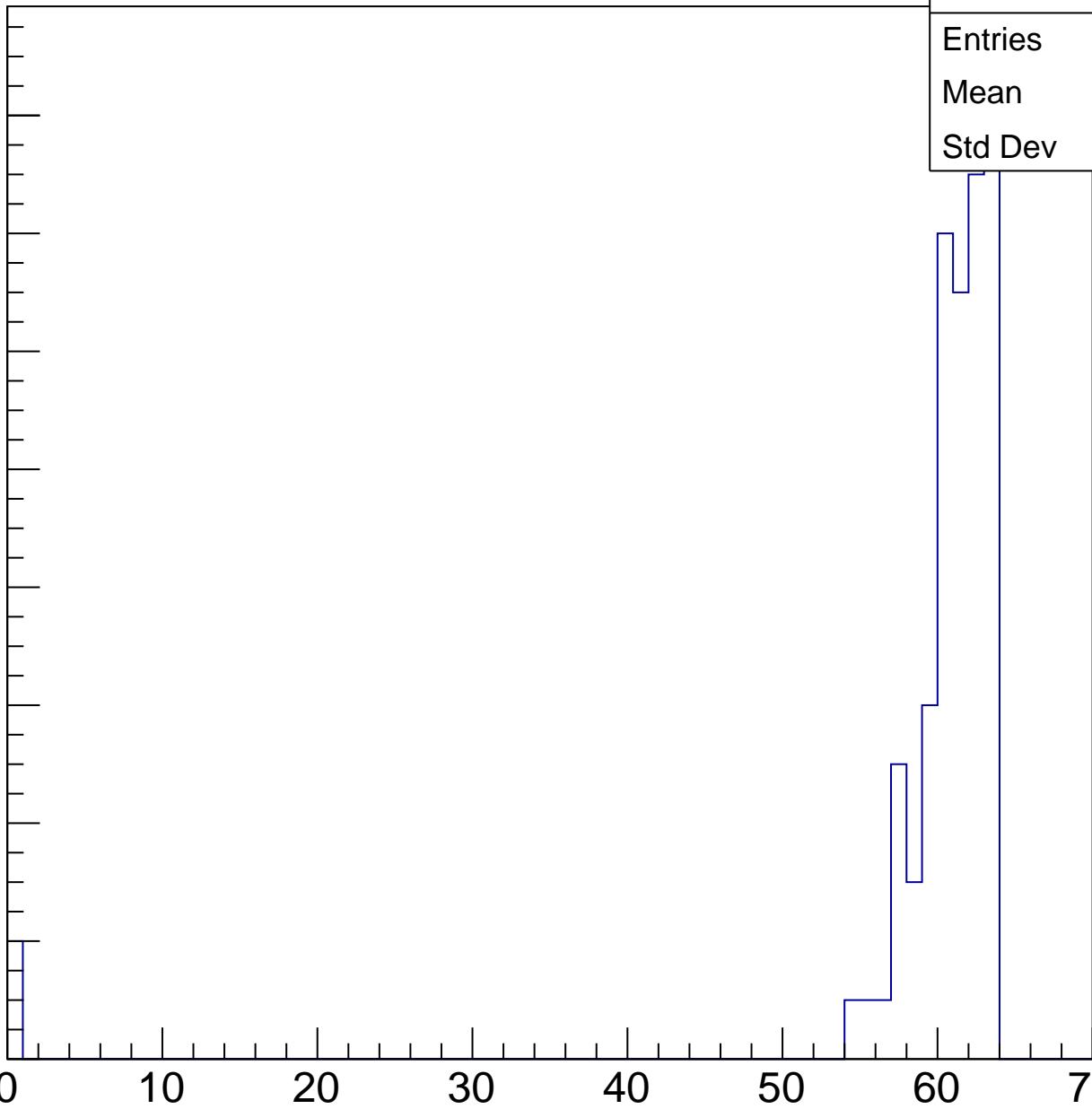
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	59.13
Std Dev	9.81

ampl



B1L001S, U25-ch101, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch101, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch102, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	30.3
Std Dev	3.167

Gaus mean : 30.9663

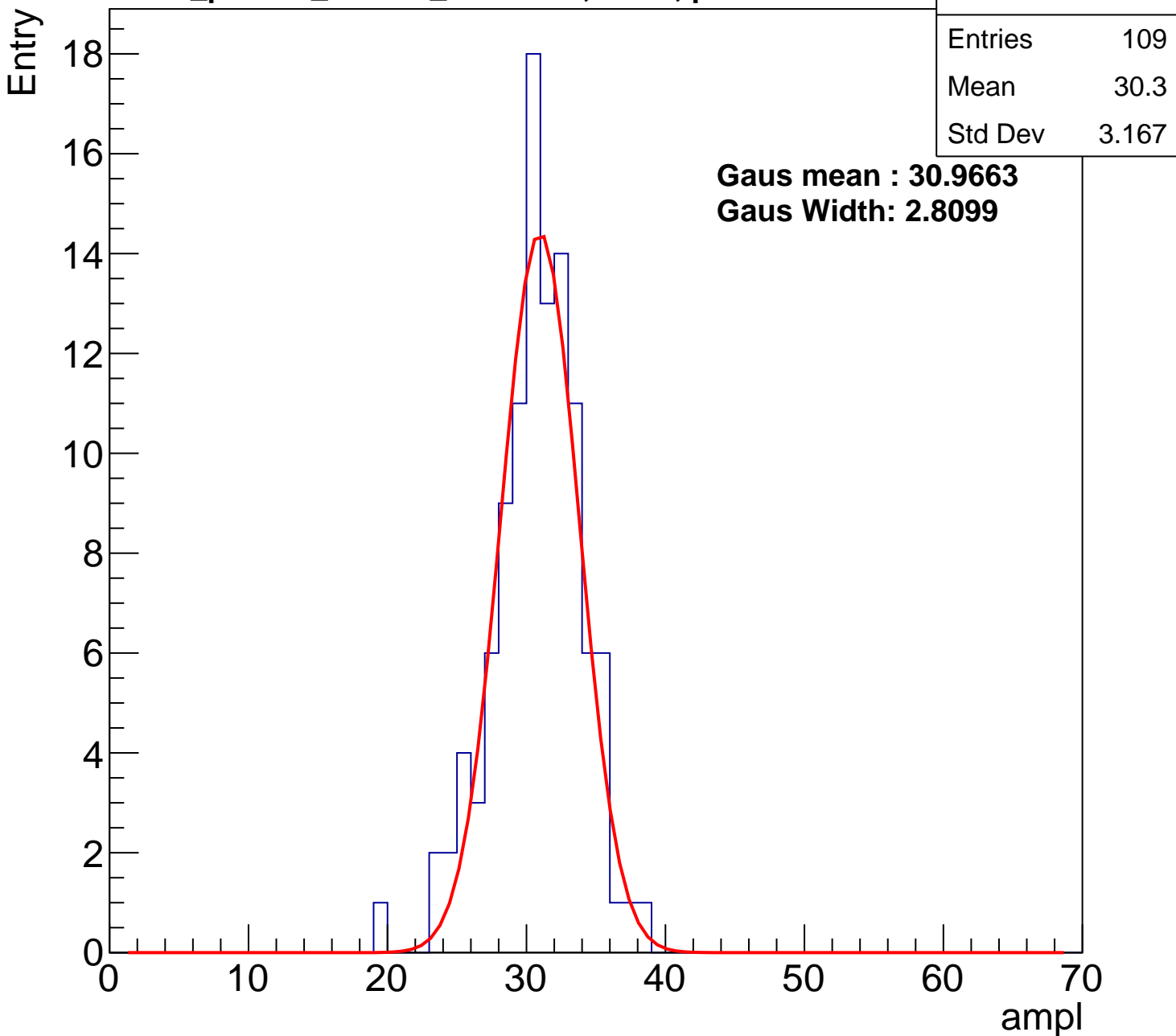
Gaus Width: 2.8099

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch102, adc1

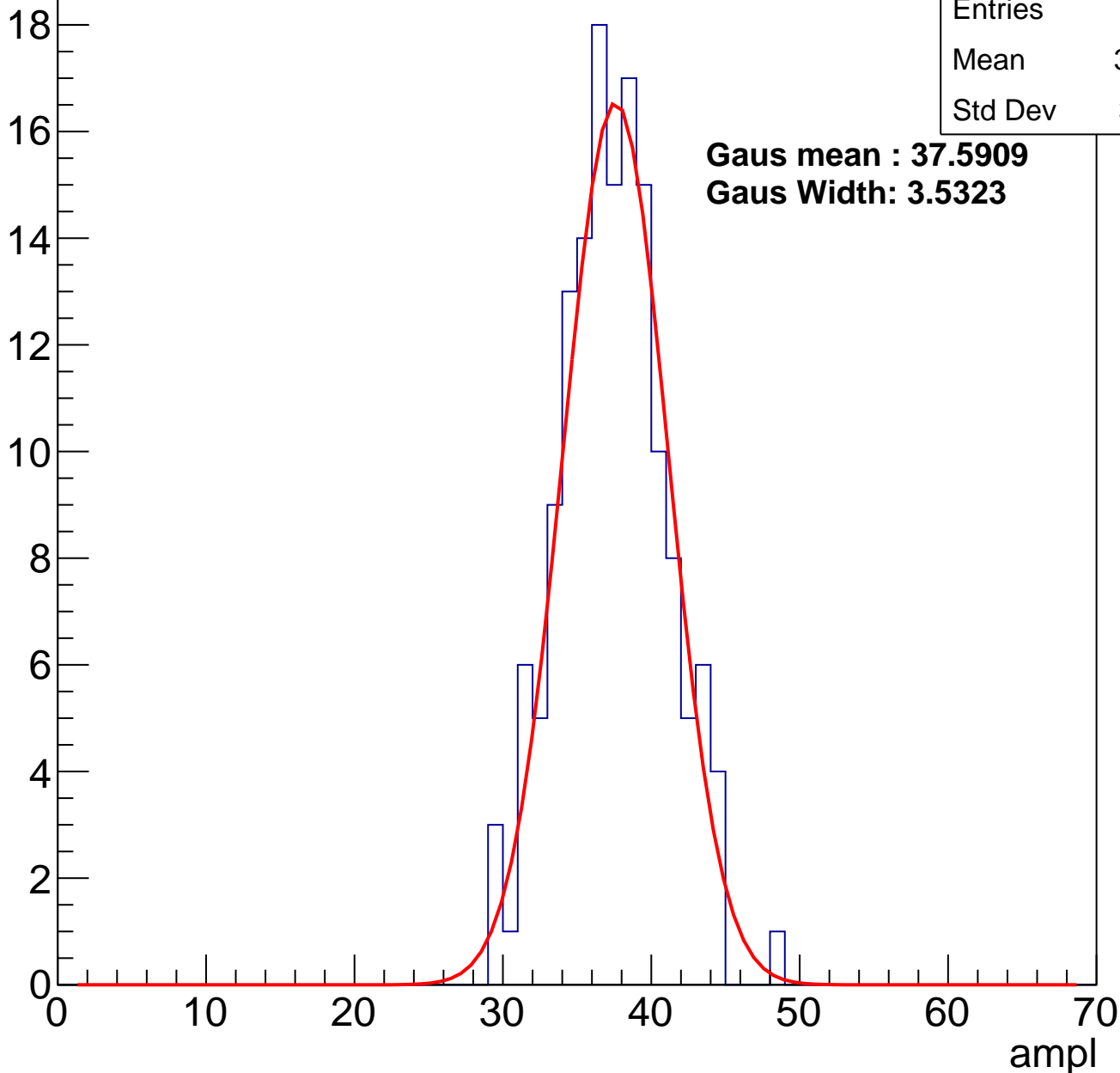
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	36.97
Std Dev	3.531

Gaus mean : 37.5909

Gaus Width: 3.5323

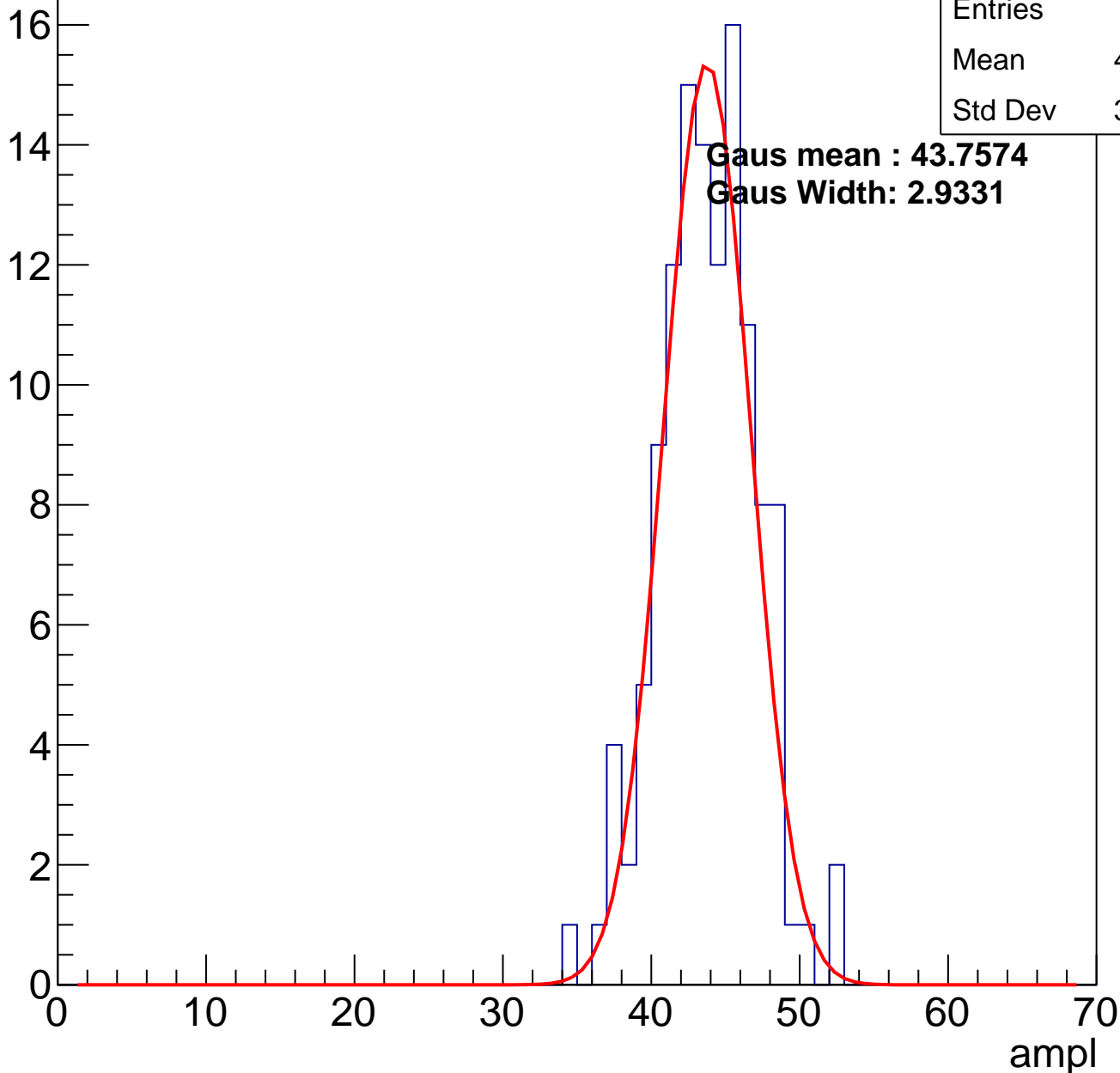
Entry



B1L001S, U25-ch102, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch102, adc3

calib_packv5_042523_0143.root, FC#2, port C2

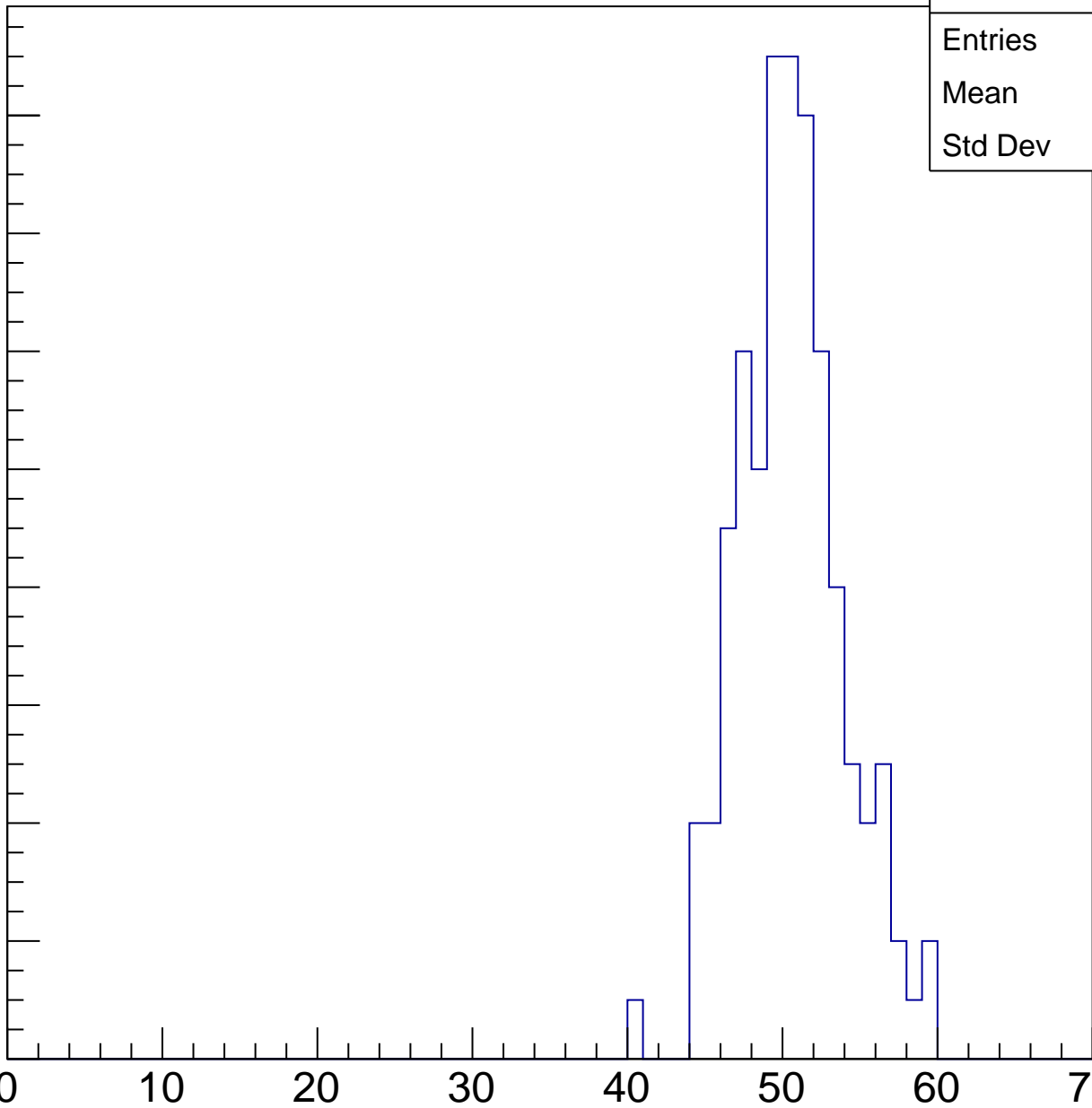
Entries	129
Mean	50.09
Std Dev	3.378

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch102, adc4

calib_packv5_042523_0143.root, FC#2, port C2

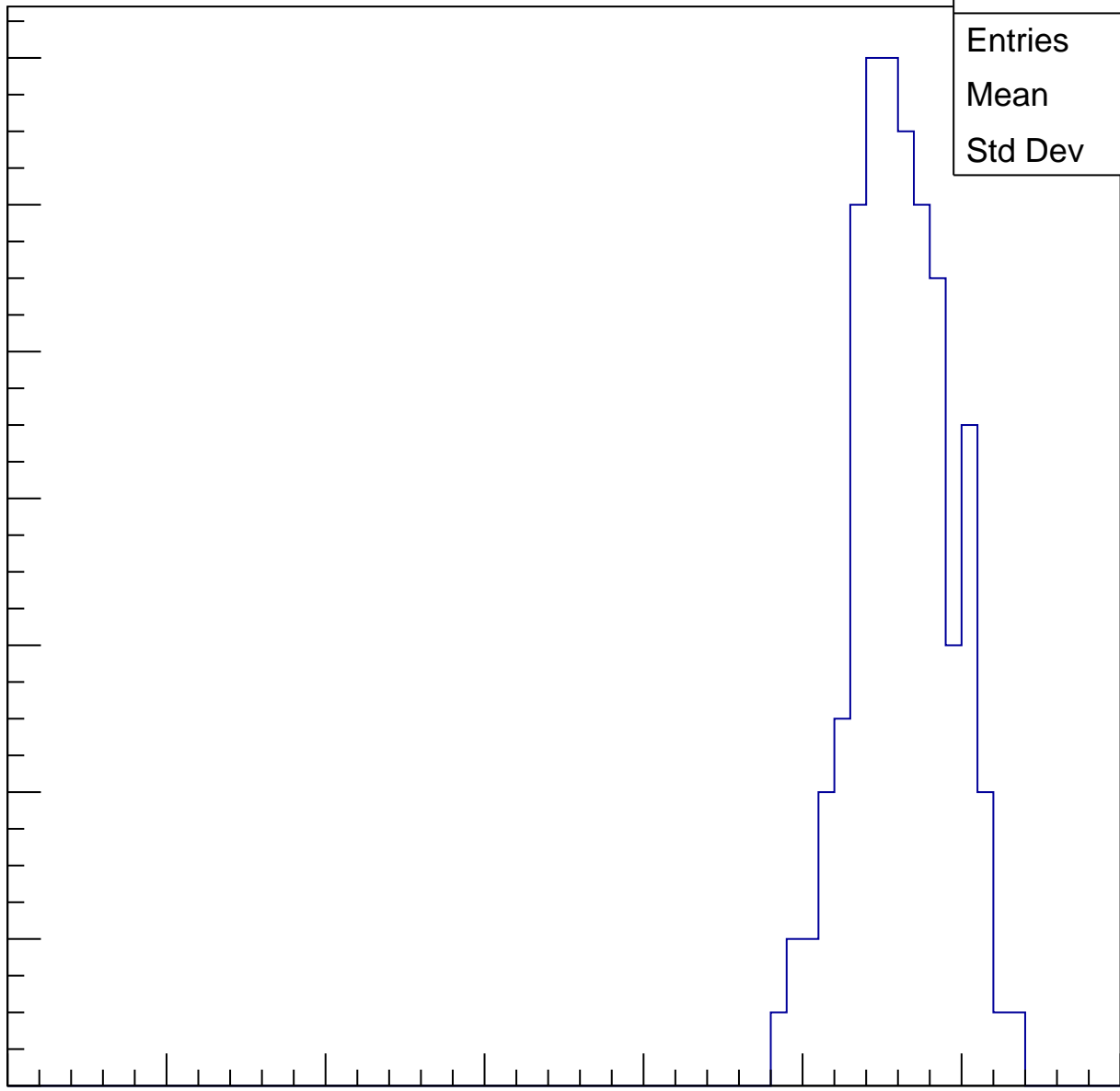
Entries	111
Mean	55.72
Std Dev	3.035

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch102, adc5

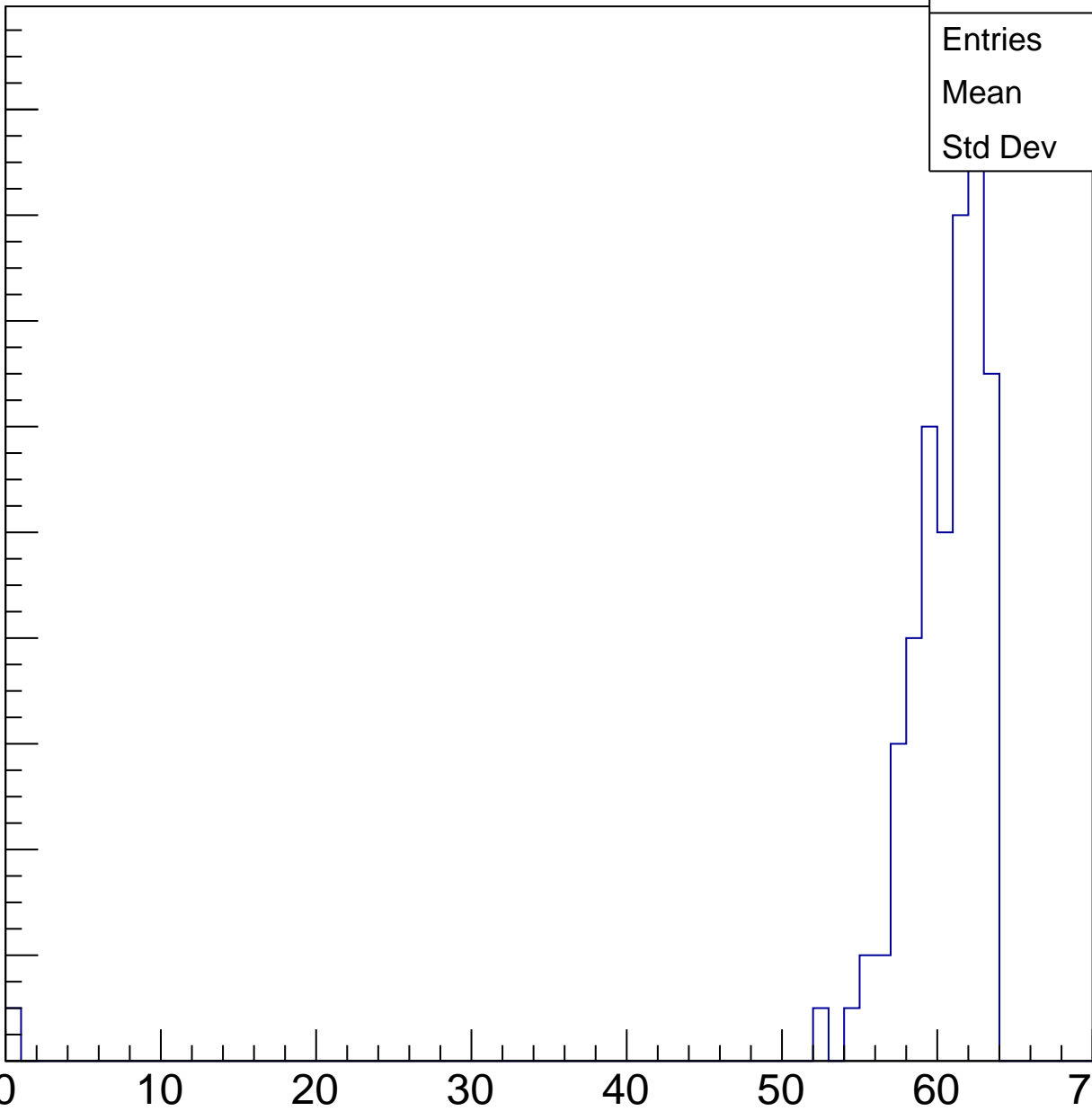
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.51
Std Dev	6.685

ampl

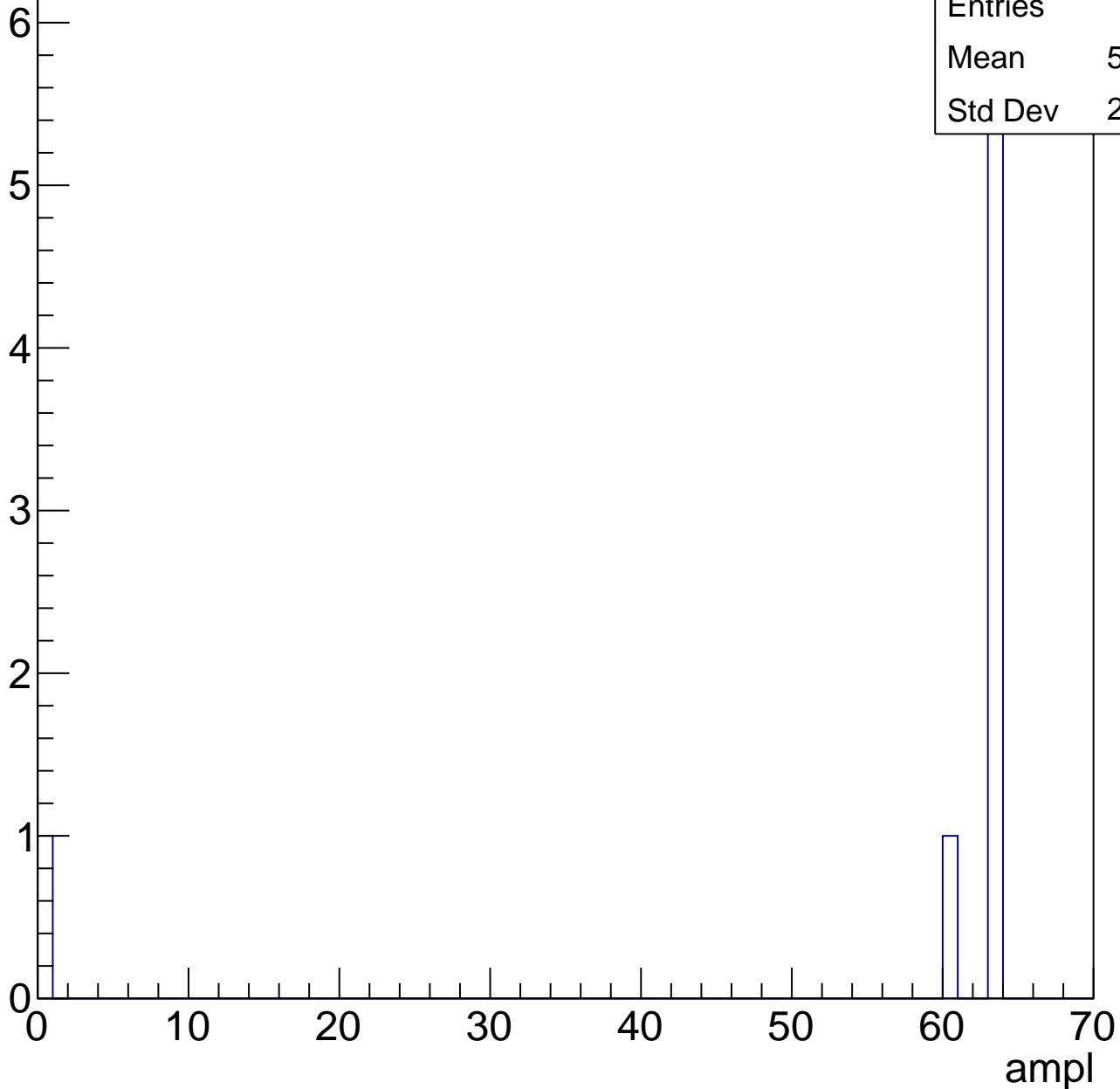


B1L001S, U25-ch102, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	54.75
Std Dev	20.72



B1L001S, U25-ch102, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U25-ch103, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	31.11
Std Dev	3.296

Gaus mean : 31.4471

Gaus Width: 3.3273

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

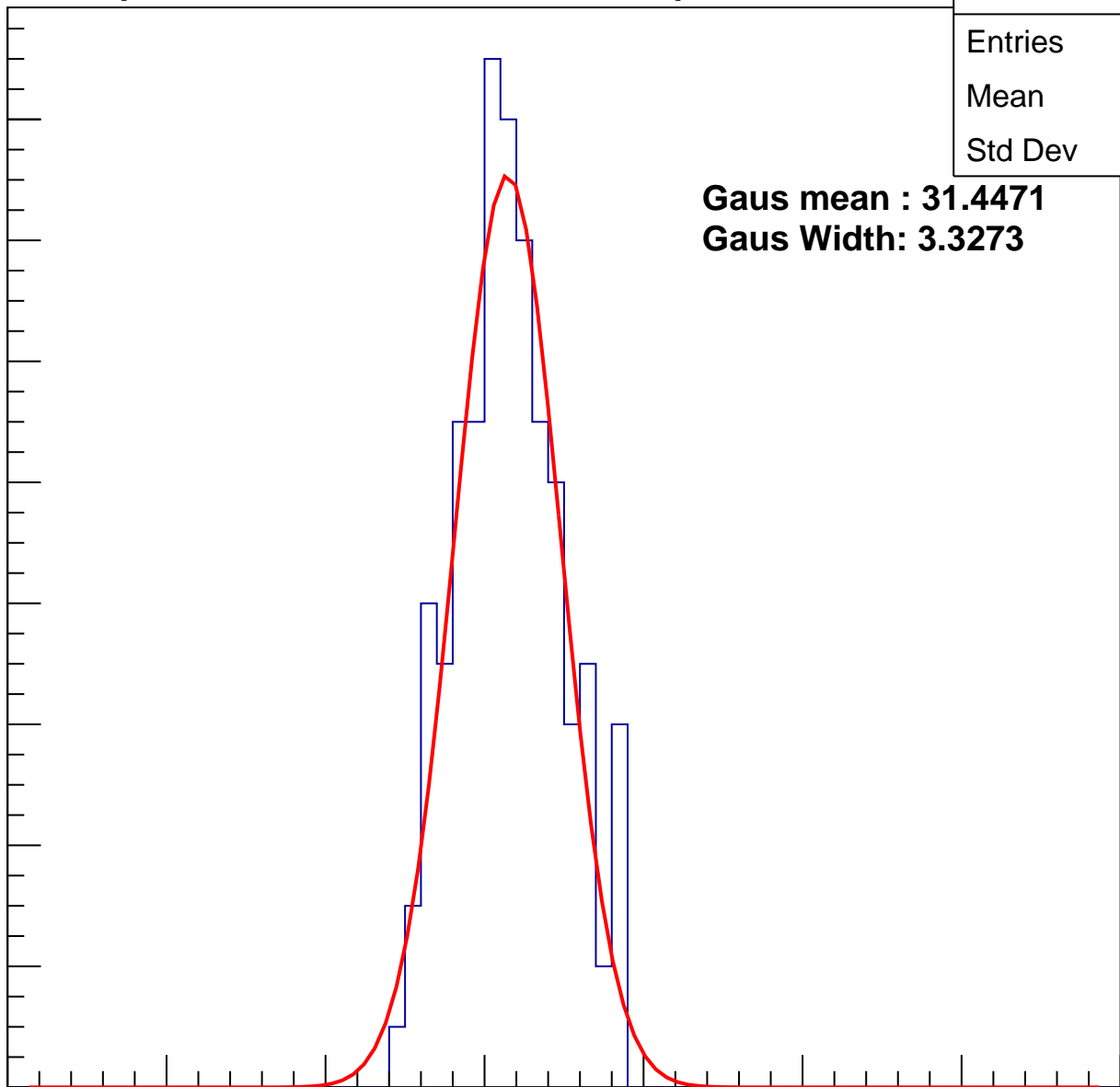
40

50

60

70

ampl



B1L001S, U25-ch103, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.58
Std Dev	4.061

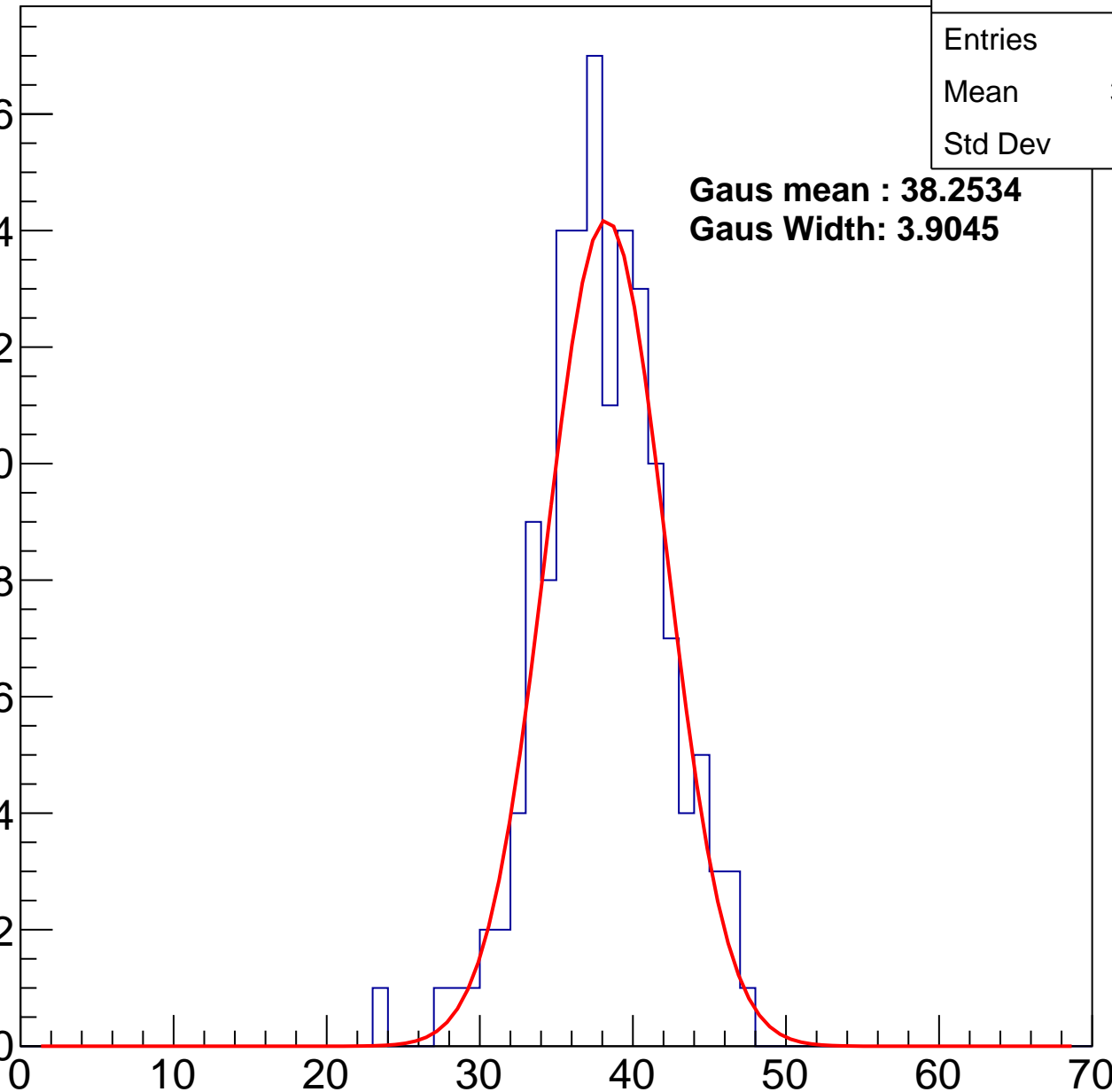
Gaus mean : 38.2534

Gaus Width: 3.9045

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch103, adc2

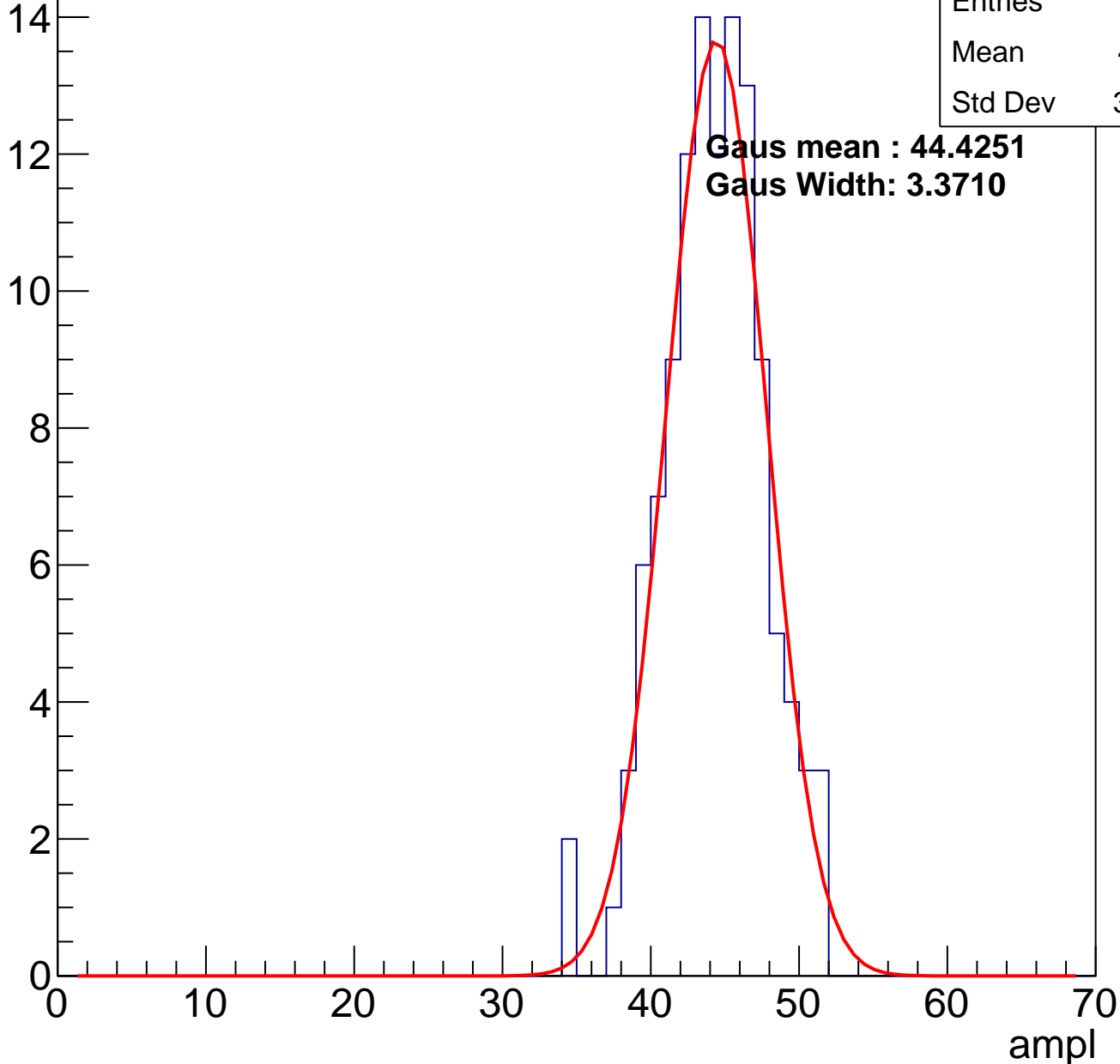
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	117
Mean	43.81
Std Dev	3.384

Gaus mean : 44.4251

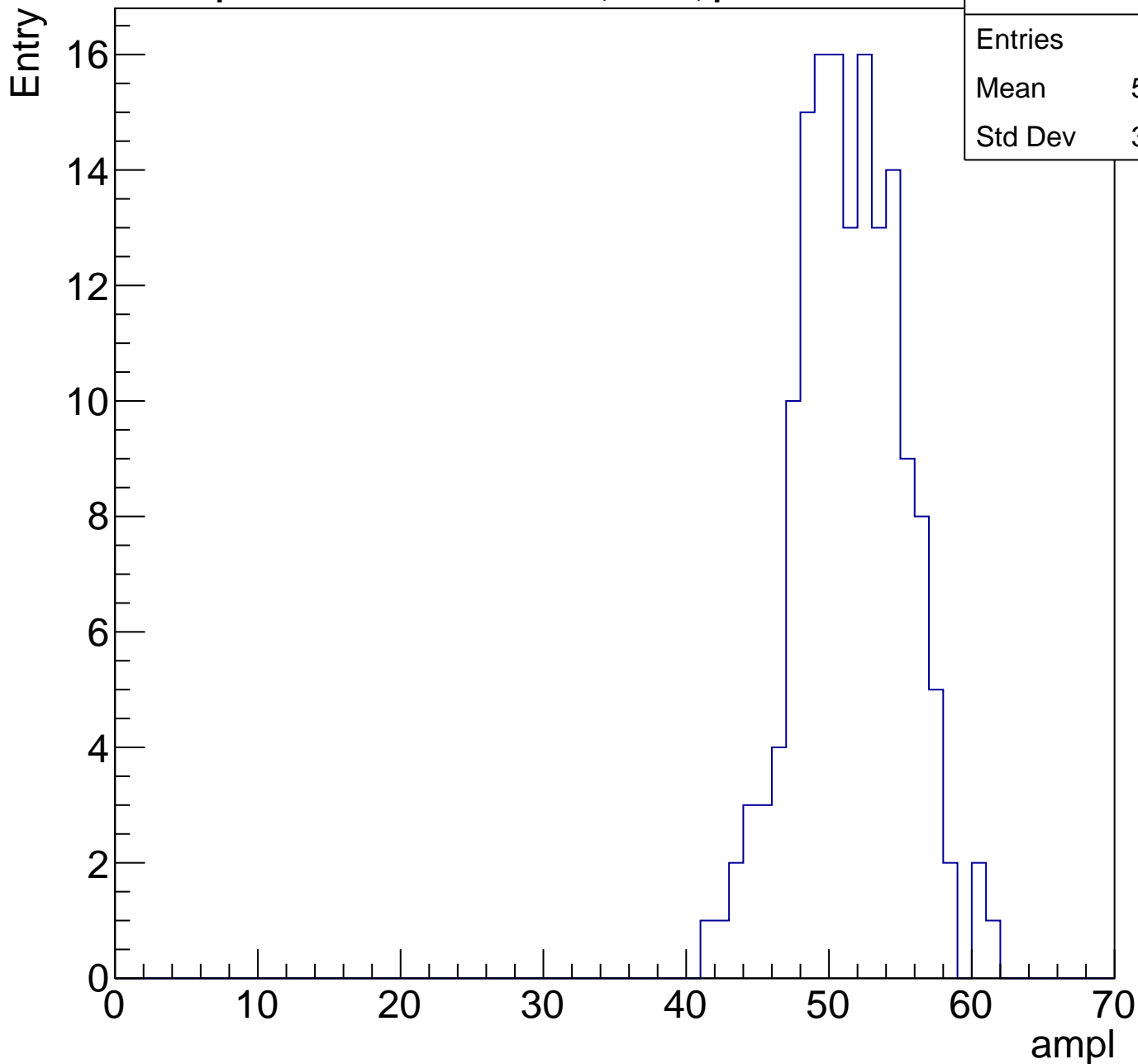
Gaus Width: 3.3710



B1L001S, U25-ch103, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	51.03
Std Dev	3.708



B1L001S, U25-ch103, adc4

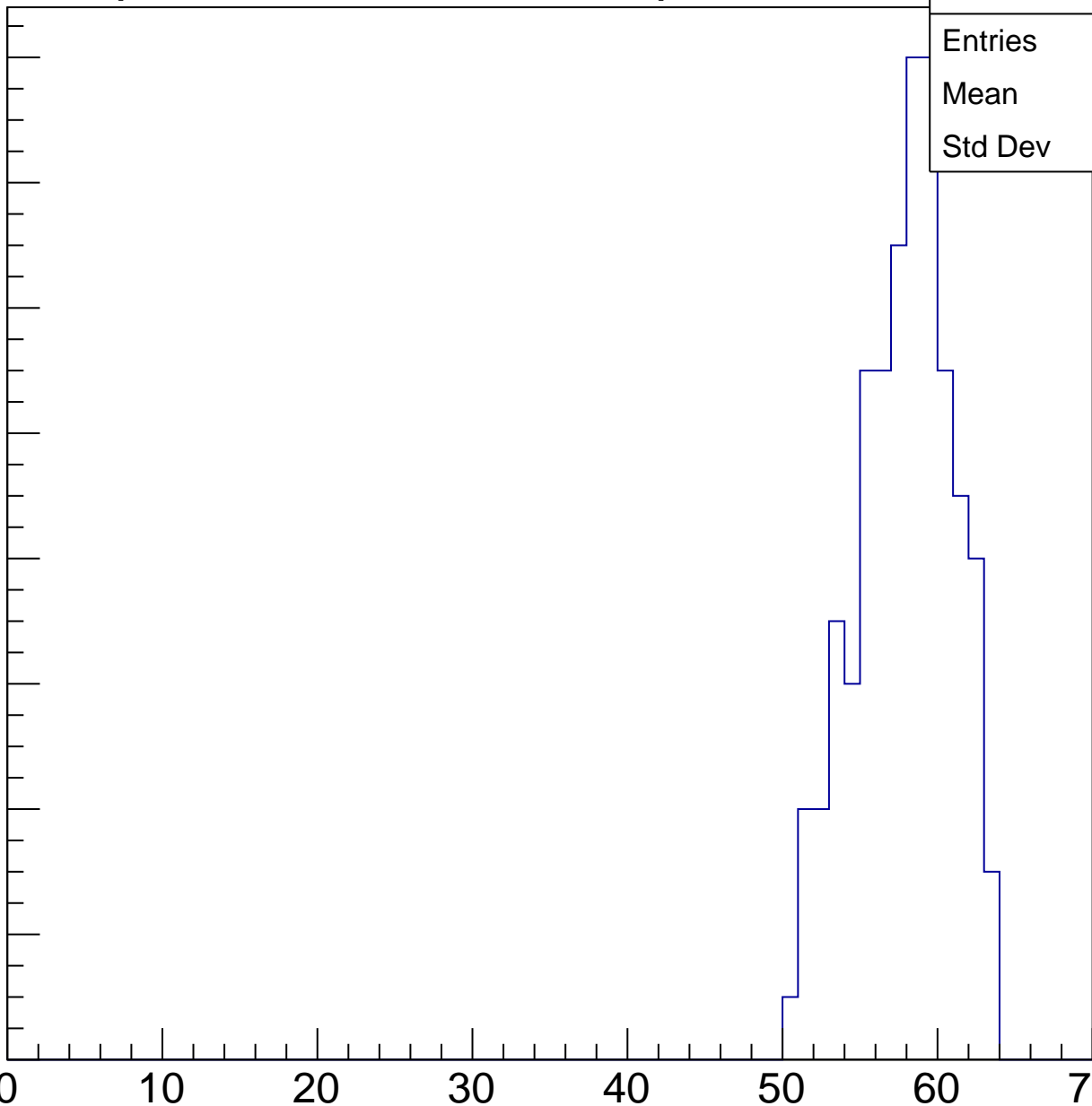
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	57.38
Std Dev	3.066

ampl



B1L001S, U25-ch103, adc5

calib_packv5_042523_0143.root, FC#2, port C2

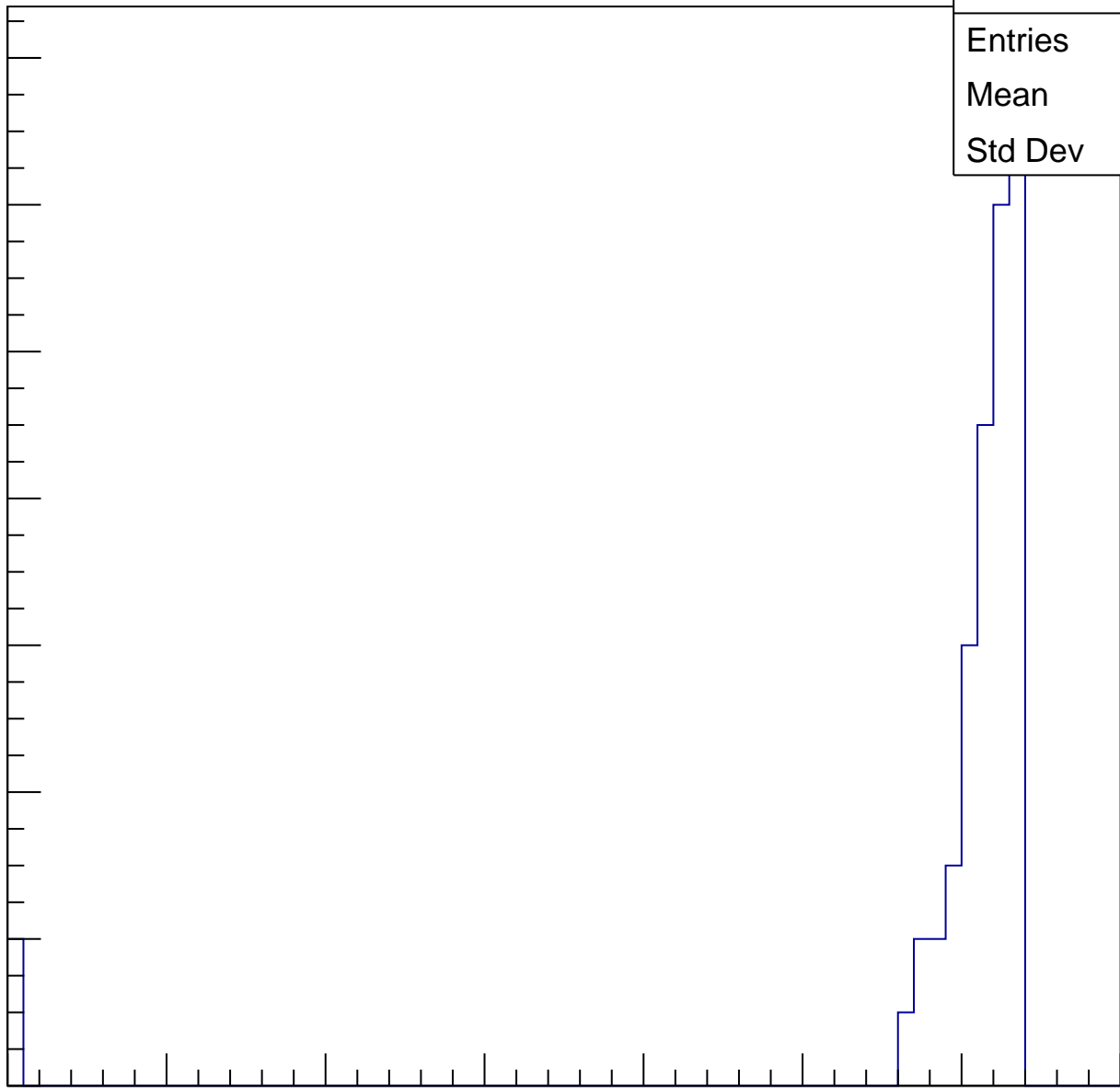
Entries	51
Mean	58.78
Std Dev	12.01

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

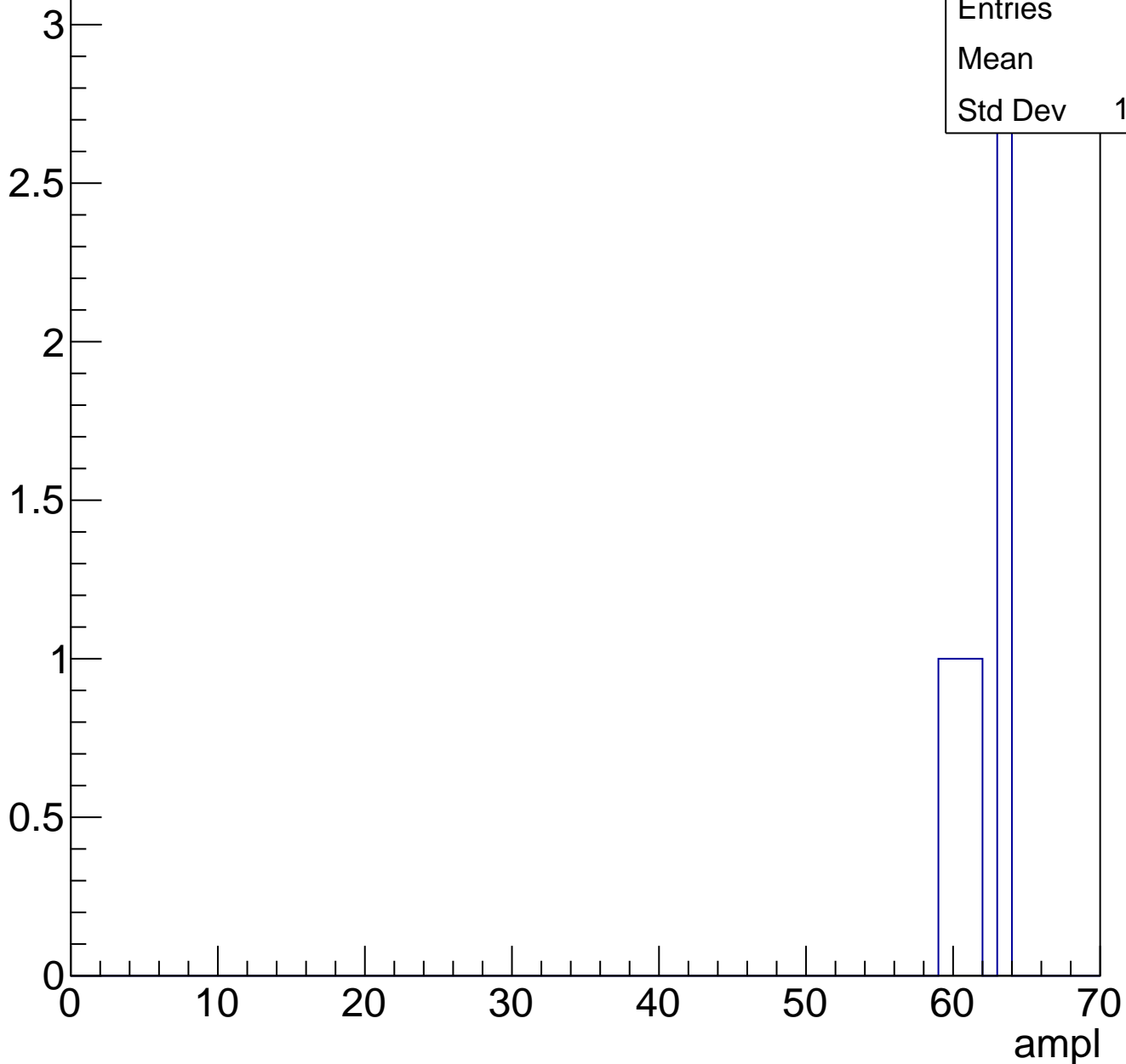
ampl



B1L001S, U25-ch103, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch103, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



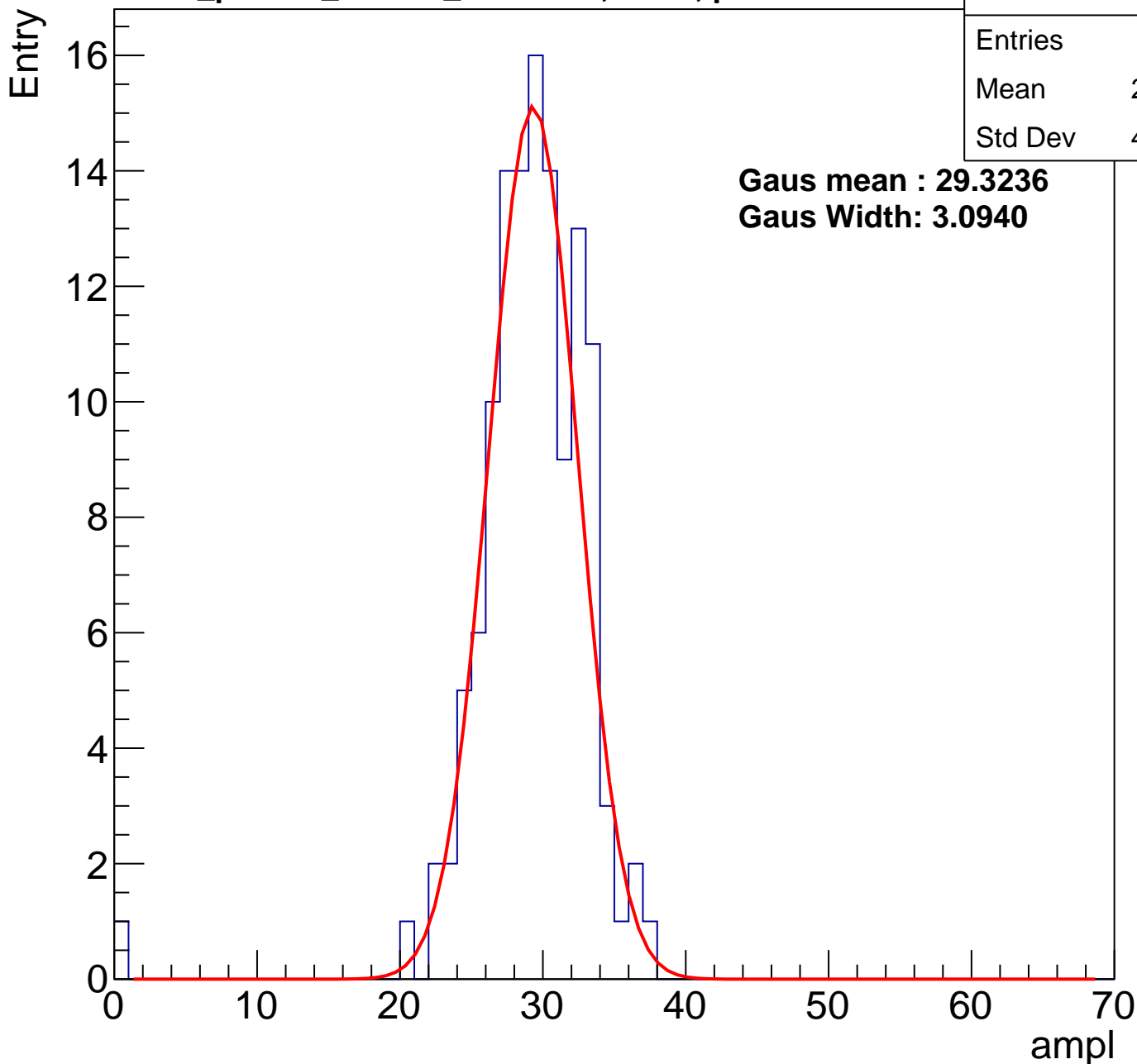
B1L001S, U25-ch104, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	28.78
Std Dev	4.082

Gaus mean : 29.3236

Gaus Width: 3.0940



B1L001S, U25-ch104, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

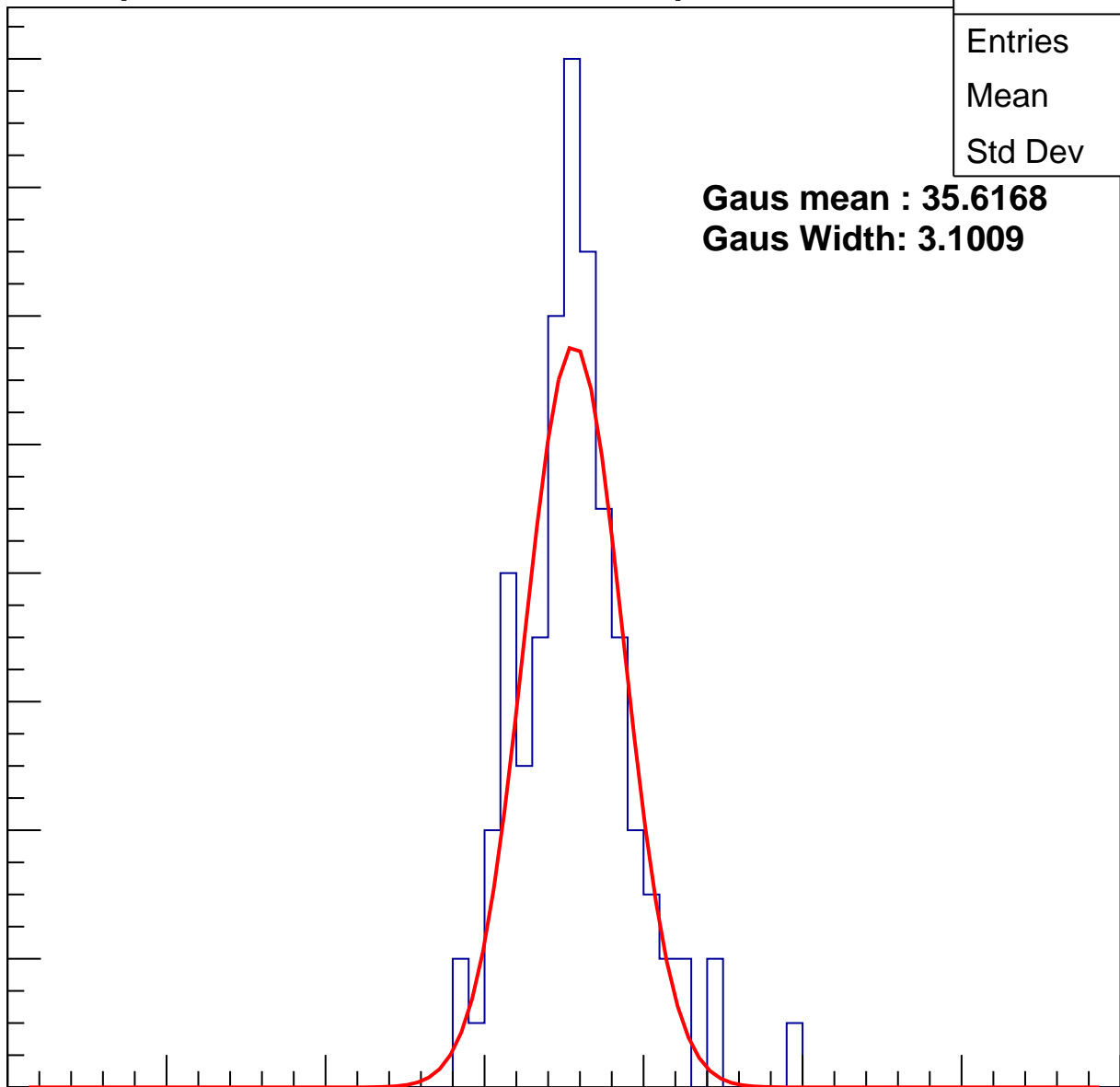
Entries	98
Mean	35.29
Std Dev	3.525

Gaus mean : 35.6168

Gaus Width: 3.1009

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch104, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

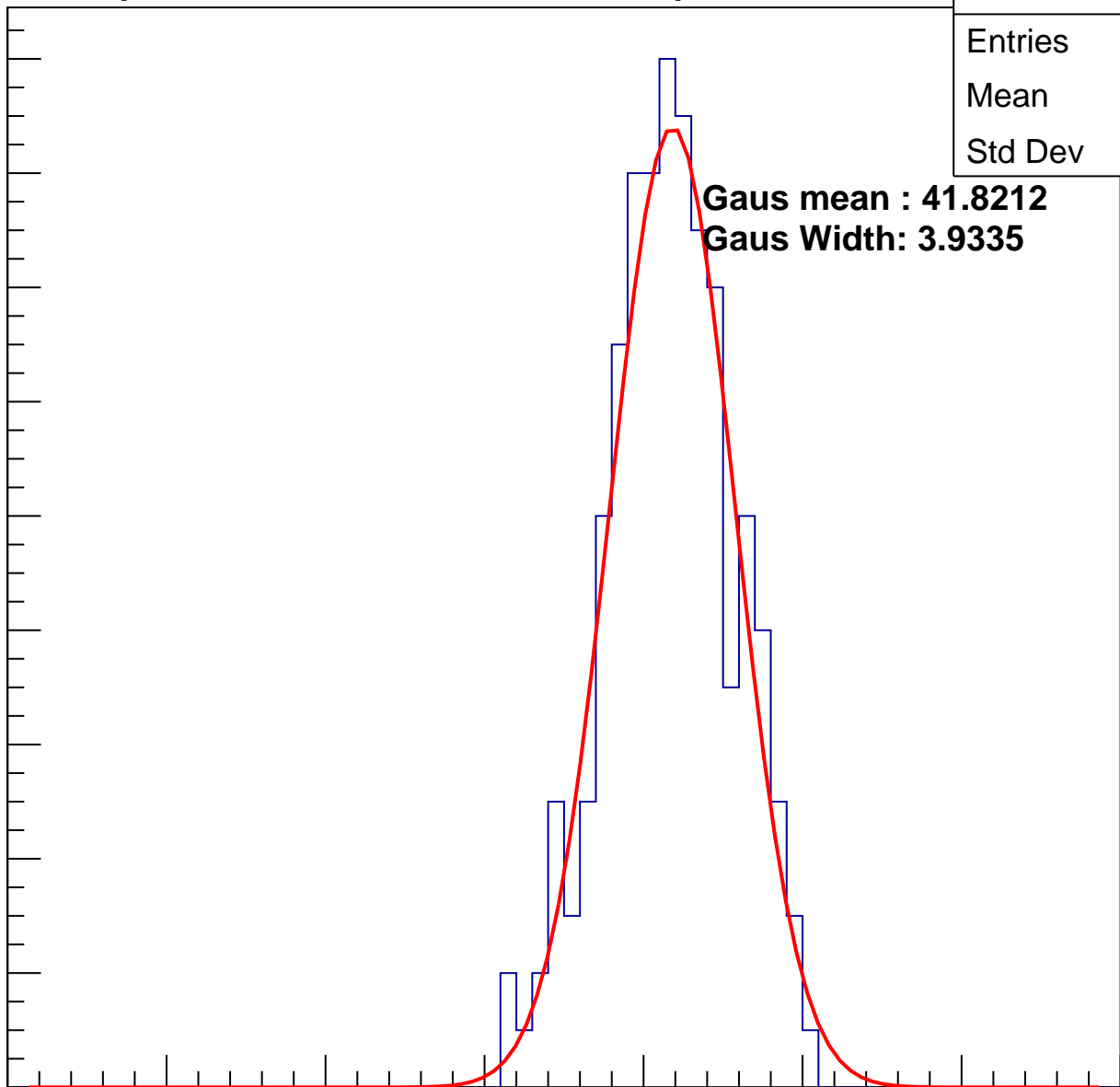
Entries	171
Mean	41.19
Std Dev	3.906

Gaus mean : 41.8212

Gaus Width: 3.9335

0 10 20 30 40 50 60 70

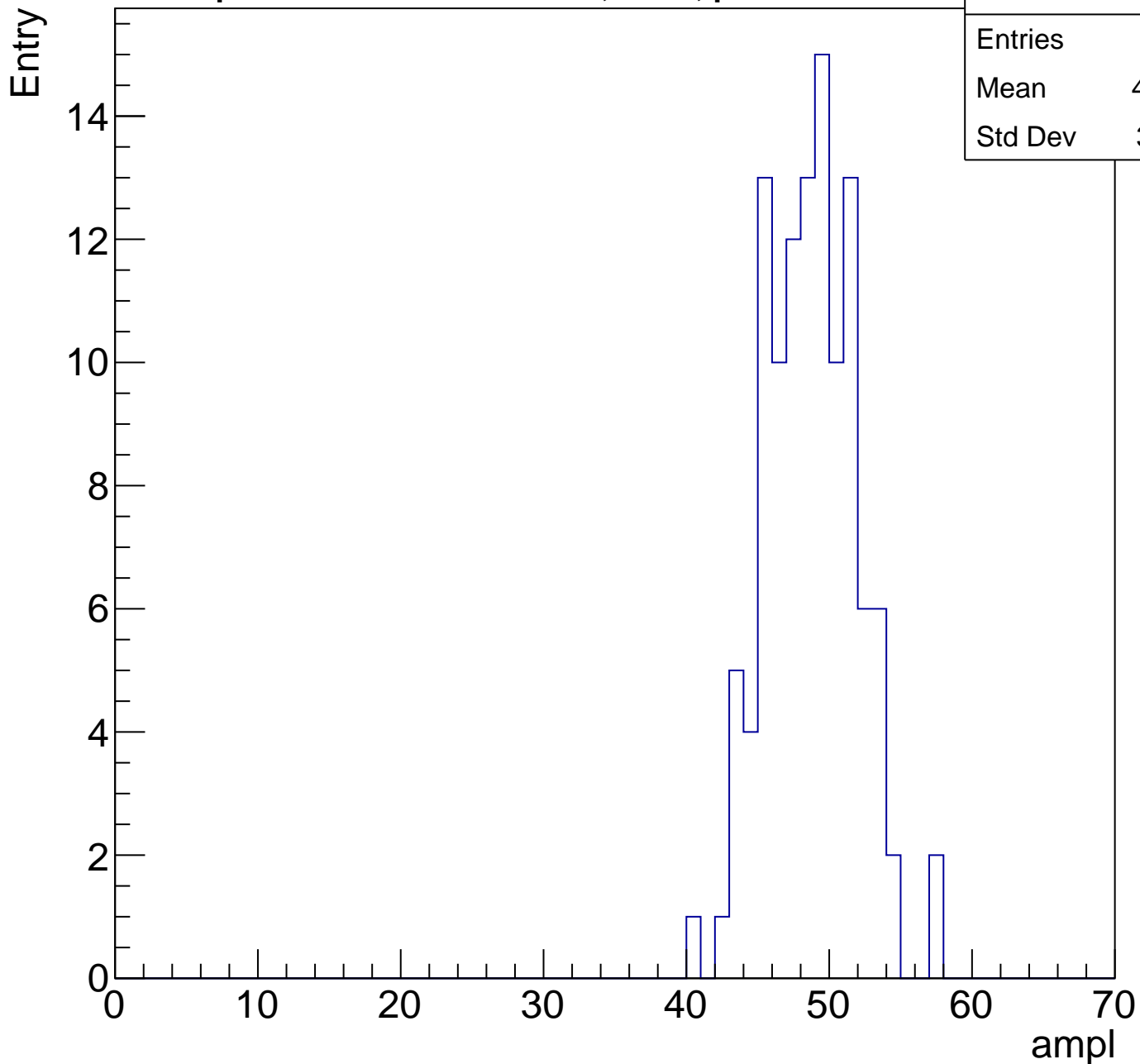
ampl



B1L001S, U25-ch104, adc3

calib_packv5_042523_0143.root, FC#2, port C2

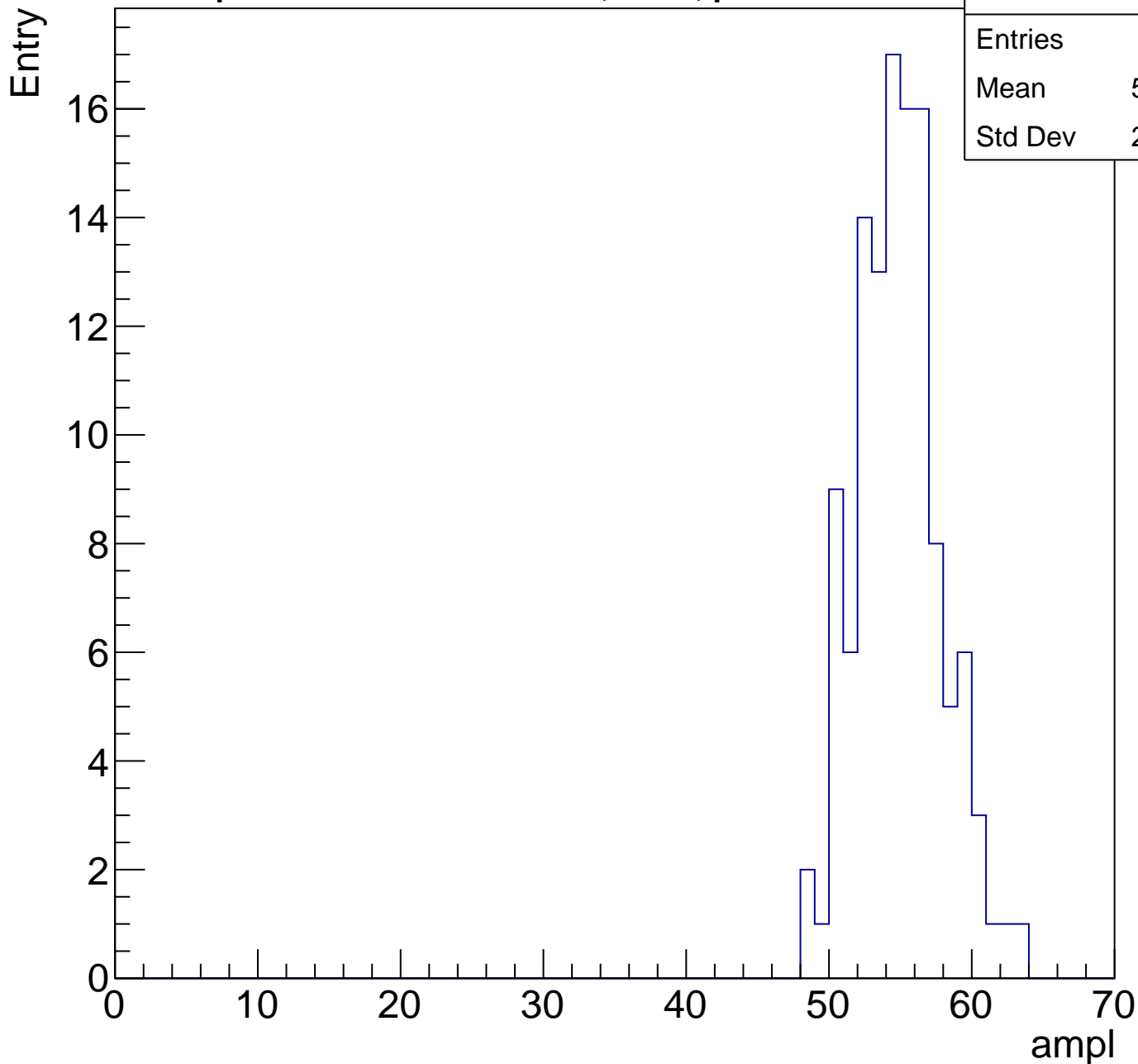
Entries	113
Mean	48.28
Std Dev	3.121



B1L001S, U25-ch104, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	54.44
Std Dev	2.935



B1L001S, U25-ch104, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	58.29
Std Dev	8.638

Entry

14

12

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

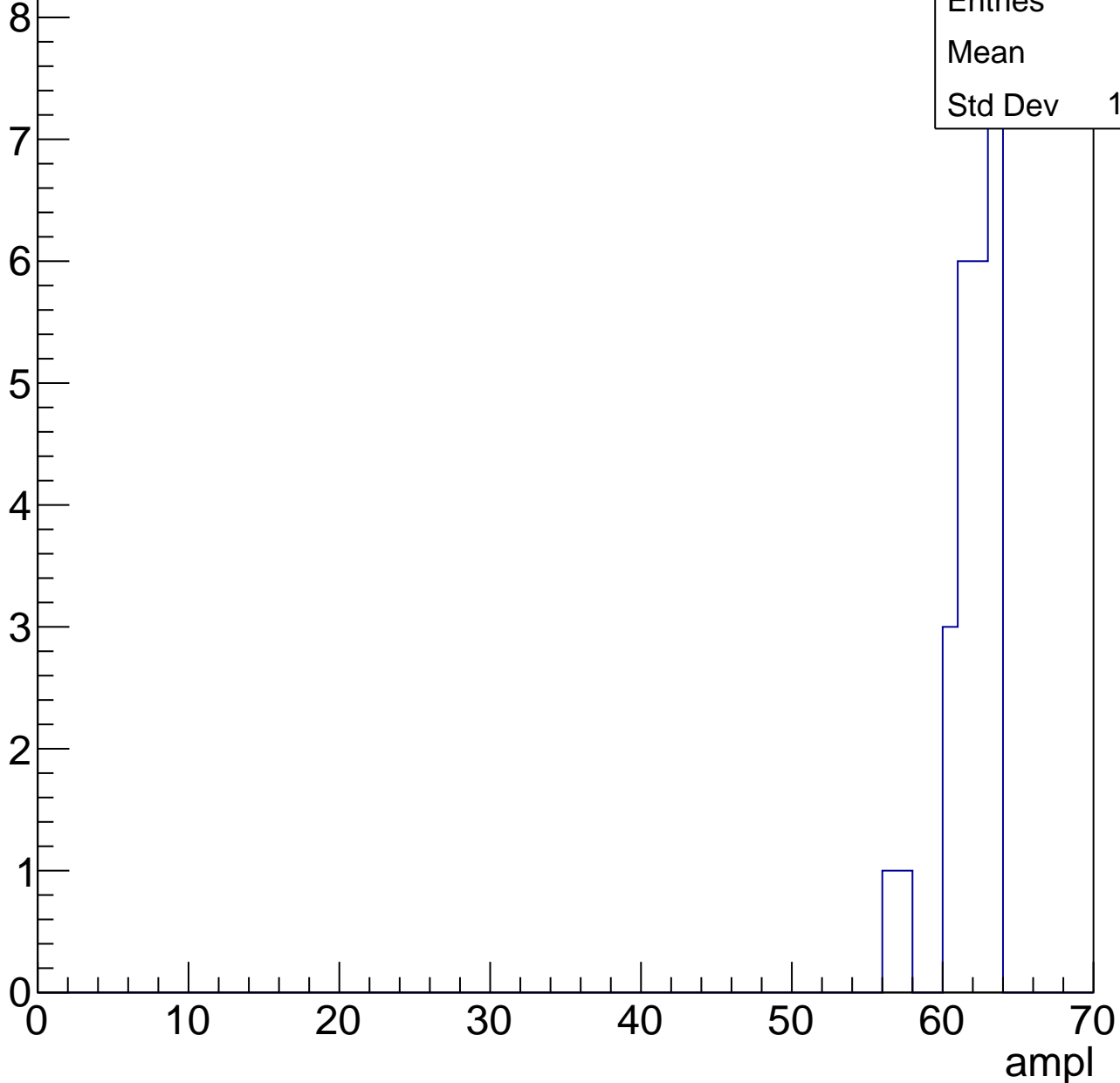
70

B1L001S, U25-ch104, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	25
Mean	61.4
Std Dev	1.766



B1L001S, U25-ch104, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch105, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	30.83
Std Dev	2.803

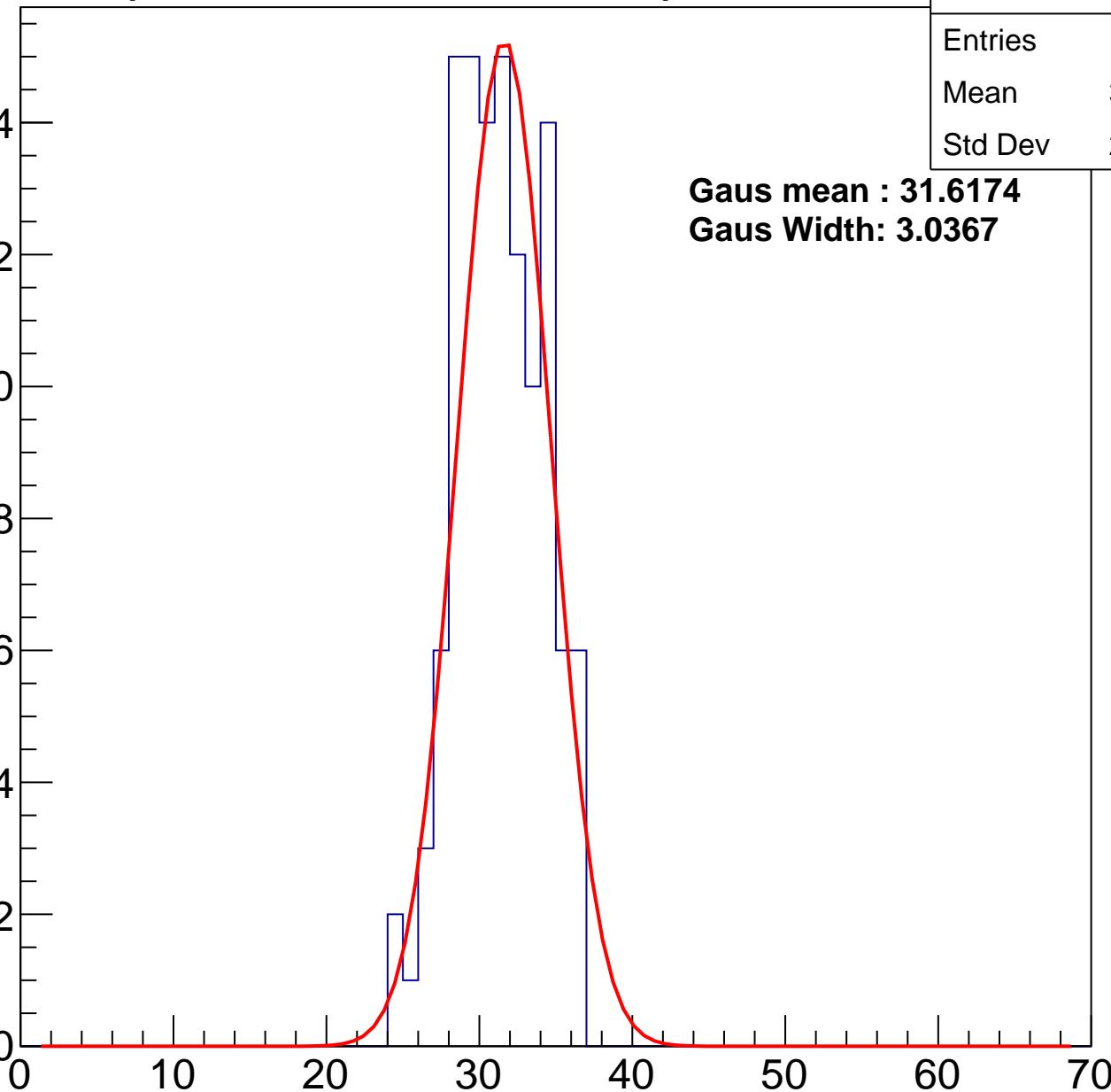
Gaus mean : 31.6174

Gaus Width: 3.0367

Entry

14
12
10
8
6
4
2
0

ampl



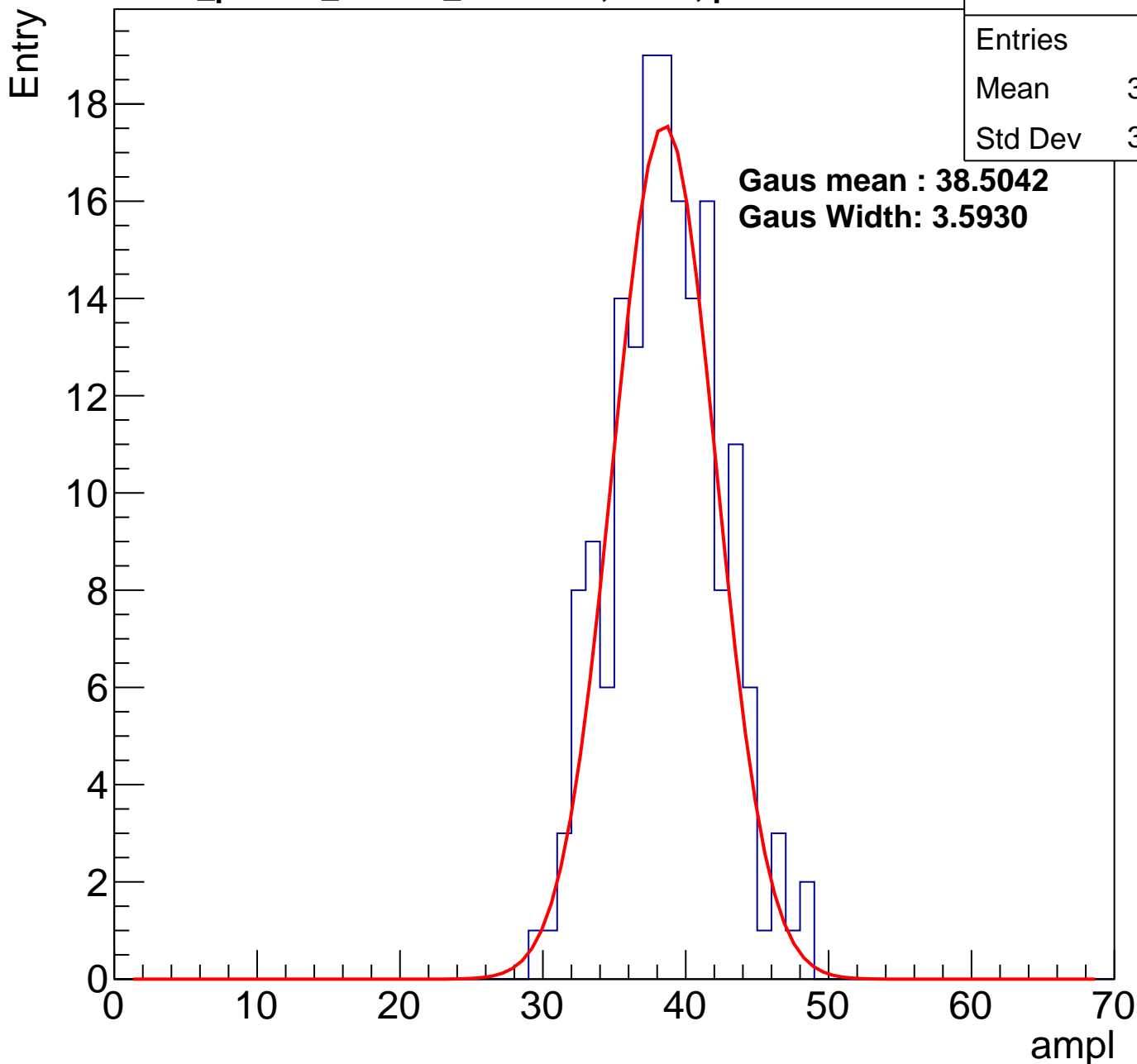
B1L001S, U25-ch105, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	171
Mean	38.19
Std Dev	3.784

Gaus mean : 38.5042

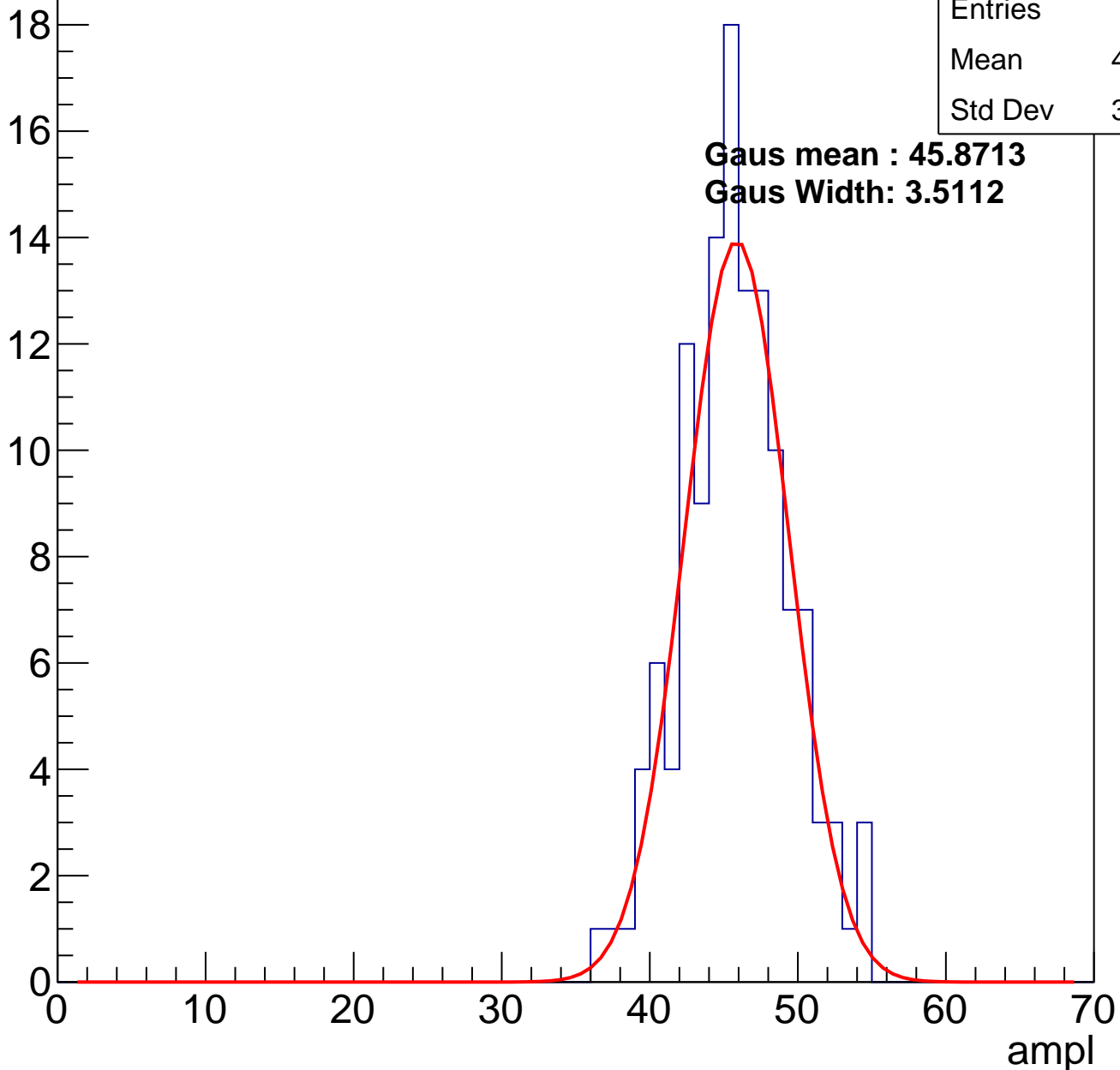
Gaus Width: 3.5930



B1L001S, U25-ch105, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch105, adc3

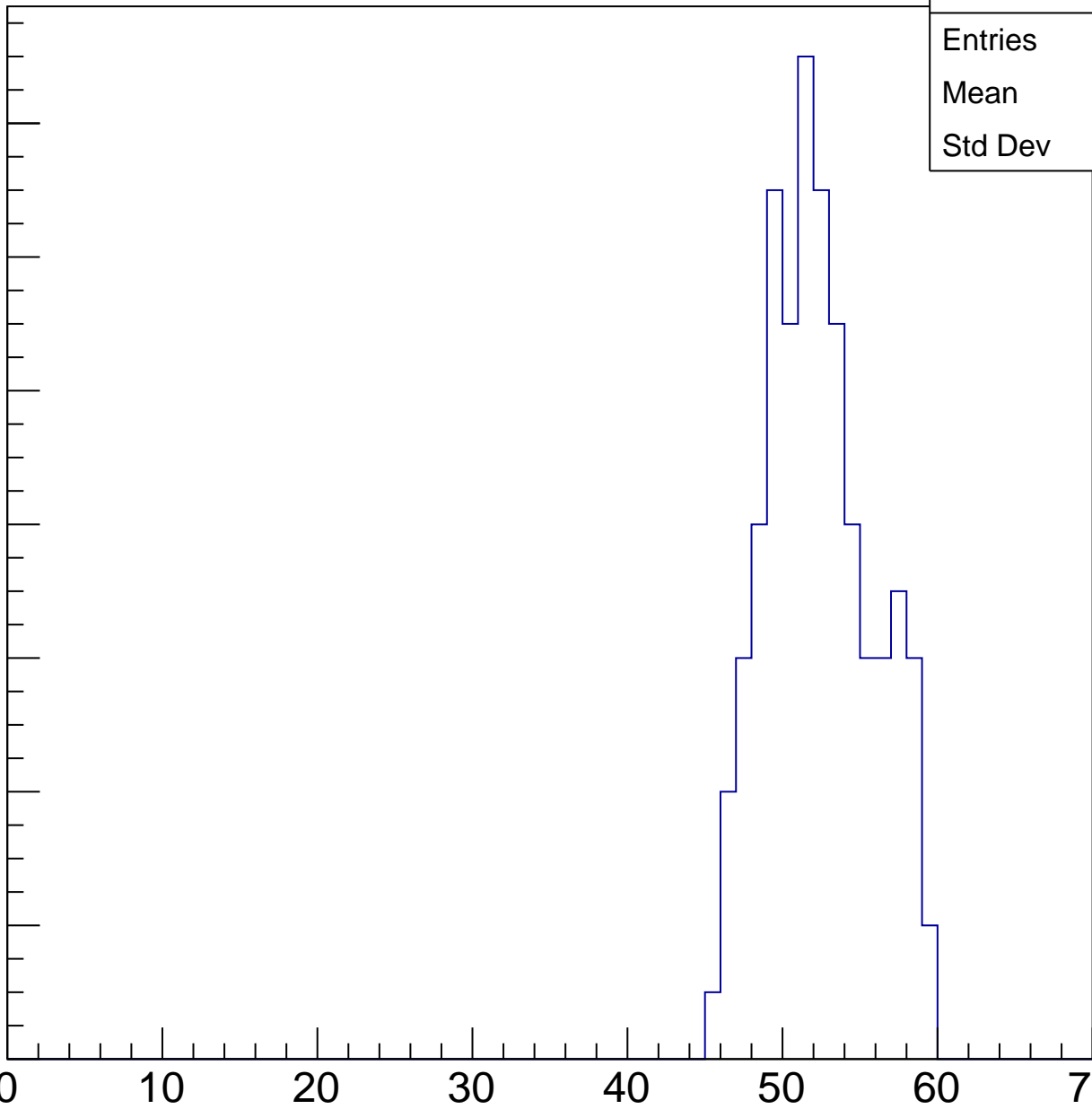
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	51.87
Std Dev	3.368

ampl



B1L001S, U25-ch105, adc4

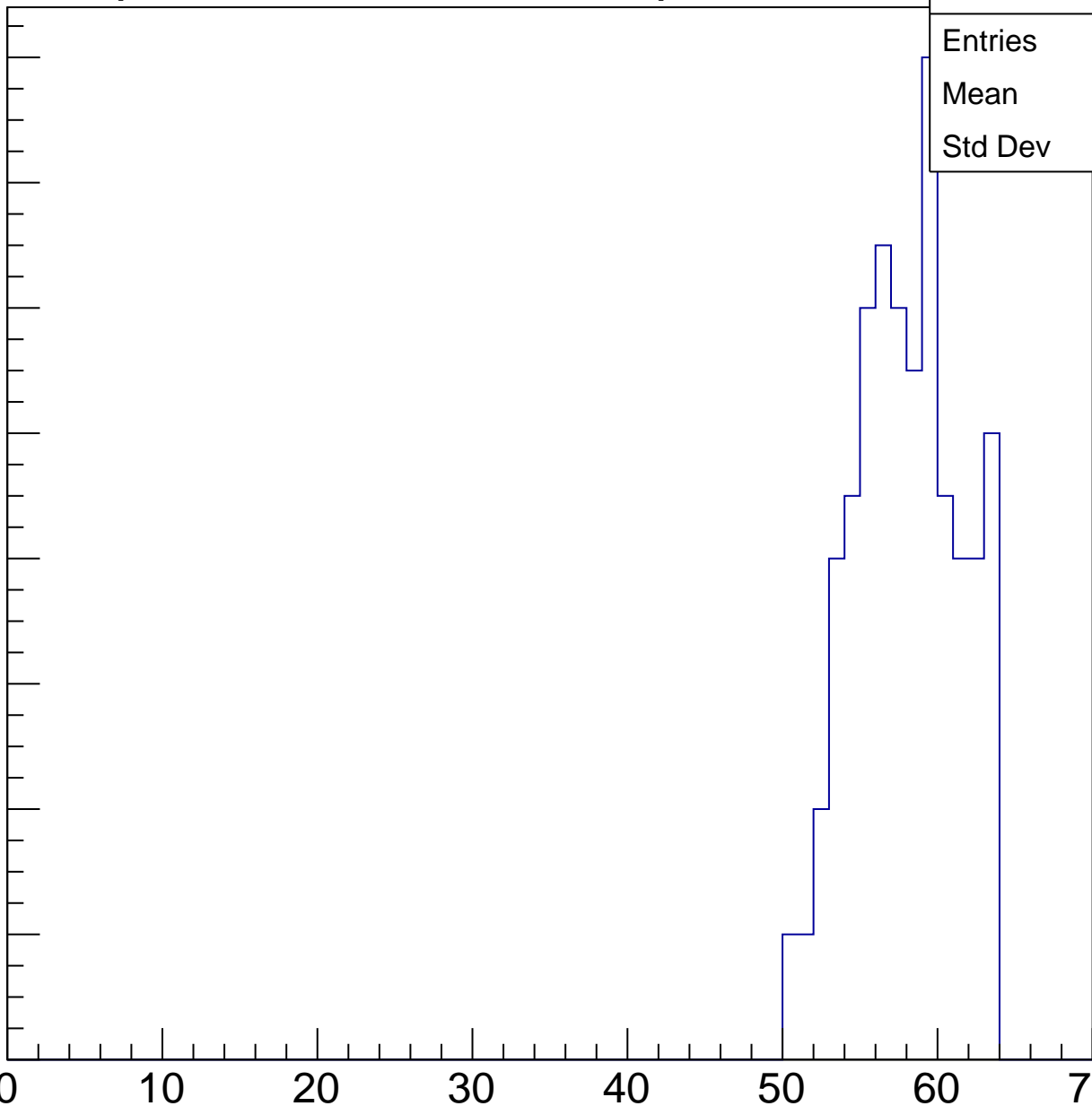
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	57.48
Std Dev	3.306

ampl



B1L001S, U25-ch105, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

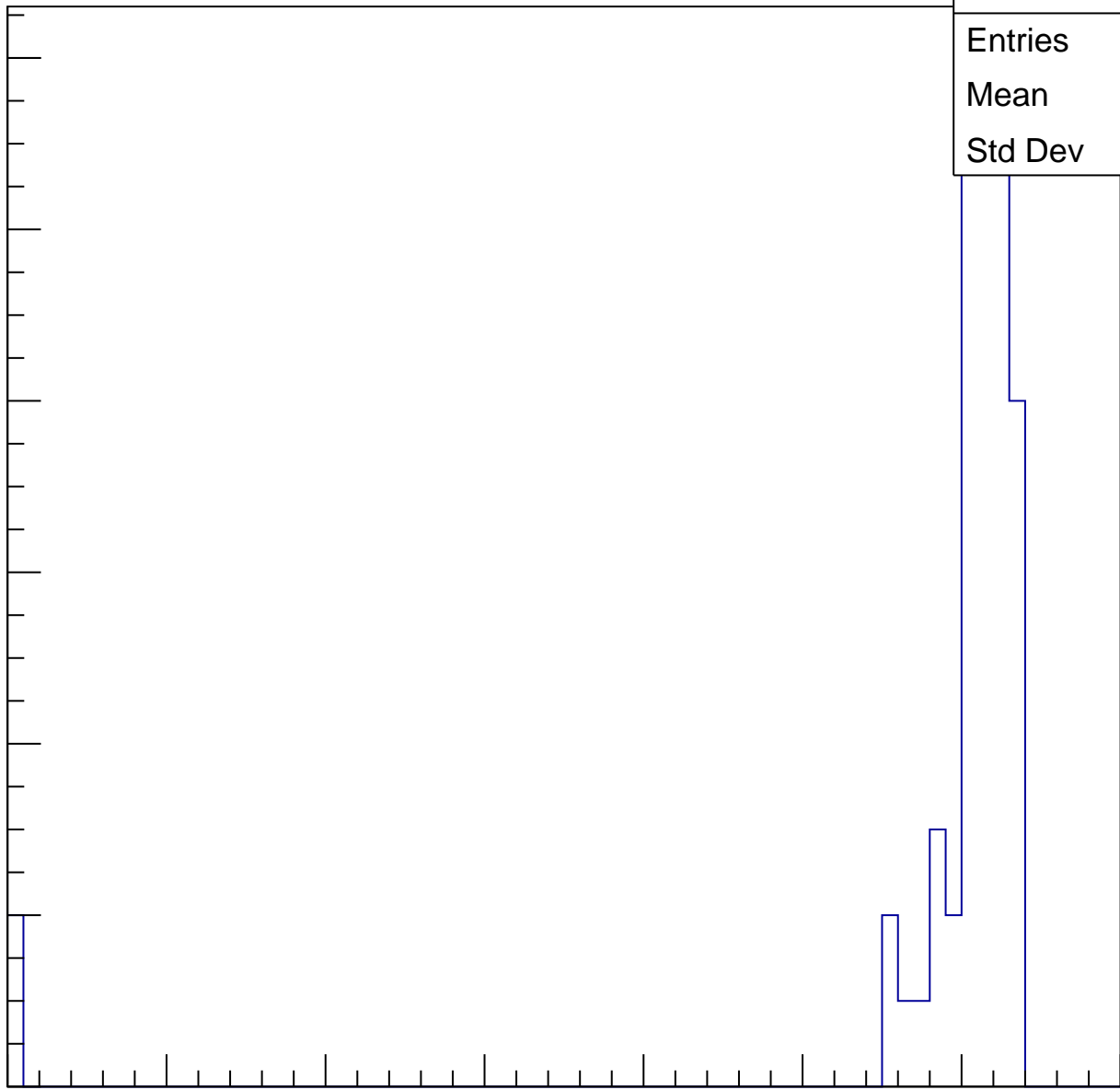
2

0

Entries	54
Mean	58.43
Std Dev	11.62

ampl

0 10 20 30 40 50 60 70



B1L001S, U25-ch105, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L001S, U25-ch105, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

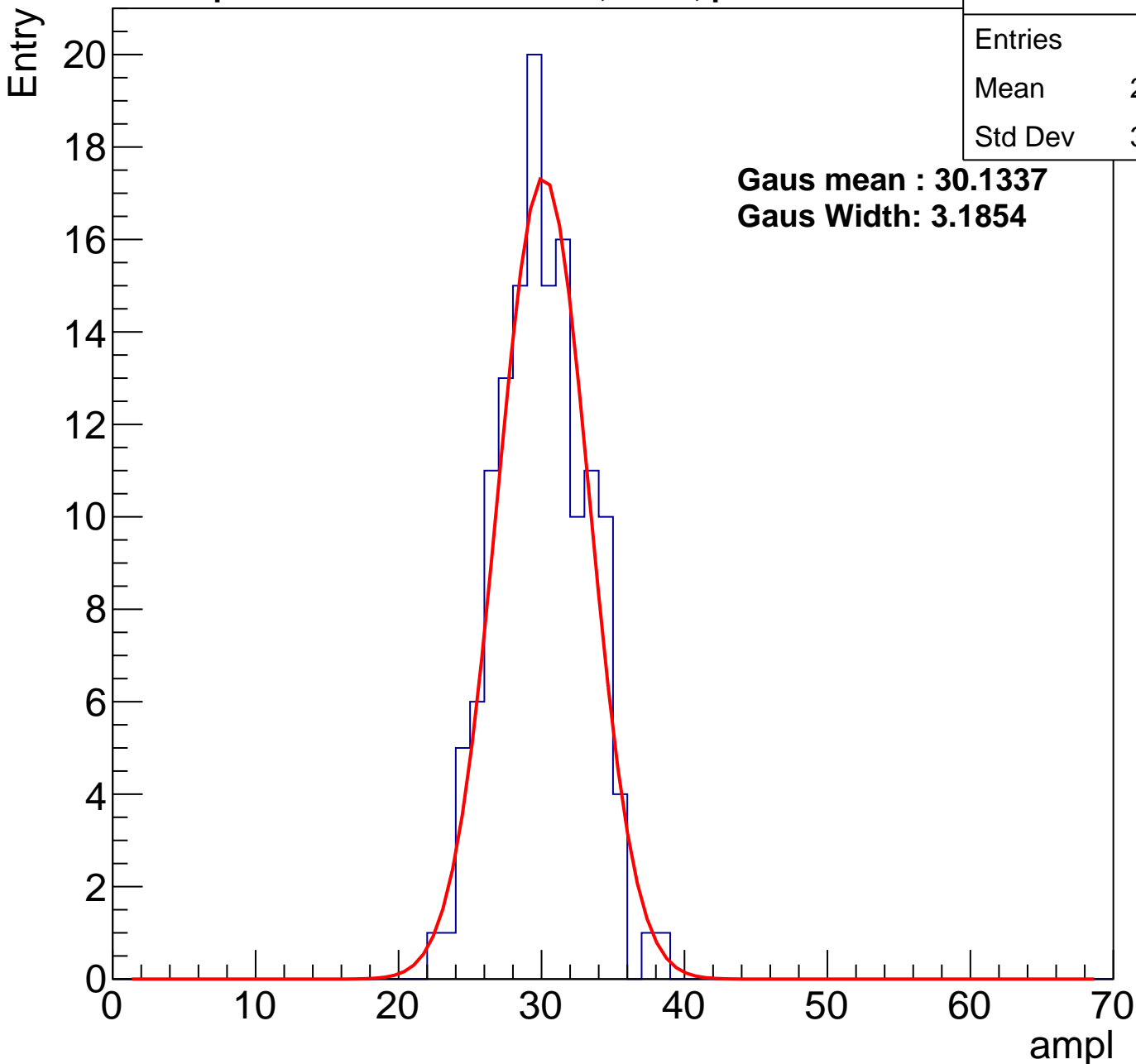
B1L001S, U25-ch106, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	29.54
Std Dev	3.064

Gaus mean : 30.1337

Gaus Width: 3.1854



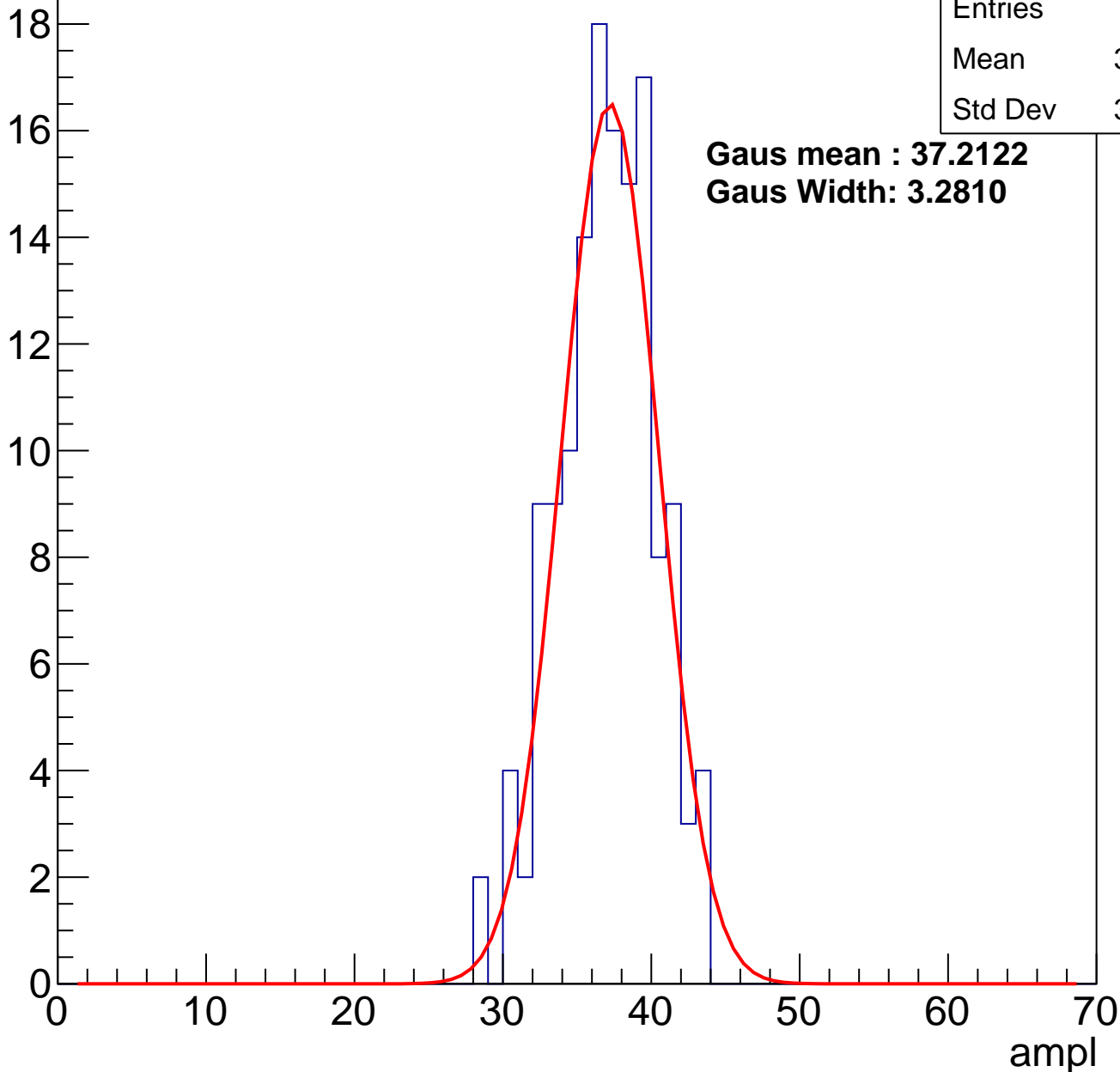
B1L001S, U25-ch106, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	36.52
Std Dev	3.217

Gaus mean : 37.2122
Gaus Width: 3.2810

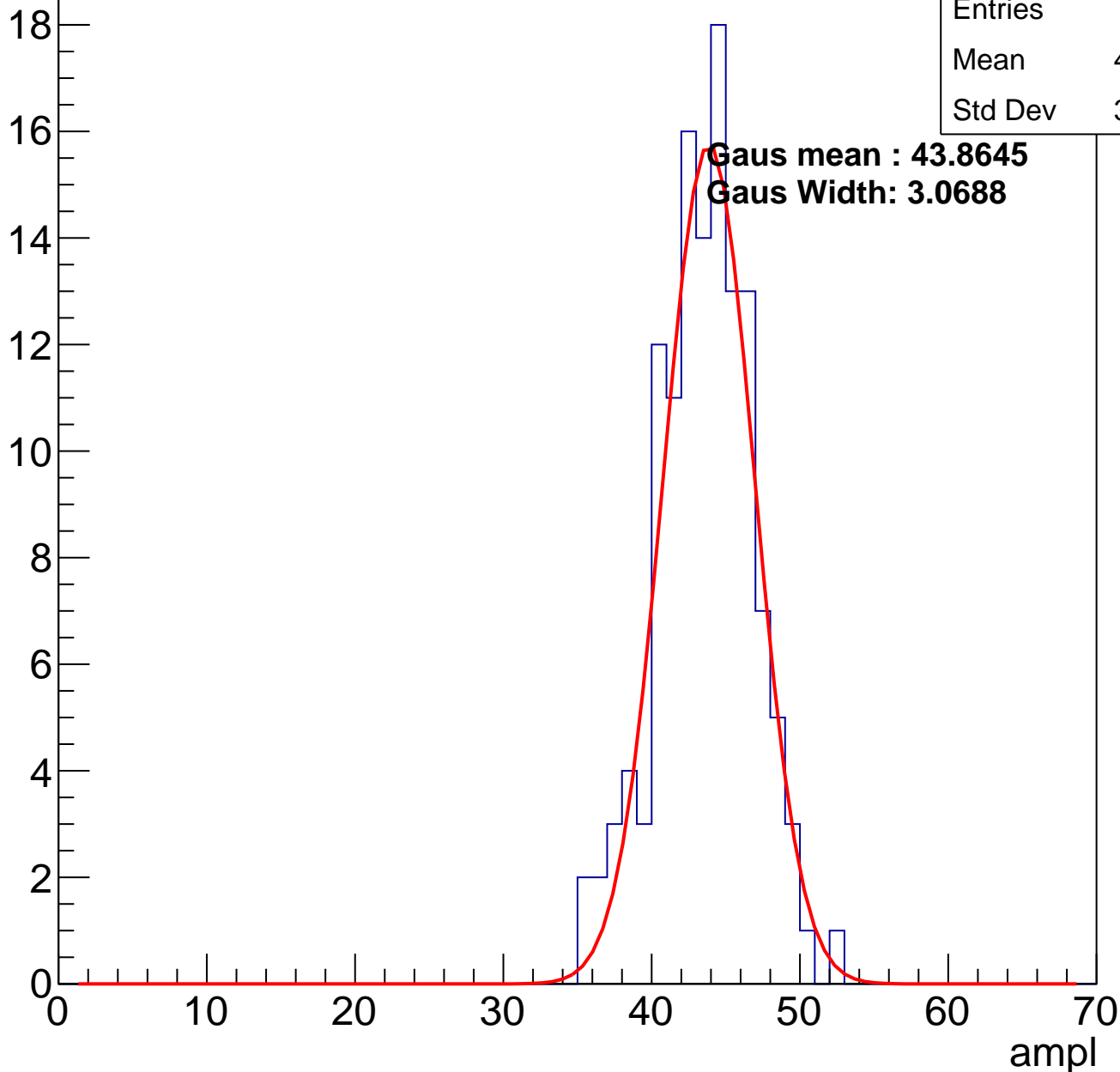
Entry



B1L001S, U25-ch106, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch106, adc3

calib_packv5_042523_0143.root, FC#2, port C2

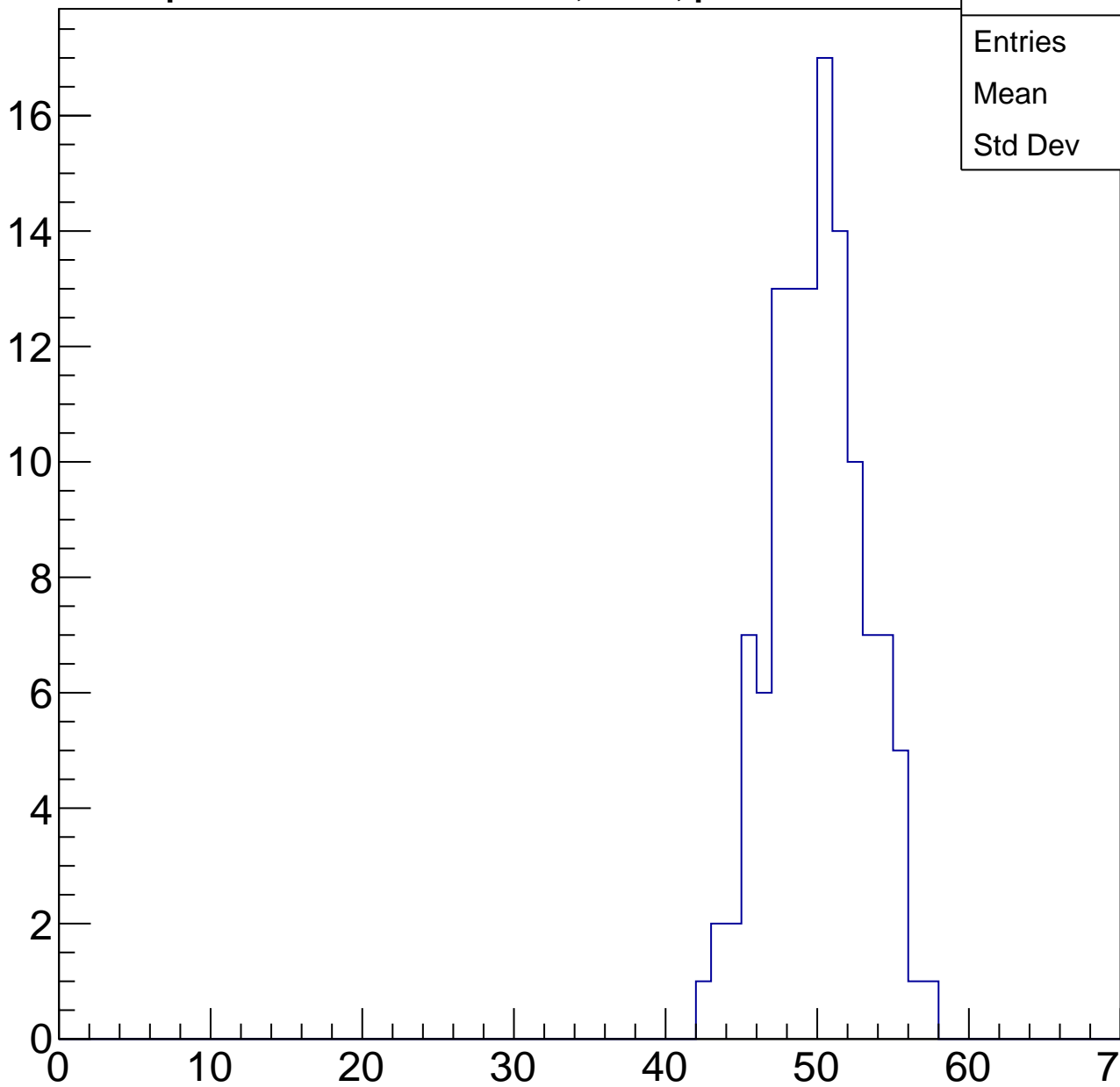
Entries	119
Mean	49.58
Std Dev	3.047

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch106, adc4

calib_packv5_042523_0143.root, FC#2, port C2

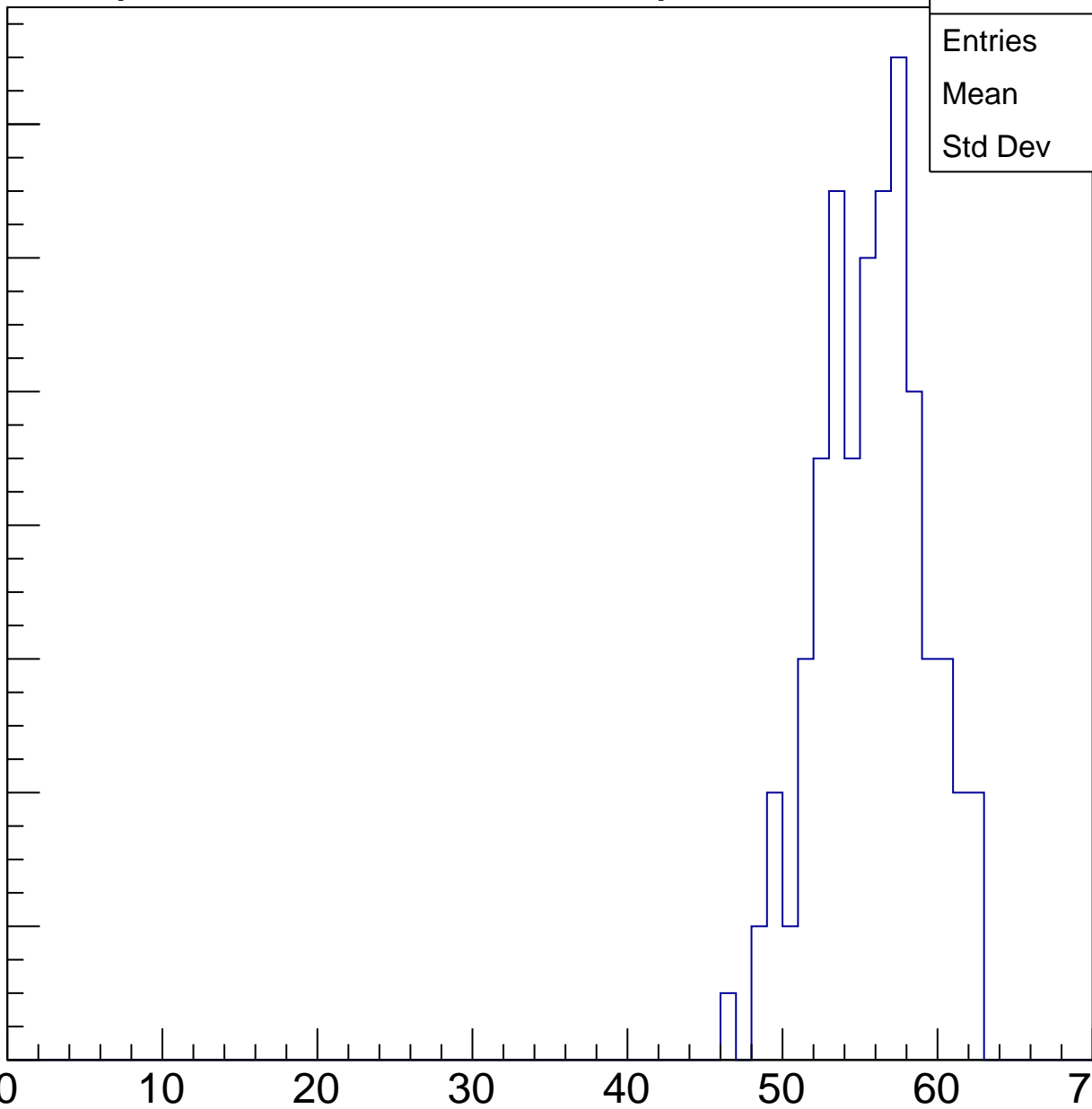
Entries	116
Mean	55.31
Std Dev	3.41

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch106, adc5

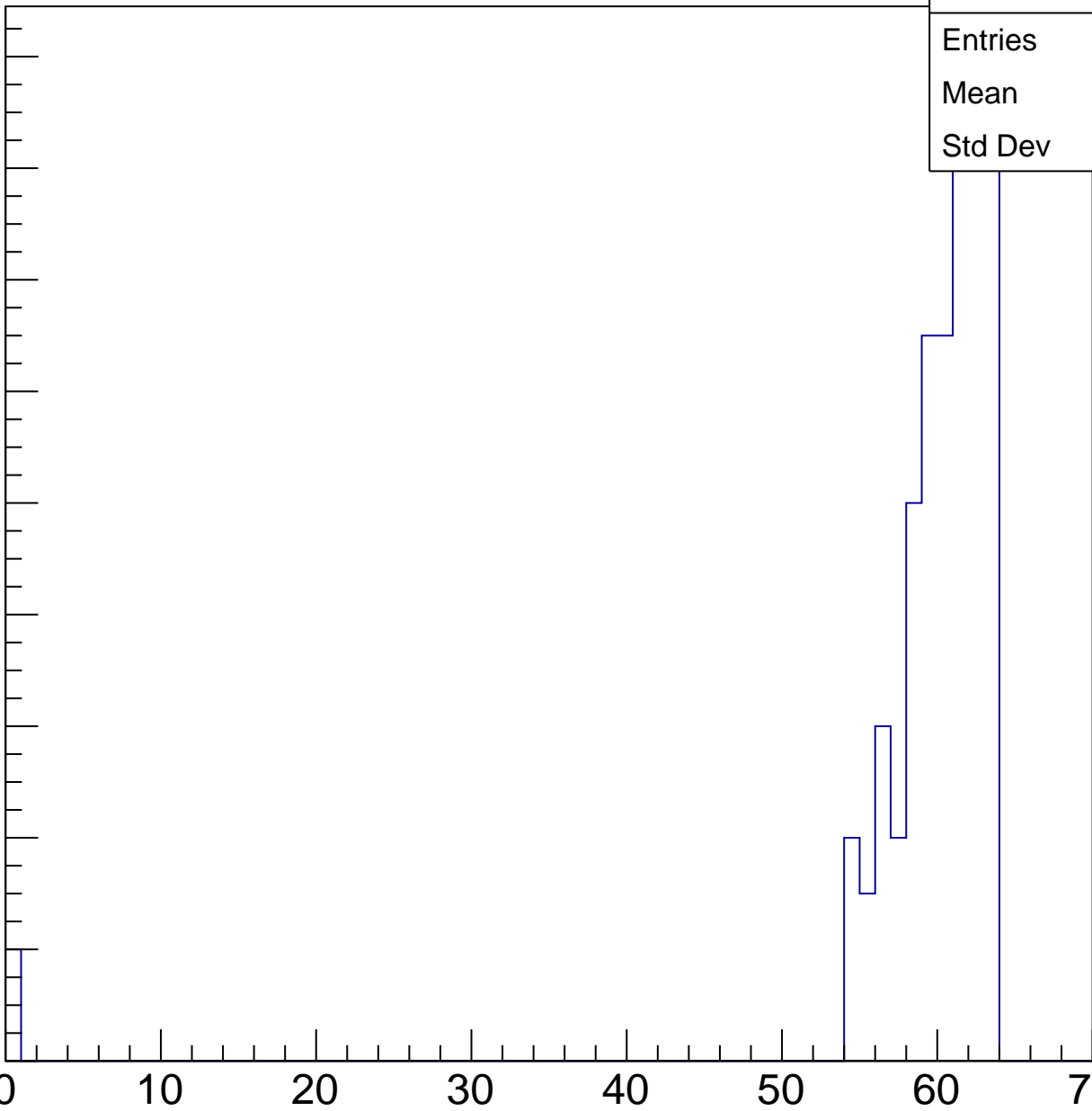
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	105
Mean	58.8
Std Dev	8.561

ampl



B1L001S, U25-ch106, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch106, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch107, adc0

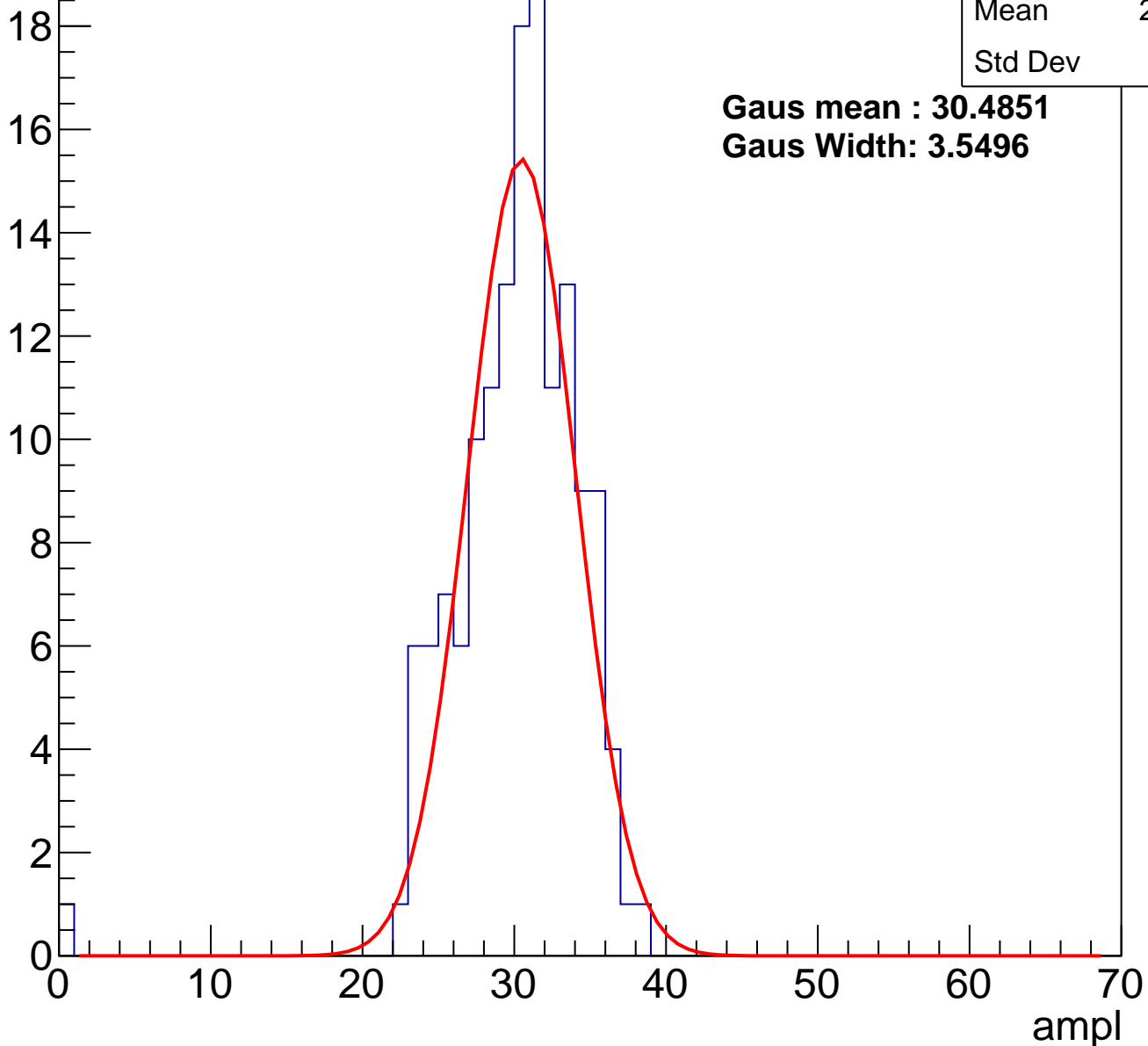
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.73
Std Dev	4.29

Gaus mean : 30.4851

Gaus Width: 3.5496

Entry



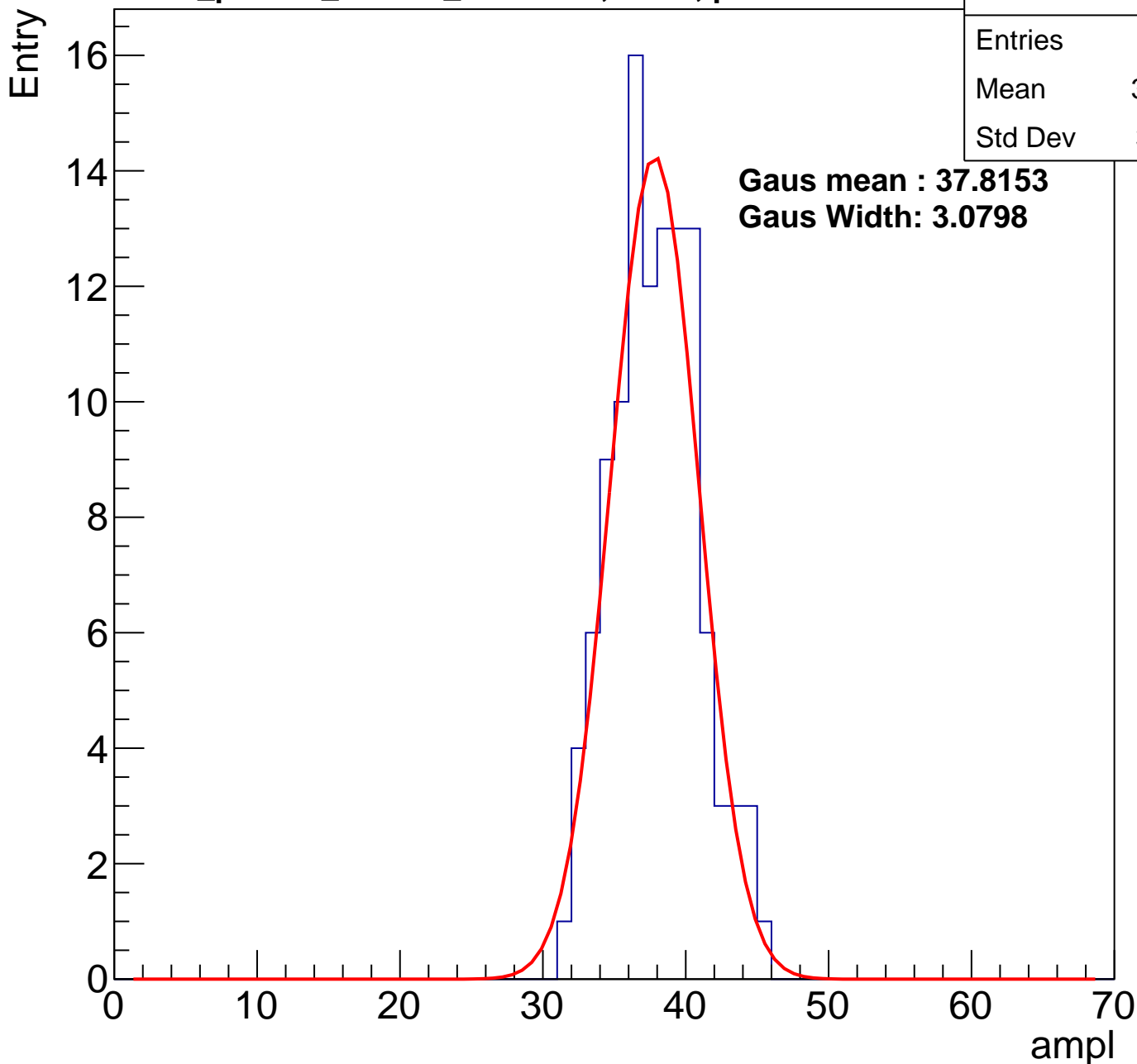
B1L001S, U25-ch107, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	37.45
Std Dev	3.001

Gaus mean : 37.8153

Gaus Width: 3.0798



B1L001S, U25-ch107, adc2

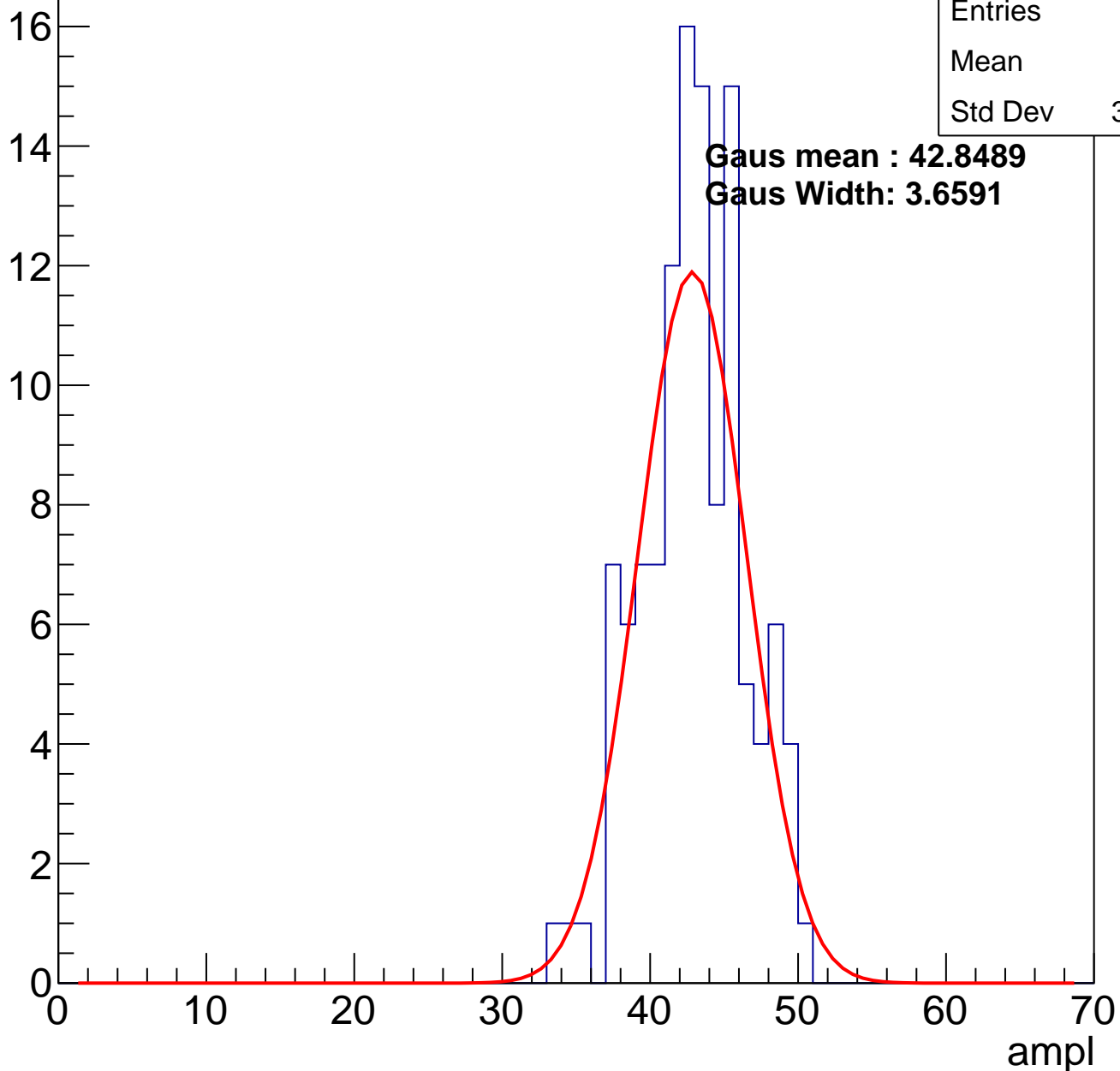
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	42.5
Std Dev	3.445

Gaus mean : 42.8489

Gaus Width: 3.6591

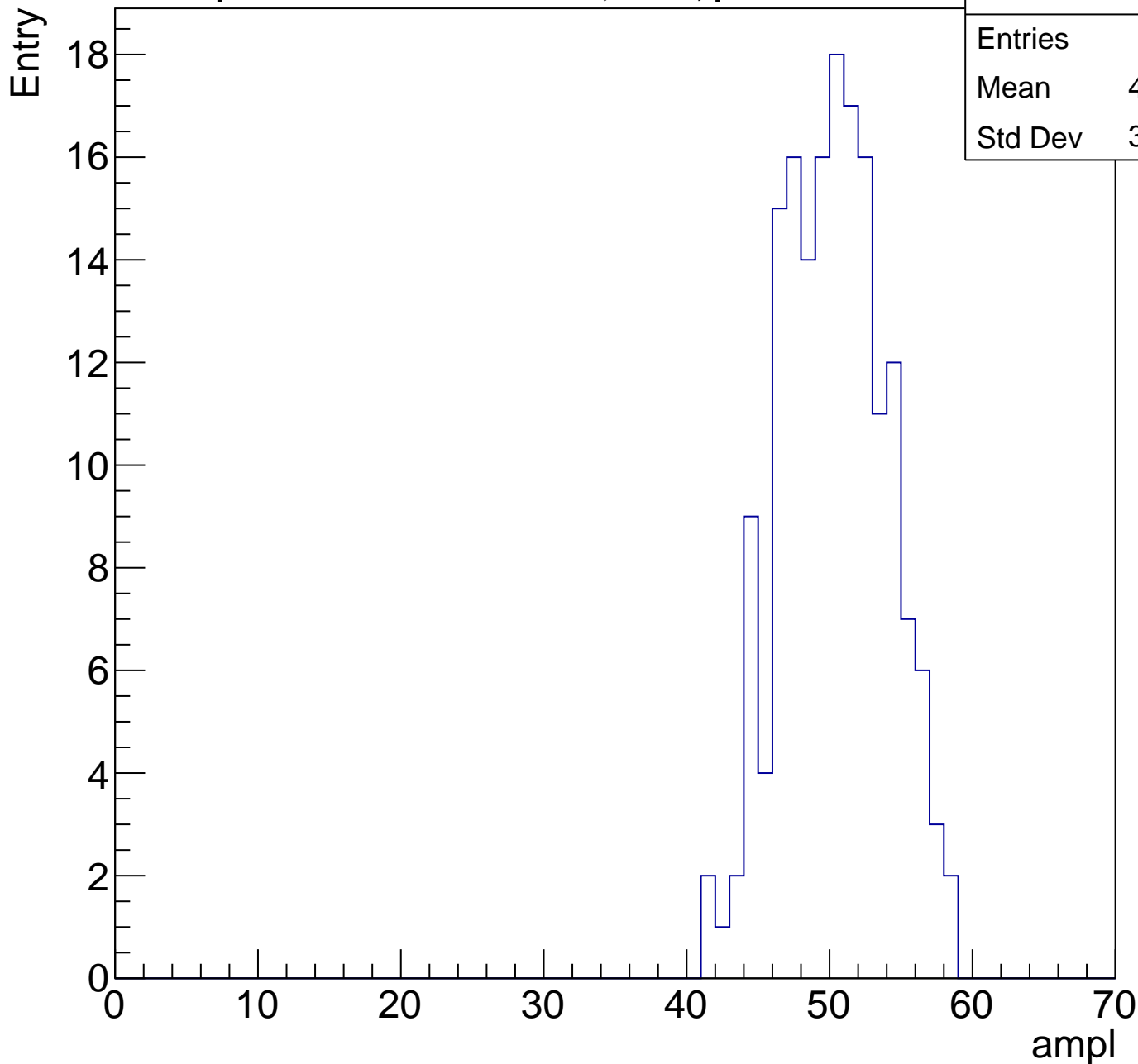
Entry



B1L001S, U25-ch107, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	171
Mean	49.84
Std Dev	3.617



B1L001S, U25-ch107, adc4

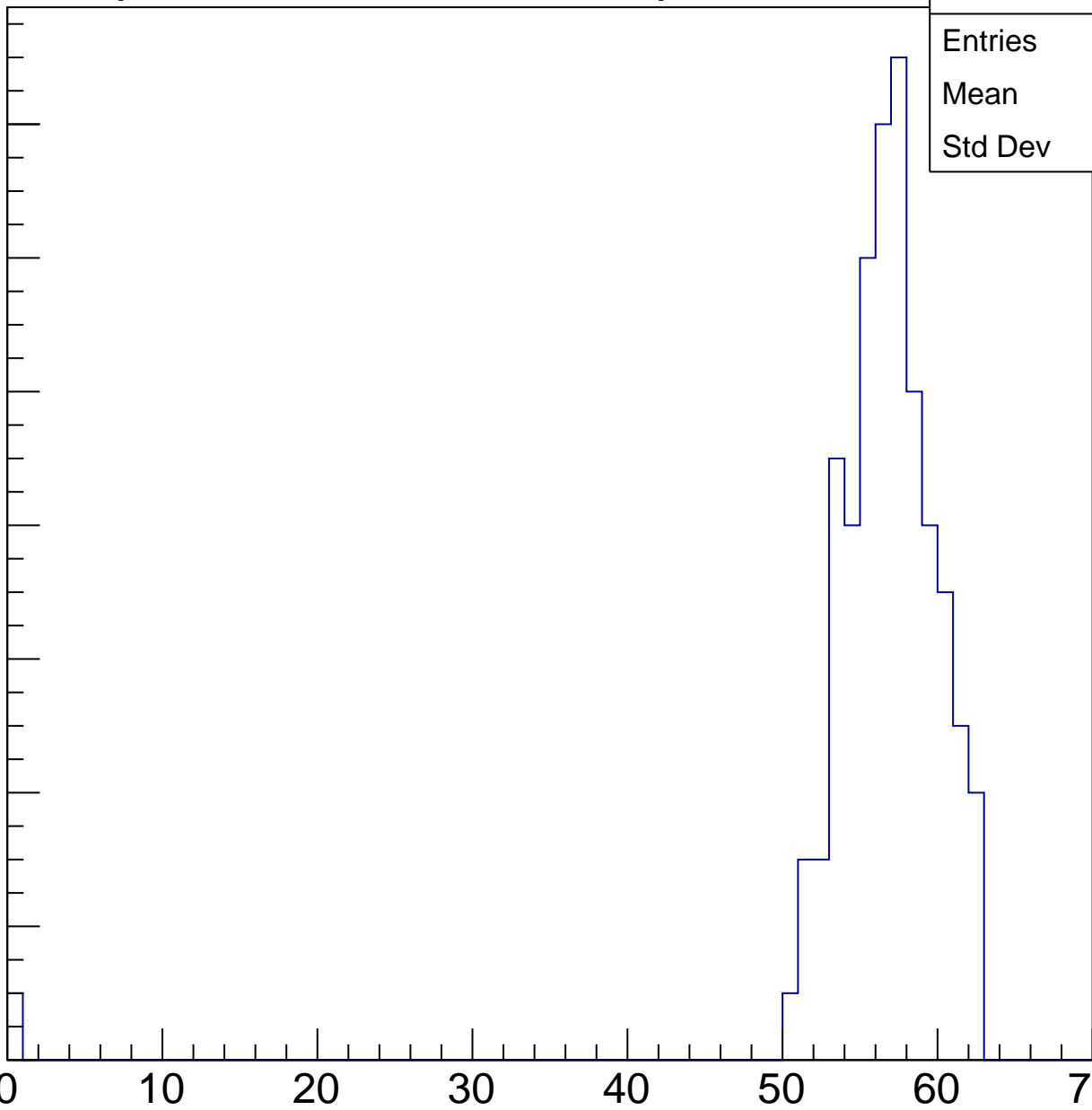
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	100
Mean	55.92
Std Dev	6.267

ampl



B1L001S, U25-ch107, adc5

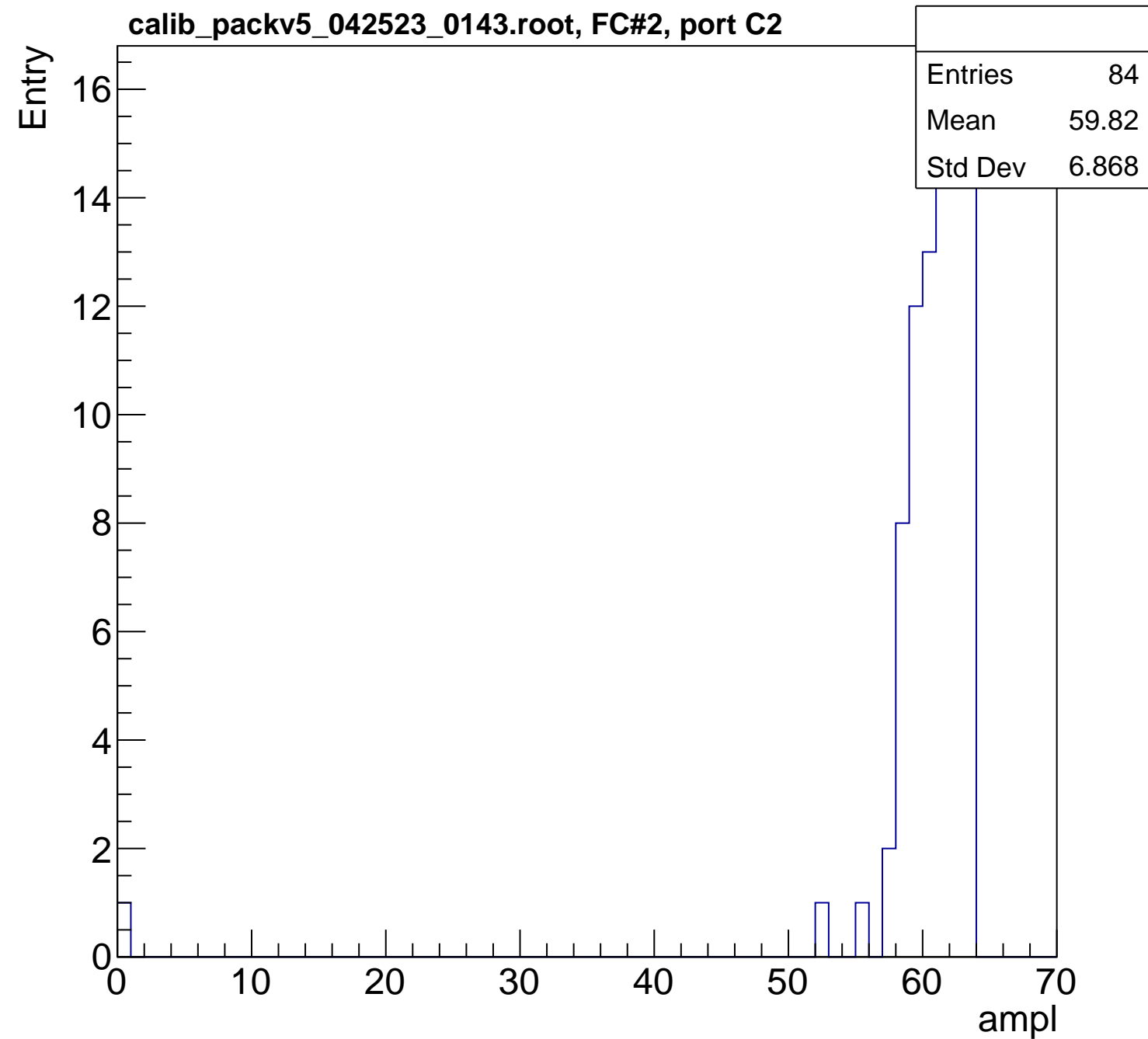
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.82
Std Dev	6.868

ampl



B1L001S, U25-ch107, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch107, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U25-ch108, adc0

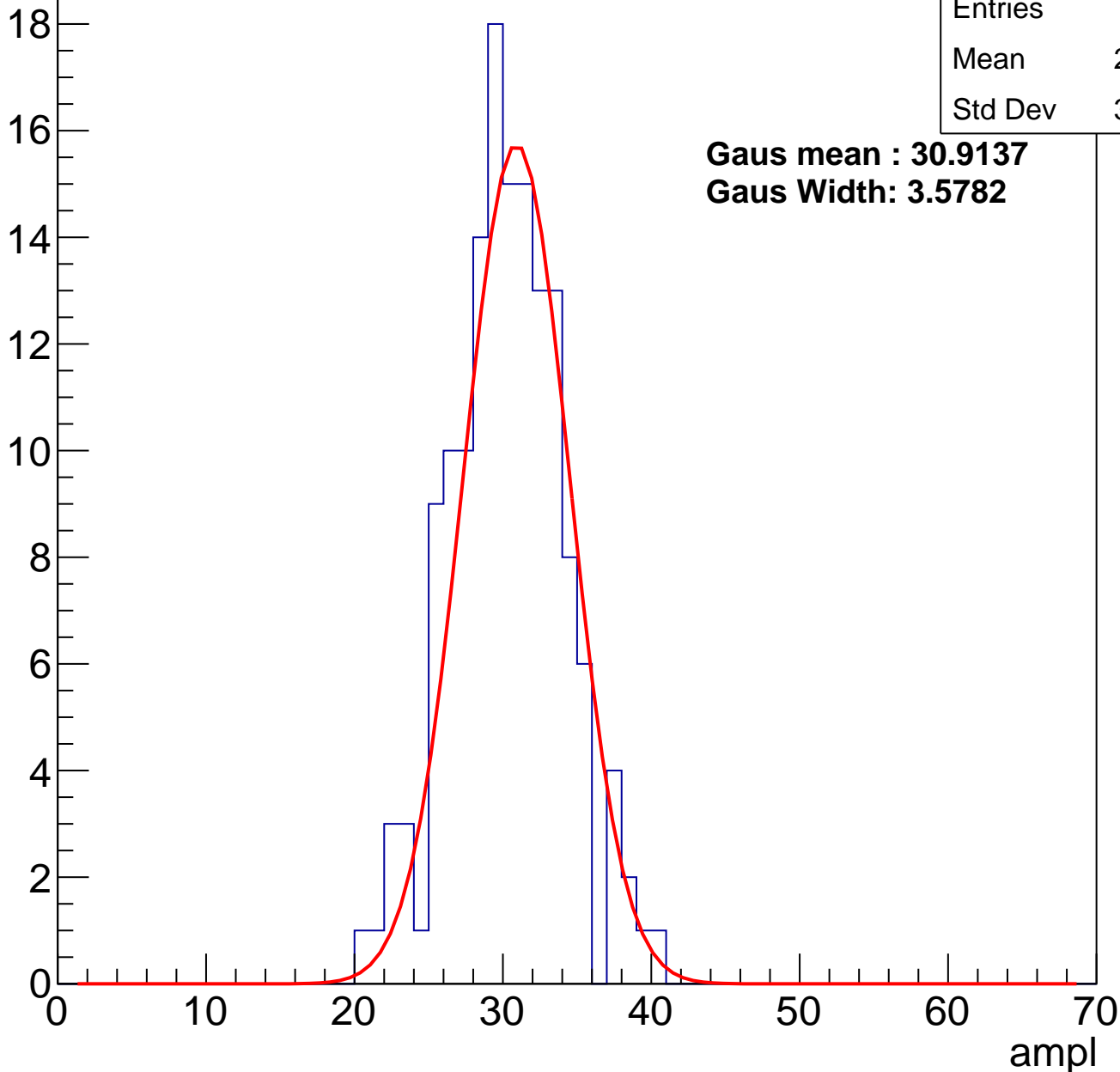
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	29.82
Std Dev	3.738

Gaus mean : 30.9137

Gaus Width: 3.5782

Entry



B1L001S, U25-ch108, adc1

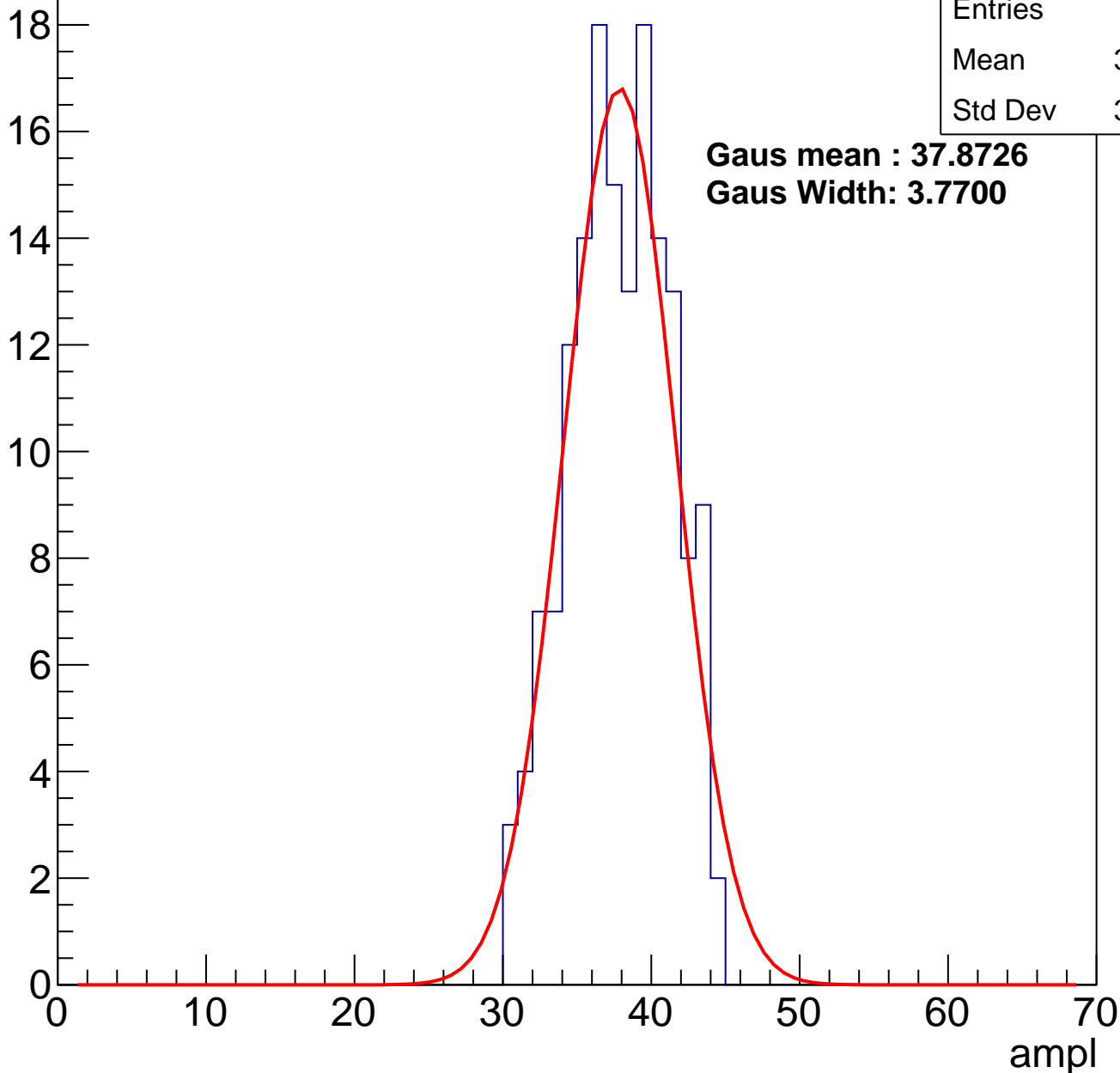
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	37.39
Std Dev	3.365

Gaus mean : 37.8726

Gaus Width: 3.7700

Entry



B1L001S, U25-ch108, adc2

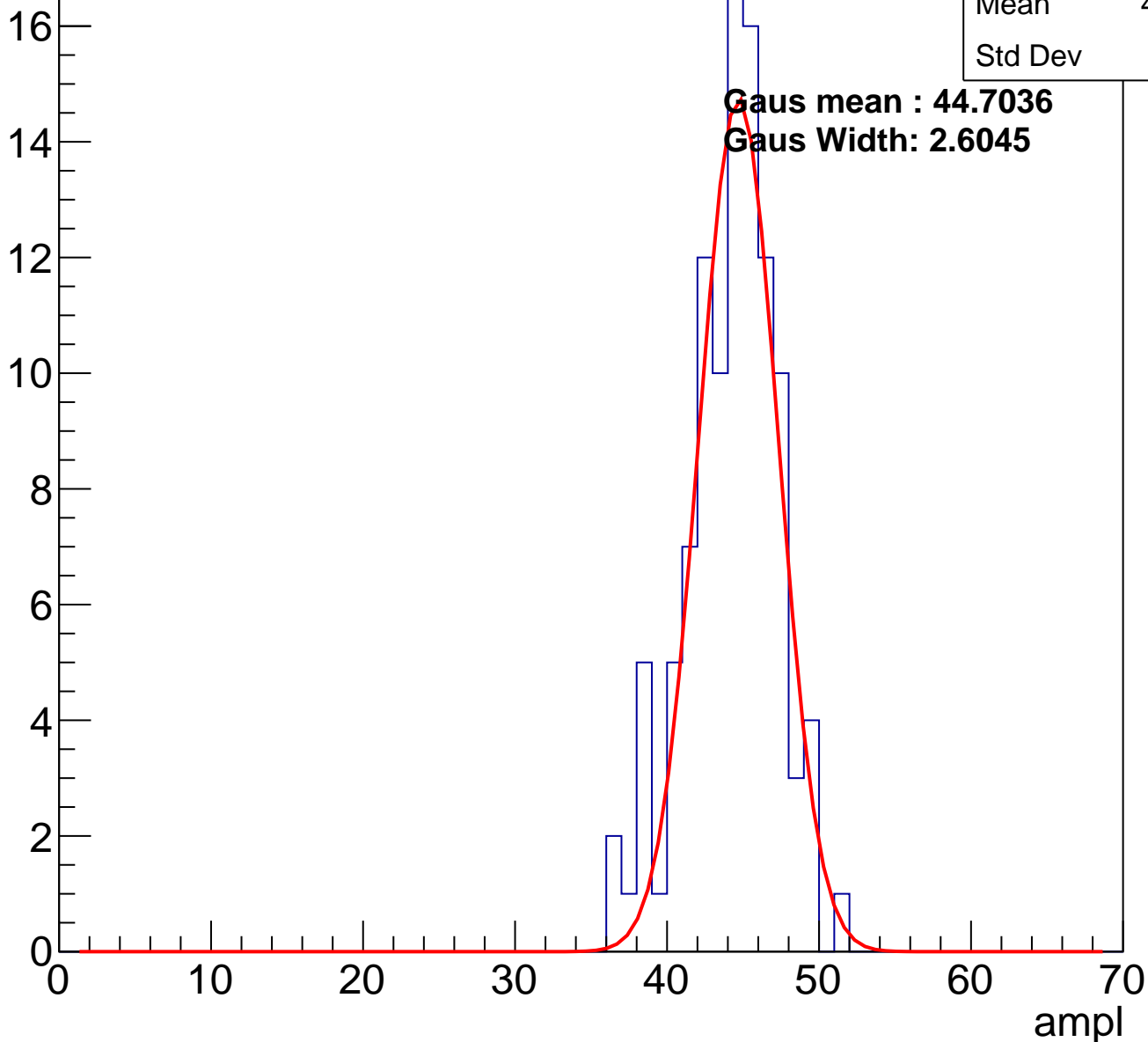
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	106
Mean	43.77
Std Dev	2.96

Gaus mean : 44.7036

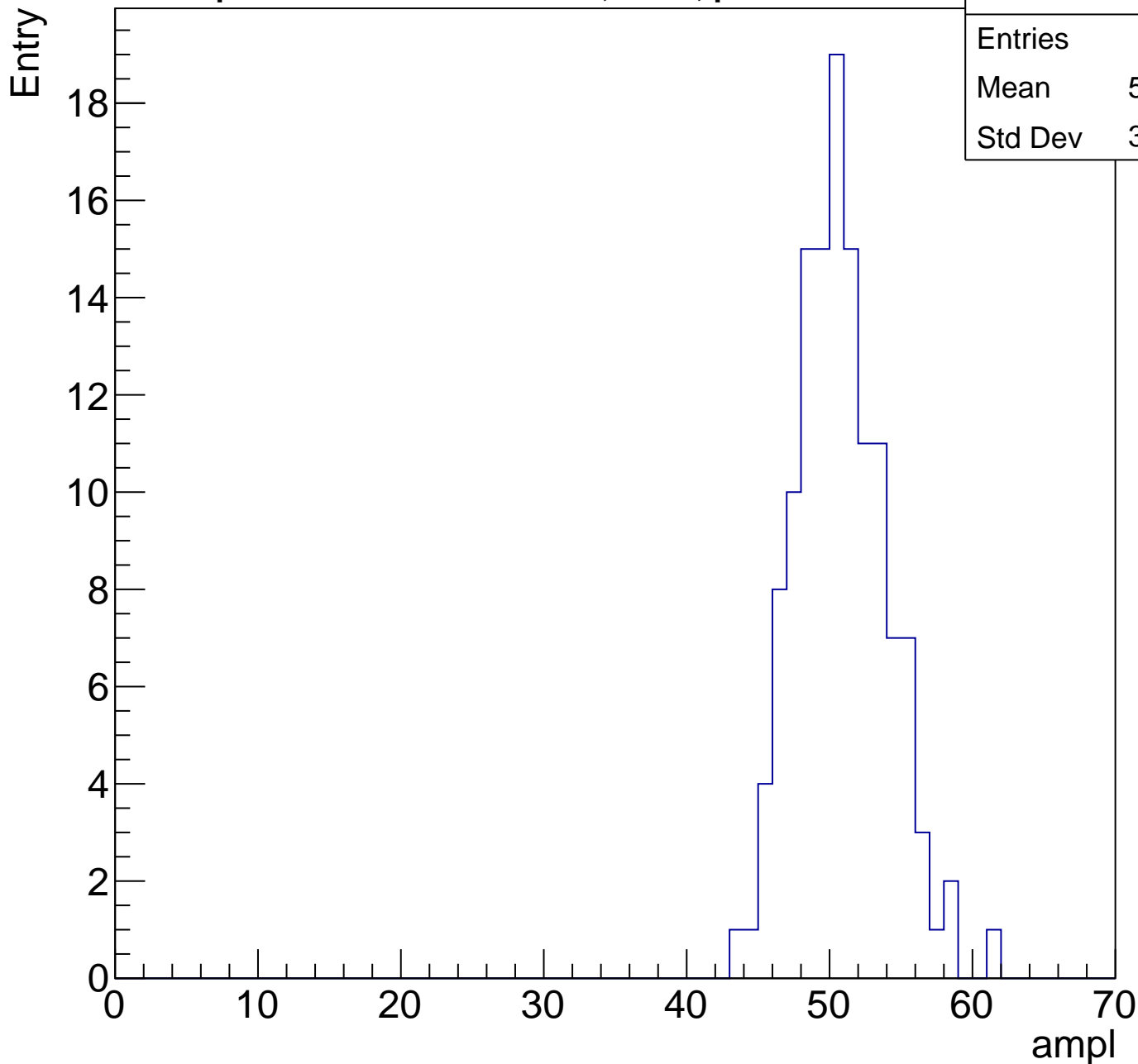
Gaus Width: 2.6045



B1L001S, U25-ch108, adc3

calib_packv5_042523_0143.root, FC#2, port C2

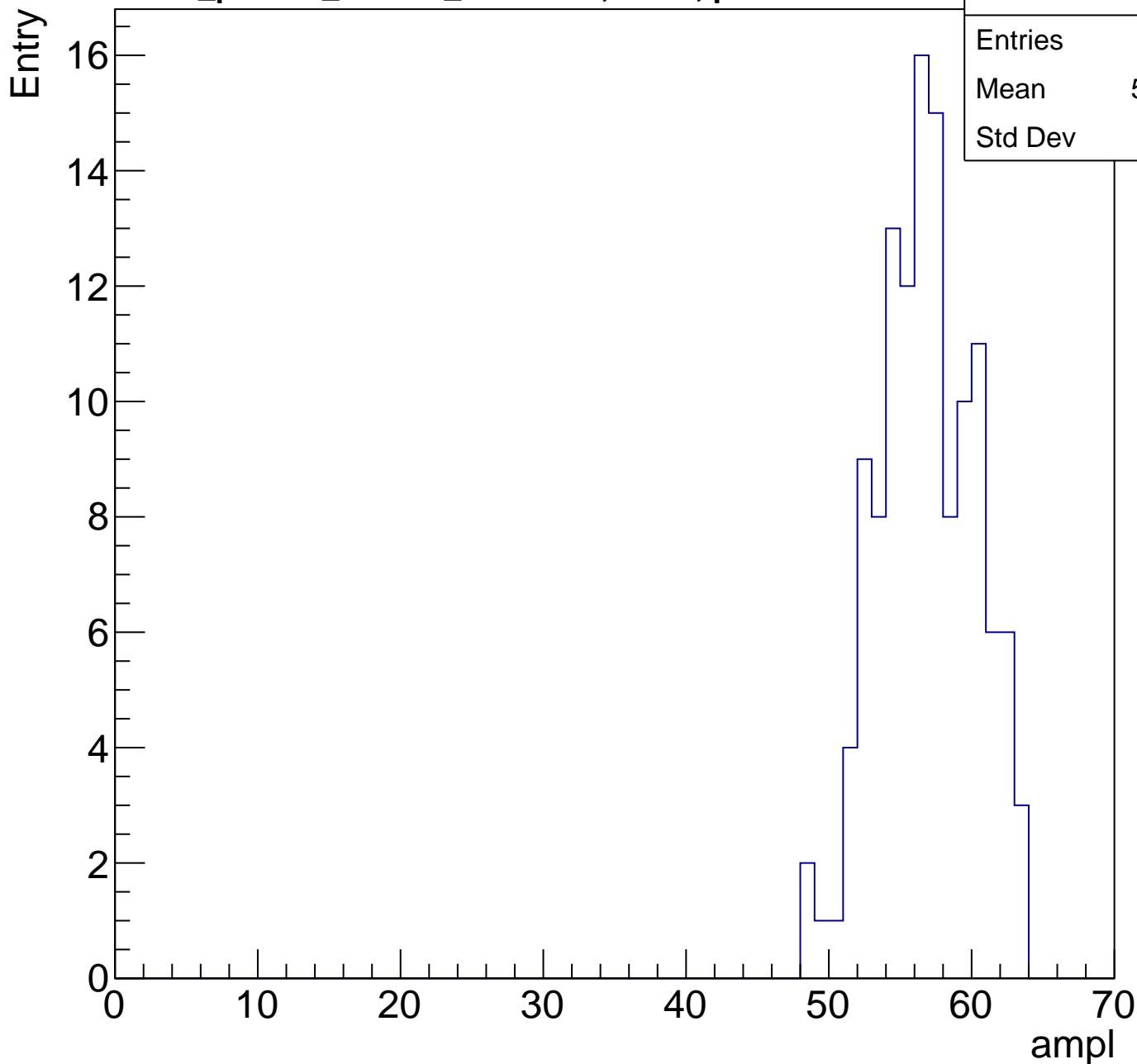
Entries	131
Mean	50.34
Std Dev	3.167



B1L001S, U25-ch108, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	56.36
Std Dev	3.35



B1L001S, U25-ch108, adc5

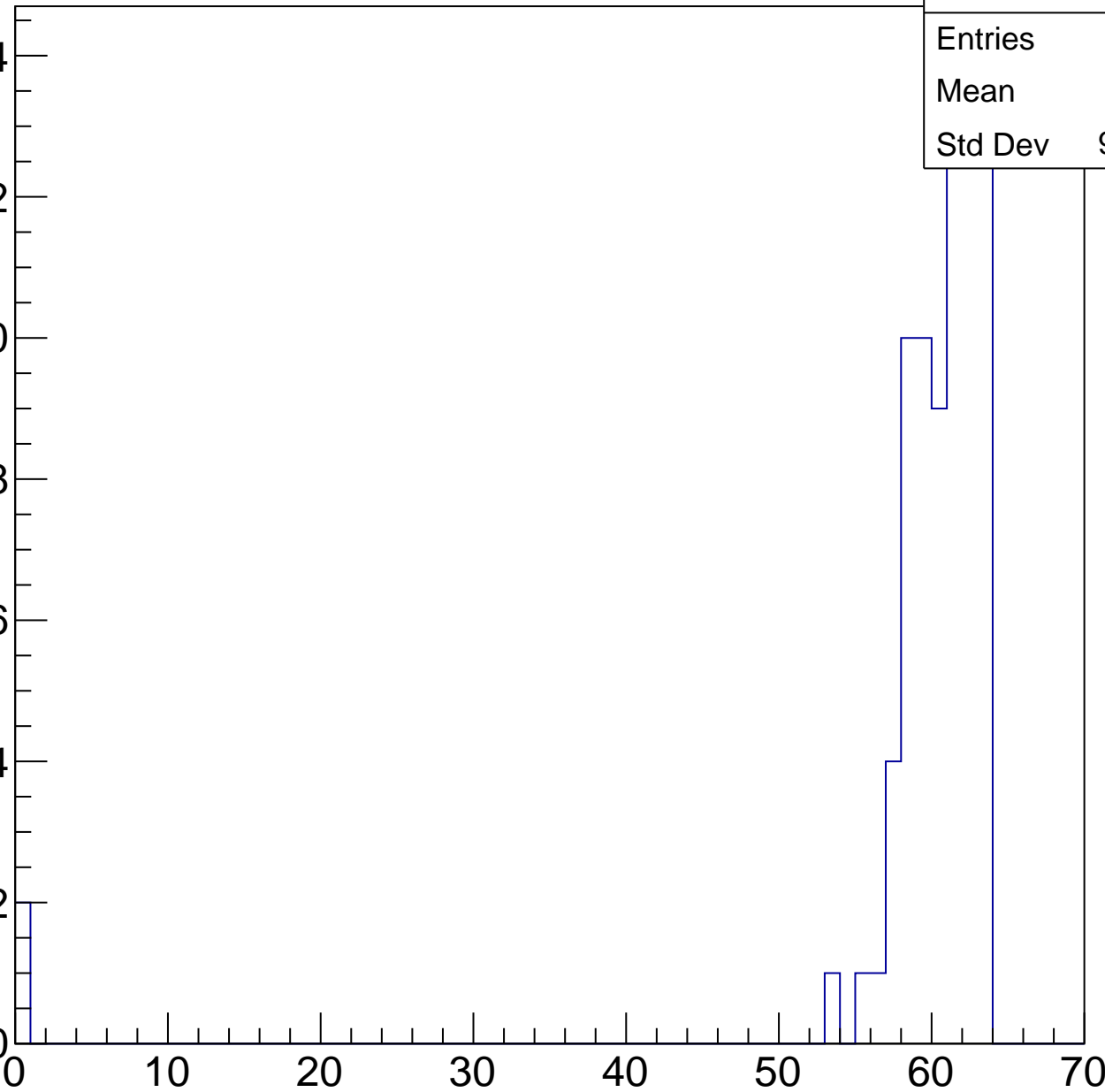
calib_packv5_042523_0143.root, FC#2, port C2

Entries	79
Mean	58.8
Std Dev	9.715

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch108, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch108, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch109, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	29.65
Std Dev	3.769

Gaus mean : 30.0195

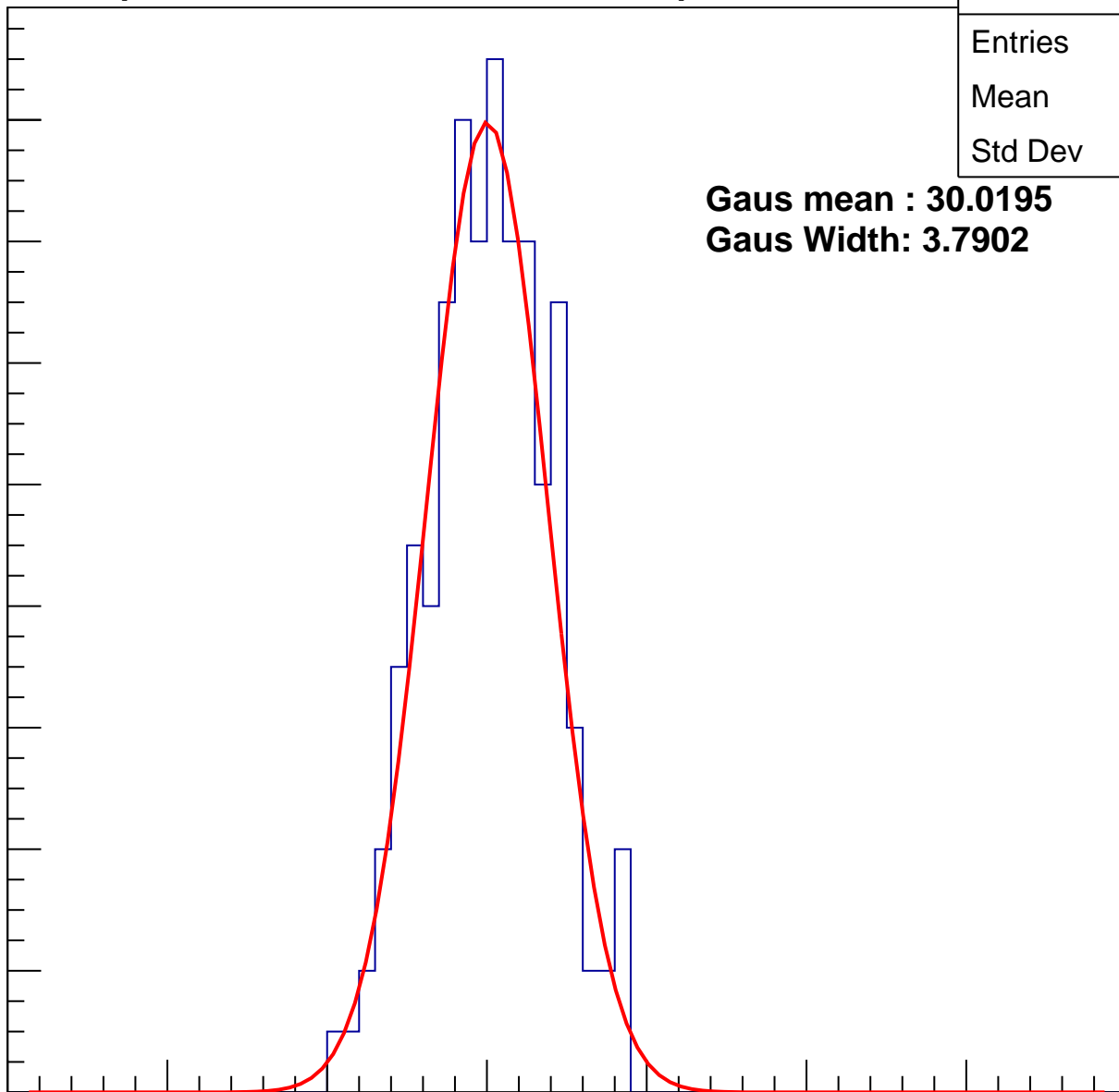
Gaus Width: 3.7902

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch109, adc1

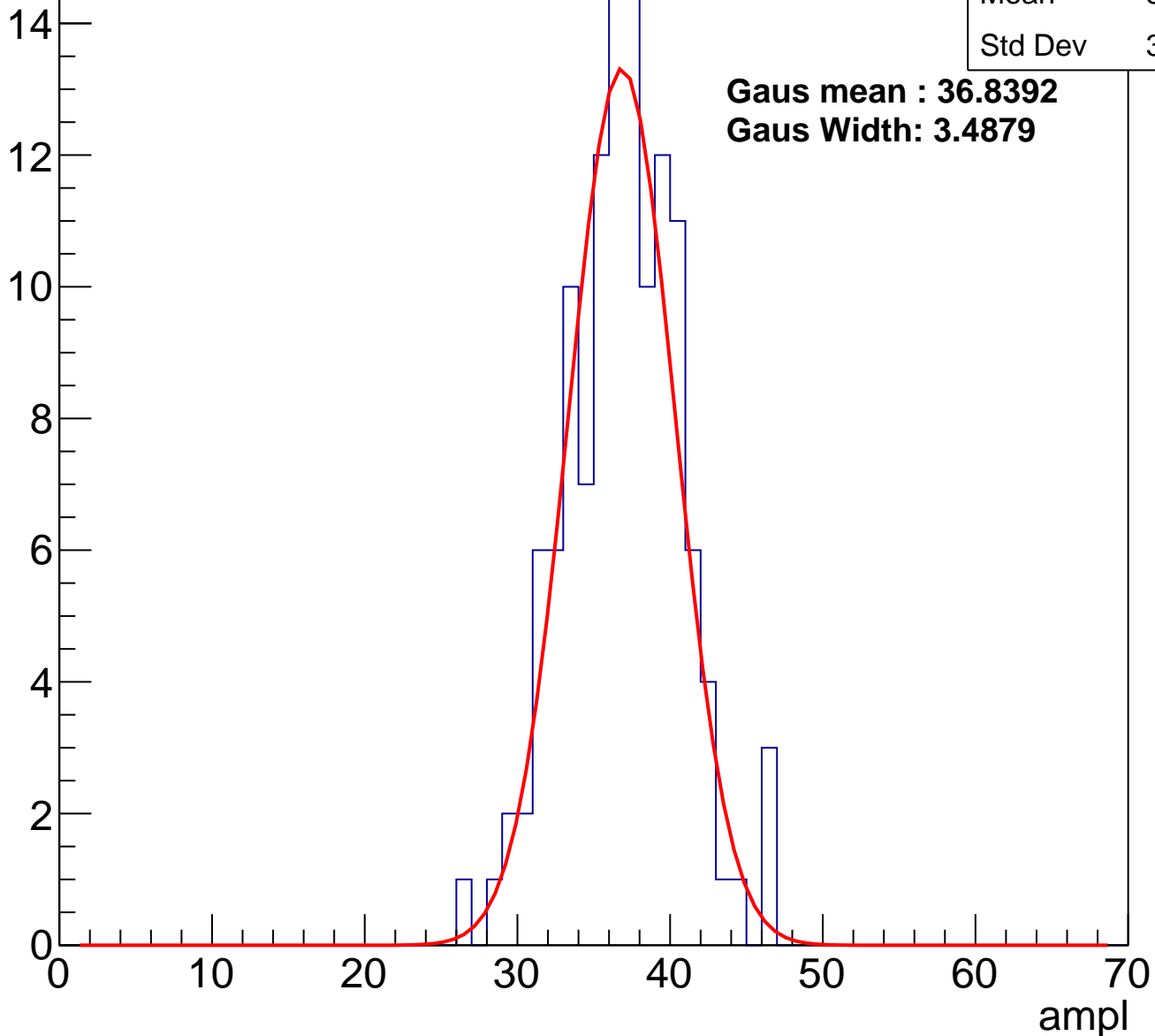
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.48
Std Dev	3.717

Gaus mean : 36.8392

Gaus Width: 3.4879

Entry



B1L001S, U25-ch109, adc2

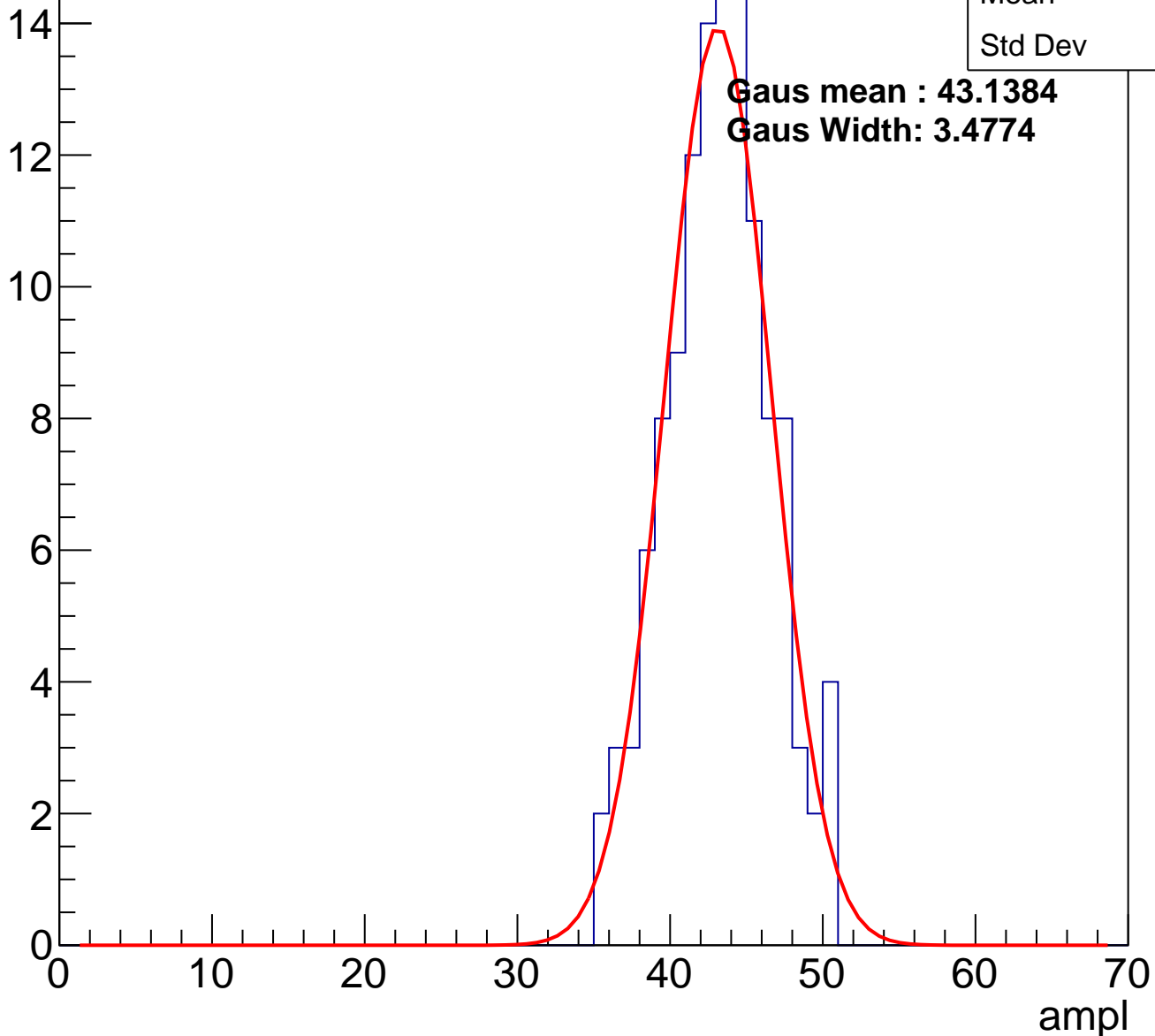
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	42.72
Std Dev	3.37

Gaus mean : 43.1384

Gaus Width: 3.4774

Entry



B1L001S, U25-ch109, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

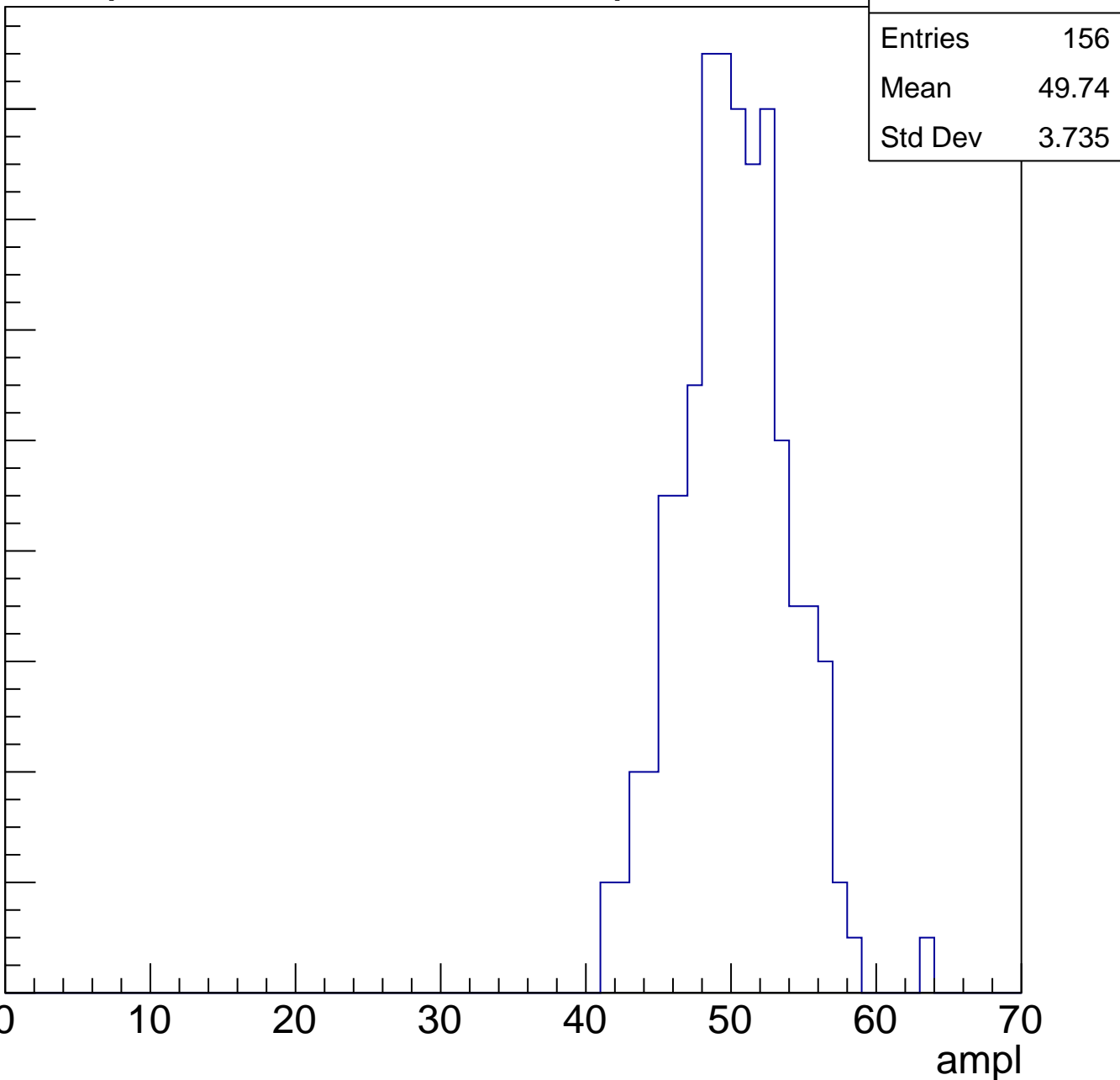
156

Mean

49.74

Std Dev

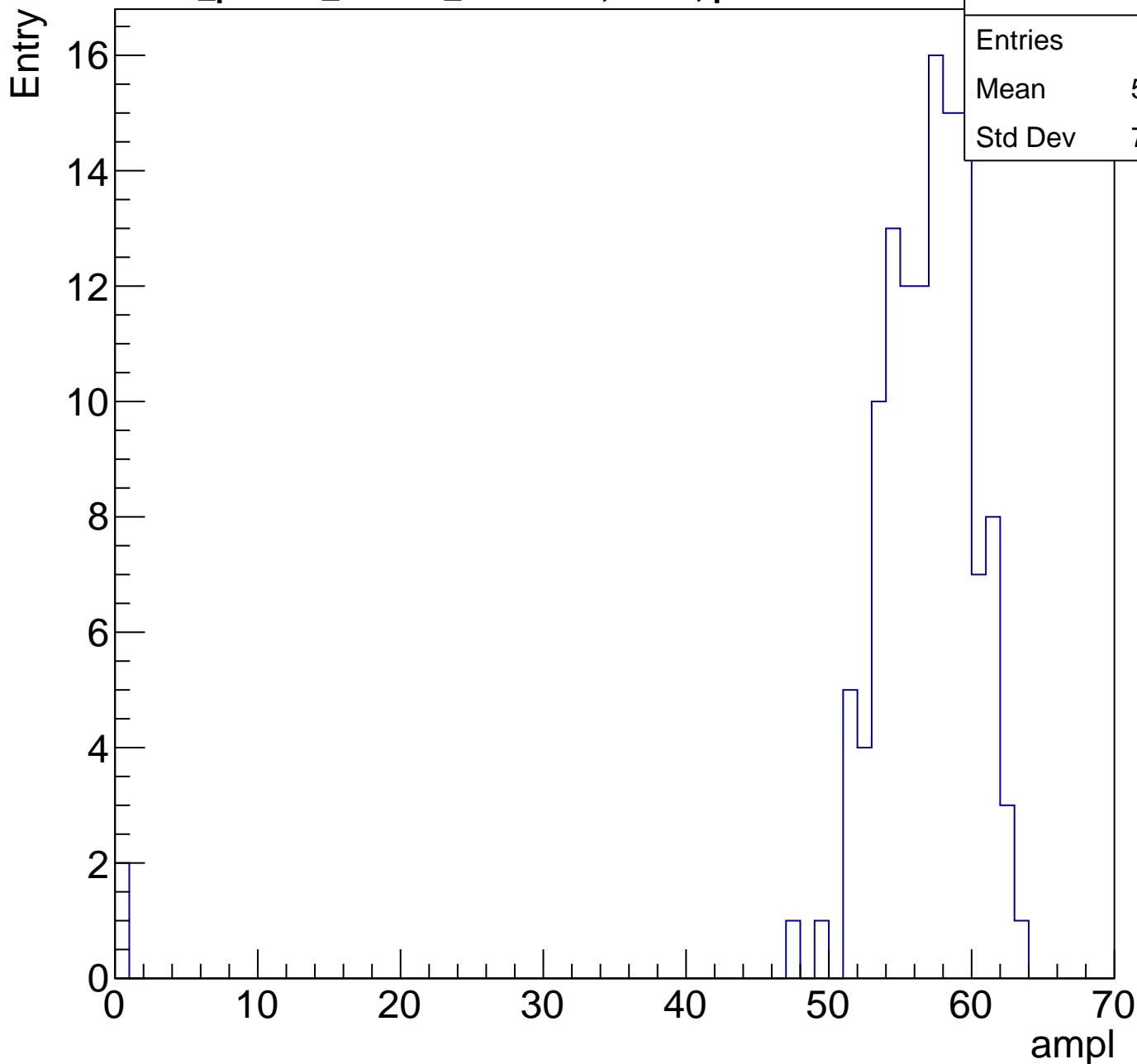
3.735



B1L001S, U25-ch109, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	55.58
Std Dev	7.695



B1L001S, U25-ch109, adc5

calib_packv5_042523_0143.root, FC#2, port C2

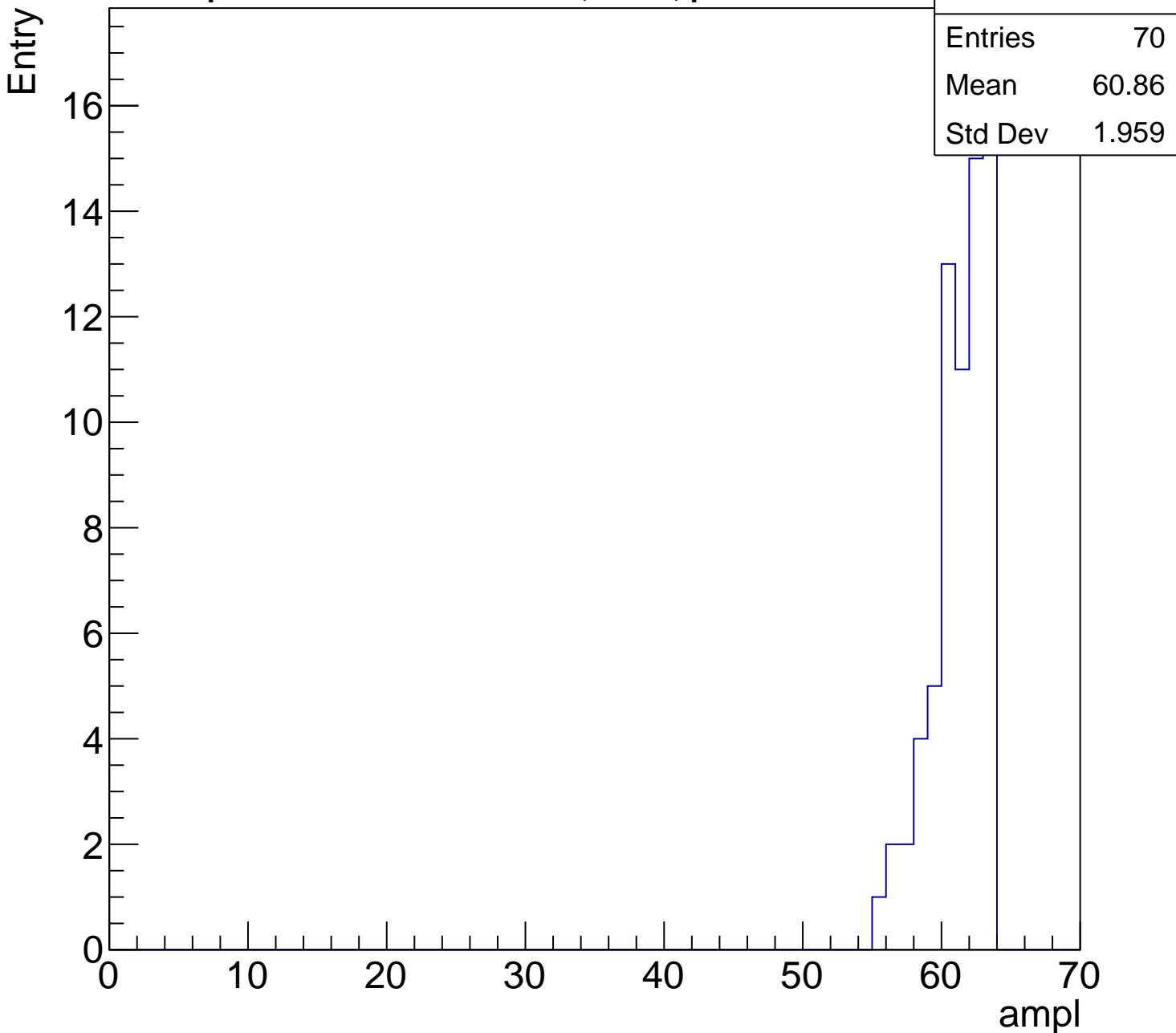
Entries	70
Mean	60.86
Std Dev	1.959

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch109, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	62
Std Dev	0.7071

B1L001S, U25-ch109, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch110, adc0

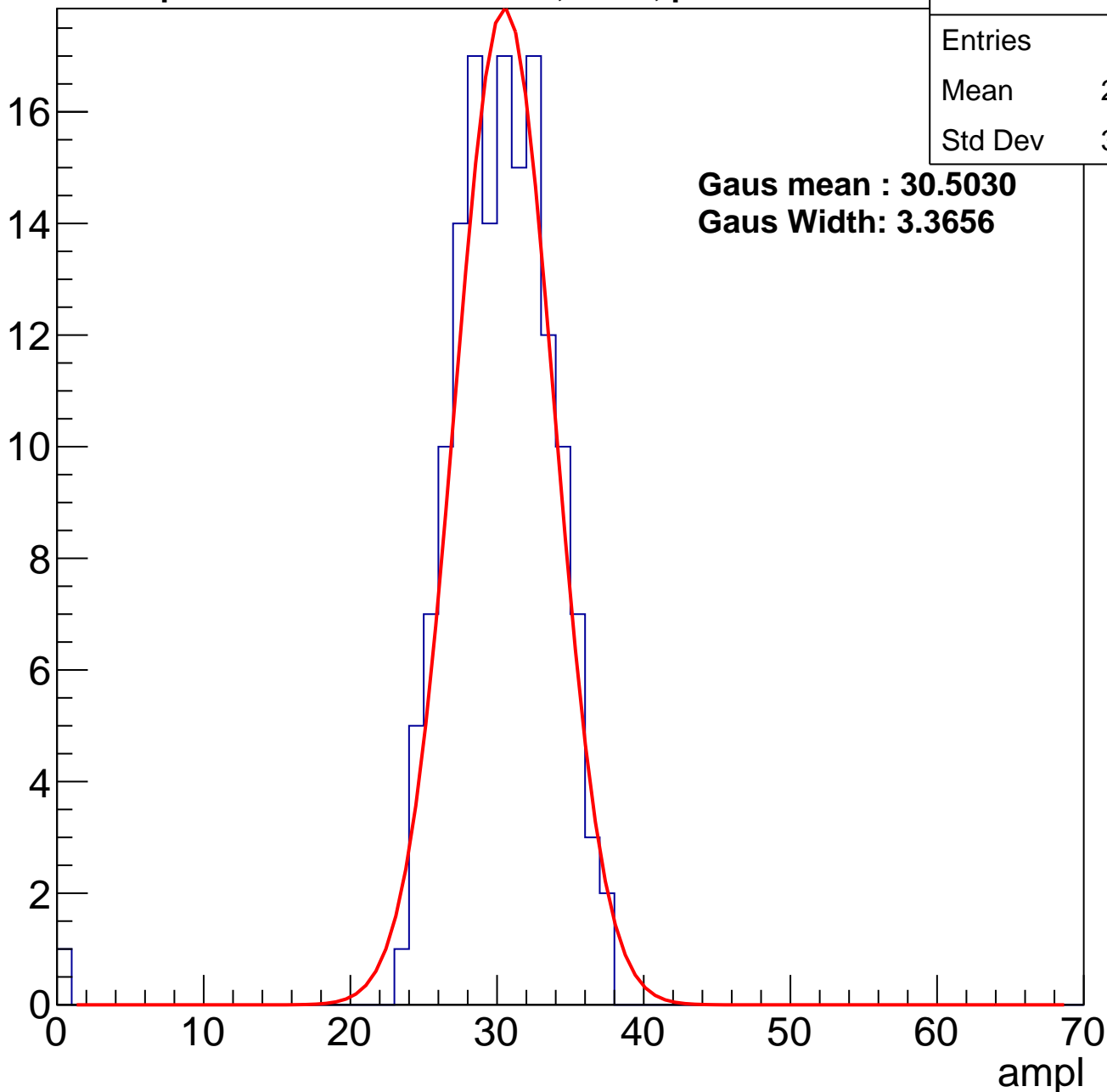
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	29.74
Std Dev	3.968

Gaus mean : 30.5030

Gaus Width: 3.3656

Entry



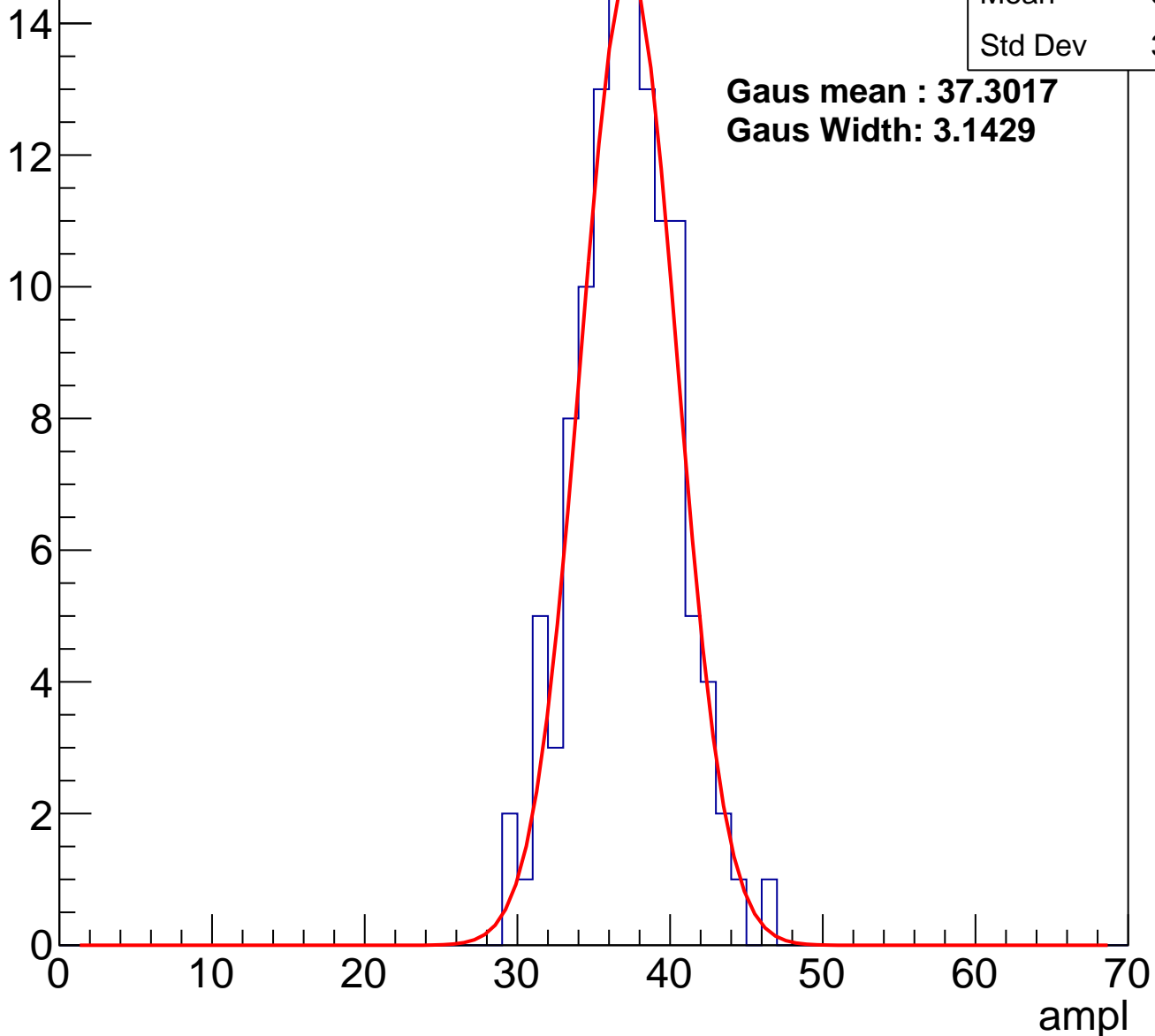
B1L001S, U25-ch110, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	36.71
Std Dev	3.221

Gaus mean : 37.3017
Gaus Width: 3.1429

Entry



B1L001S, U25-ch110, adc2

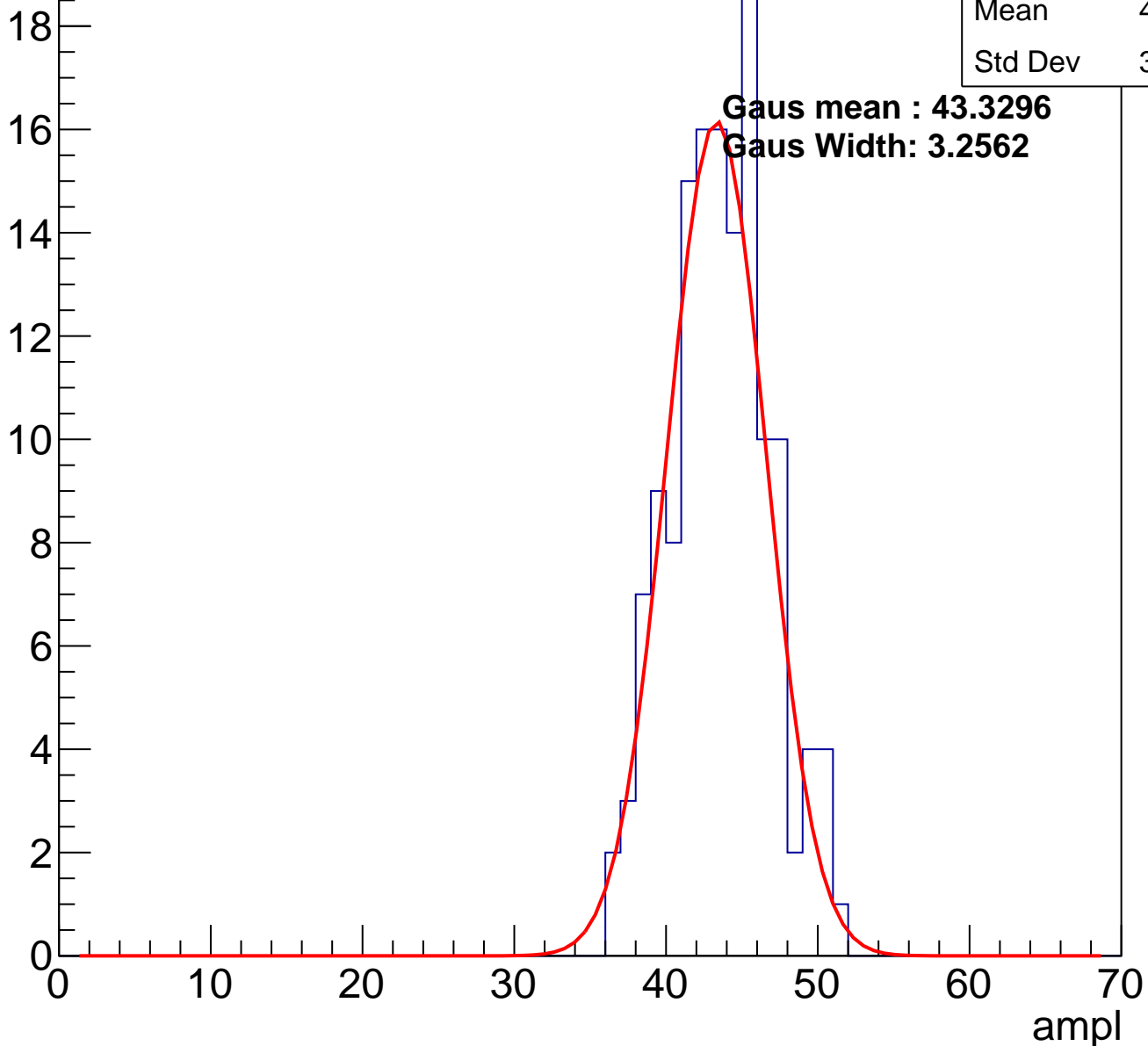
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	43.14
Std Dev	3.234

Gaus mean : 43.3296

Gaus Width: 3.2562

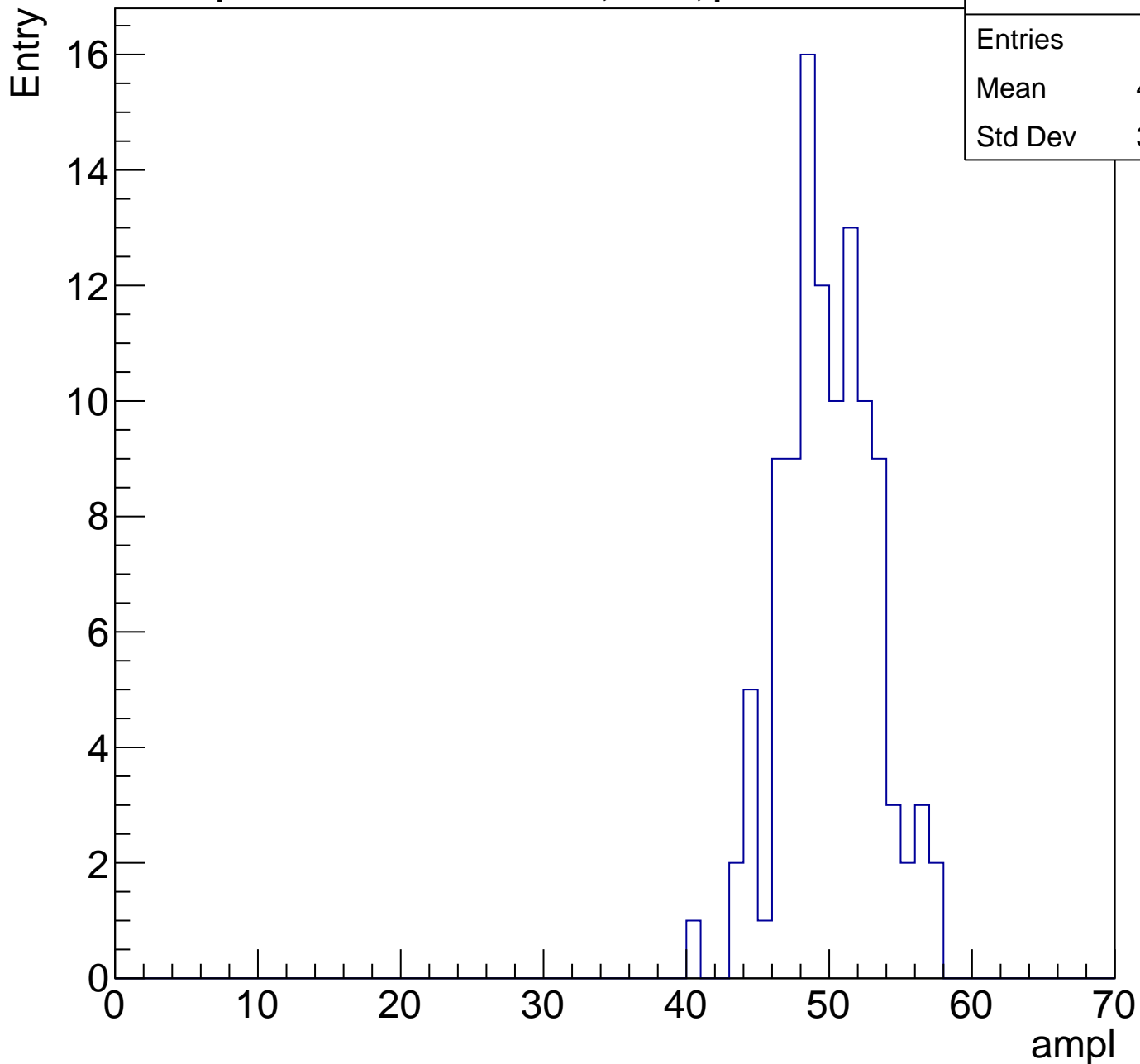
Entry



B1L001S, U25-ch110, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	49.51
Std Dev	3.231



B1L001S, U25-ch110, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	55.26
Std Dev	3.358

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

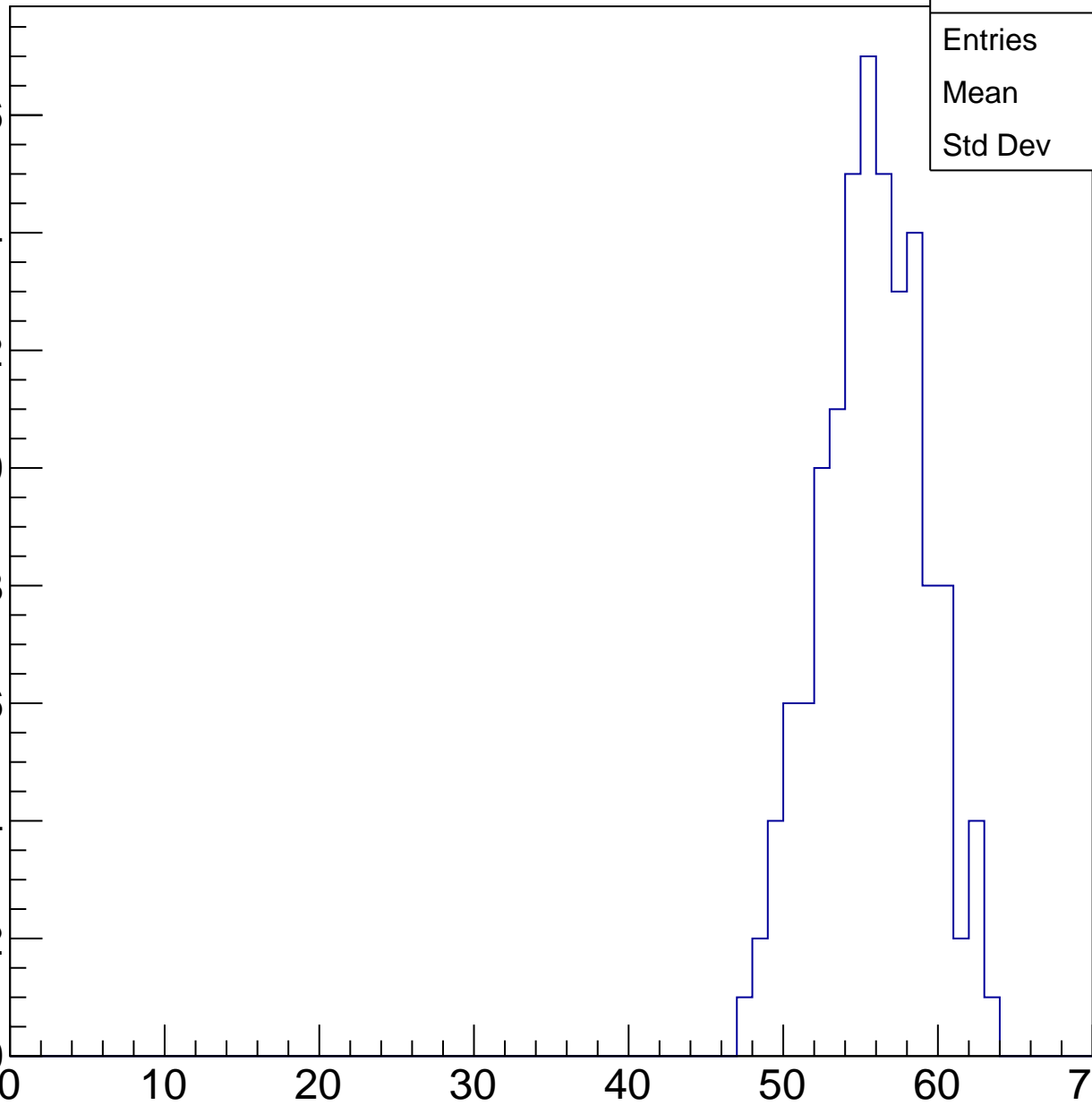
40

50

60

70

ampl



B1L001S, U25-ch110, adc5

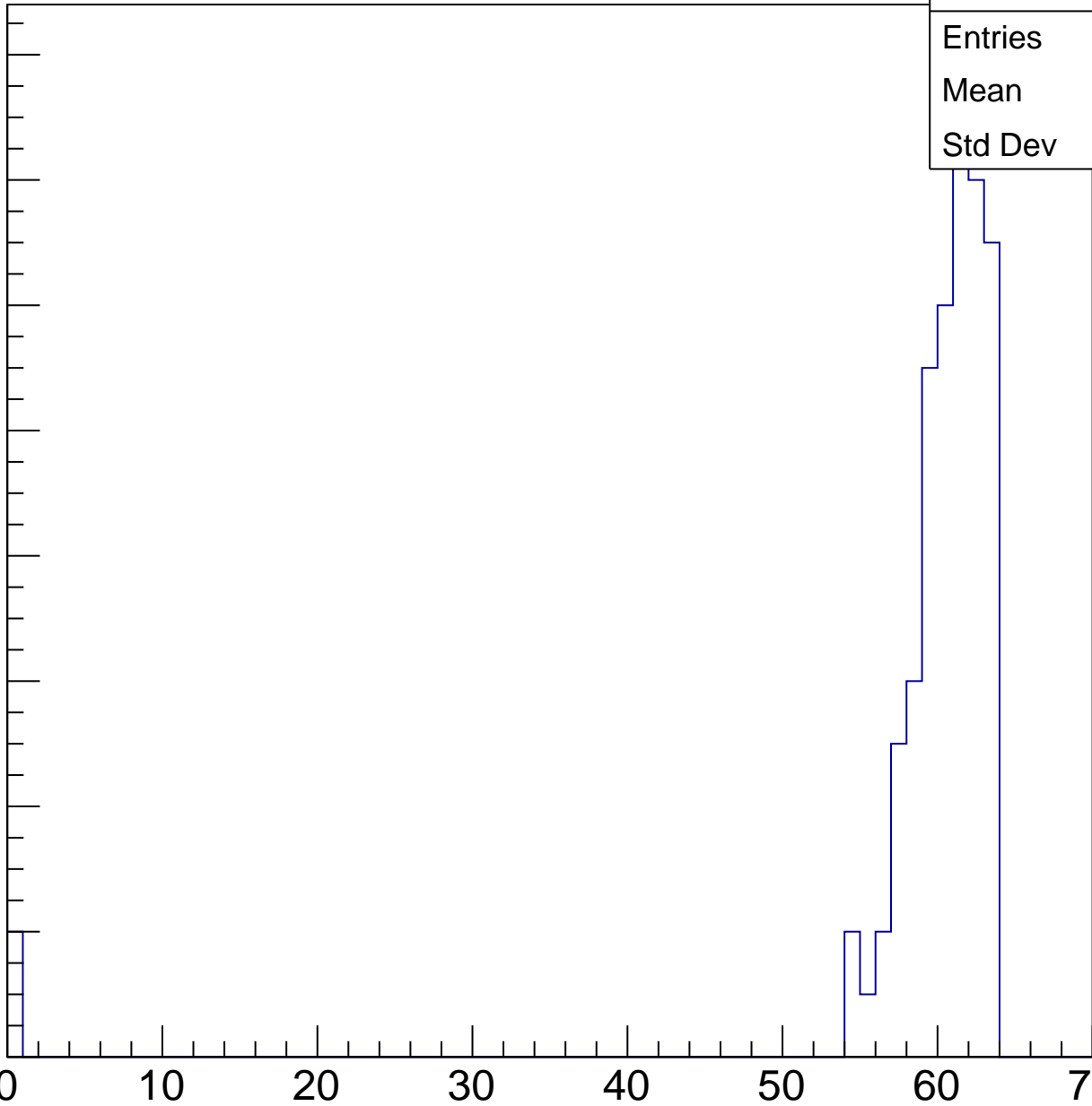
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.81
Std Dev	9.436

ampl

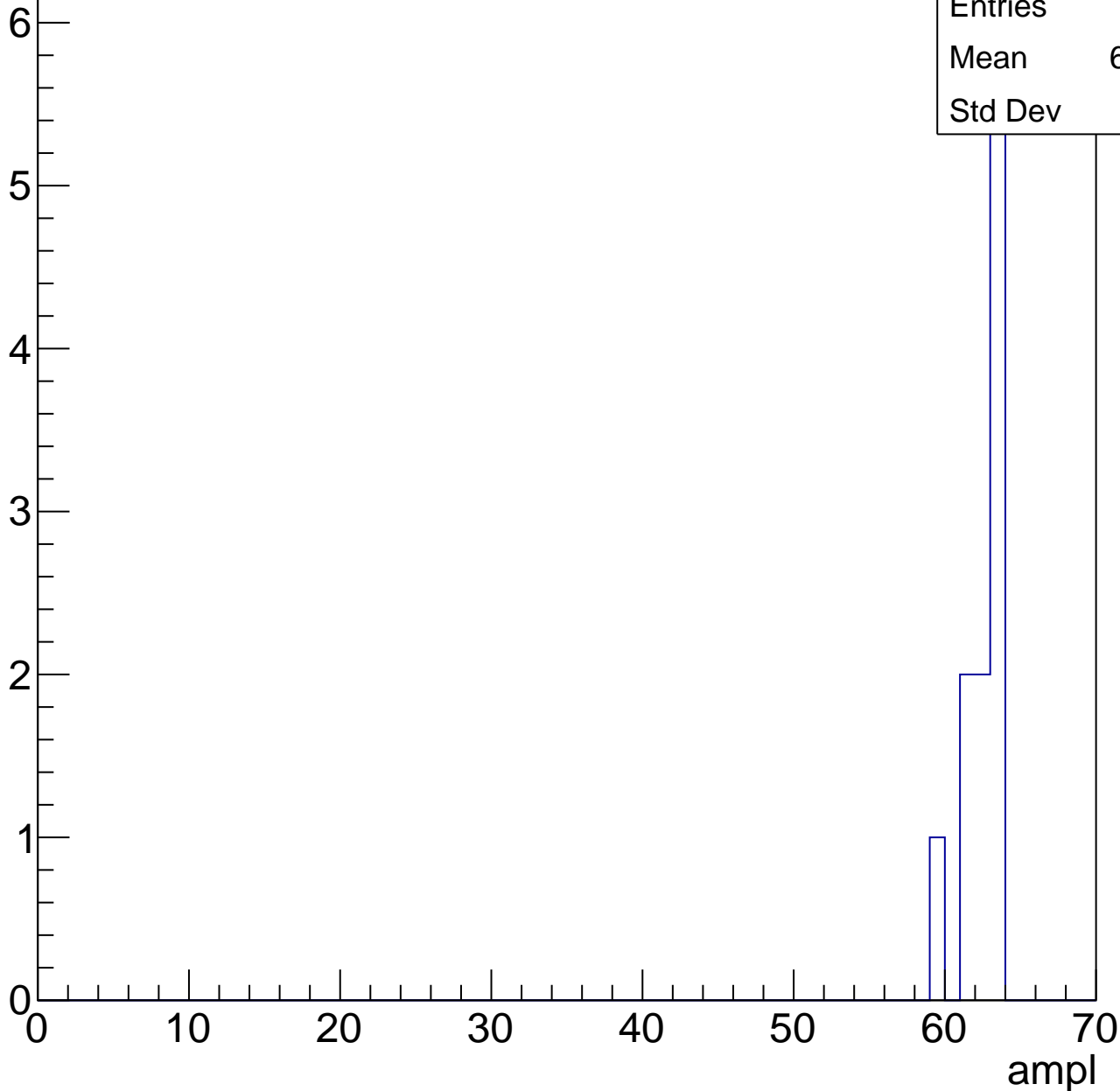


B1L001S, U25-ch110, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.09
Std Dev	1.24



B1L001S, U25-ch110, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	21
Std Dev	1

ampl

B1L001S, U25-ch111, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	168
Mean	30.76
Std Dev	3.699

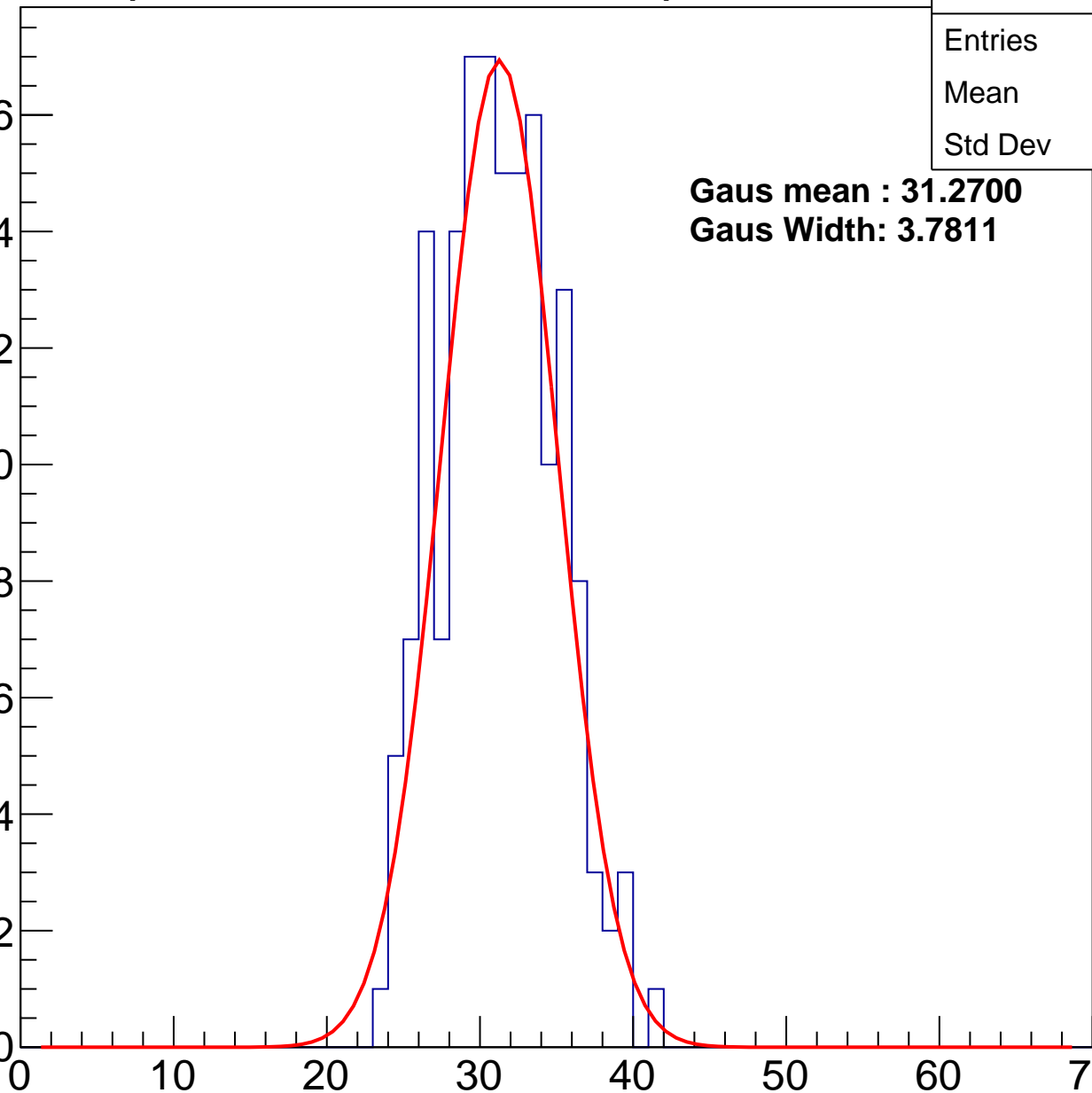
Gaus mean : 31.2700

Gaus Width: 3.7811

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch111, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	38.29
Std Dev	3.855

Gaus mean : 38.9213

Gaus Width: 4.1051

Entry

18
16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U25-ch111, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

118

Mean

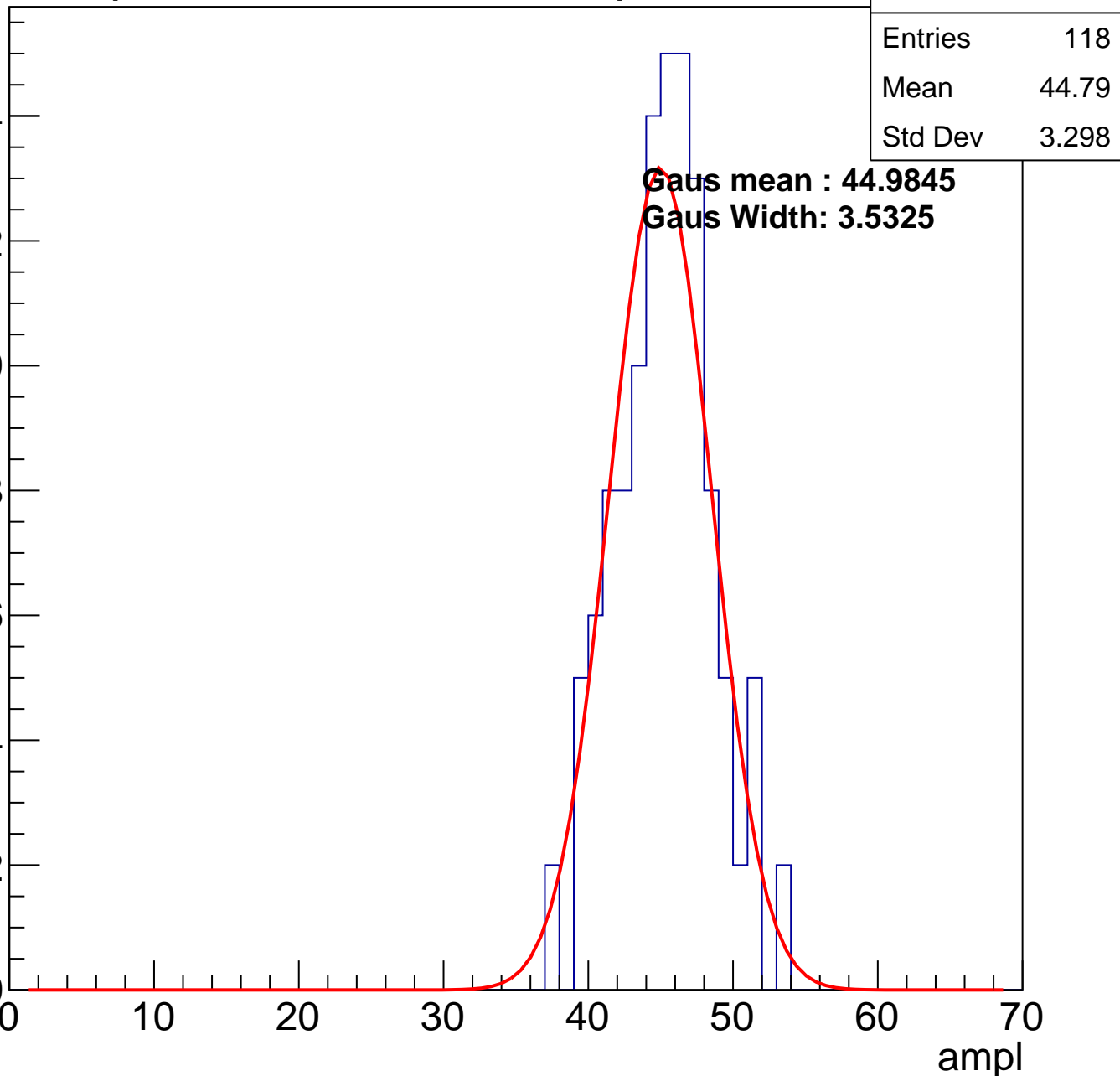
44.79

Std Dev

3.298

Gaus mean : 44.9845

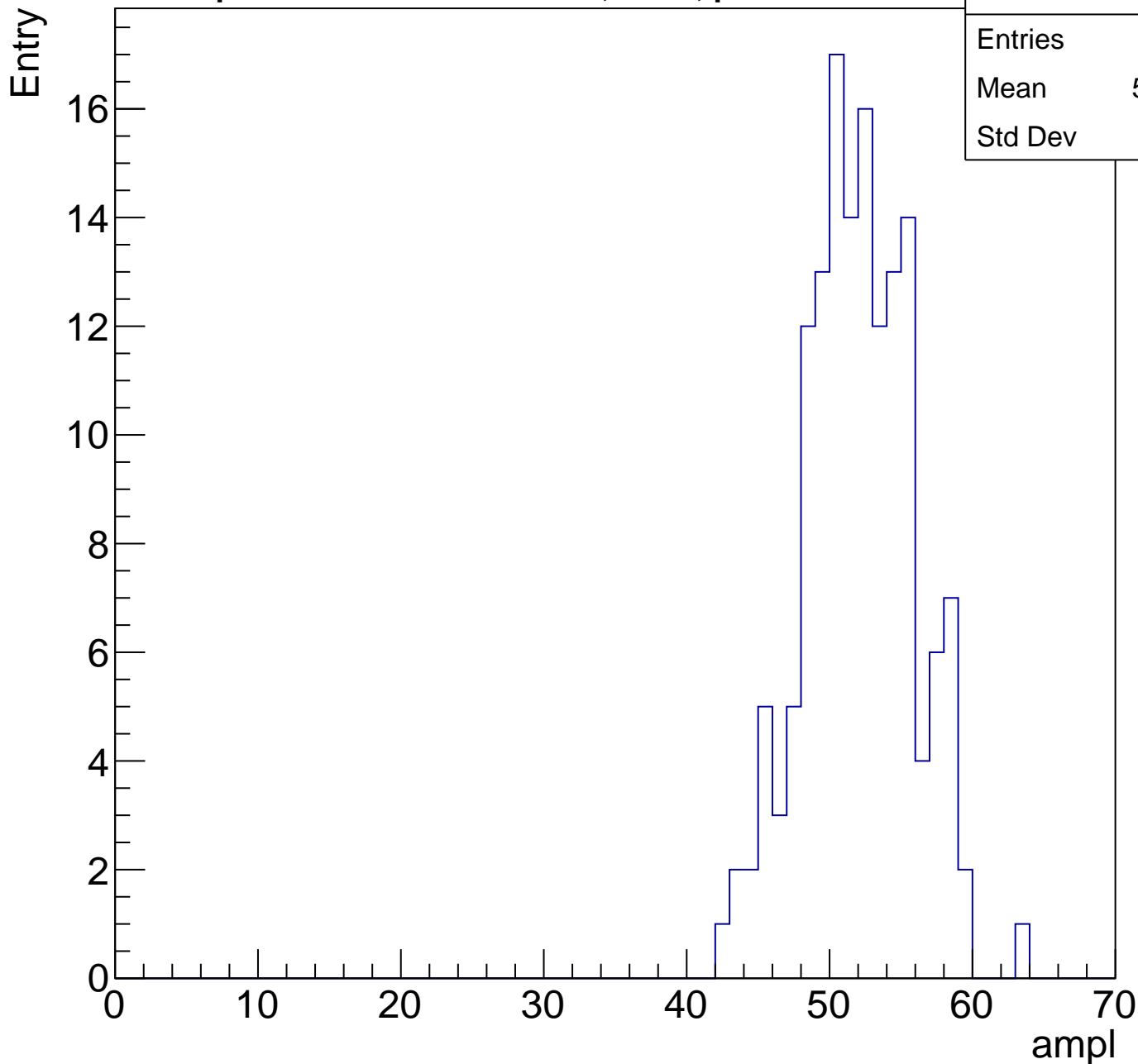
Gaus Width: 3.5325



B1L001S, U25-ch111, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	51.57
Std Dev	3.76



B1L001S, U25-ch111, adc4

calib_packv5_042523_0143.root, FC#2, port C2

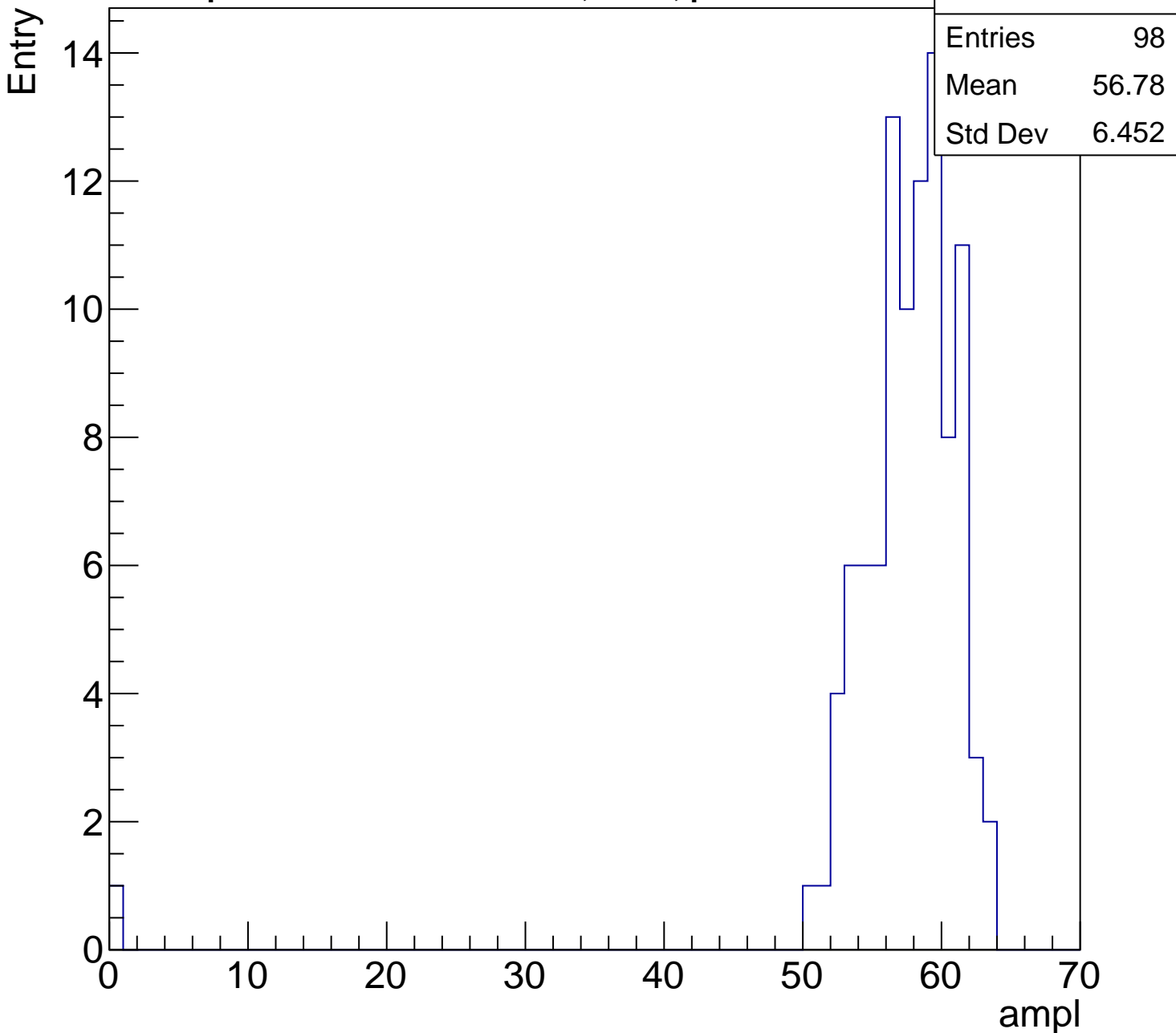
Entries	98
Mean	56.78
Std Dev	6.452

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch111, adc5

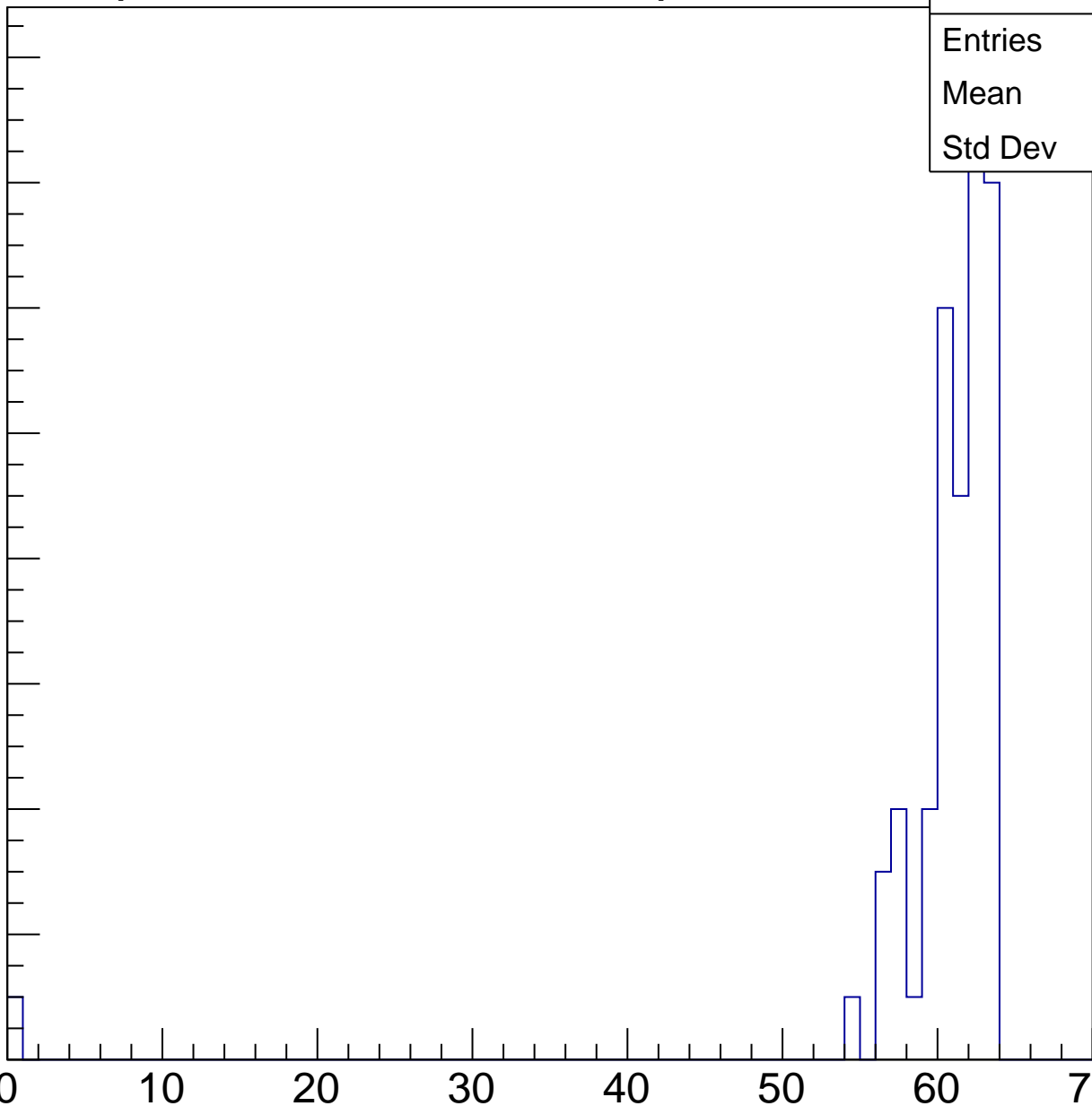
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	65
Mean	59.8
Std Dev	7.77

ampl



B1L001S, U25-ch111, adc6

calib_packv5_042523_0143.root, FC#2, port C2

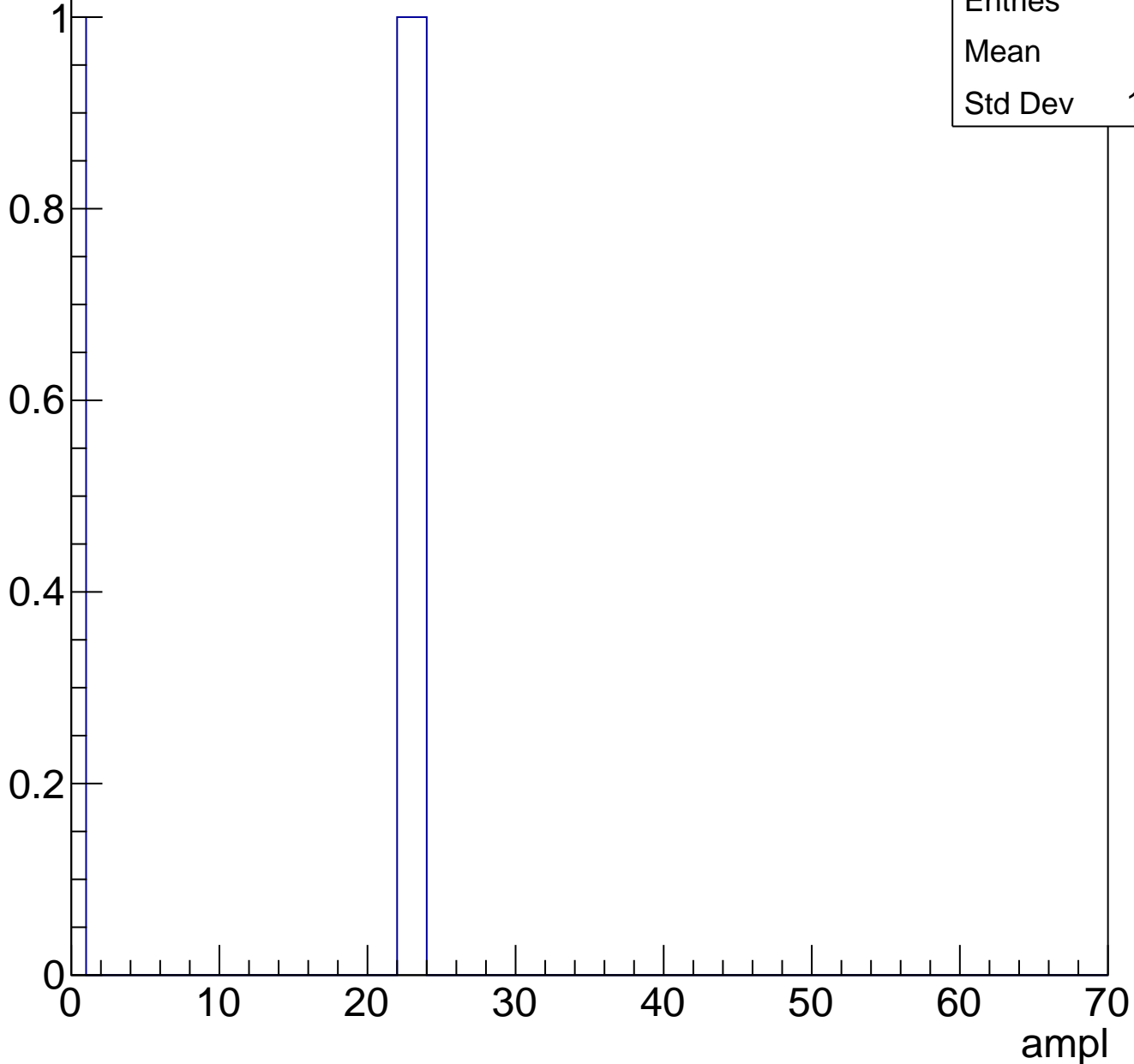
Entry



B1L001S, U25-ch111, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch112, adc0

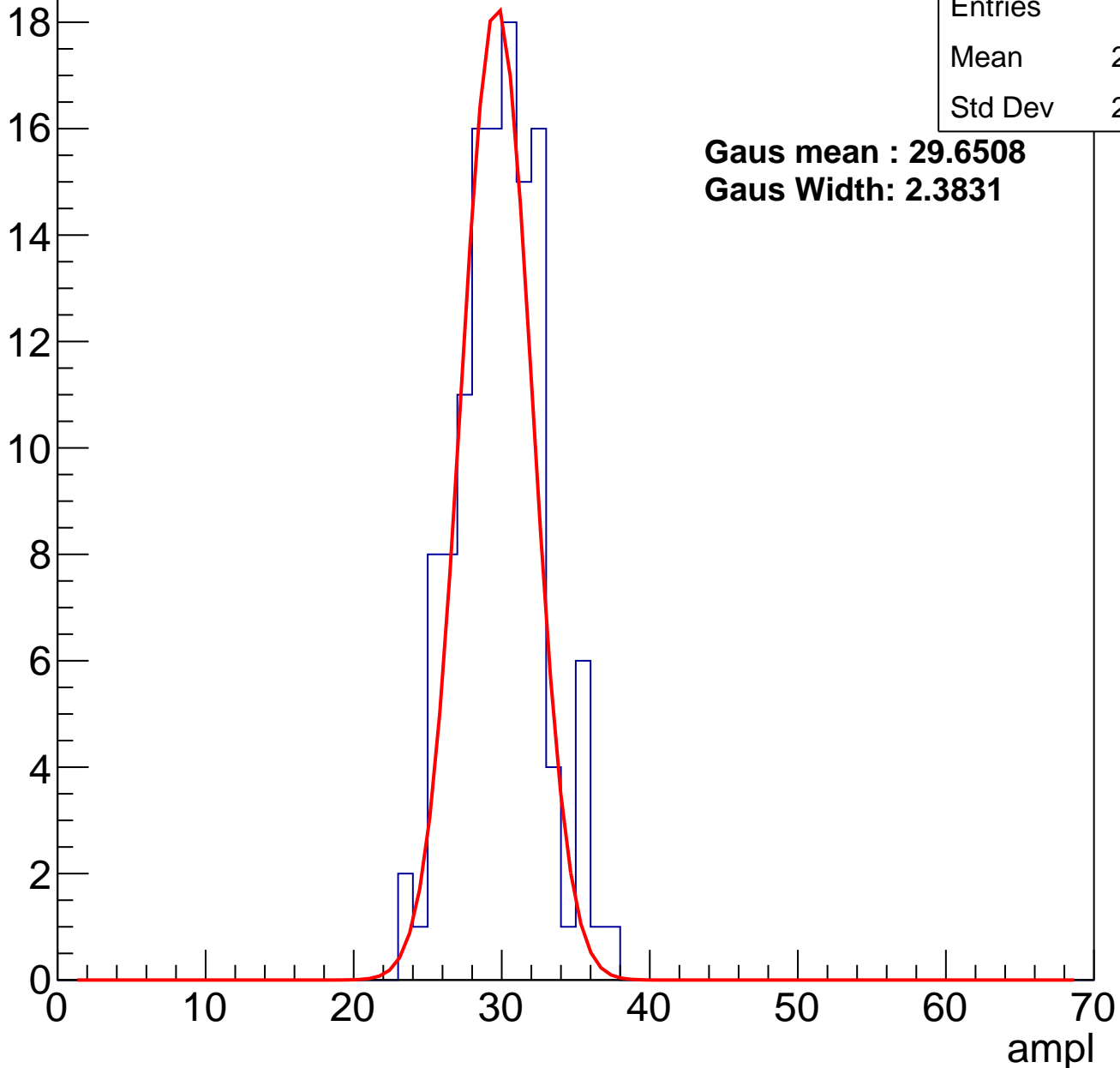
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	29.46
Std Dev	2.798

Gaus mean : 29.6508

Gaus Width: 2.3831

Entry



B1L001S, U25-ch112, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.01
Std Dev	3.204

Gaus mean : 36.5435

Gaus Width: 3.0419

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

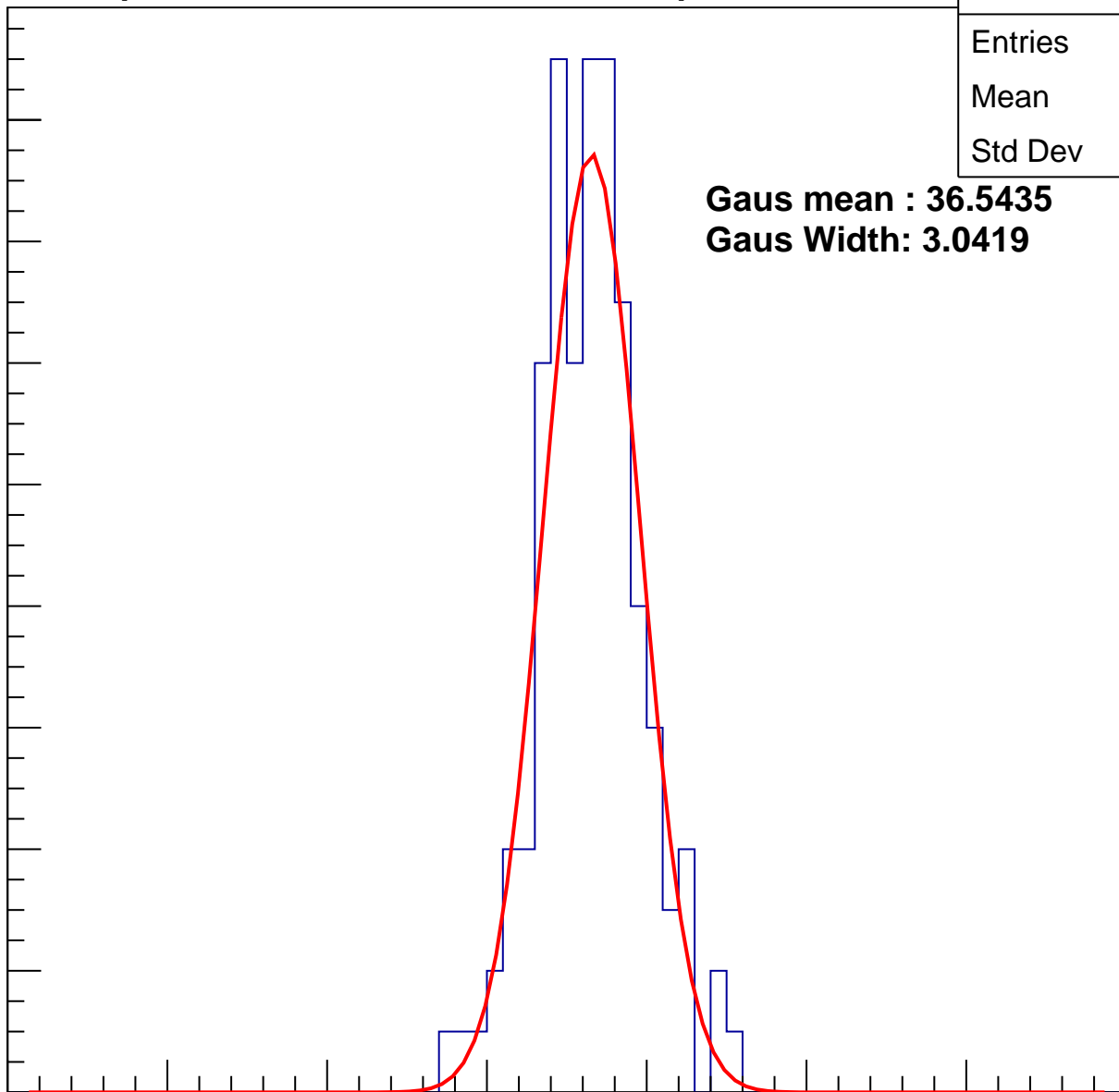
40

50

60

70

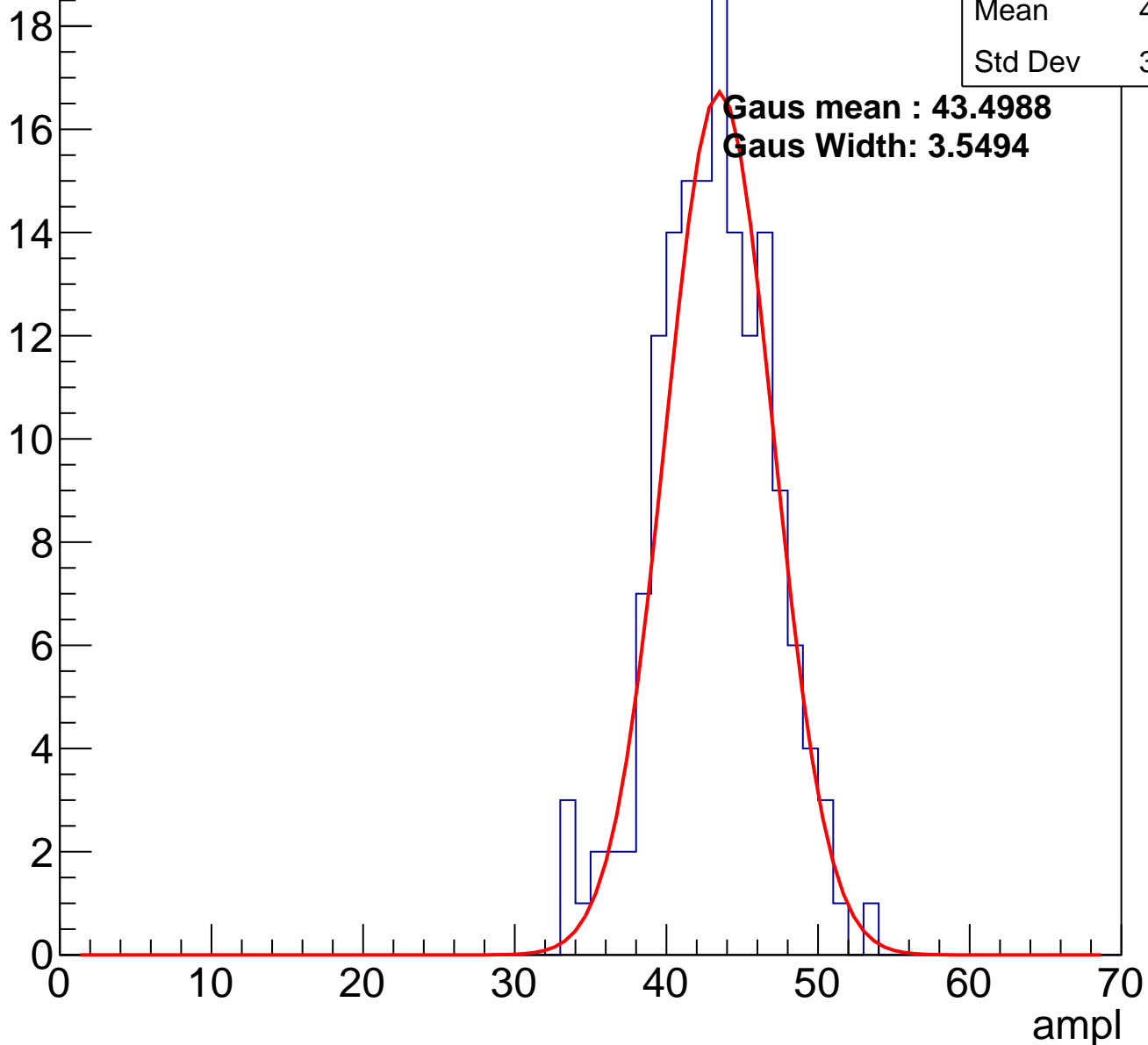
ampl



B1L001S, U25-ch112, adc2

calib_packv5_042523_0143.root, FC#2, port C2

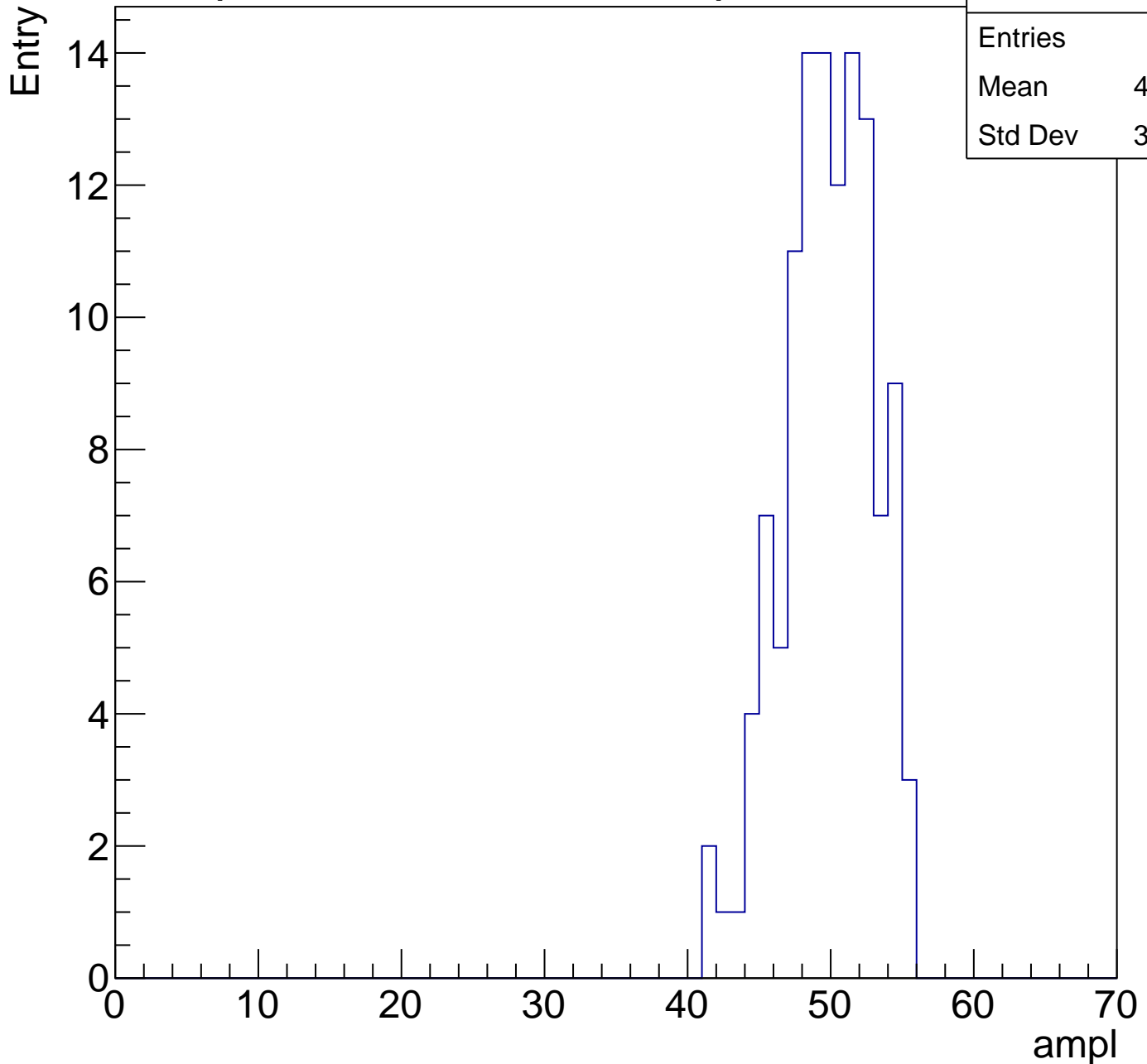
Entry



B1L001S, U25-ch112, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	49.36
Std Dev	3.125

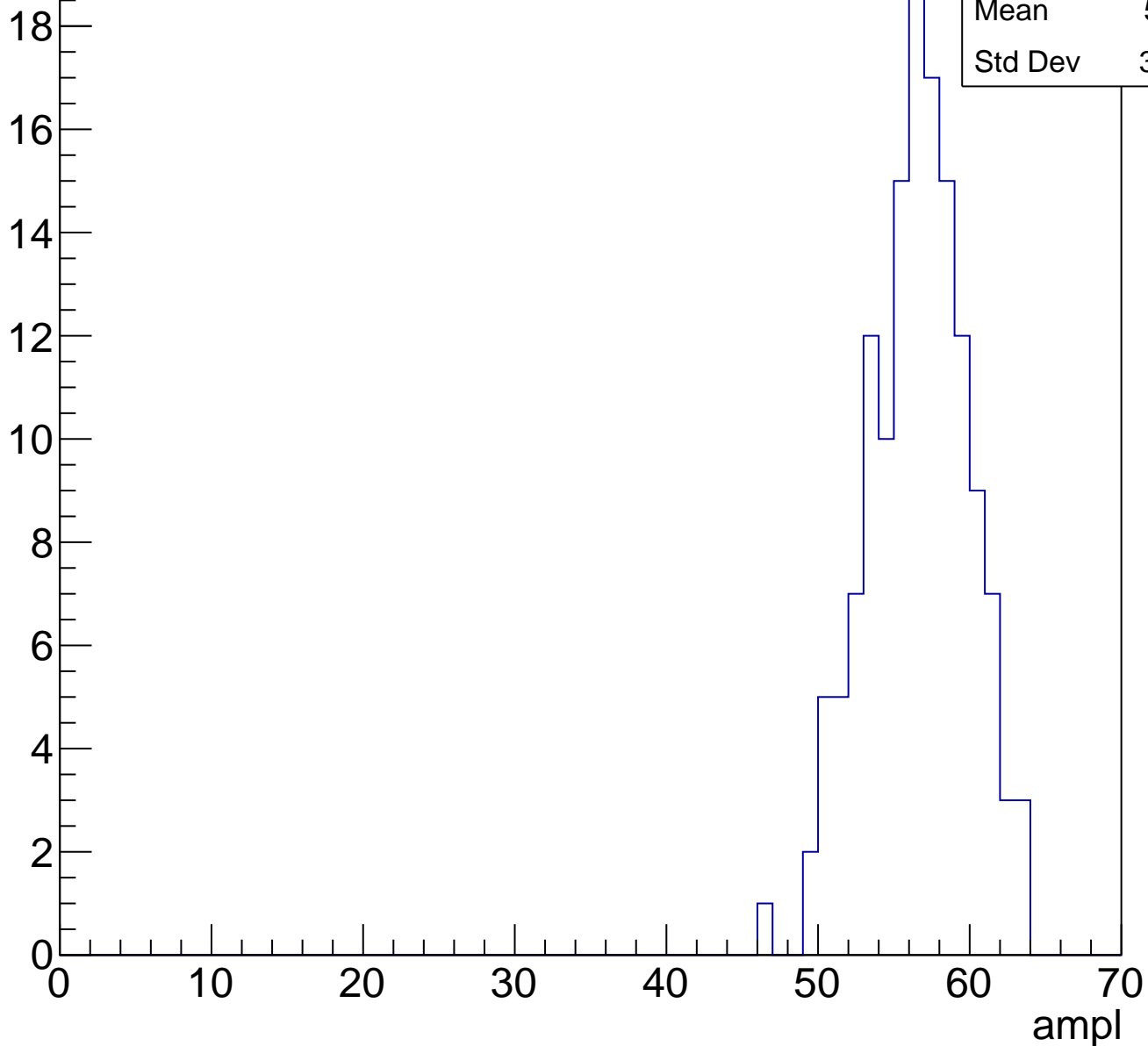


B1L001S, U25-ch112, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	56.11
Std Dev	3.288

Entry

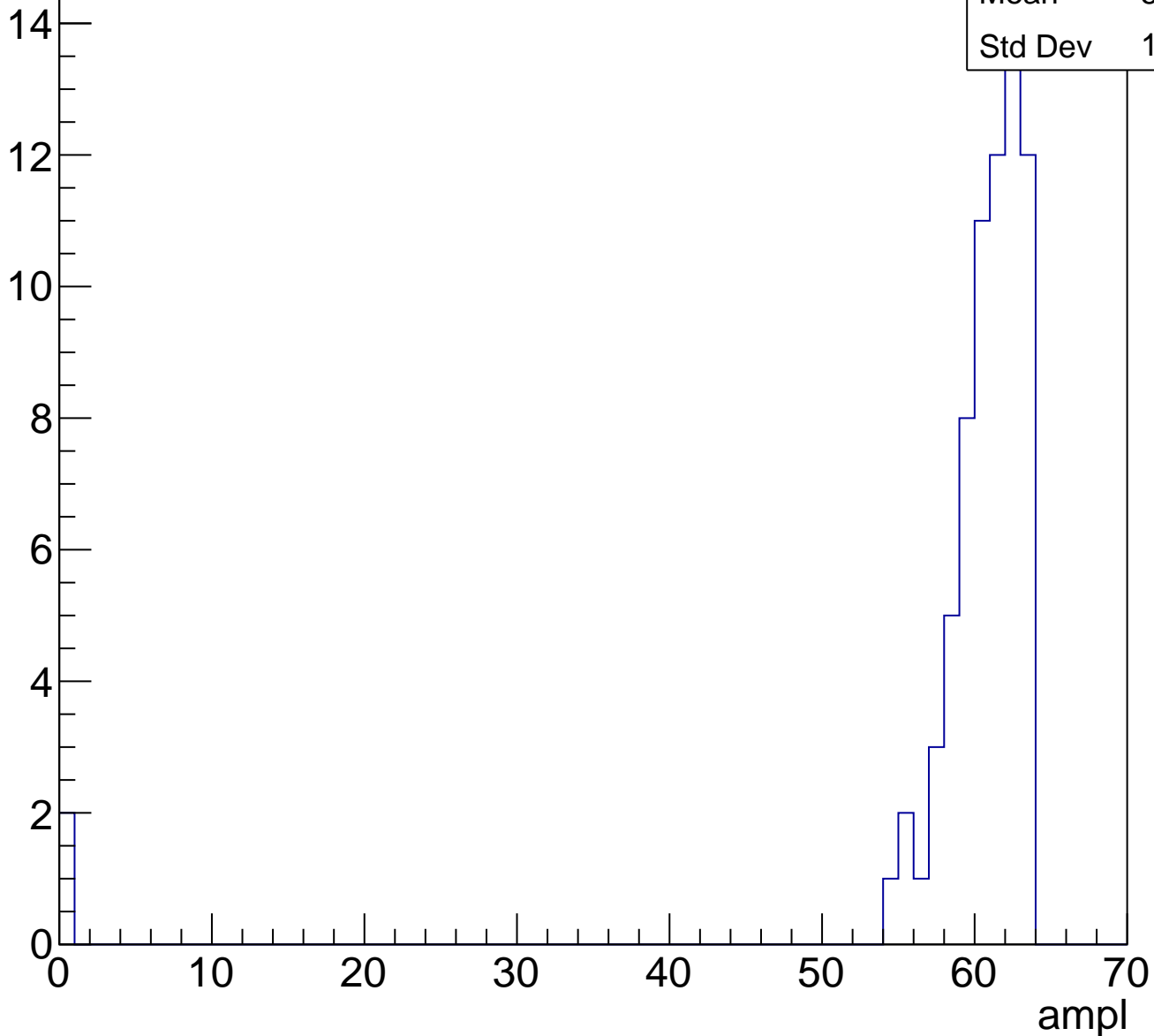


B1L001S, U25-ch112, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	72
Mean	58.76
Std Dev	10.16

Entry

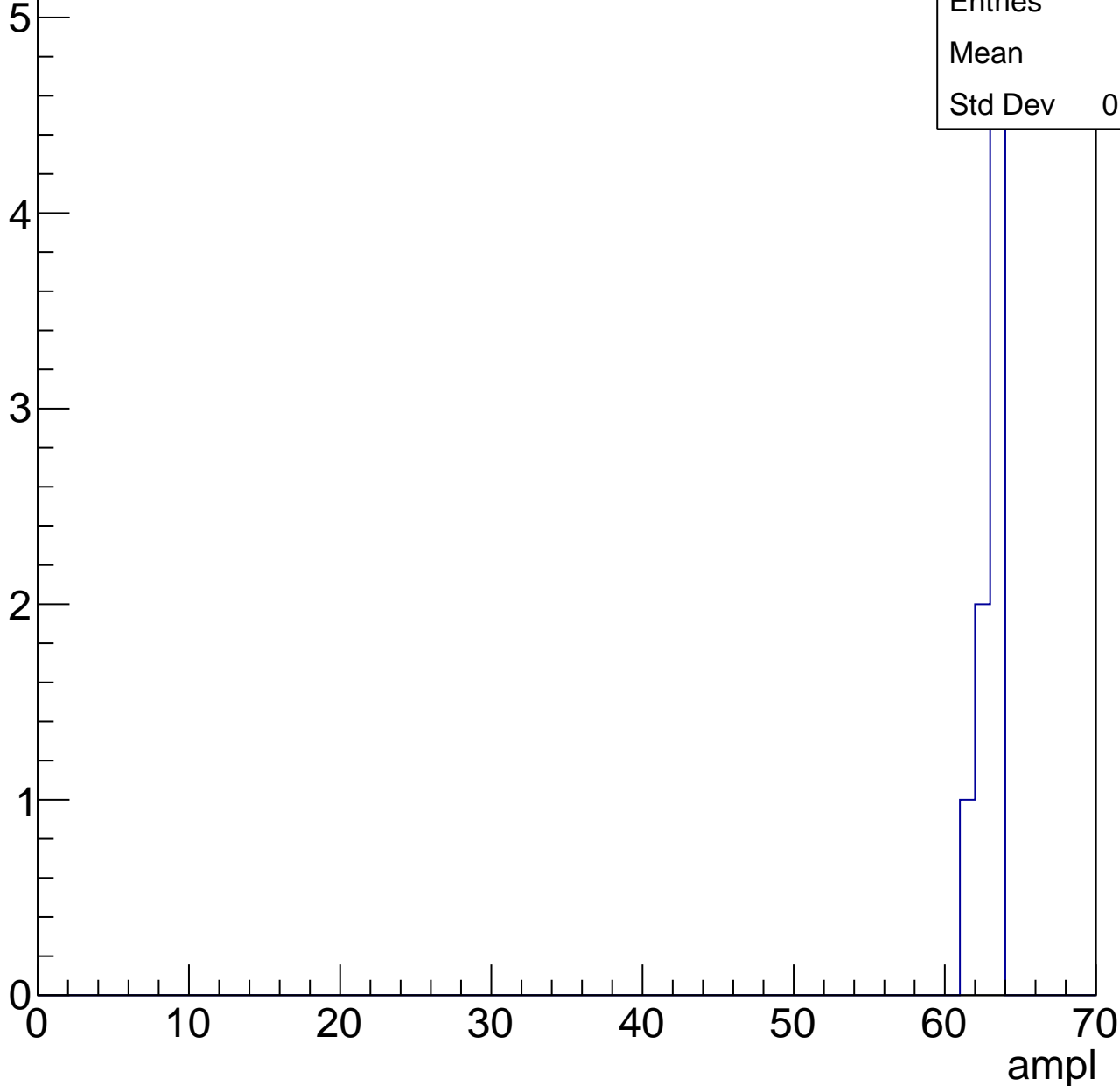


B1L001S, U25-ch112, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	62.5
Std Dev	0.7071



B1L001S, U25-ch112, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

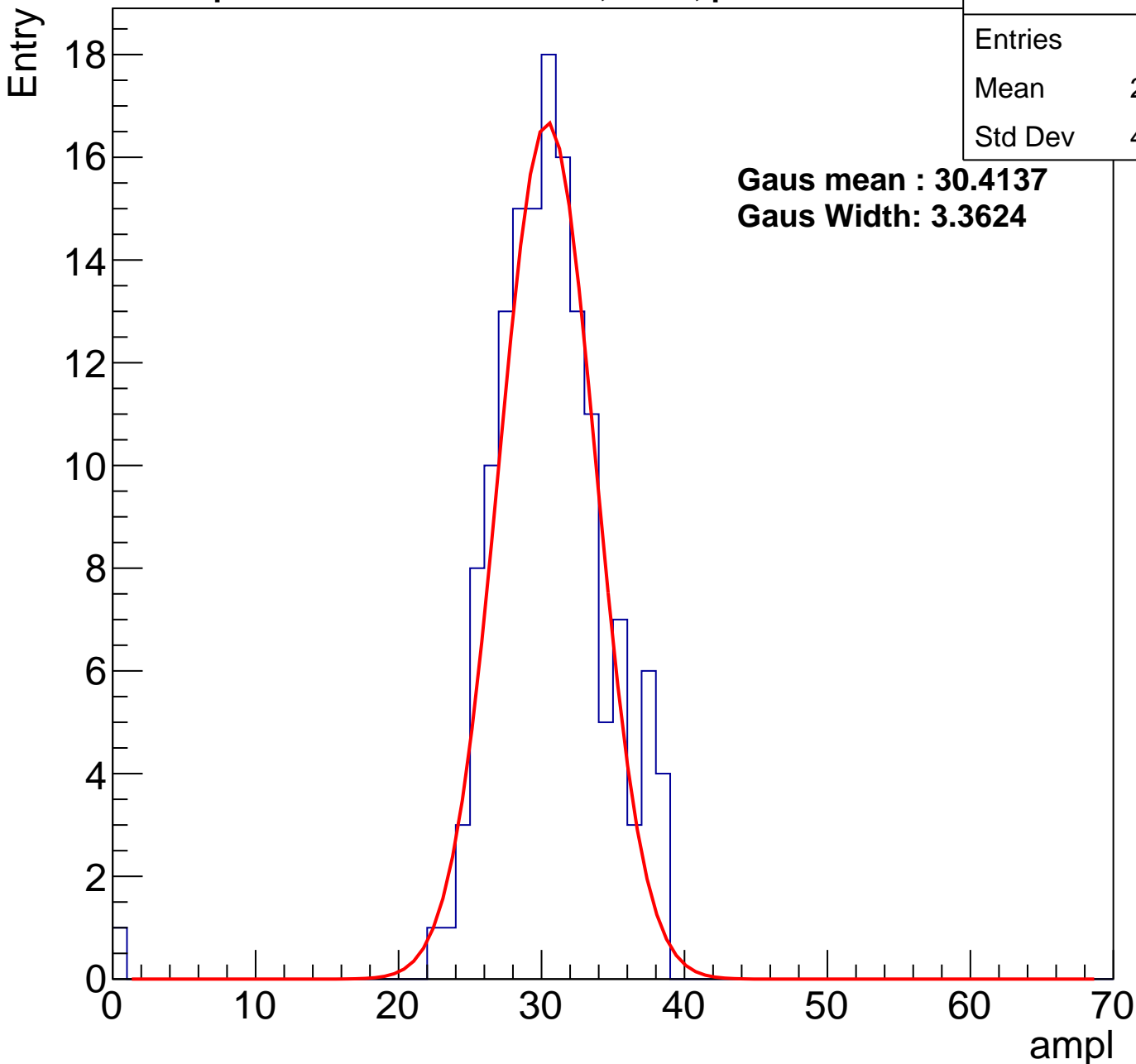
B1L001S, U25-ch113, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	29.97
Std Dev	4.284

Gaus mean : 30.4137

Gaus Width: 3.3624



B1L001S, U25-ch113, adc1

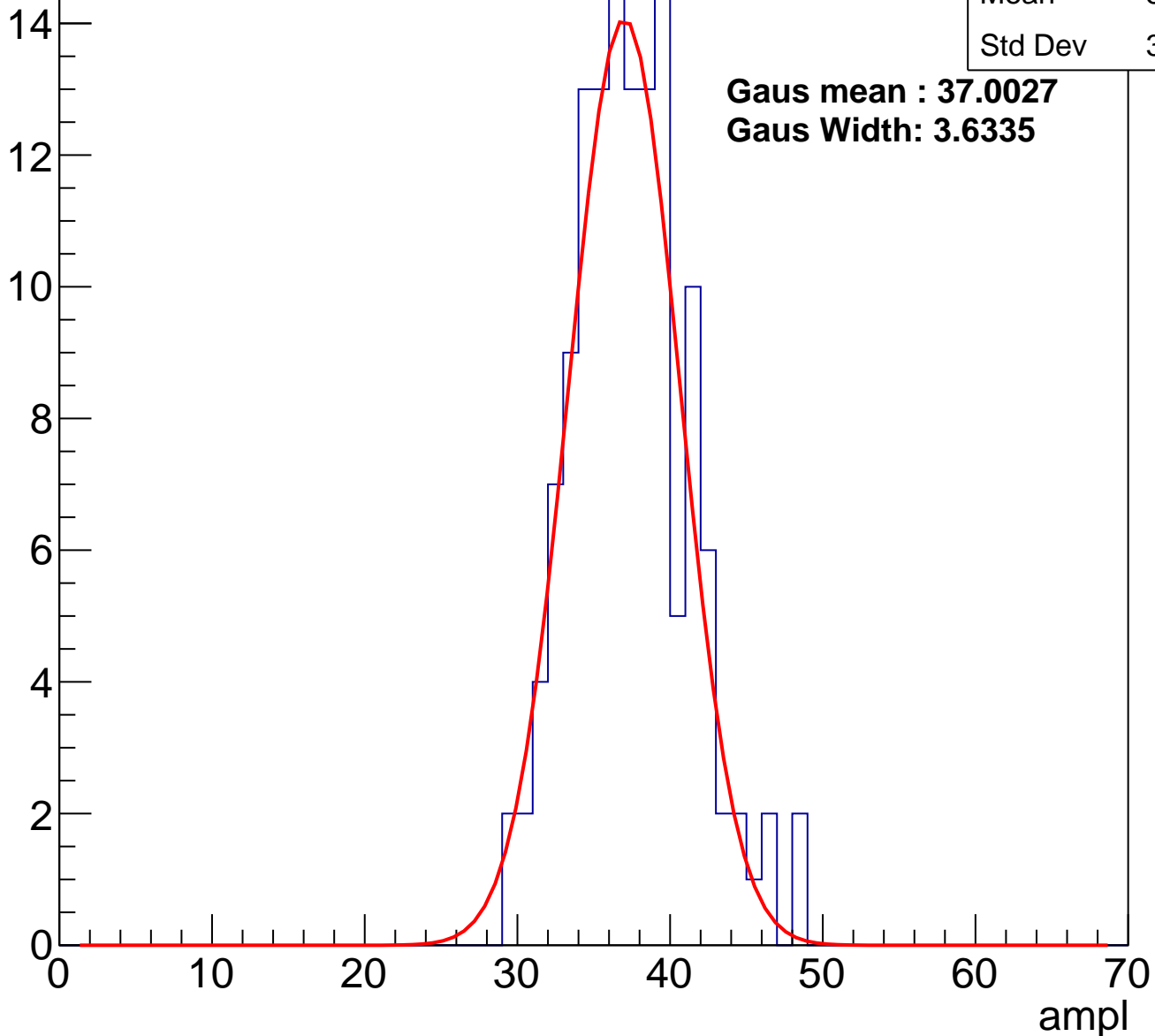
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	36.98
Std Dev	3.776

Gaus mean : 37.0027

Gaus Width: 3.6335

Entry



B1L001S, U25-ch113, adc2

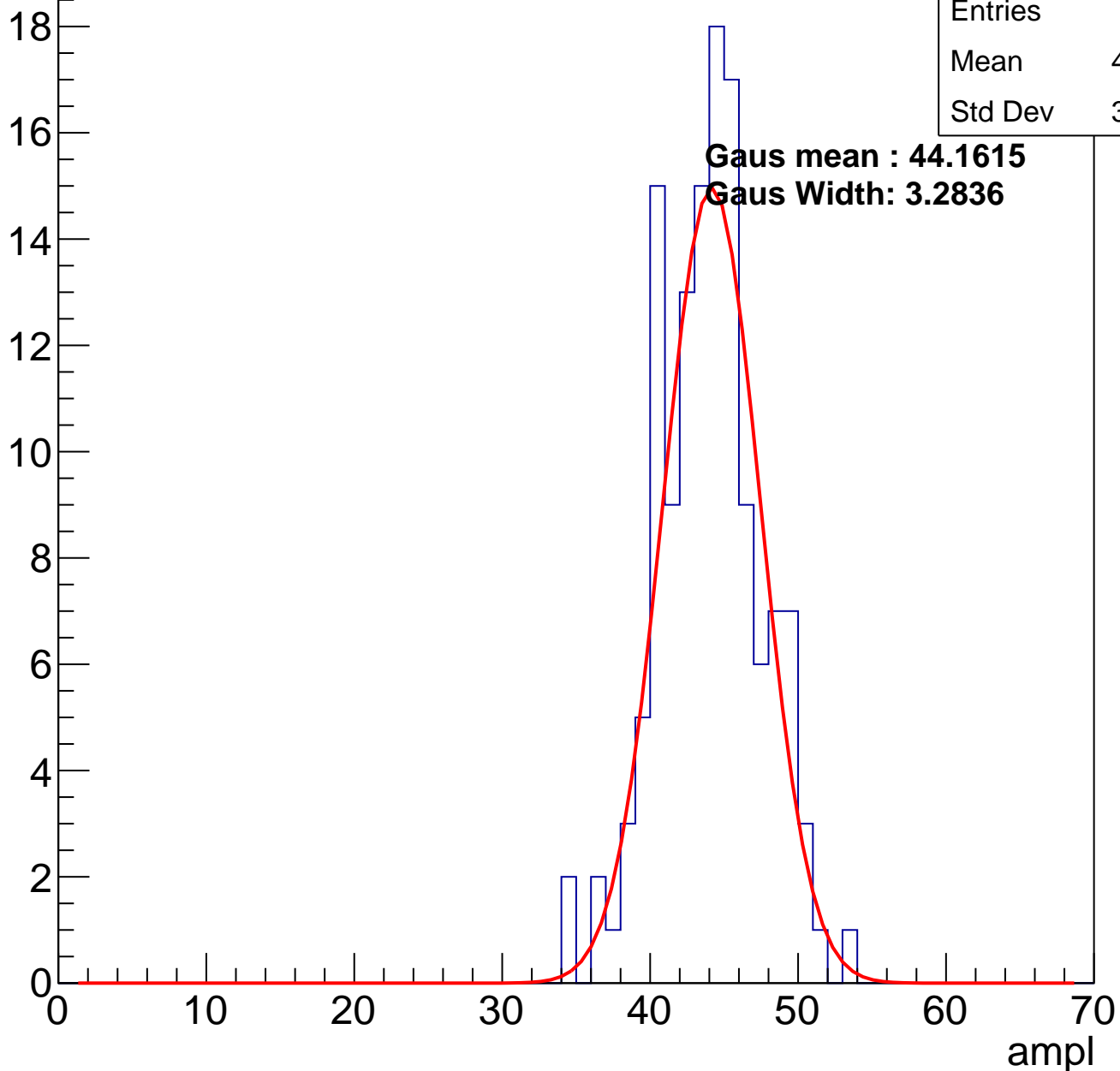
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	43.52
Std Dev	3.455

Gaus mean : 44.1615

Gaus Width: 3.2836

Entry



B1L001S, U25-ch113, adc3

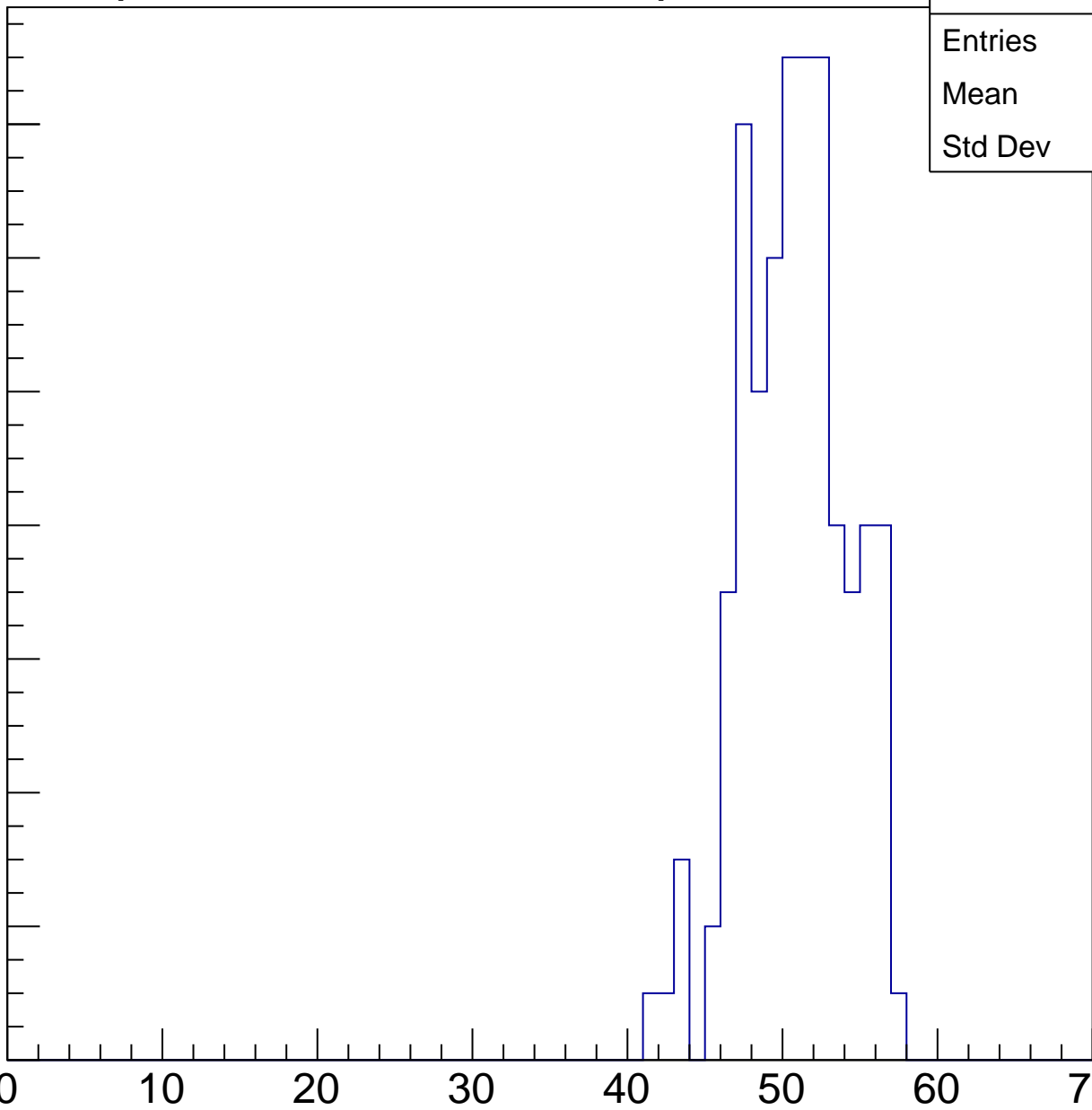
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	50.33
Std Dev	3.337

ampl



B1L001S, U25-ch113, adc4

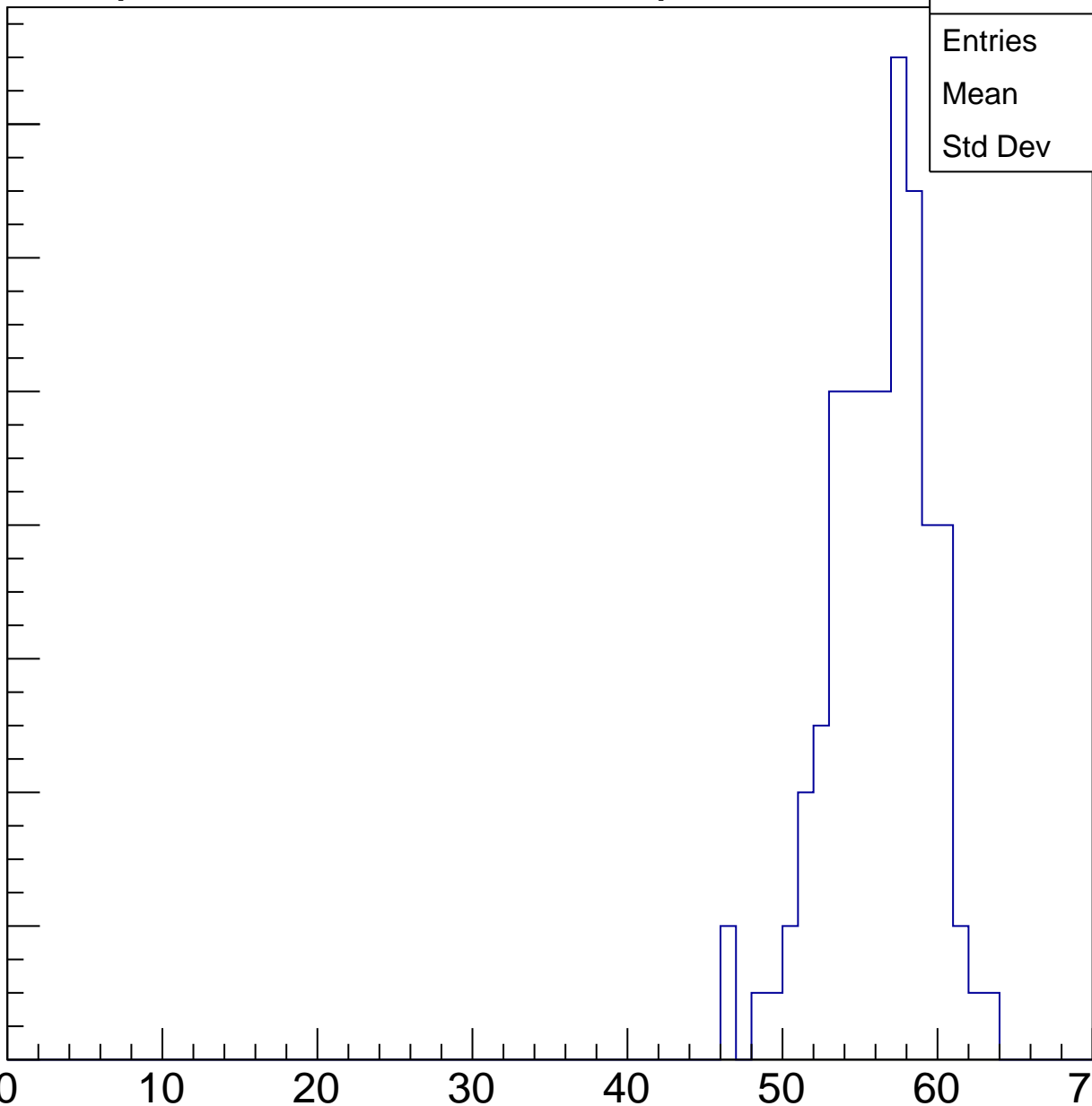
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	103
Mean	55.74
Std Dev	3.268

ampl



B1L001S, U25-ch113, adc5

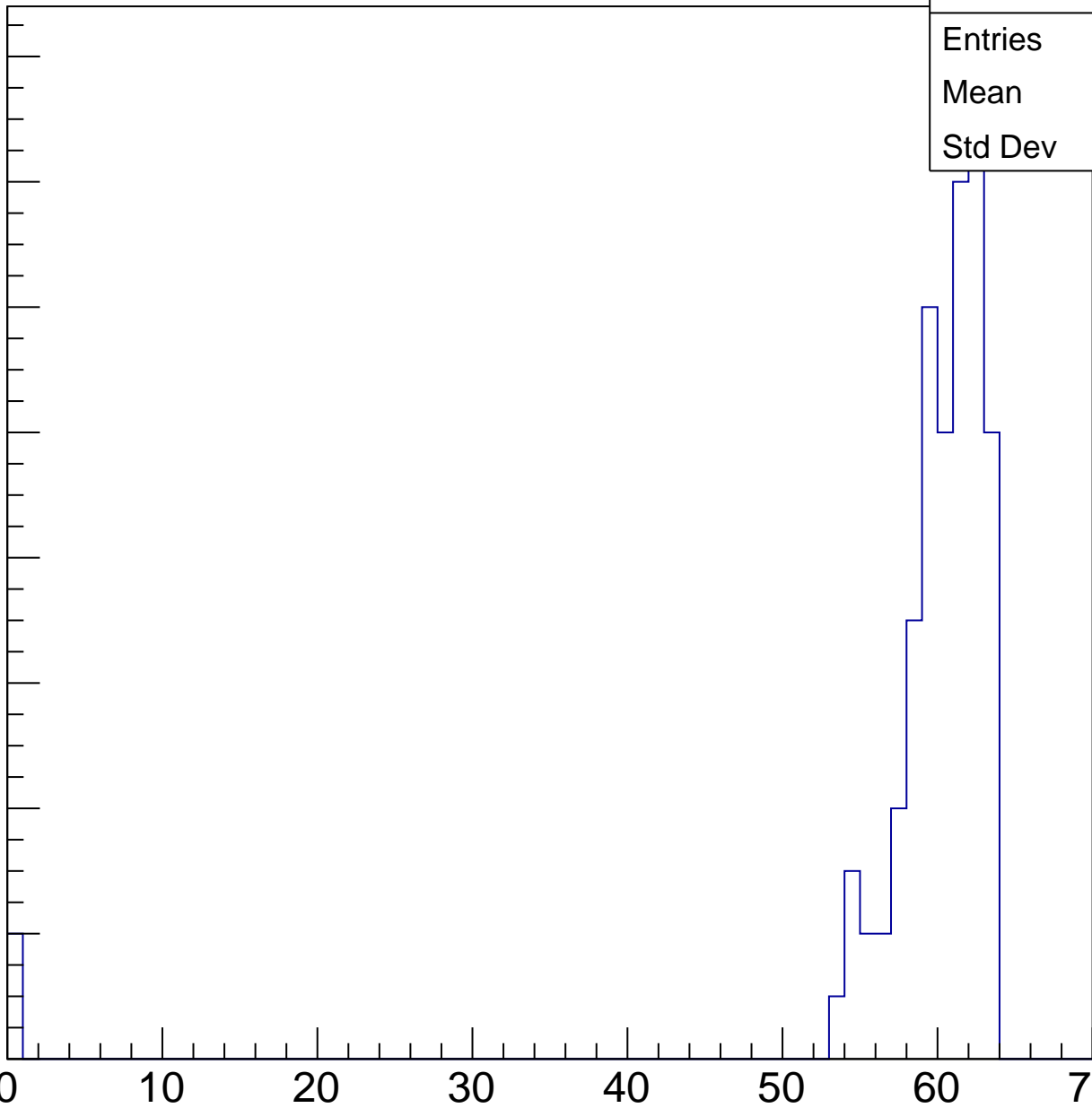
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	58.49
Std Dev	9.503

ampl

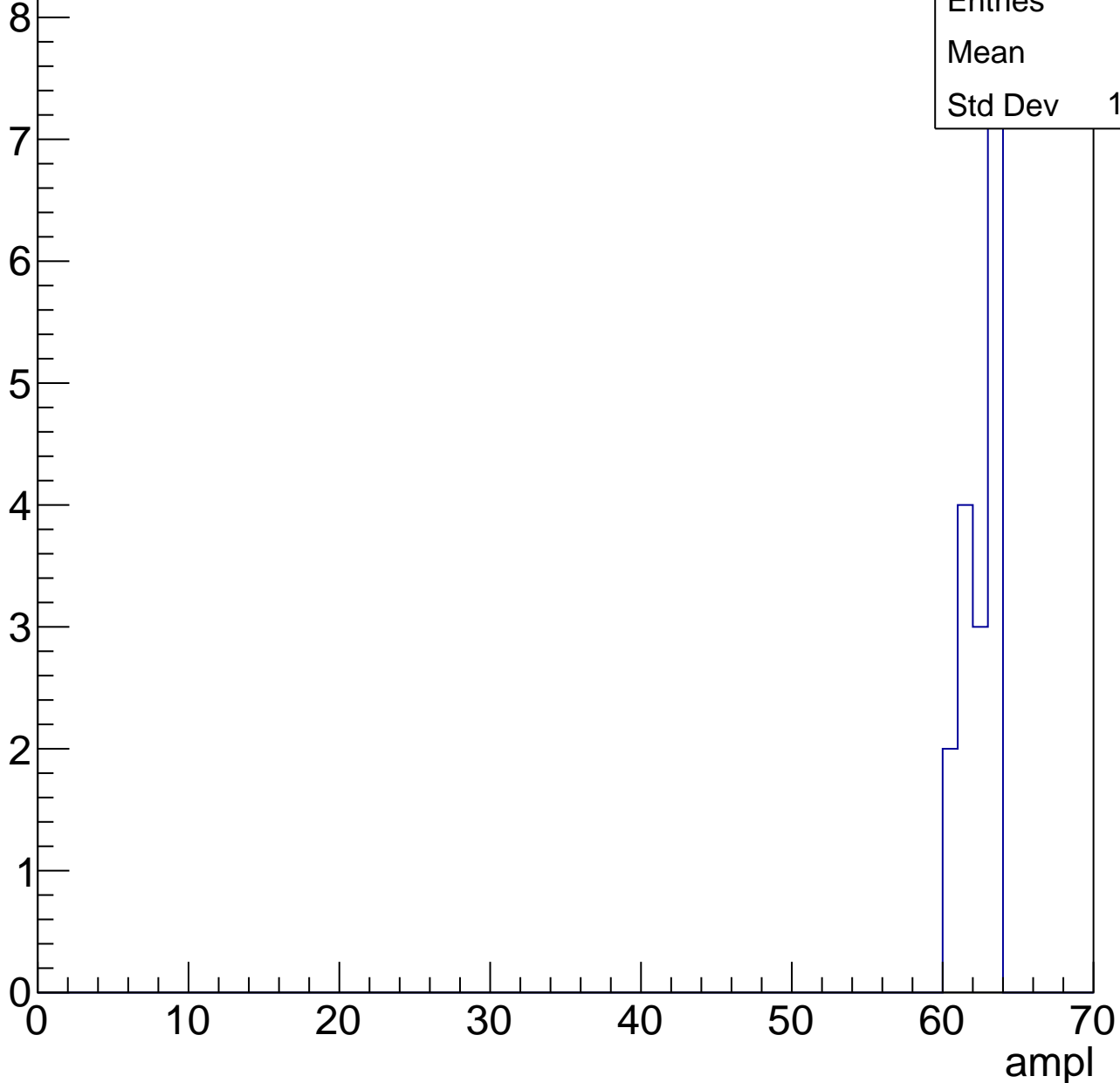


B1L001S, U25-ch113, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

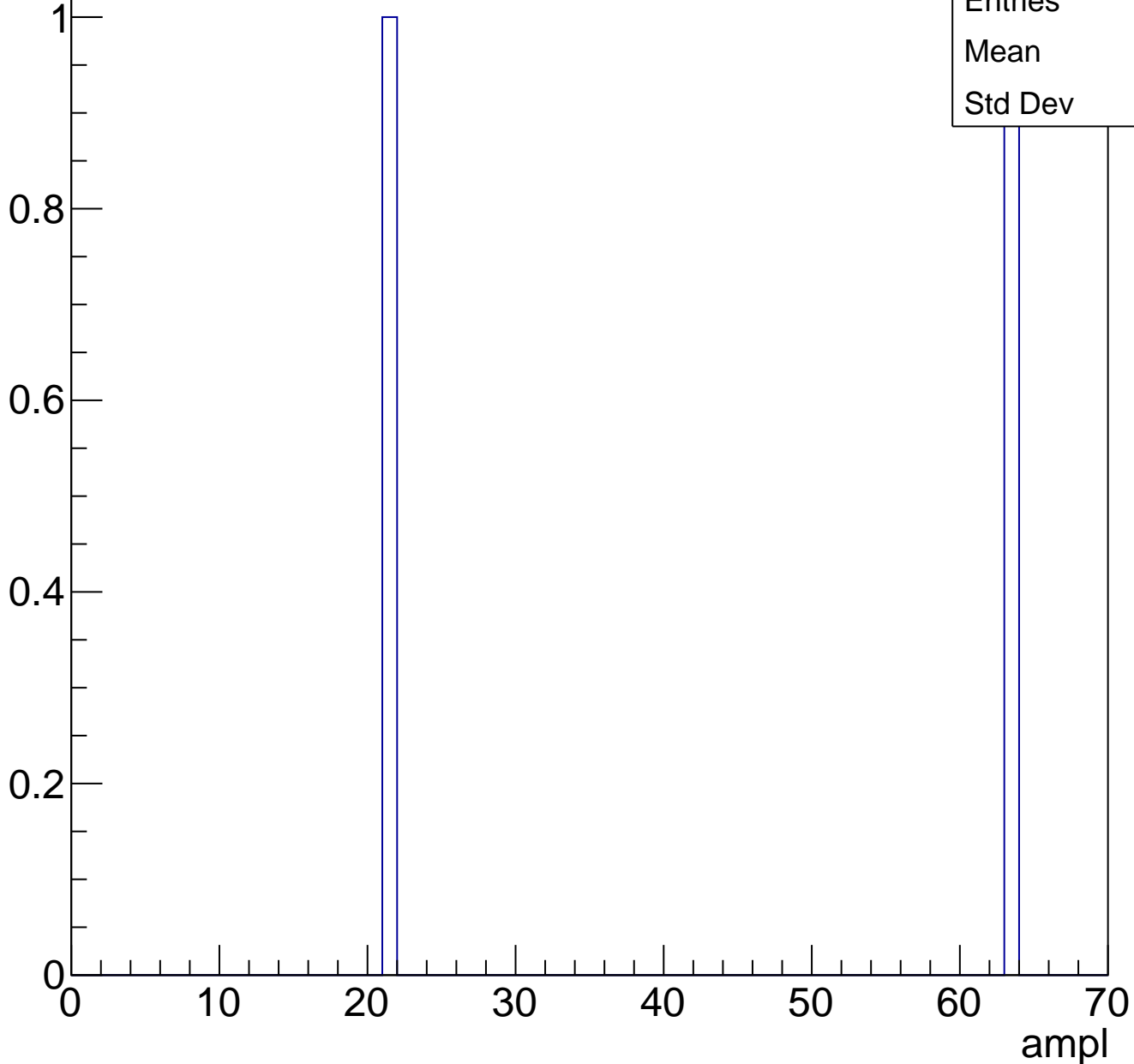
Entries	17
Mean	62
Std Dev	1.085



B1L001S, U25-ch113, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch114, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	30.66
Std Dev	3.42

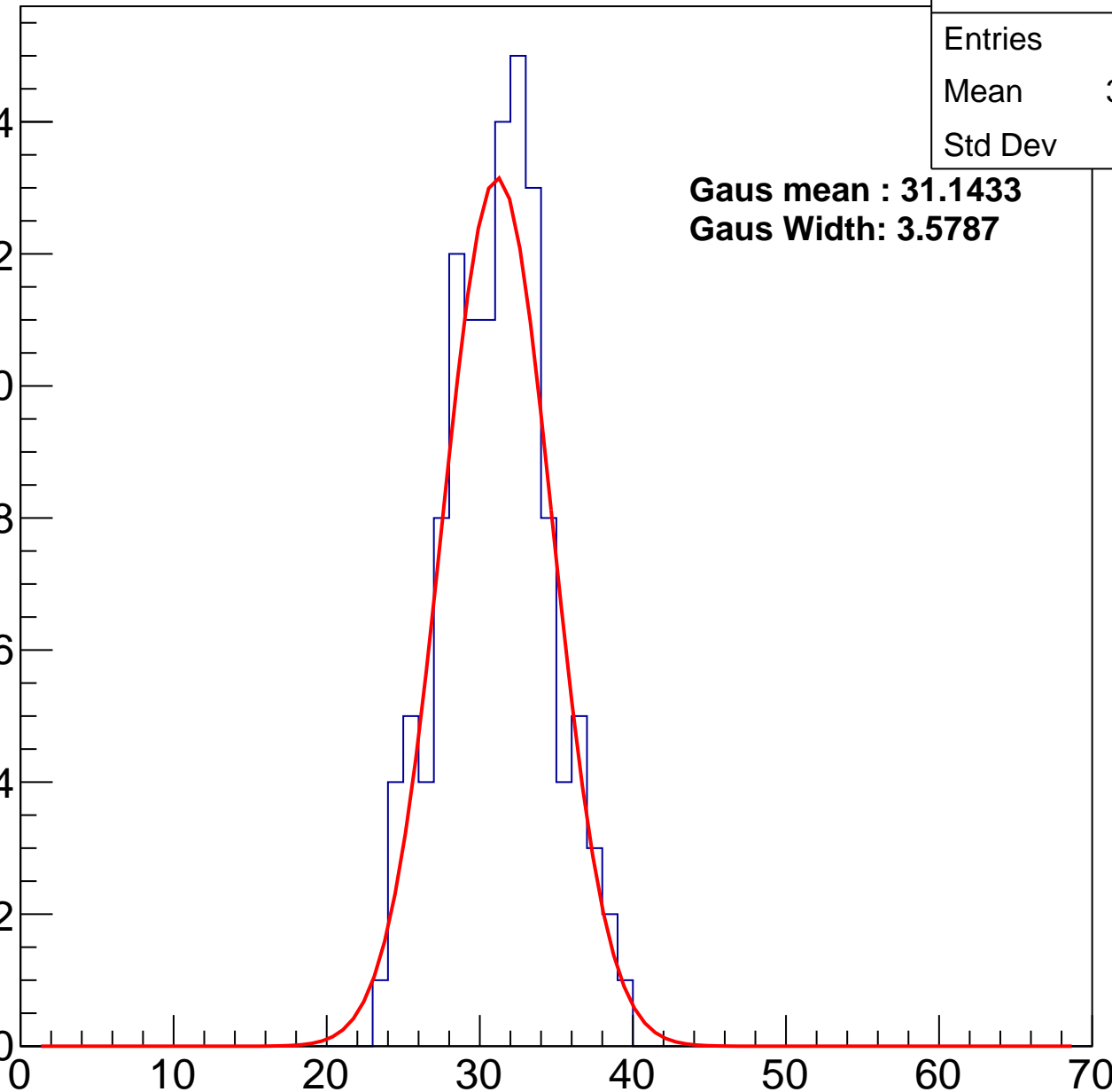
Gaus mean : 31.1433

Gaus Width: 3.5787

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch114, adc1

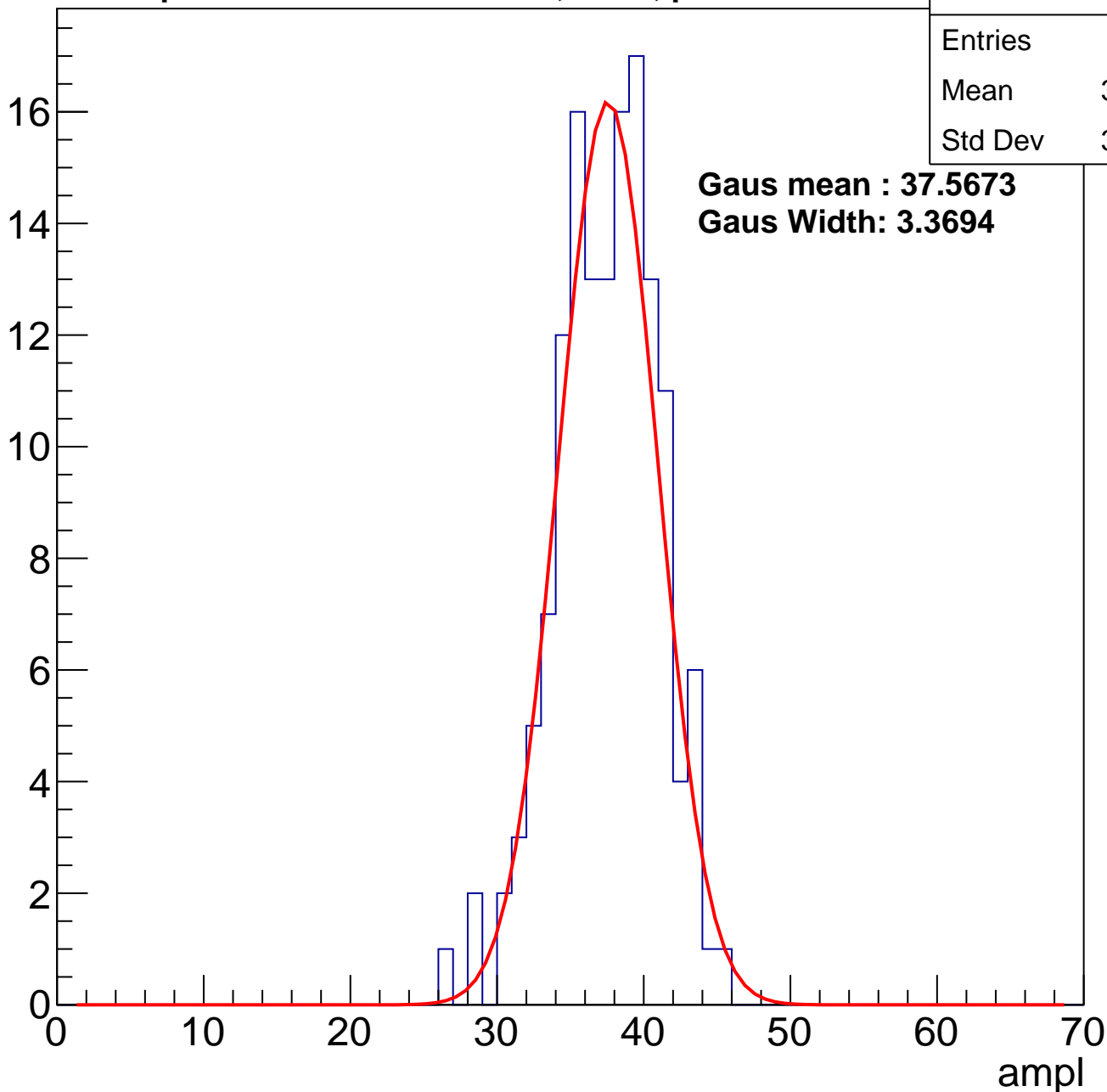
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	37.06
Std Dev	3.465

Gaus mean : 37.5673

Gaus Width: 3.3694

Entry



B1L001S, U25-ch114, adc2

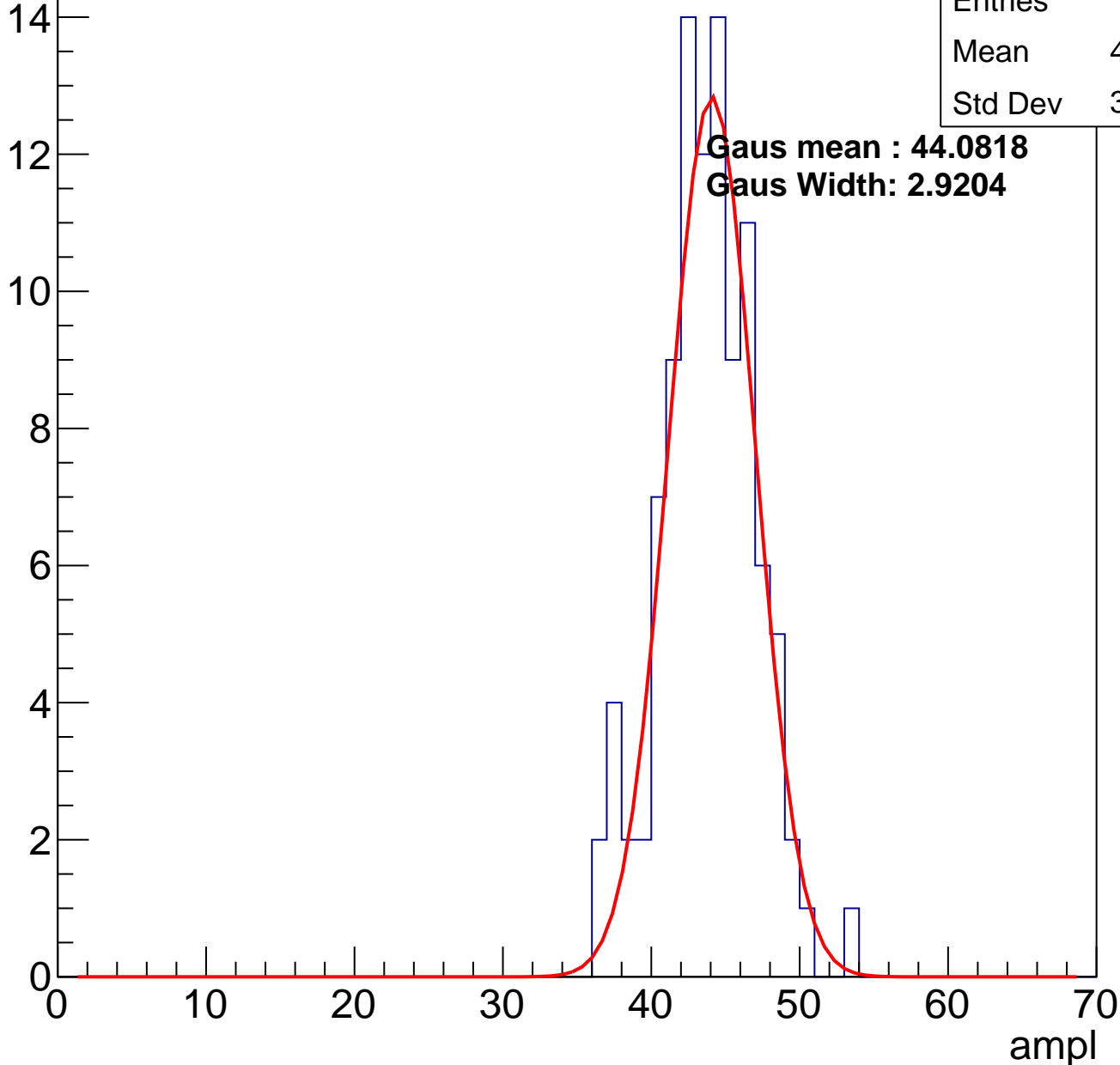
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	101
Mean	43.34
Std Dev	3.173

Gaus mean : 44.0818

Gaus Width: 2.9204

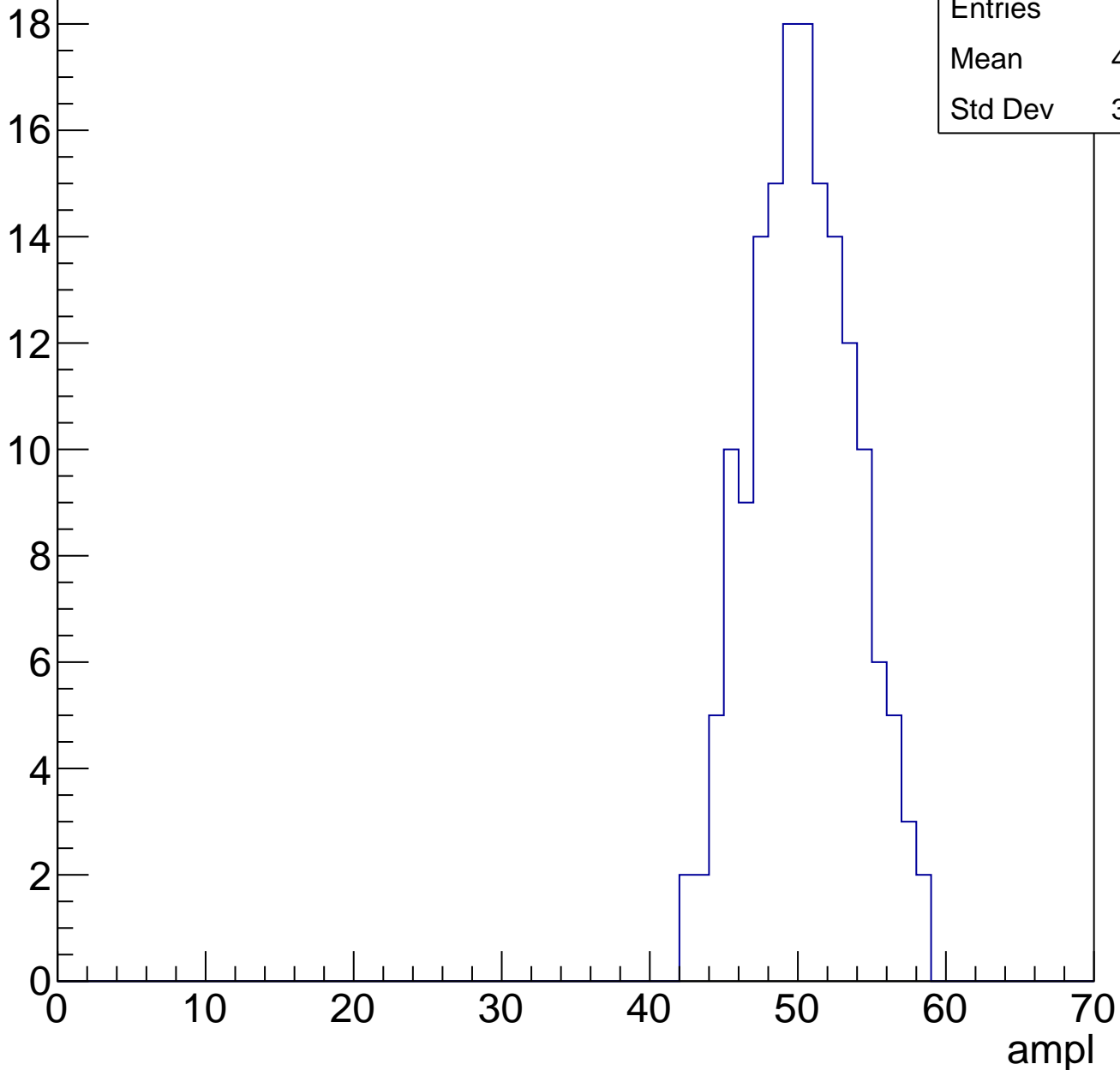


B1L001S, U25-ch114, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	49.88
Std Dev	3.487

Entry



B1L001S, U25-ch114, adc4

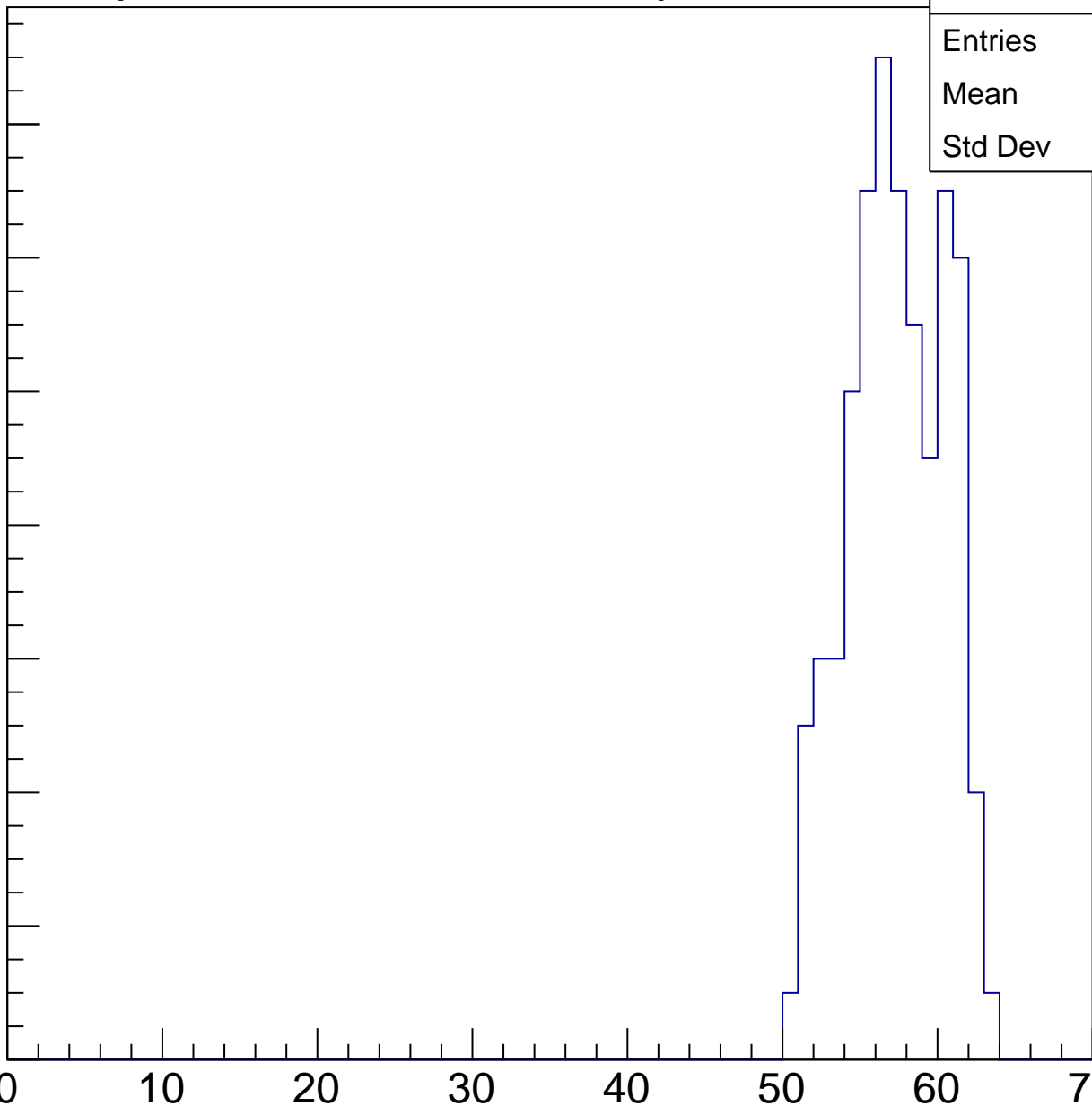
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	56.83
Std Dev	3.069

ampl



B1L001S, U25-ch114, adc5

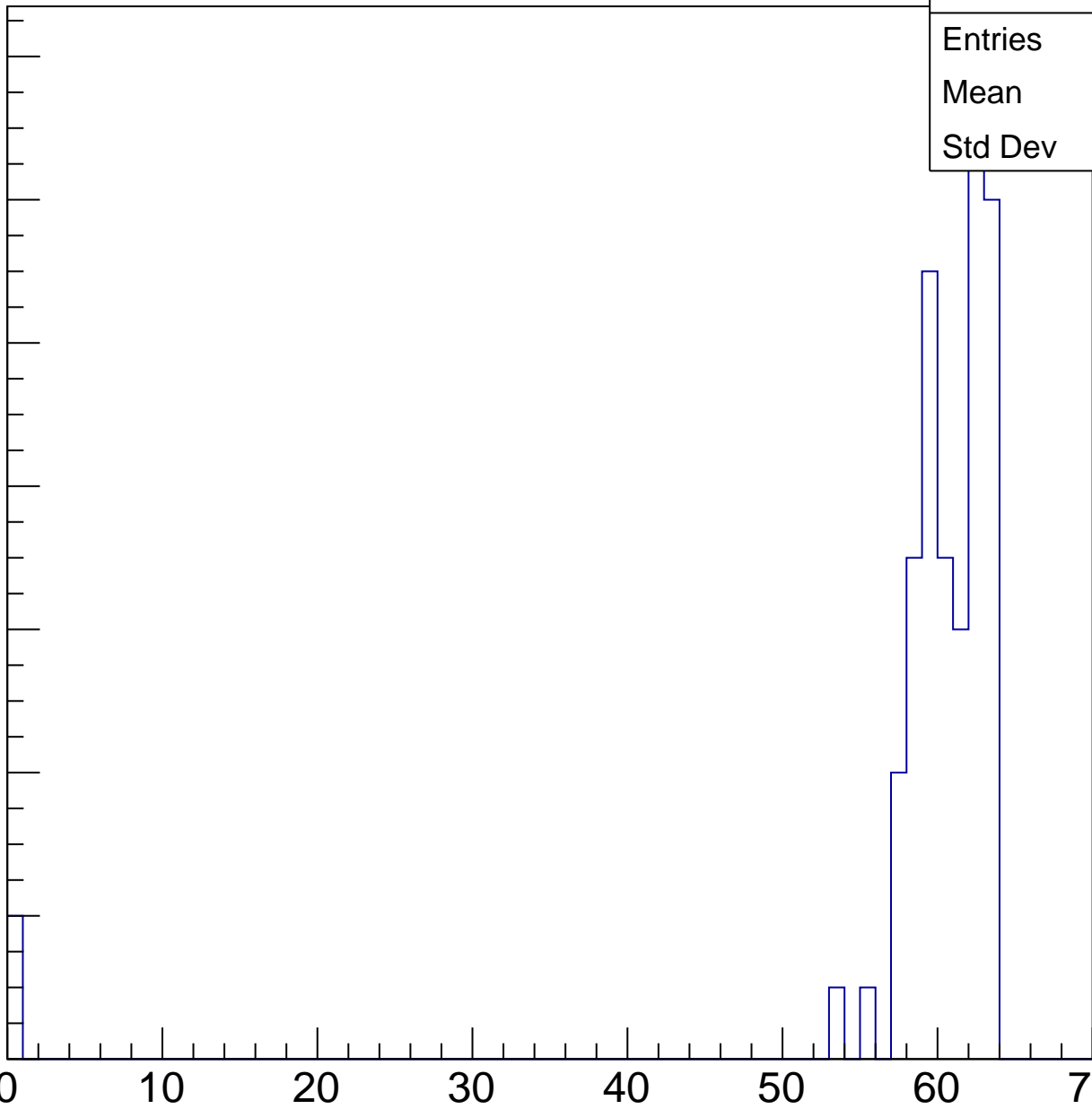
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	65
Mean	58.48
Std Dev	10.65

ampl

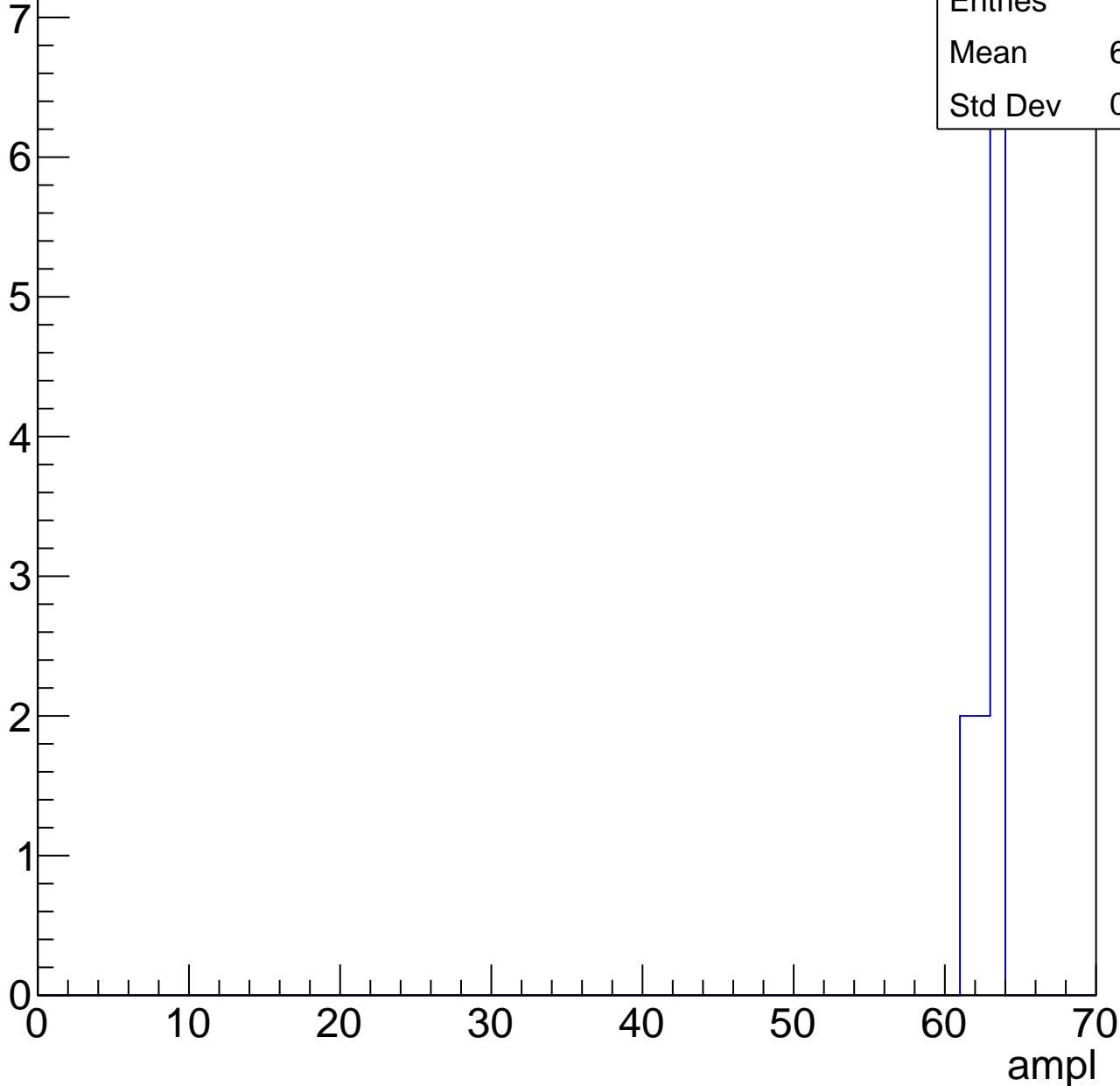


B1L001S, U25-ch114, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.45
Std Dev	0.782



B1L001S, U25-ch114, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U25-ch115, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	29.46
Std Dev	4.377

Gaus mean : 30.4431

Gaus Width: 3.5319

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

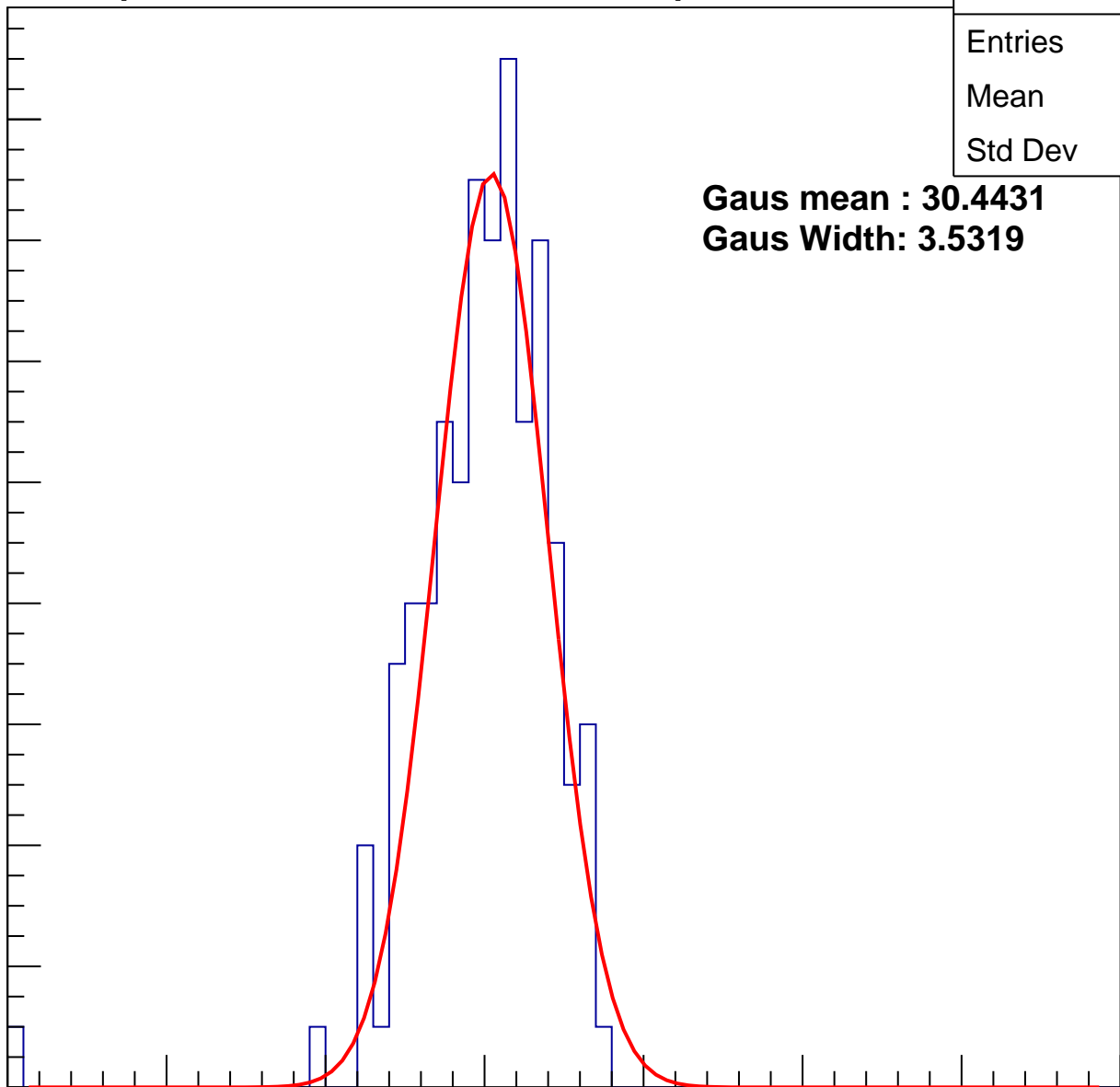
40

50

60

70

ampl



B1L001S, U25-ch115, adc1

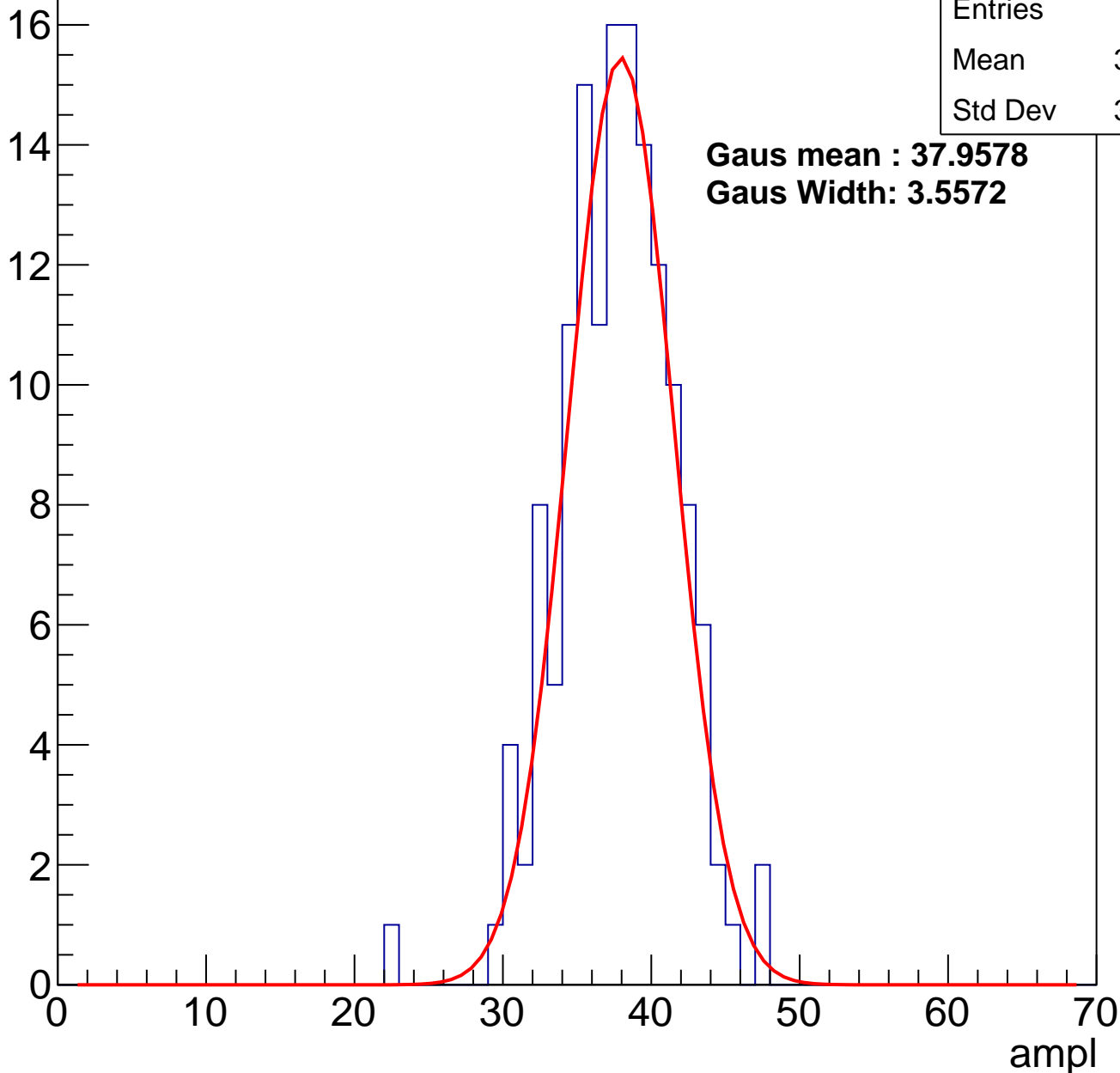
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.28
Std Dev	3.818

Gaus mean : 37.9578

Gaus Width: 3.5572

Entry



B1L001S, U25-ch115, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	43.33
Std Dev	3.66

Gaus mean : 43.7498

Gaus Width: 3.8544

Entry

16

14

12

10

8

6

4

2

0

0

10

20

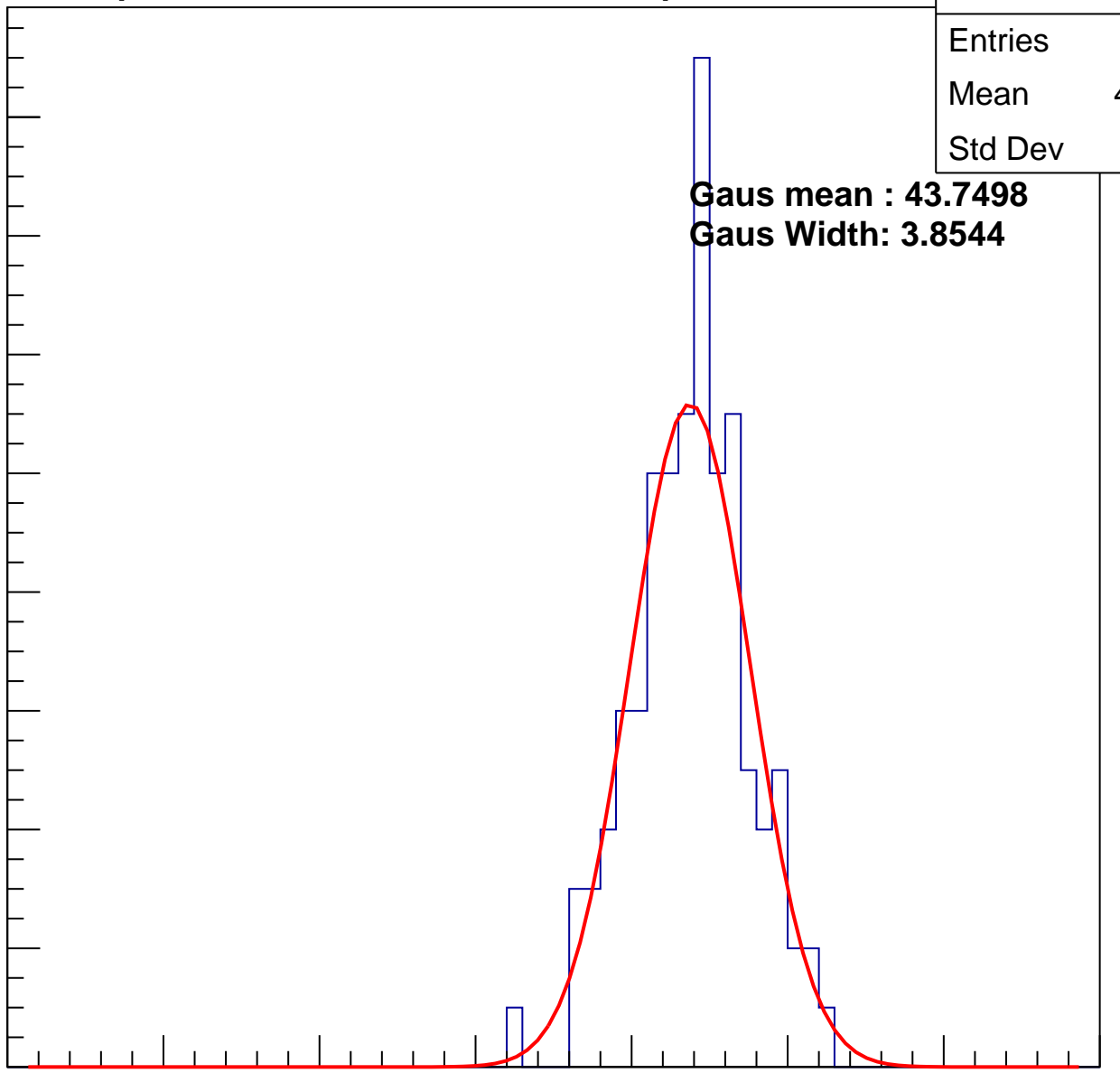
30

40

50

60

ampl



B1L001S, U25-ch115, adc3

calib_packv5_042523_0143.root, FC#2, port C2

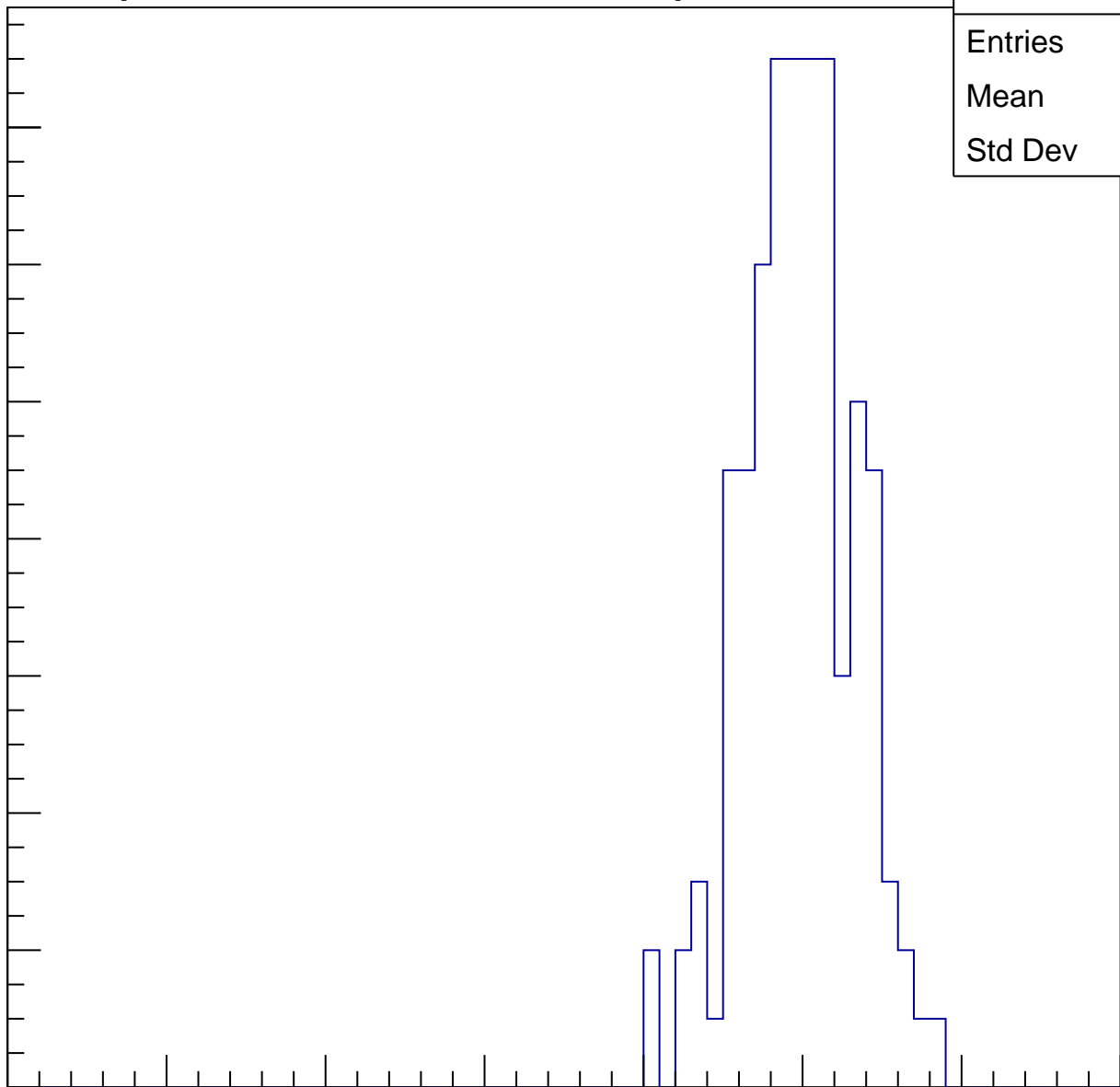
Entries	130
Mean	49.31
Std Dev	3.435

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch115, adc4

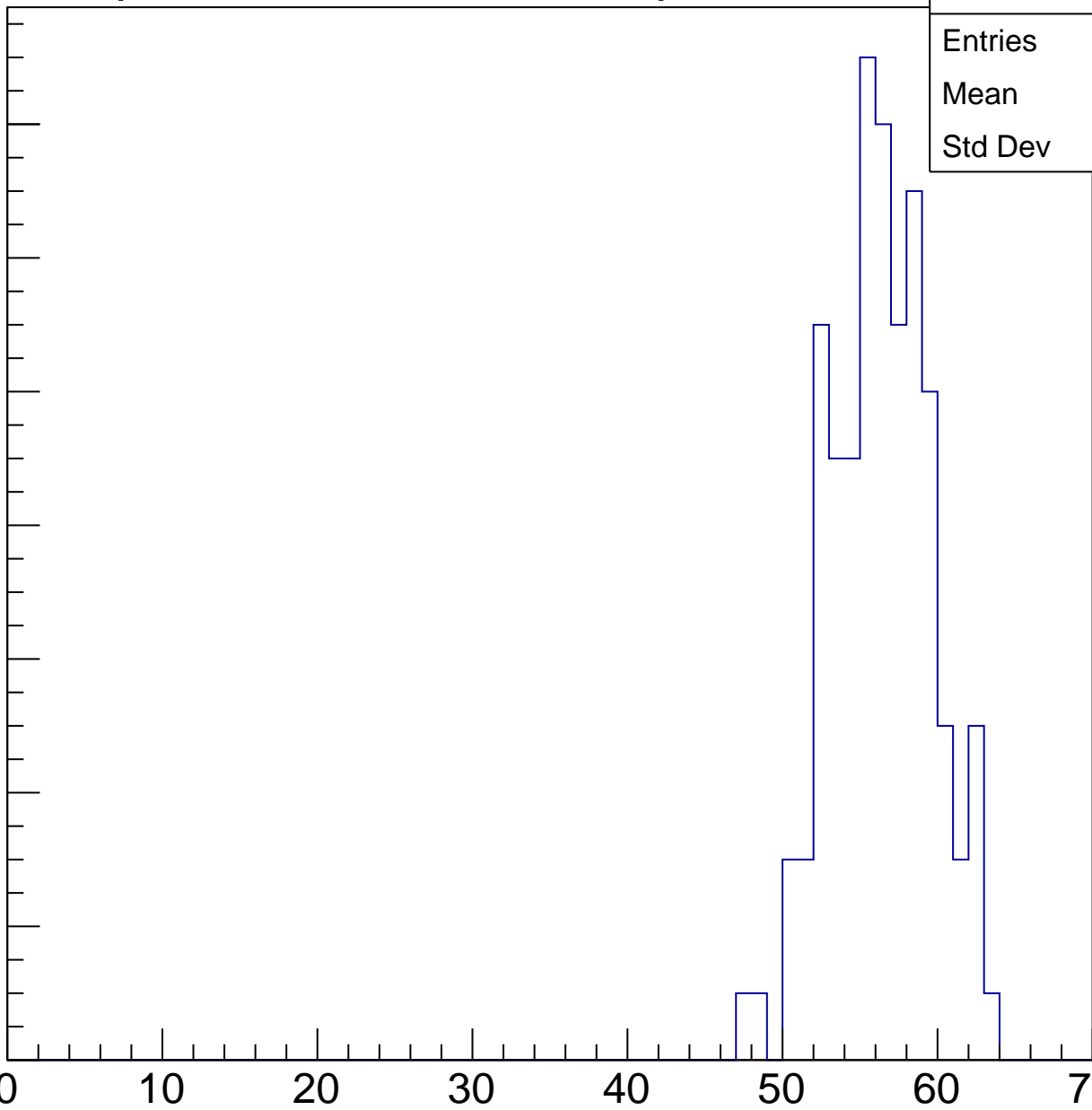
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	114
Mean	55.87
Std Dev	3.205

ampl



B1L001S, U25-ch115, adc5

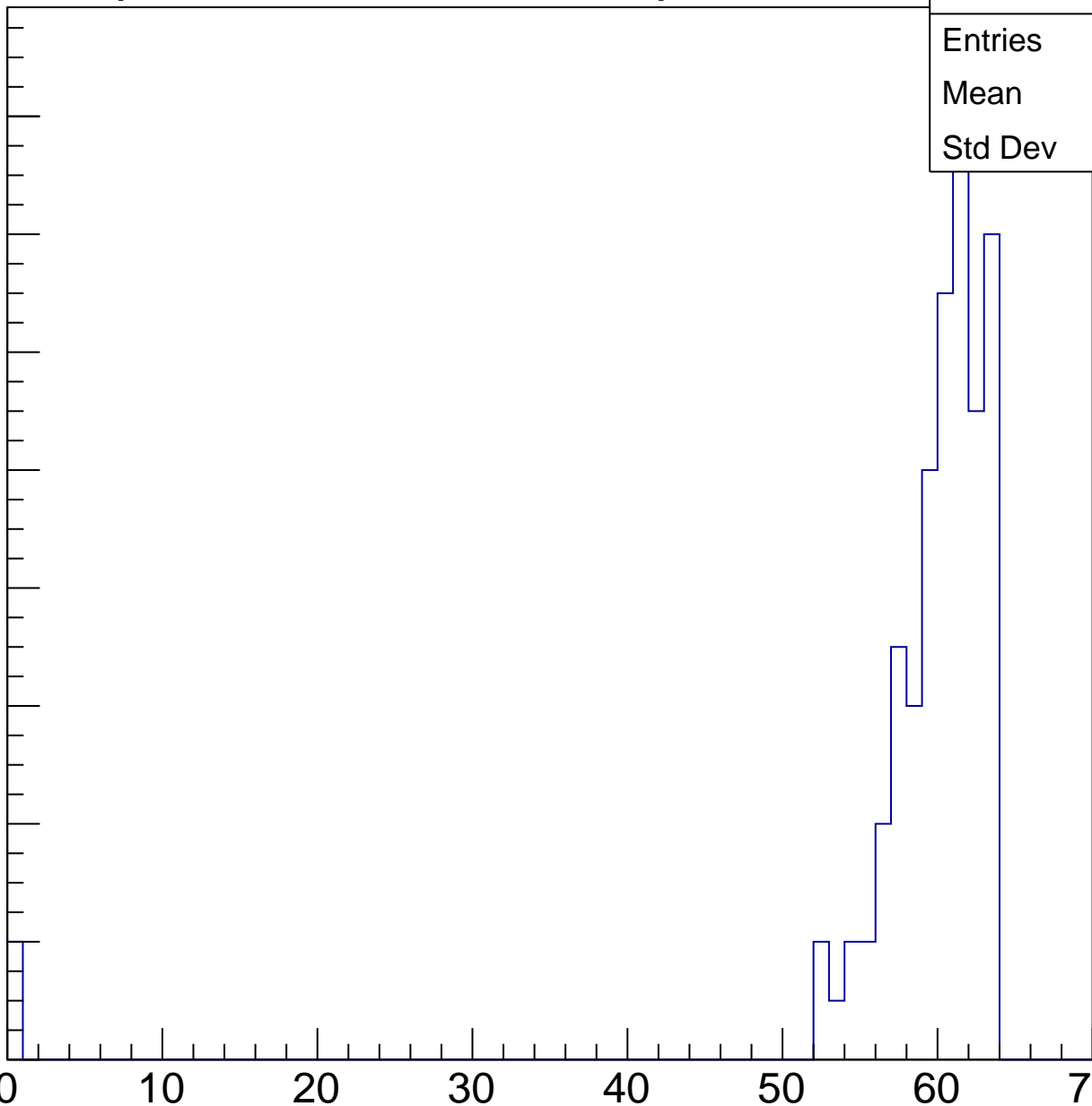
calib_packv5_042523_0143.root, FC#2, port C2

Entries	91
Mean	58.43
Std Dev	9.152

Entry

16
14
12
10
8
6
4
2
0

ampl

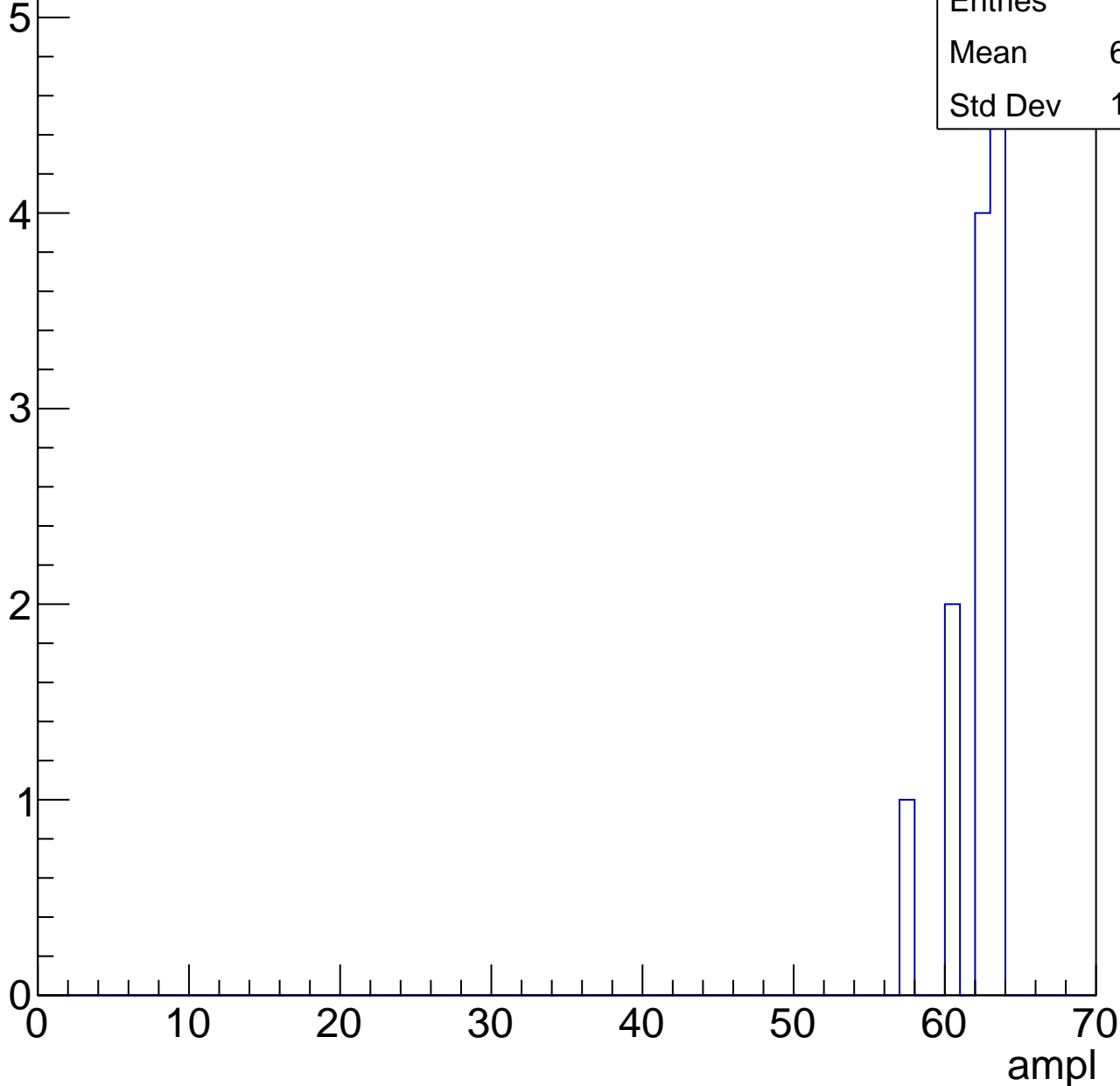


B1L001S, U25-ch115, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	61.67
Std Dev	1.748



B1L001S, U25-ch115, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch116, adc0

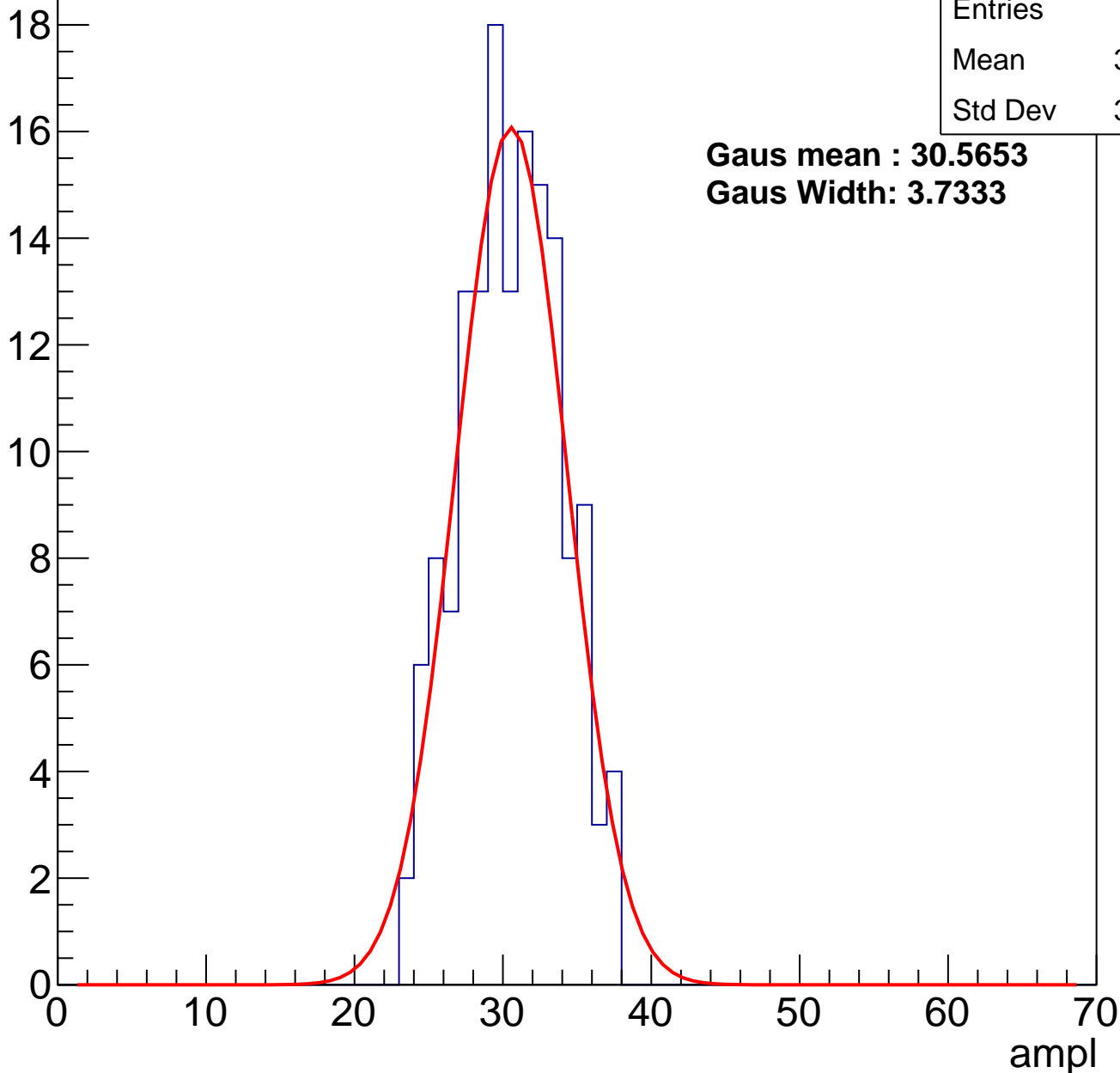
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	30.07
Std Dev	3.349

Gaus mean : 30.5653

Gaus Width: 3.7333

Entry



B1L001S, U25-ch116, adc1

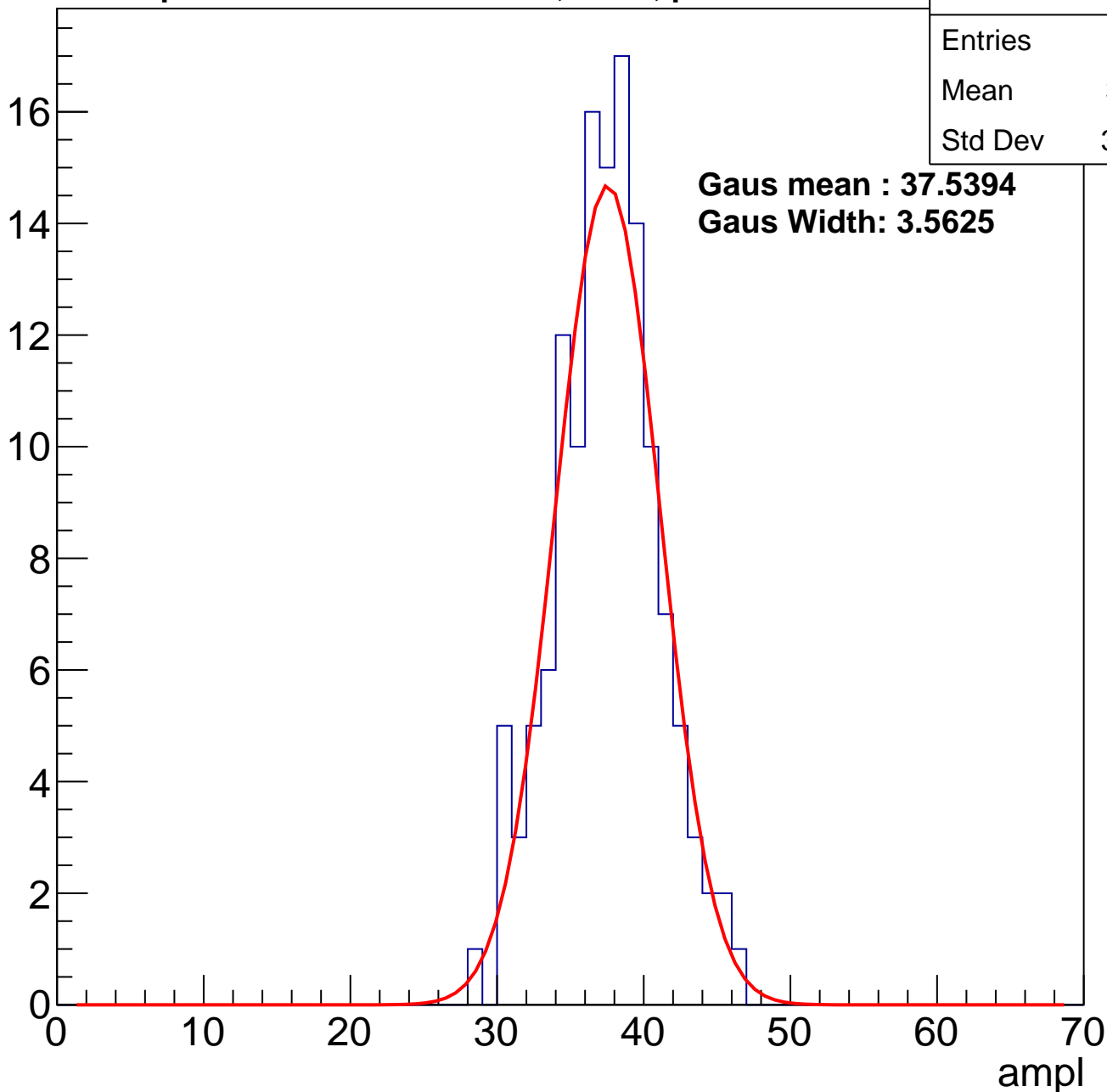
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	37.01
Std Dev	3.492

Gaus mean : 37.5394

Gaus Width: 3.5625

Entry



B1L001S, U25-ch116, adc2

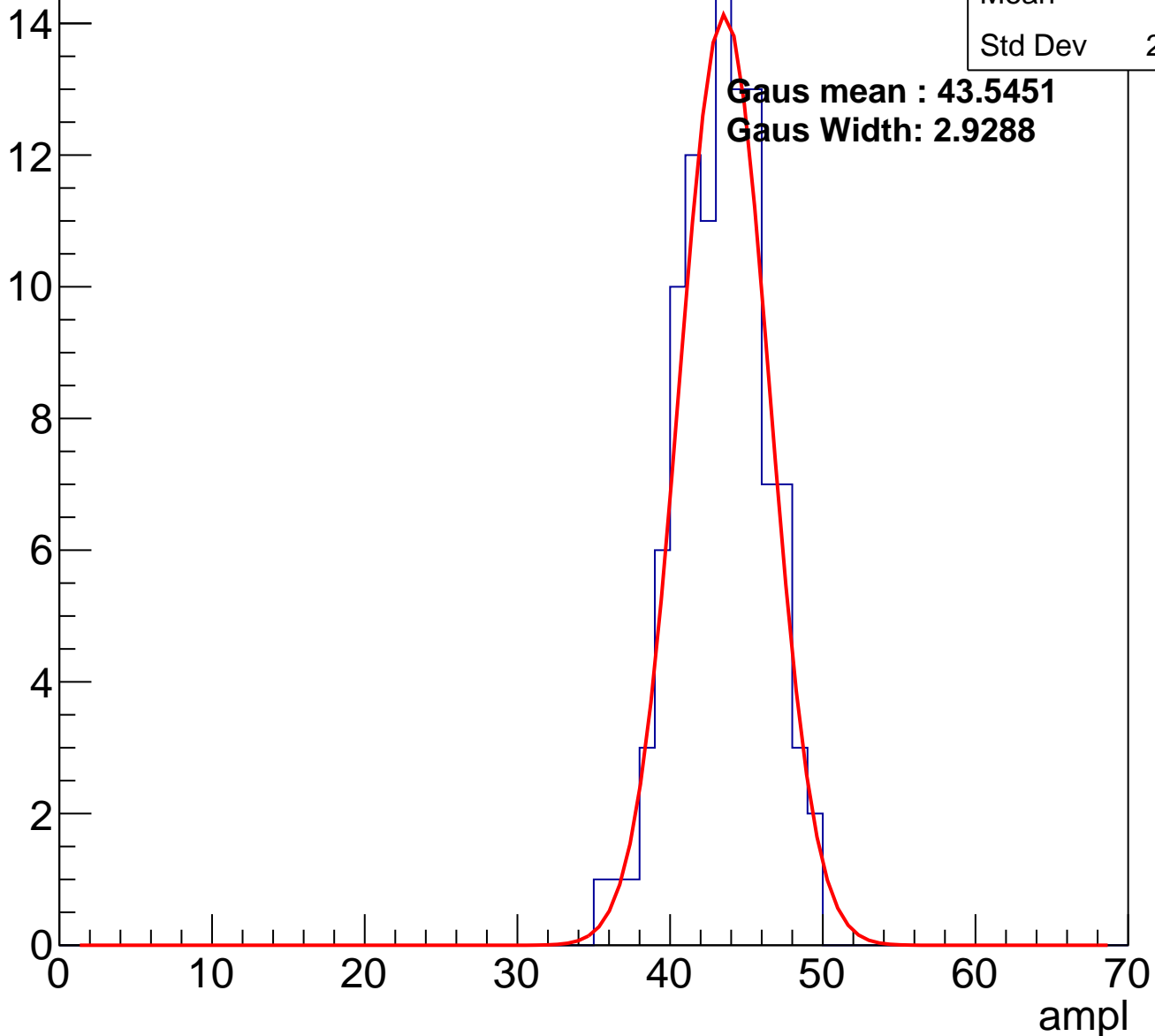
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	42.9
Std Dev	2.854

Gaus mean : 43.5451

Gaus Width: 2.9288

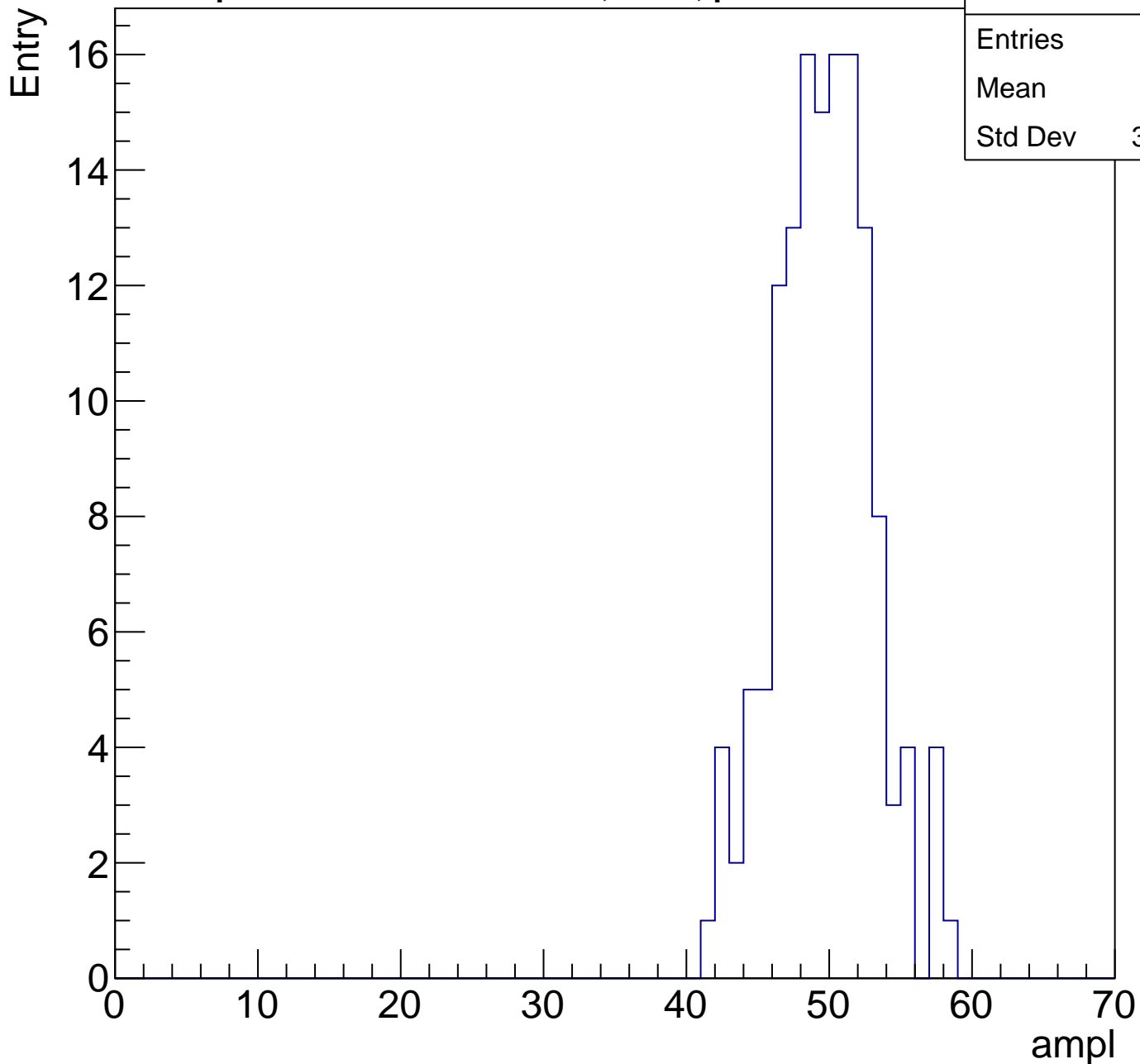
Entry



B1L001S, U25-ch116, adc3

calib_packv5_042523_0143.root, FC#2, port C2

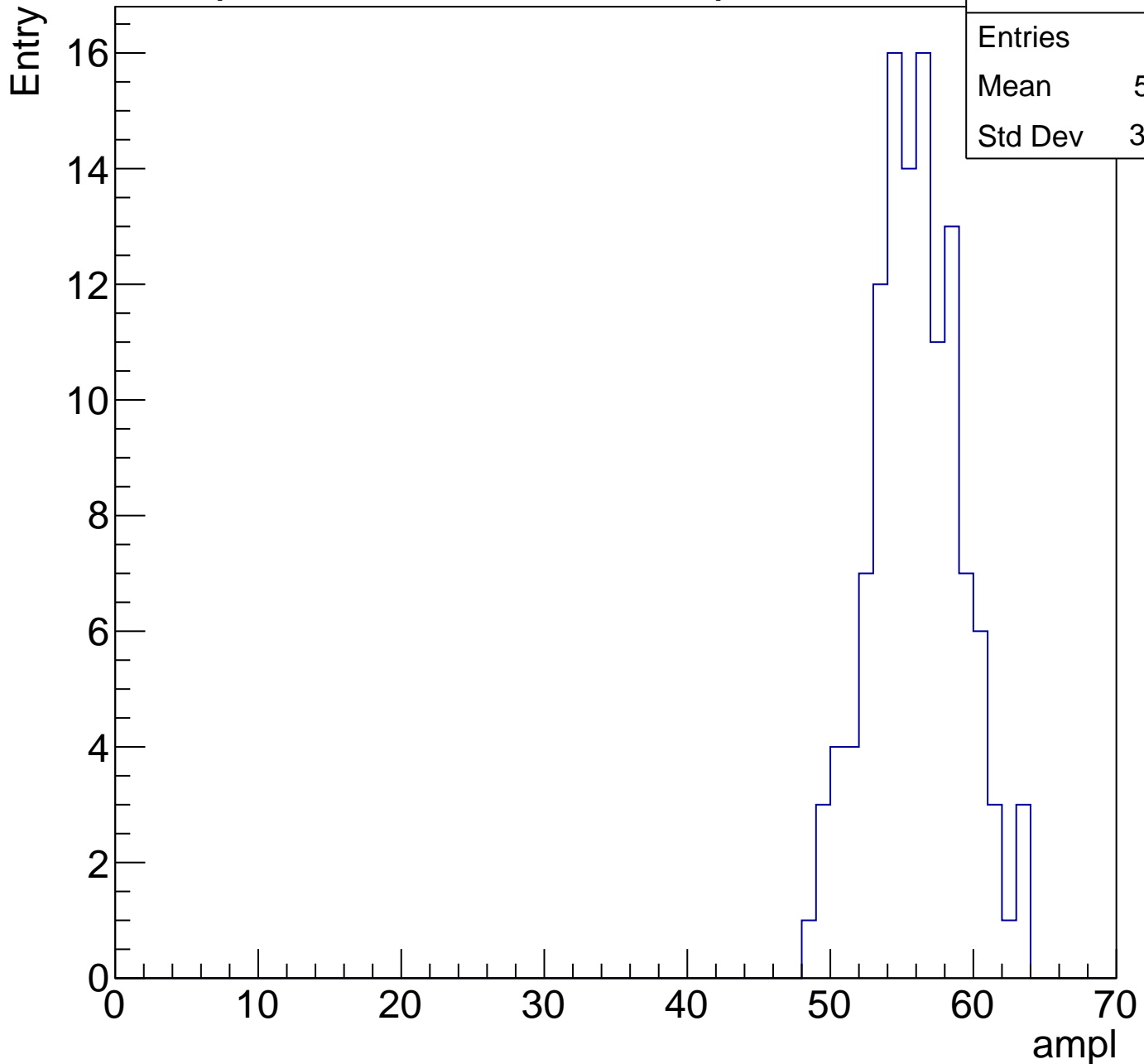
Entries	138
Mean	49.2
Std Dev	3.393



B1L001S, U25-ch116, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	55.51
Std Dev	3.155



B1L001S, U25-ch116, adc5

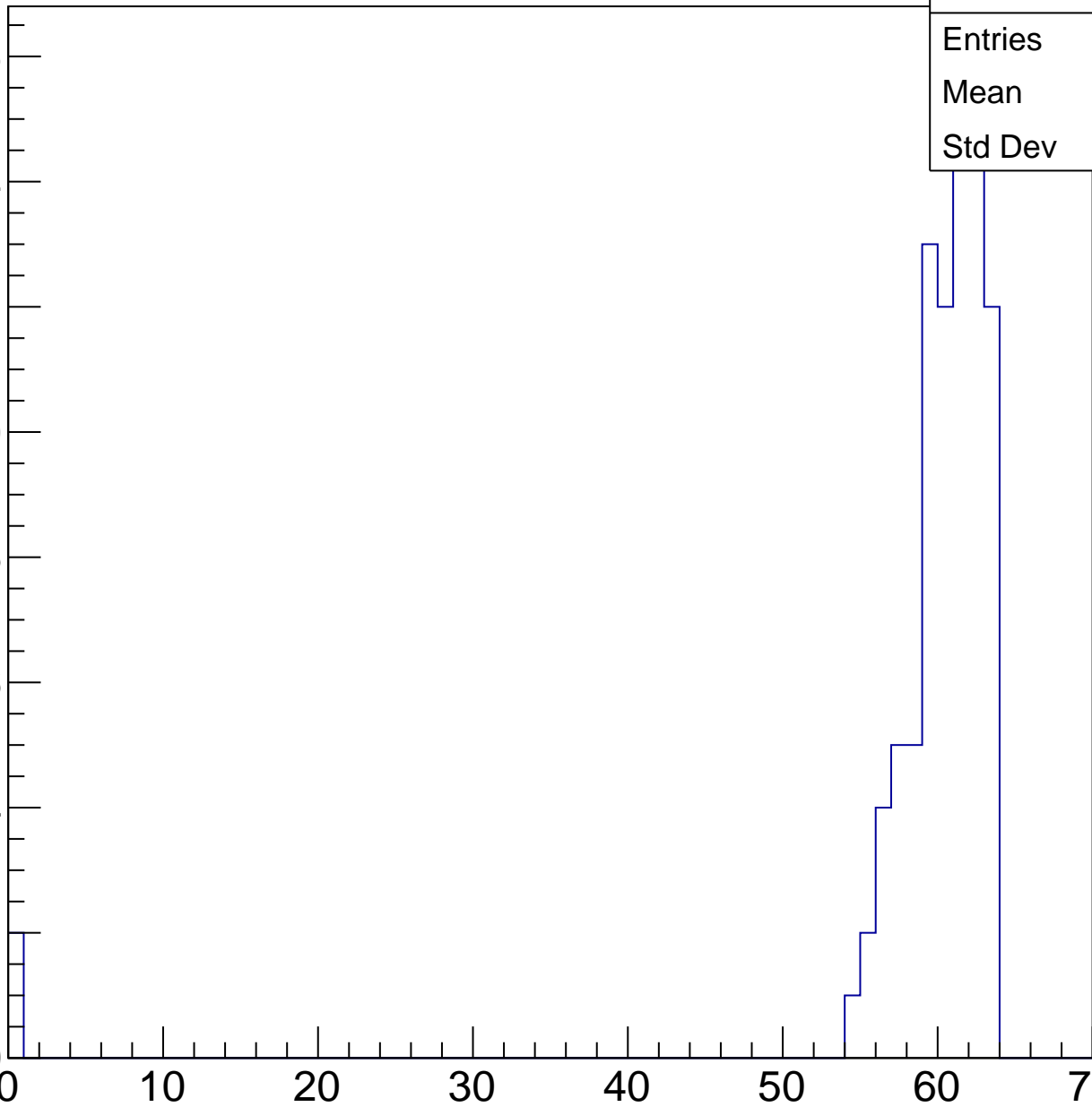
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.77
Std Dev	9.274

ampl

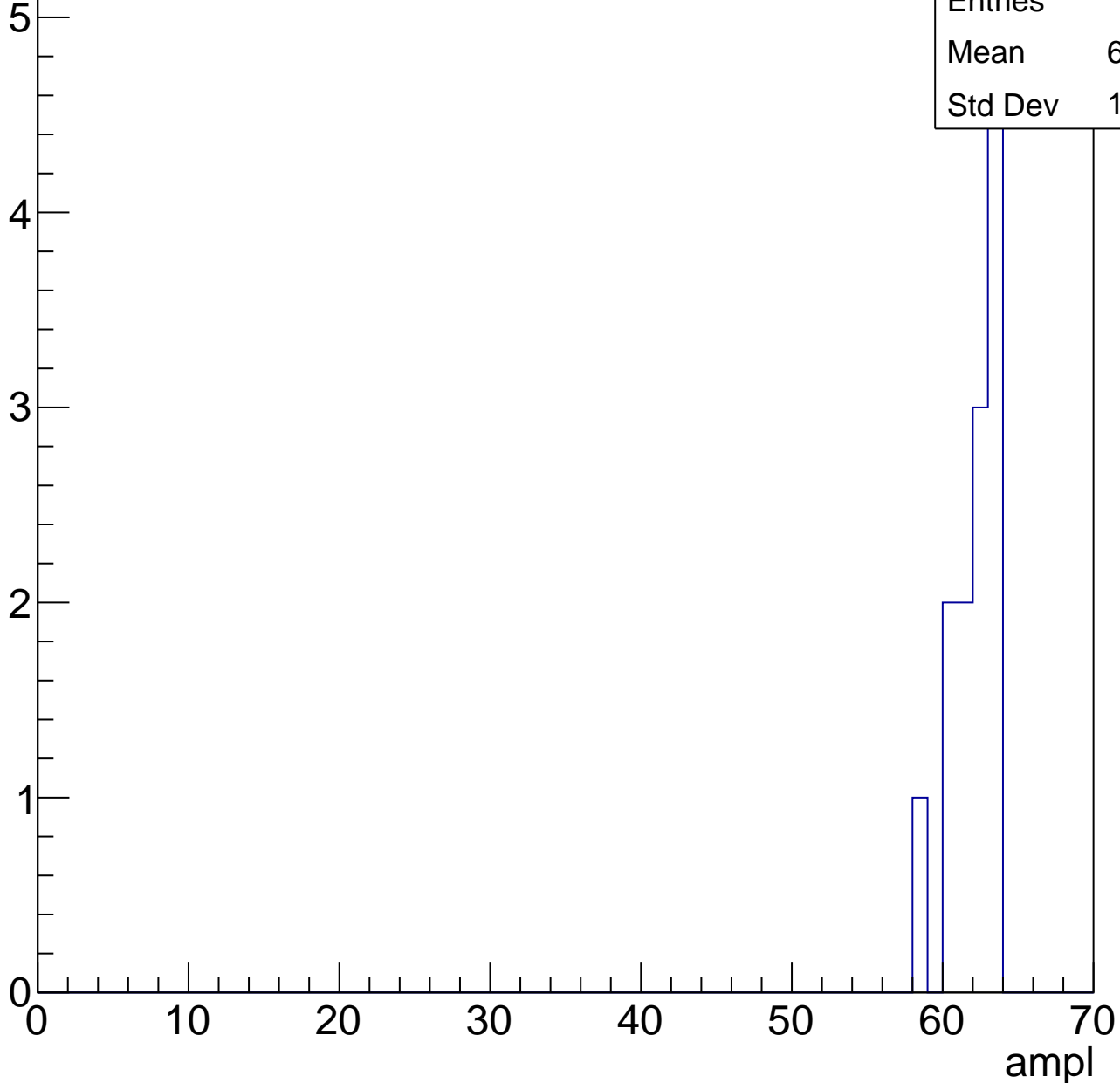


B1L001S, U25-ch116, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

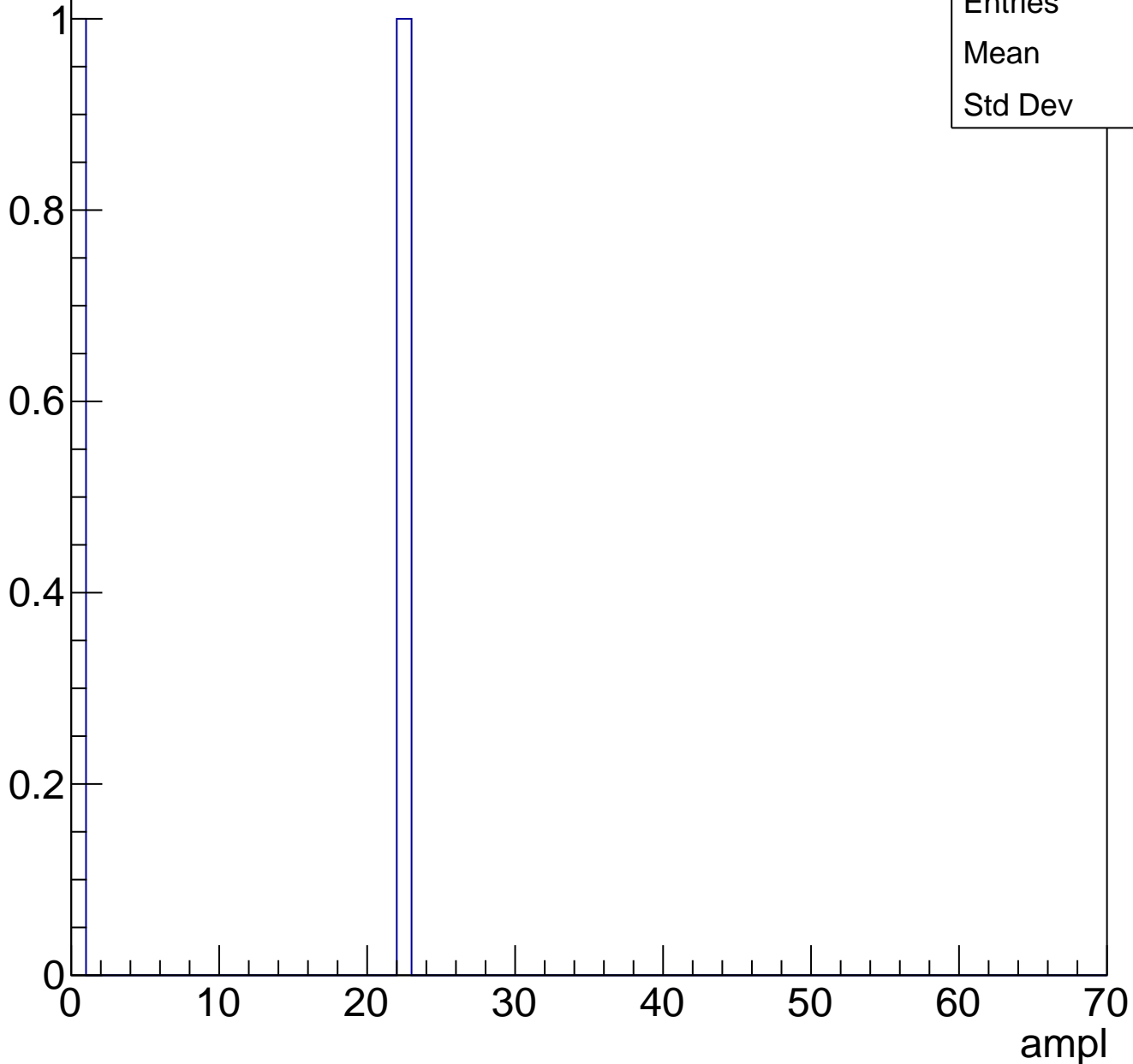
Entries	13
Mean	61.62
Std Dev	1.496



B1L001S, U25-ch116, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U25-ch117, adc0

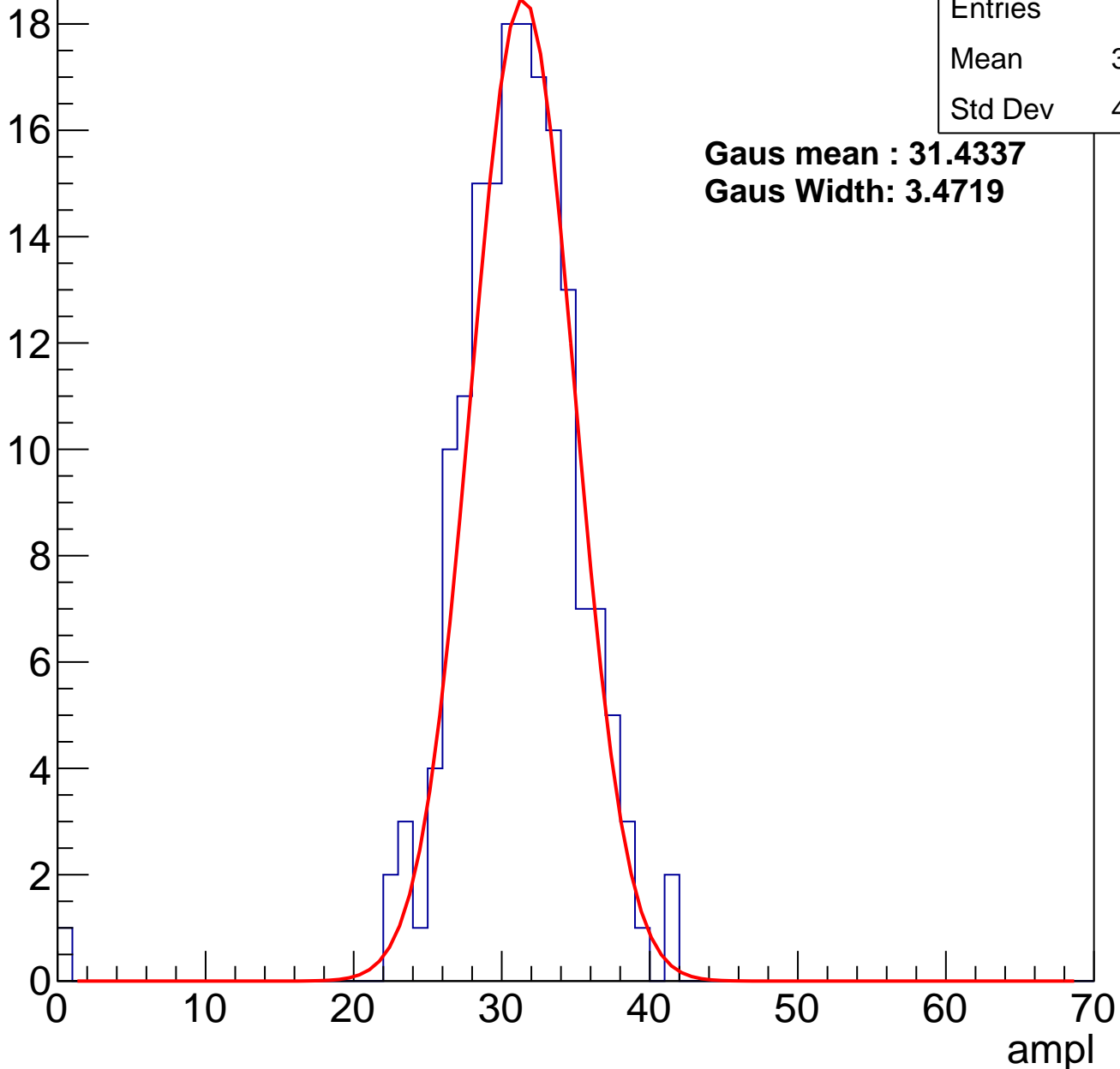
calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	30.64
Std Dev	4.352

Gaus mean : 31.4337

Gaus Width: 3.4719

Entry



B1L001S, U25-ch117, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	98
Mean	37.69
Std Dev	3.154

Gaus mean : 38.0553

Gaus Width: 3.4685

0

2

4

6

8

10

12

B1L001S, U25-ch117, adc2

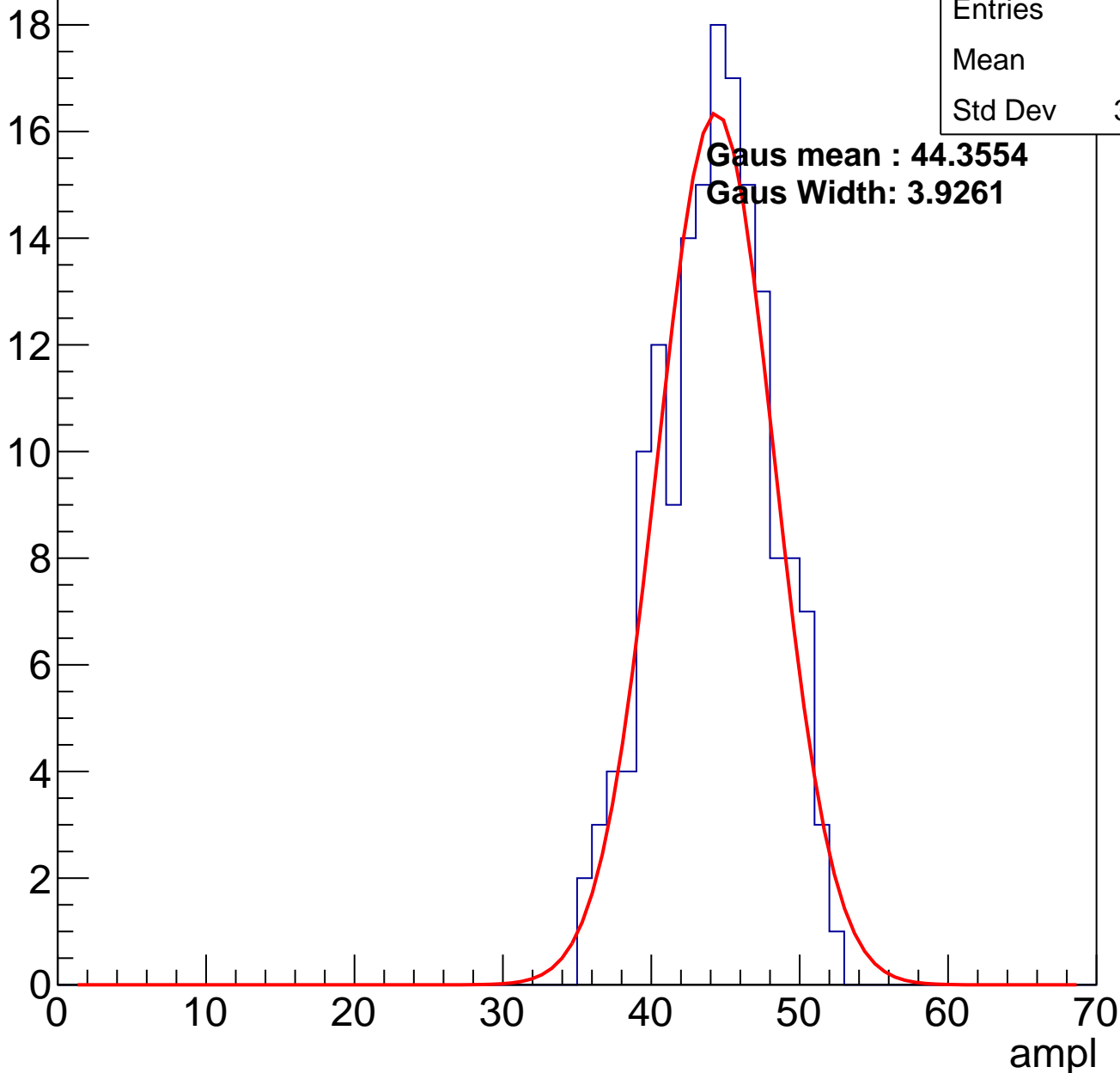
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	163
Mean	43.8
Std Dev	3.729

Gaus mean : 44.3554

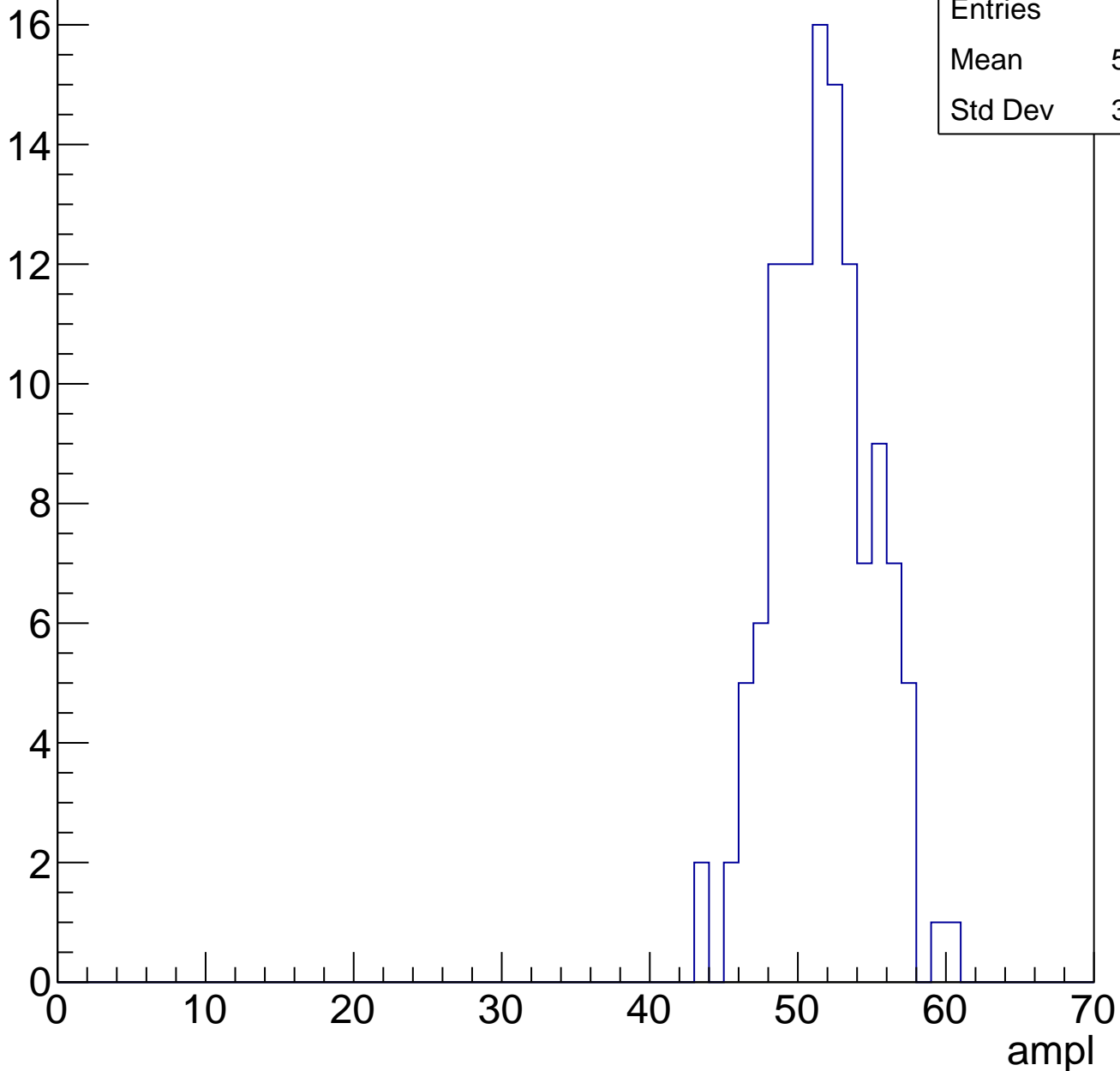
Gaus Width: 3.9261



B1L001S, U25-ch117, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch117, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	57.12
Std Dev	5.784

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

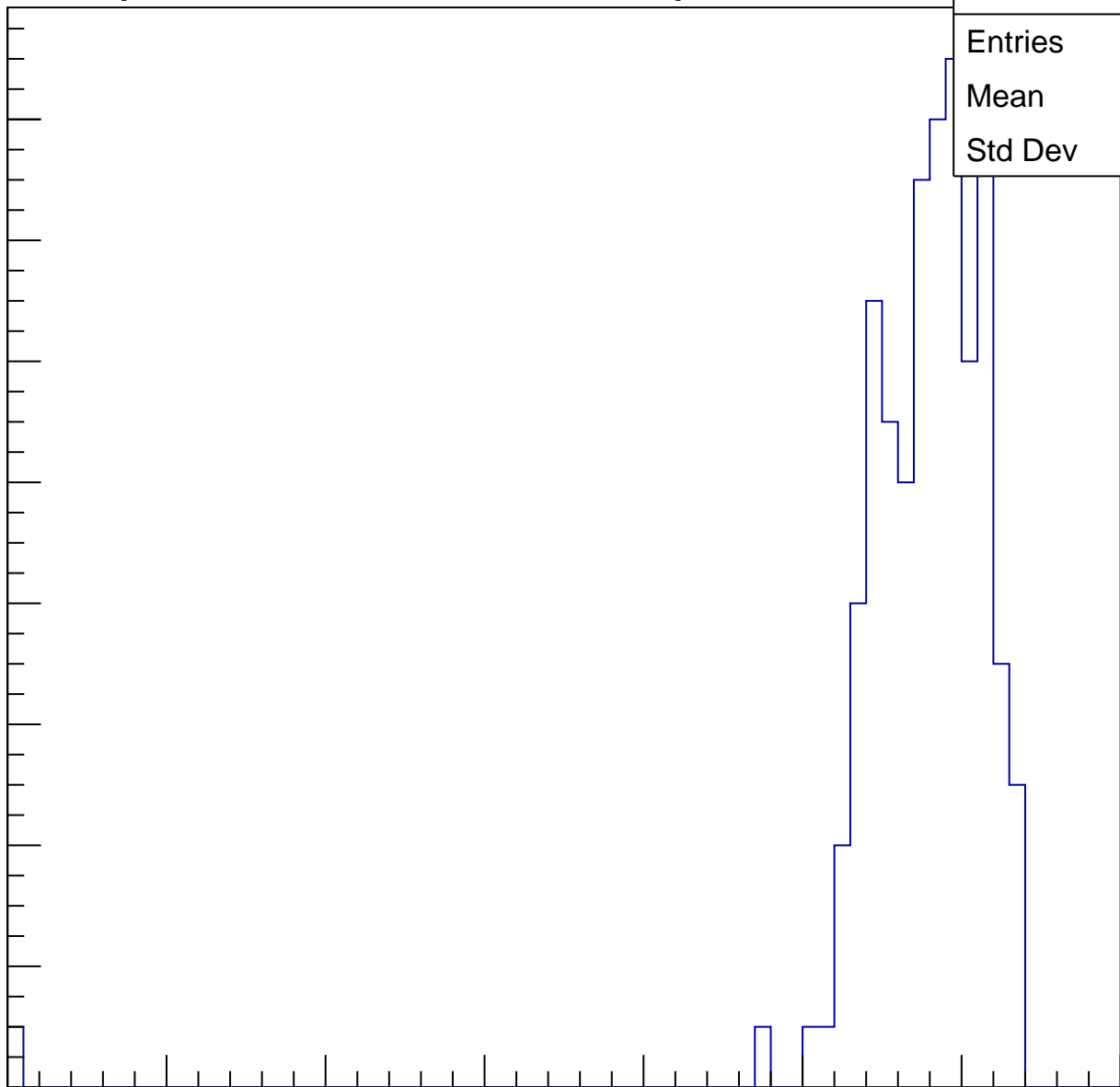
40

50

60

70

ampl



B1L001S, U25-ch117, adc5

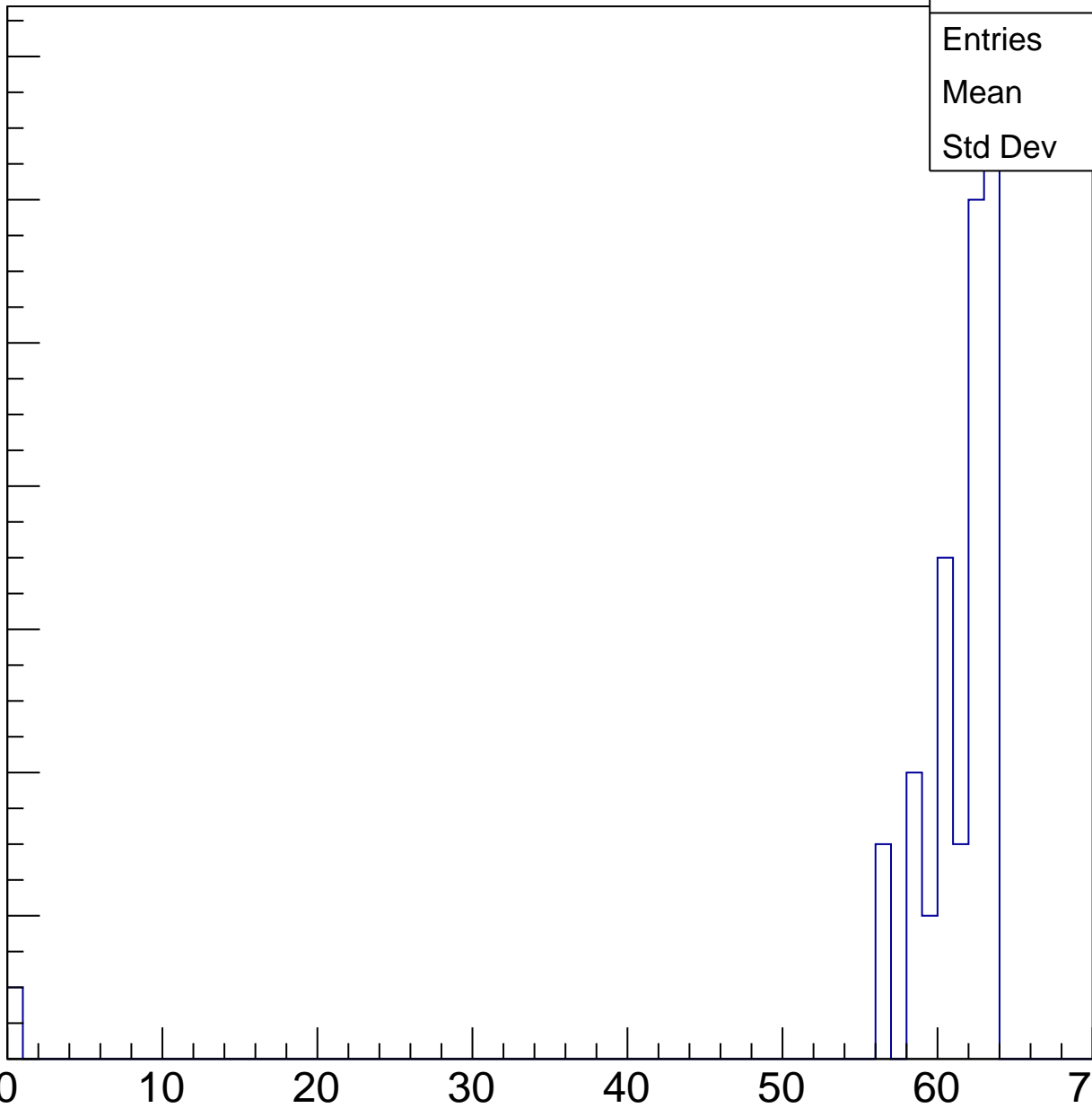
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	46
Mean	59.72
Std Dev	9.136

ampl



B1L001S, U25-ch117, adc6

calib_packv5_042523_0143.root, FC#2, port C2

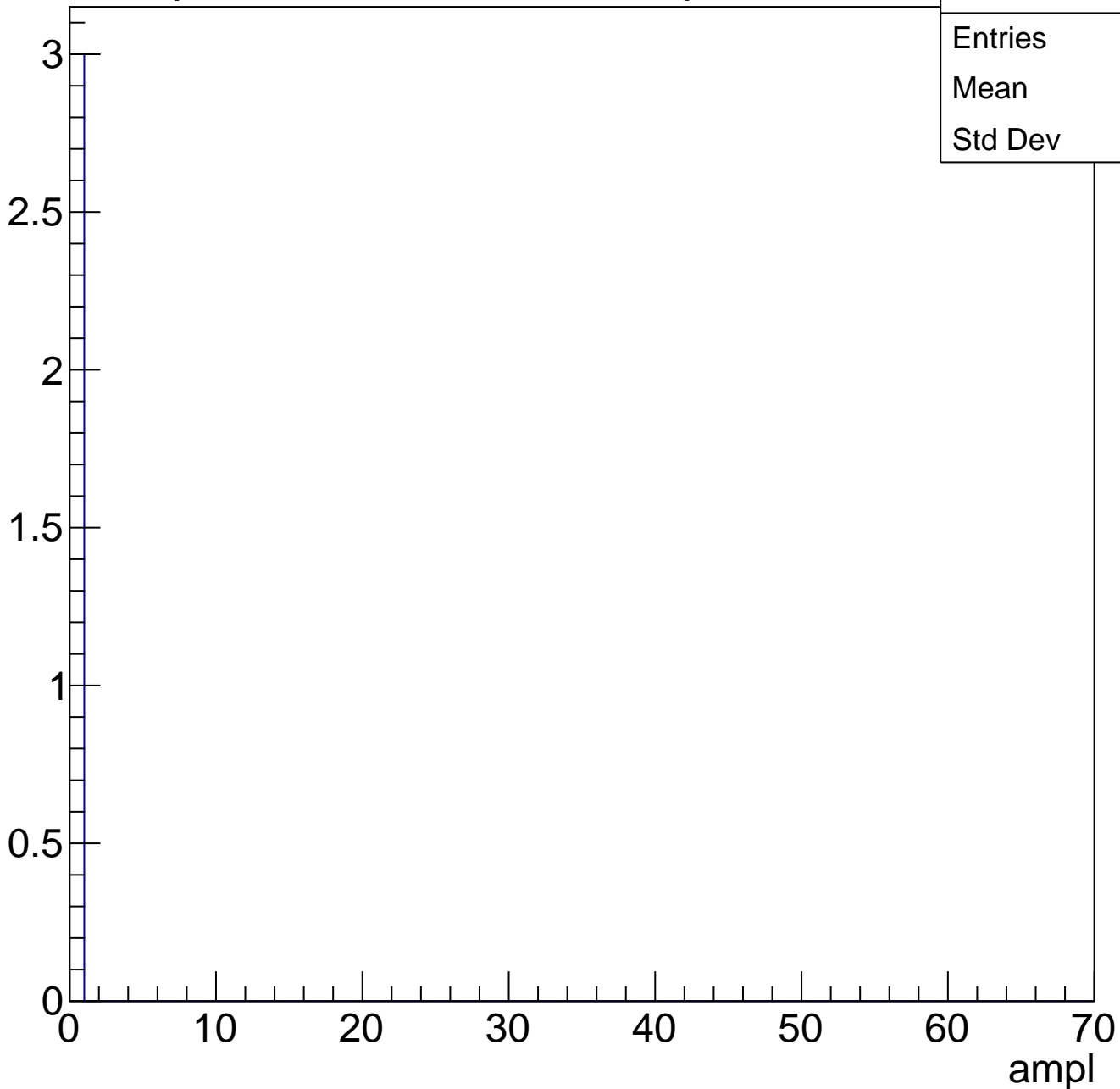
Entry



B1L001S, U25-ch117, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L001S, U25-ch118, adc0

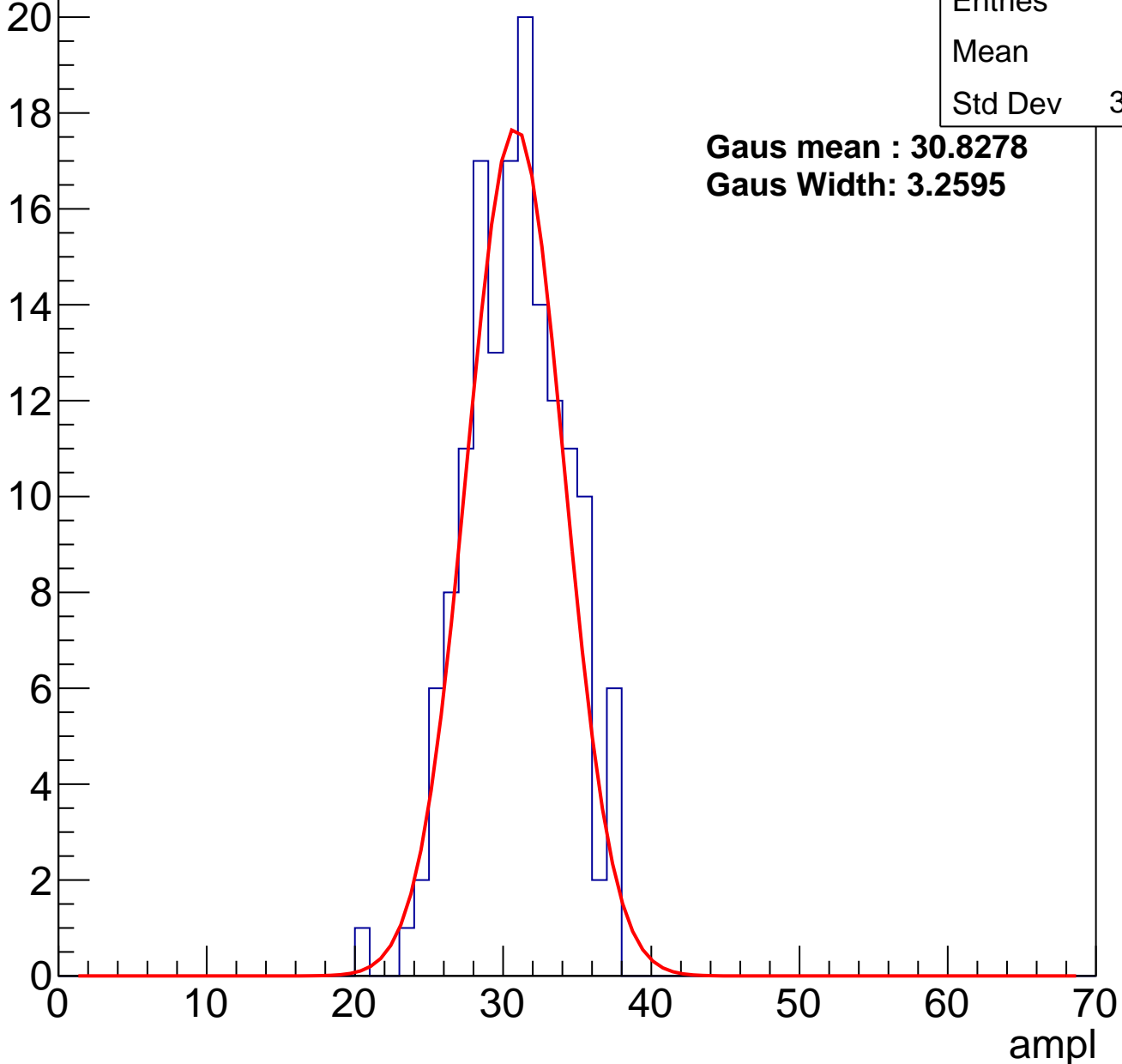
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	30.4
Std Dev	3.286

Gaus mean : 30.8278

Gaus Width: 3.2595

Entry



B1L001S, U25-ch118, adc1

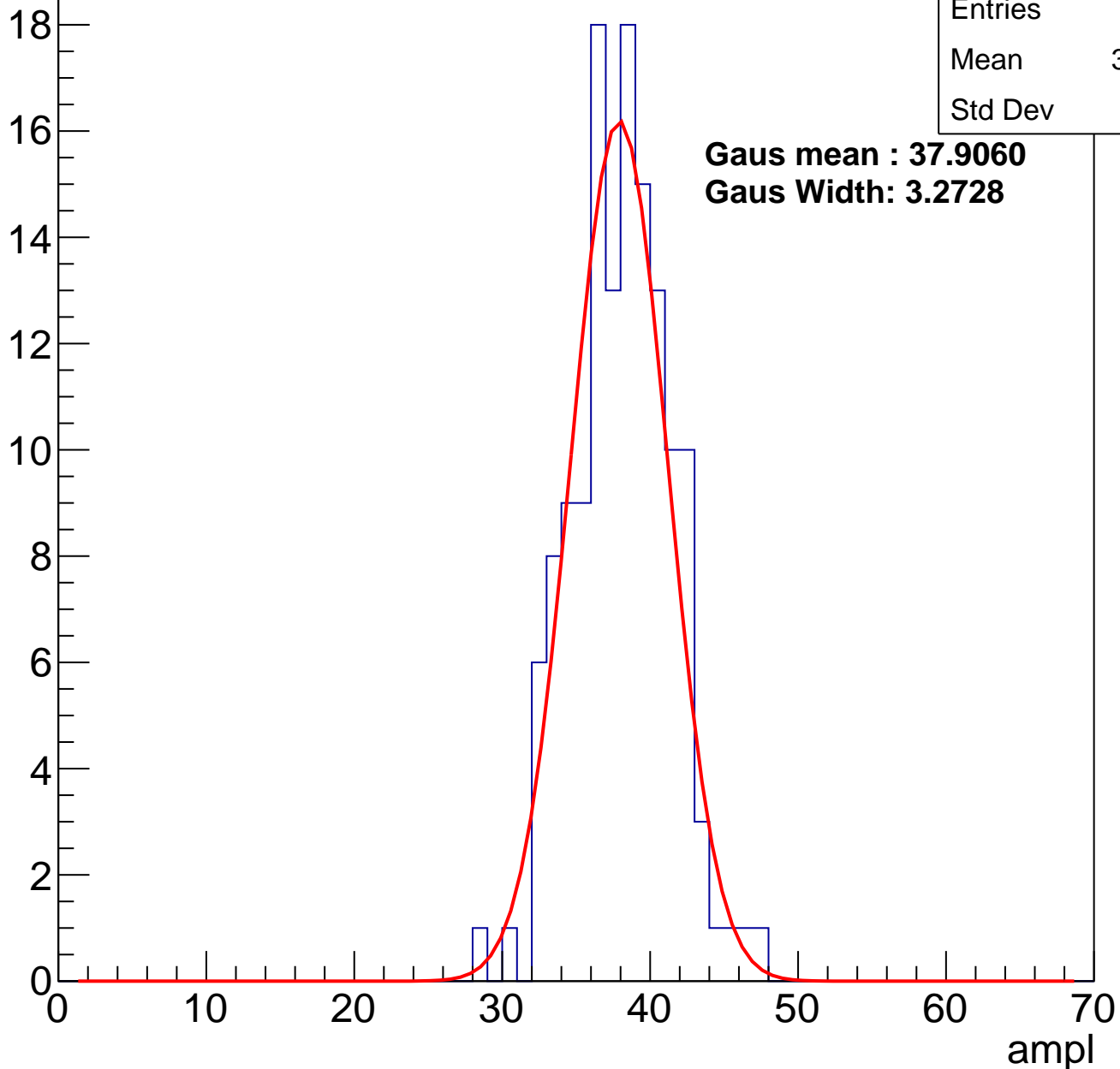
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	37.64
Std Dev	3.29

Gaus mean : 37.9060

Gaus Width: 3.2728

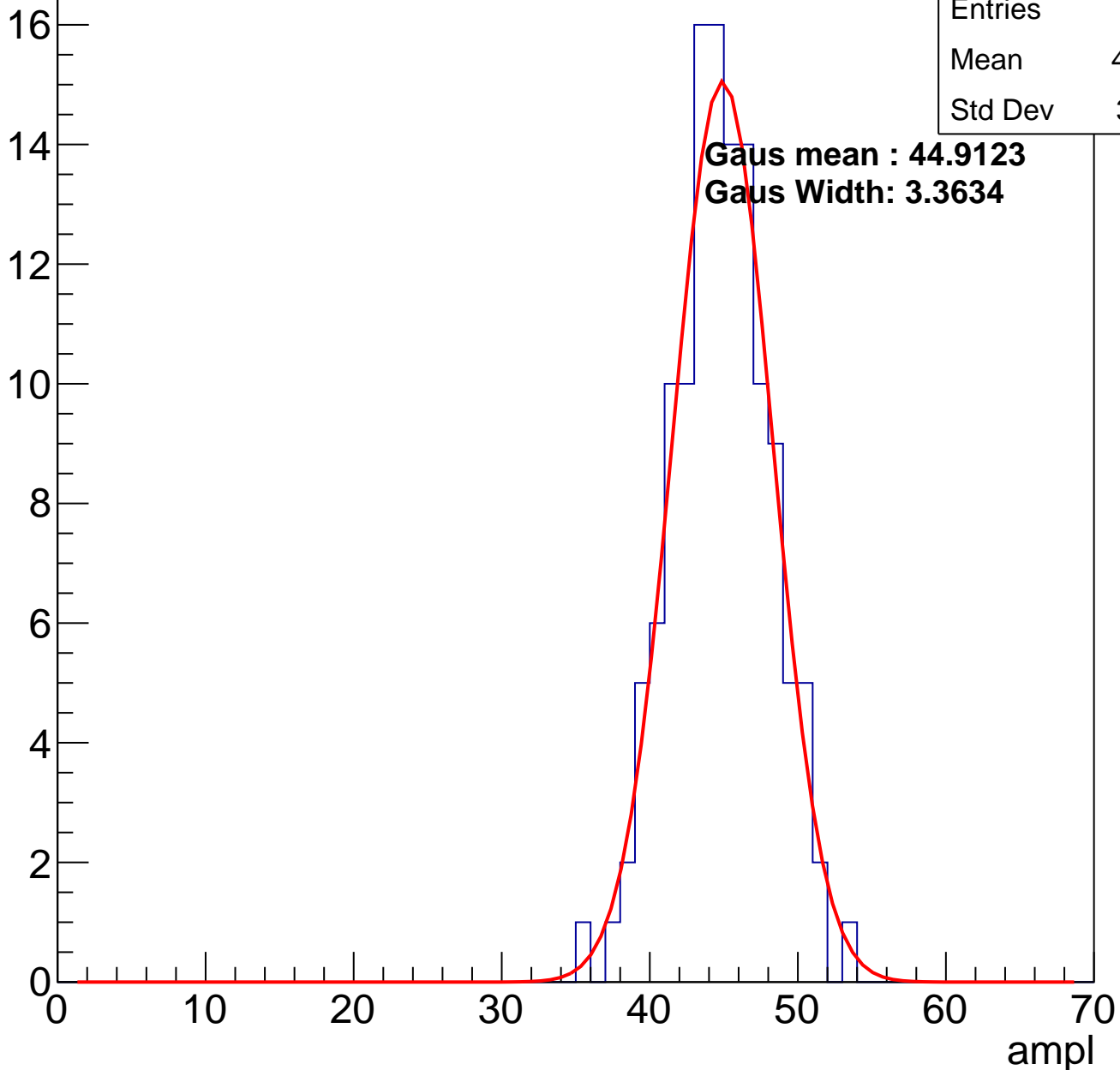
Entry



B1L001S, U25-ch118, adc2

calib_packv5_042523_0143.root, FC#2, port C2

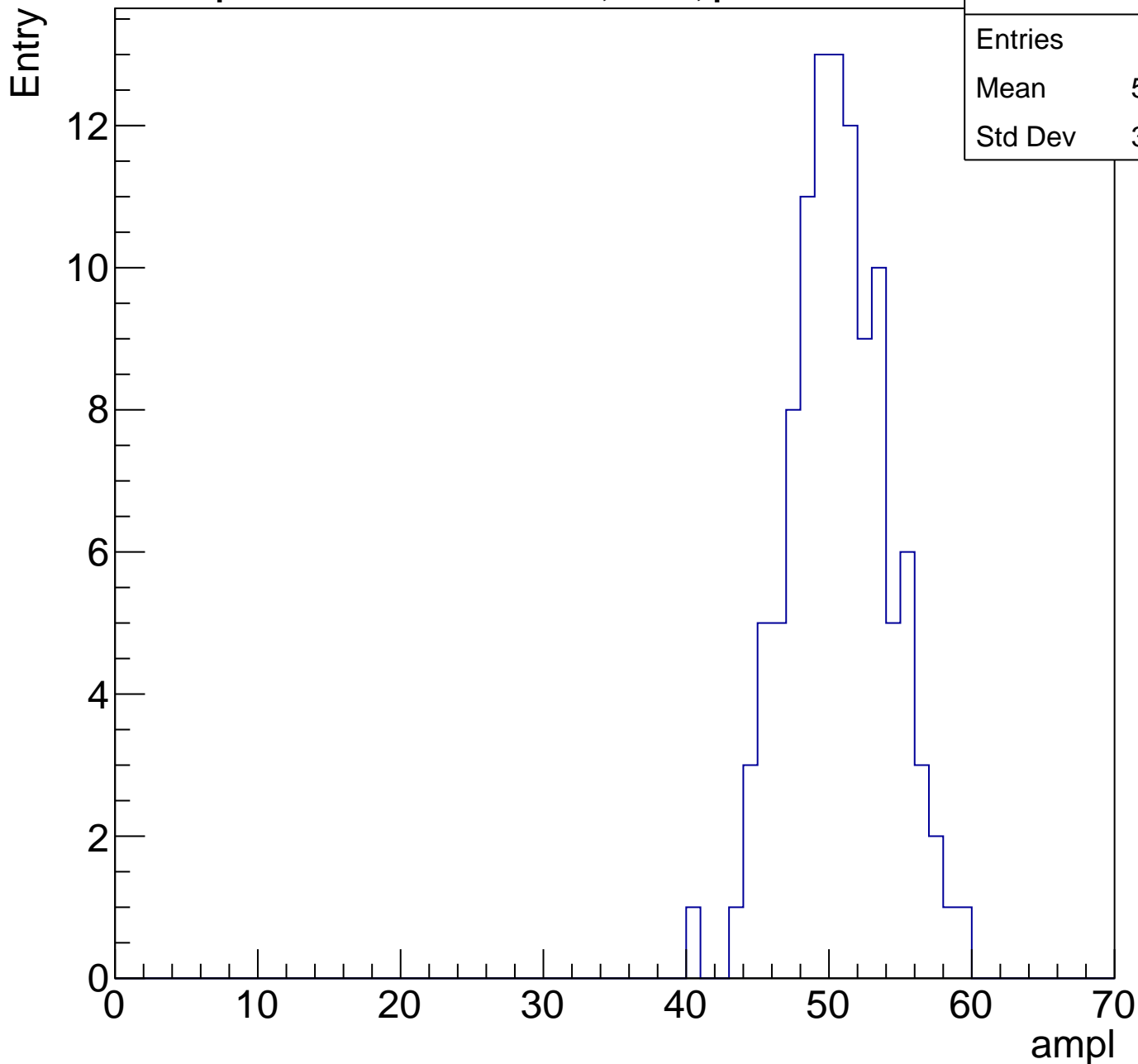
Entry



B1L001S, U25-ch118, adc3

calib_packv5_042523_0143.root, FC#2, port C2

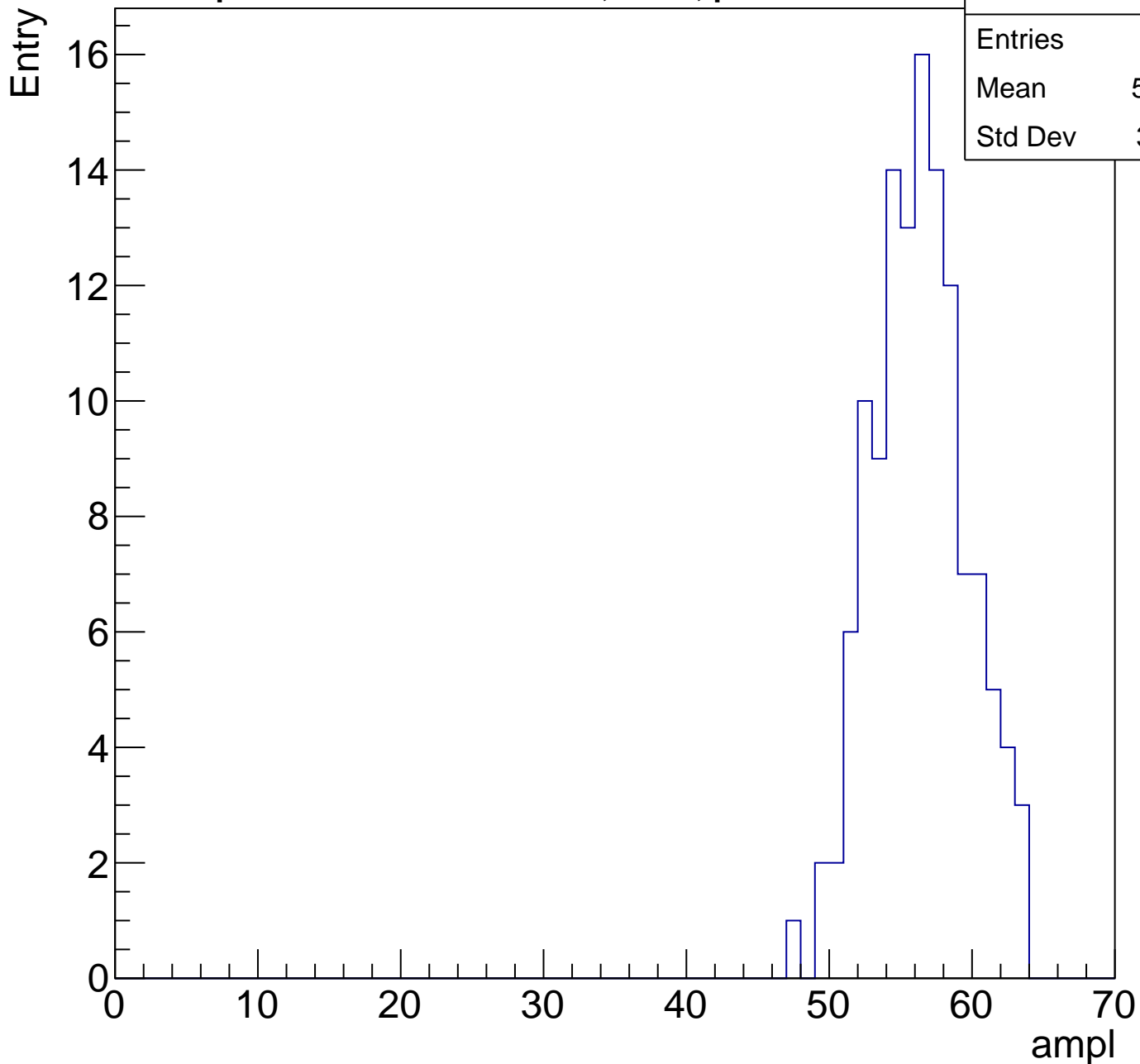
Entries	109
Mean	50.18
Std Dev	3.462



B1L001S, U25-ch118, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	55.87
Std Dev	3.301



B1L001S, U25-ch118, adc5

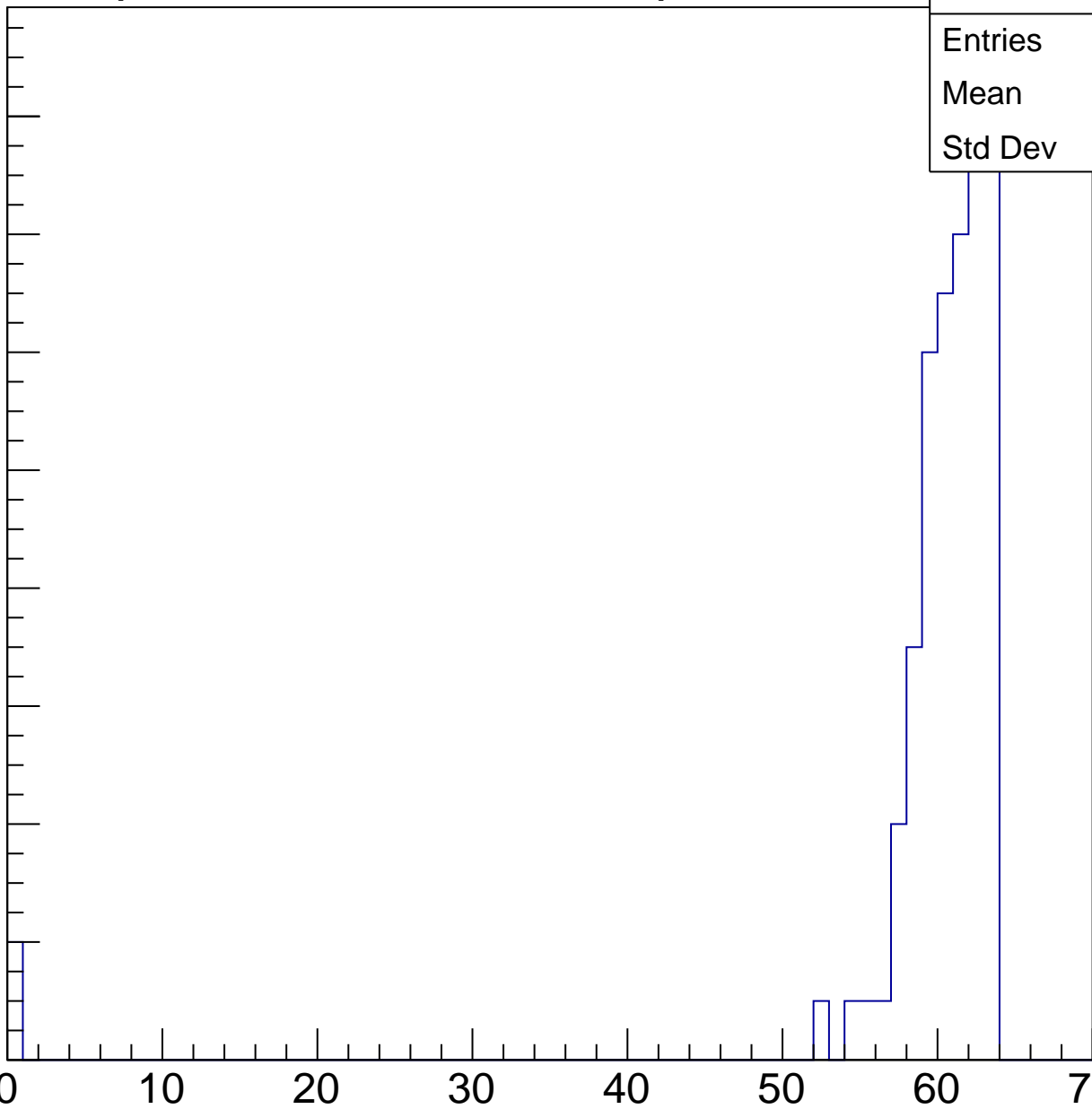
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.06
Std Dev	9.222

ampl



B1L001S, U25-ch118, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	61
Std Dev	0

B1L001S, U25-ch118, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

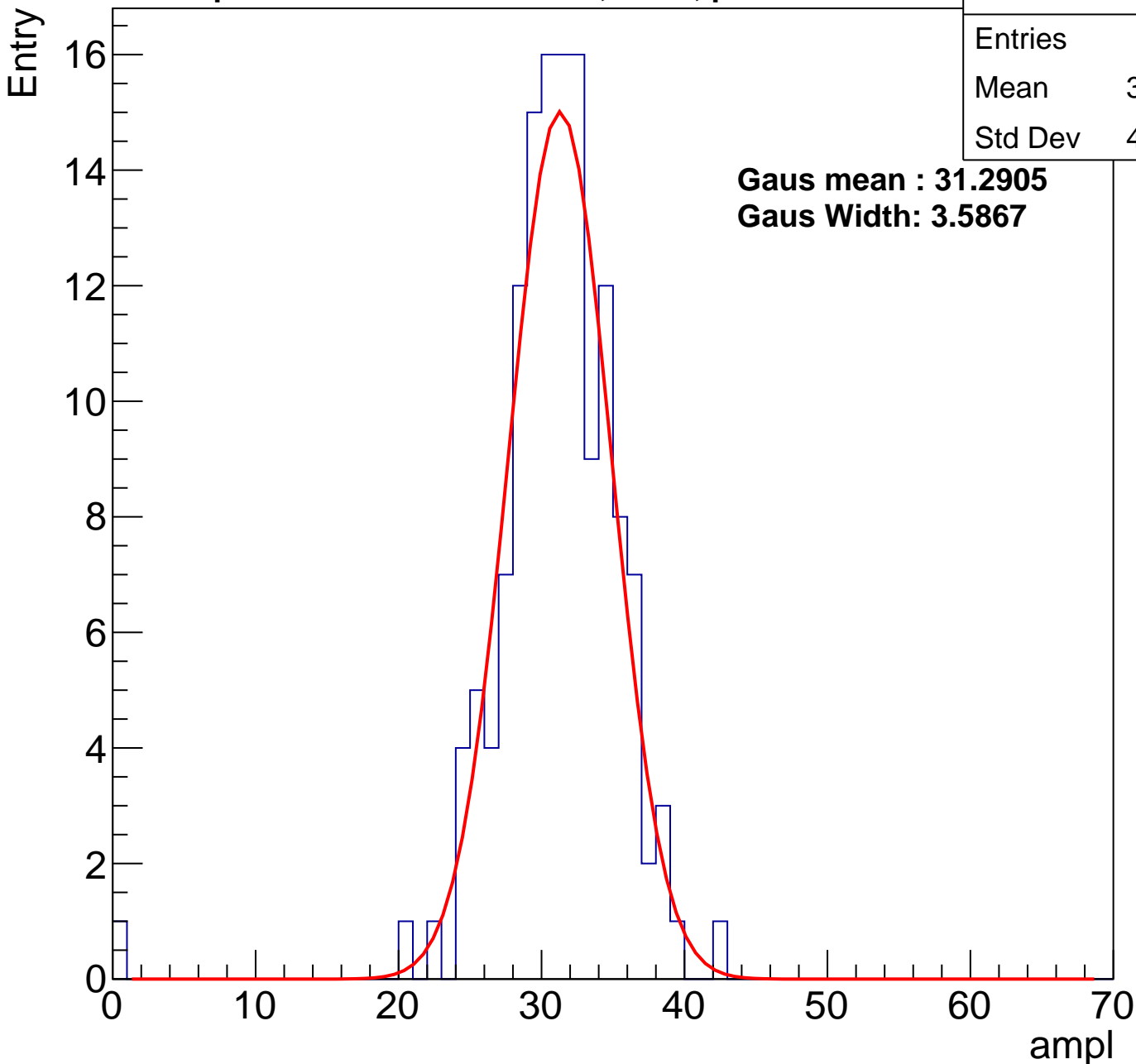


B1L001S, U25-ch119, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	30.65
Std Dev	4.444

Gaus mean : 31.2905
Gaus Width: 3.5867



B1L001S, U25-ch119, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	37.99
Std Dev	3.602

Gaus mean : 38.5288

Gaus Width: 3.6463

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

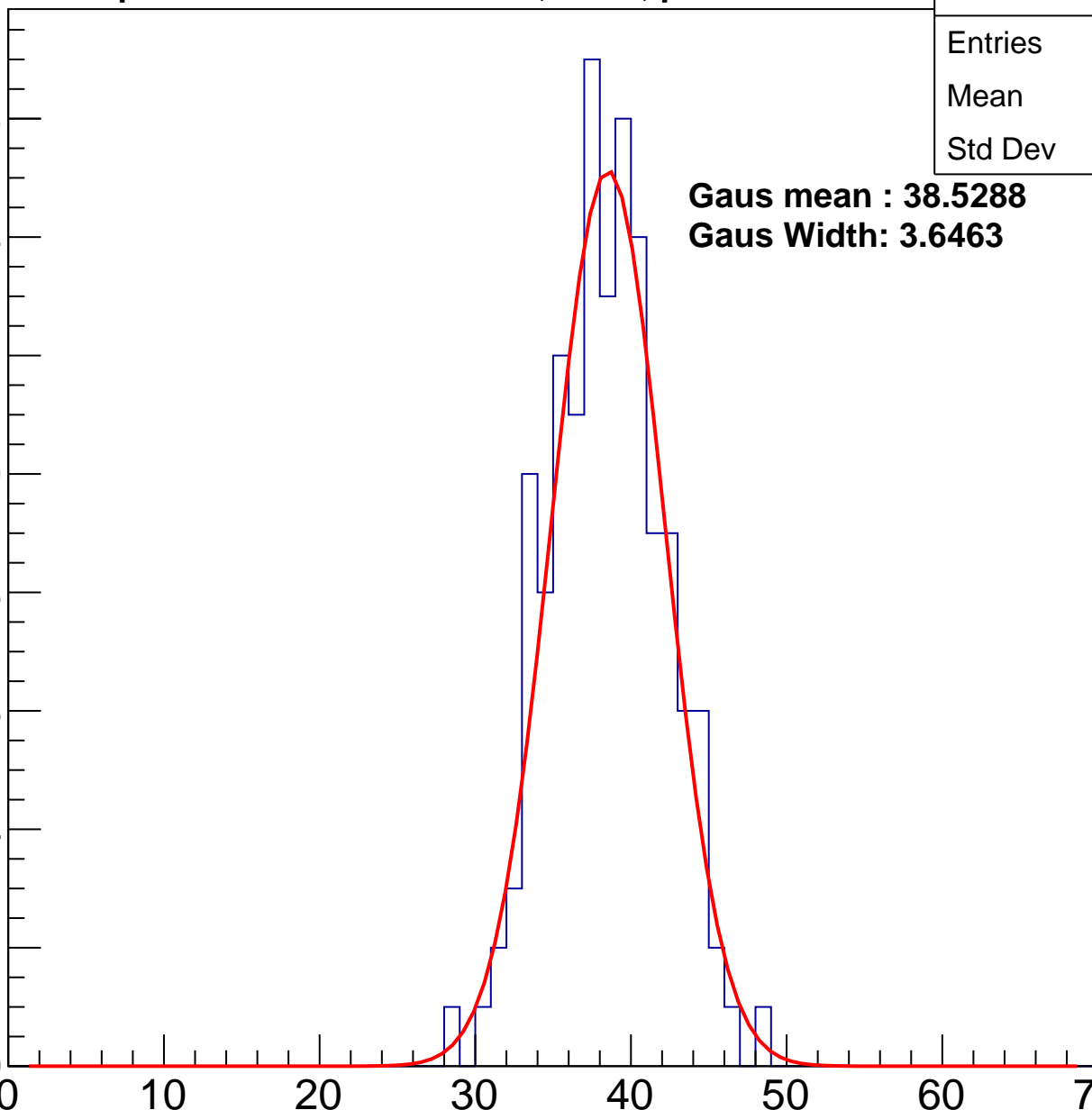
40

50

60

70

ampl



B1L001S, U25-ch119, adc2

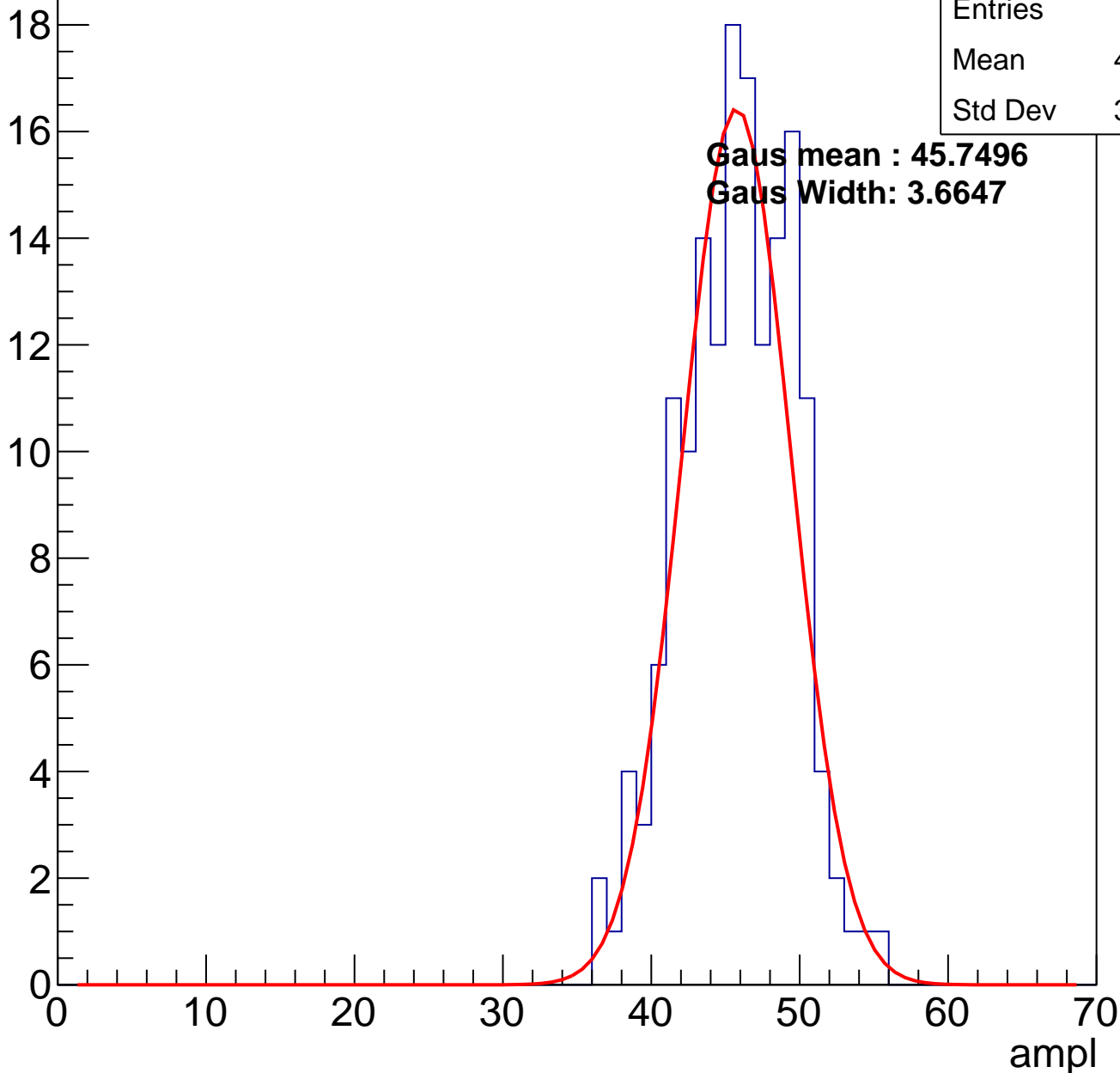
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	45.32
Std Dev	3.714

Gaus mean : 45.7496

Gaus Width: 3.6647

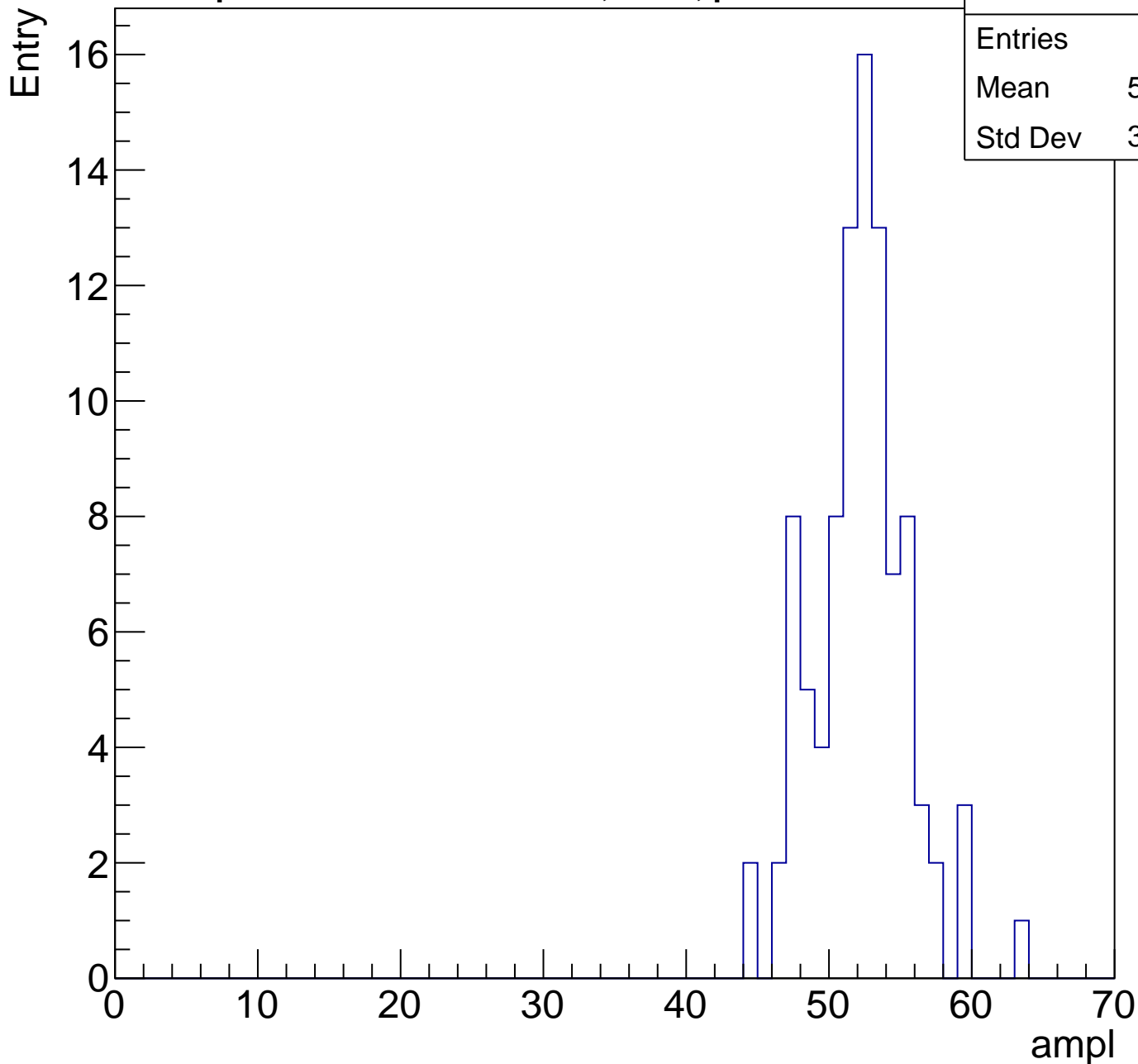
Entry



B1L001S, U25-ch119, adc3

calib_packv5_042523_0143.root, FC#2, port C2

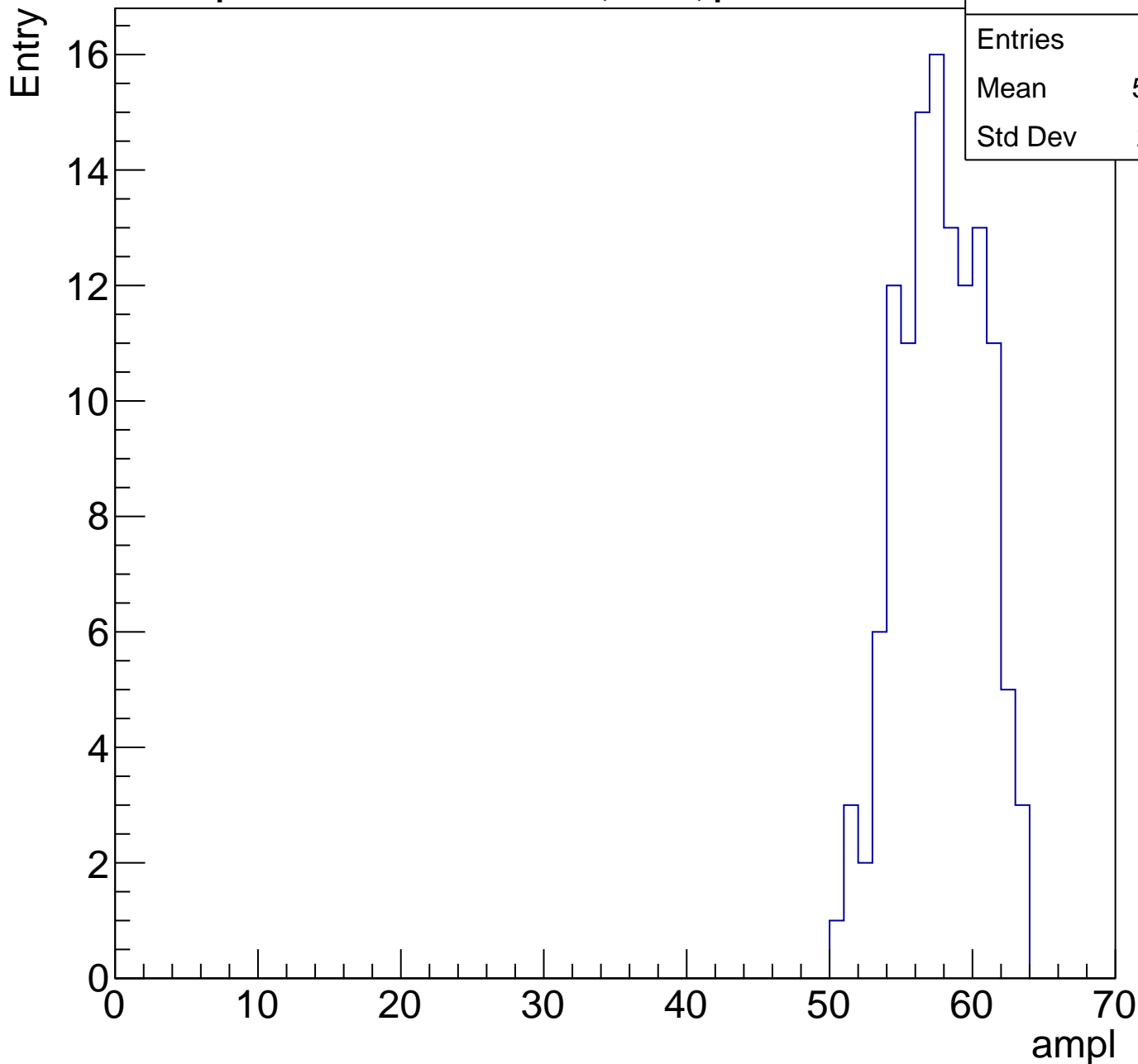
Entries	95
Mean	51.75
Std Dev	3.299



B1L001S, U25-ch119, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	57.25
Std Dev	2.921



B1L001S, U25-ch119, adc5

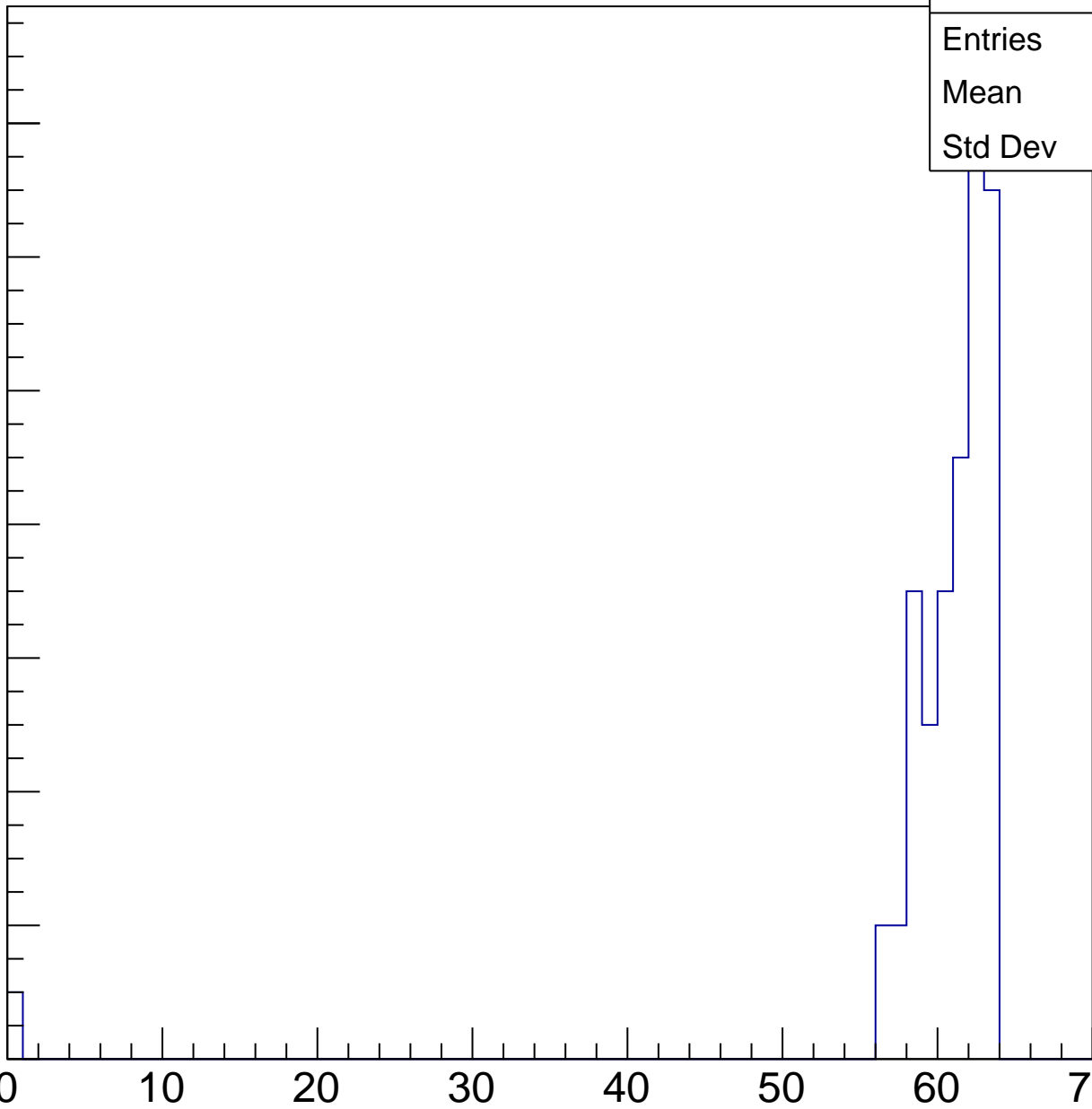
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	61
Mean	59.75
Std Dev	7.96

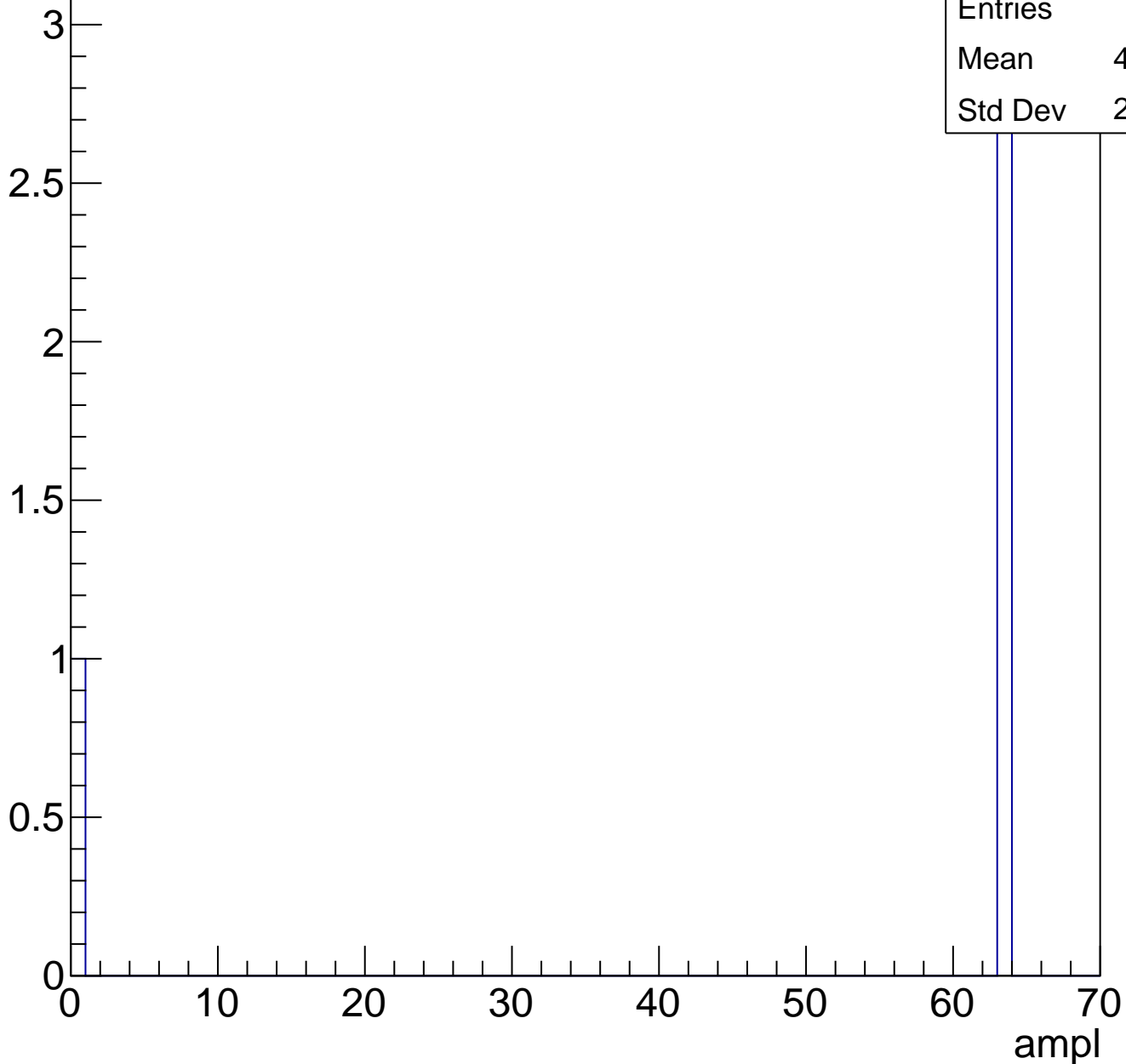
ampl



B1L001S, U25-ch119, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch119, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

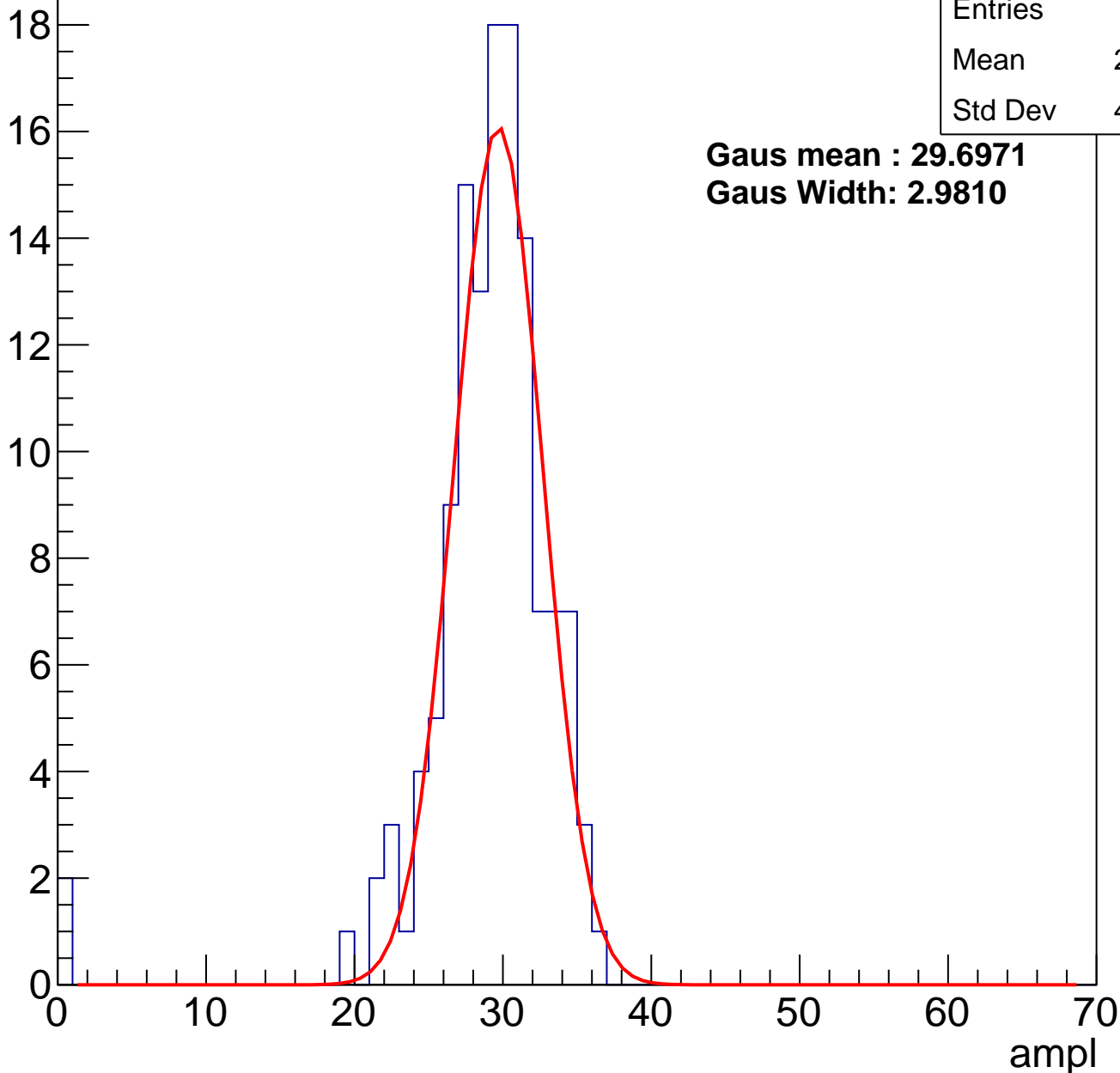


Entries	0
Mean	0
Std Dev	0

B1L001S, U25-ch120, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	130
Mean	28.49
Std Dev	4.783

B1L001S, U25-ch120, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	35.72
Std Dev	3.321

Gaus mean : 36.3967

Gaus Width: 3.6995

Entry

14

12

10

8

6

4

2

0

0

10

20

30

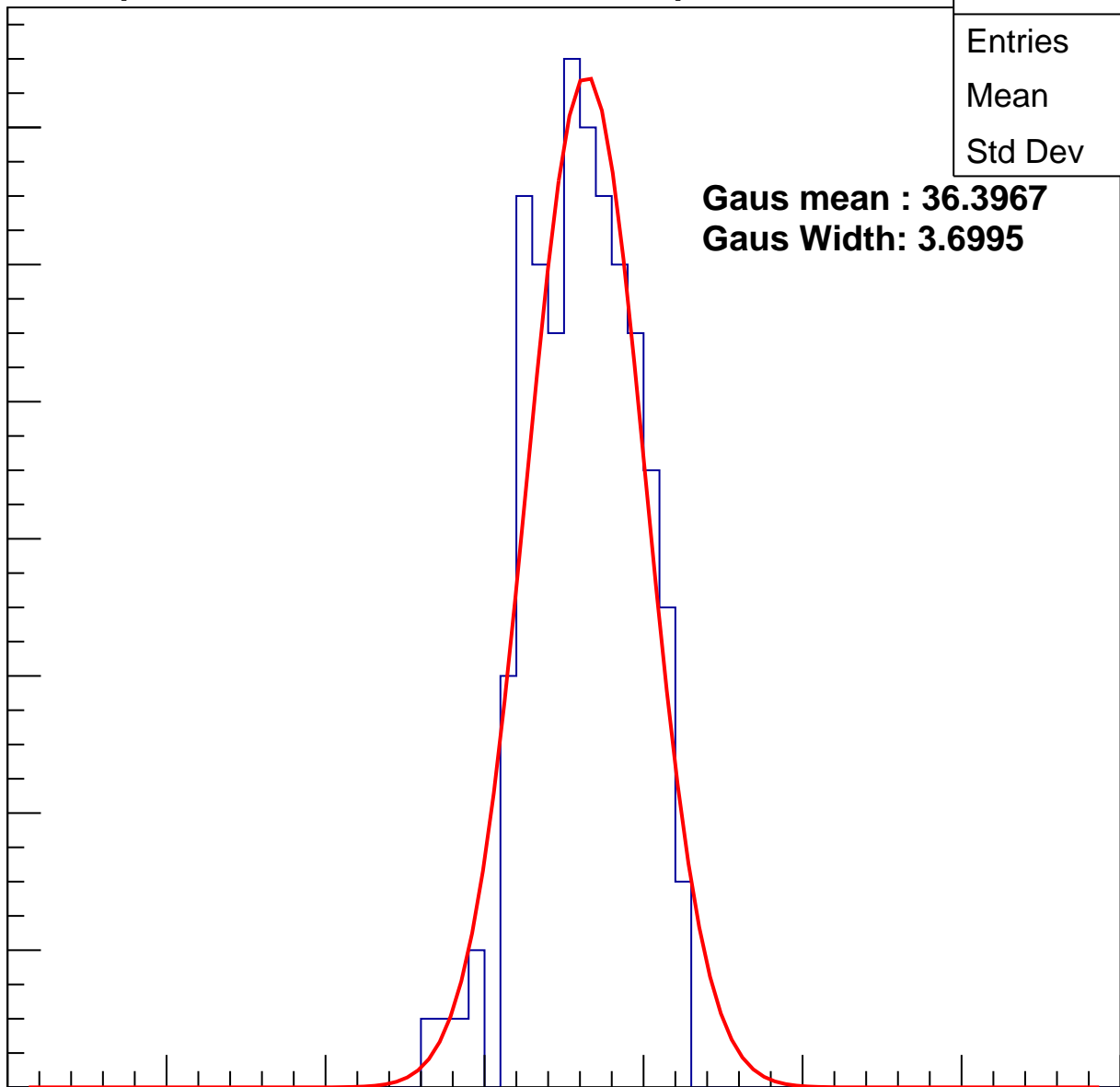
40

50

60

70

ampl



B1L001S, U25-ch120, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

135

Mean

41.93

Std Dev

3.456

Gaus mean : 42.5920

Gaus Width: 3.7062

1

3

5

7

9

11

13

15

17

19

21

23

25

27

29

31

33

35

37

39

41

43

45

47

49

51

53

55

57

59

61

63

65

67

69

71

73

75

77

79

81

83

85

87

89

91

93

95

97

99

101

103

105

107

109

111

113

115

117

119

121

123

125

127

129

131

133

135

137

139

141

143

145

147

149

151

153

155

157

159

161

163

165

167

169

171

173

175

177

179

181

183

185

187

189

191

193

195

197

199

201

203

205

207

209

211

213

215

217

219

221

223

225

227

229

231

233

235

237

239

241

243

245

247

249

251

253

255

257

259

261

263

265

267

269

271

273

275

277

279

281

283

285

287

289

291

293

295

297

299

301

303

305

307

309

311

313

315

317

319

321

323

325

327

329

331

333

335

337

339

341

343

345

347

349

351

353

355

357

359

361

363

365

367

369

371

373

375

377

379

381

383

385

387

389

391

393

395

397

399

401

403

405

407

409

411

413

415

417

419

421

423

425

427

429

431

433

435

437

439

441

443

445

447

449

451

453

455

457

459

461

463

465

467

469

471

473

475

477

479

481

483

485

487

489

491

493

495

497

499

501

503

505

507

509

511

513

515

517

519

521

523

525

527

529

531

533

535

537

539

541

543

545

547

549

551

553

555

557

559

561

563

565

567

569

571

573

575

577

579

581

583

585

587

589

591

593

595

597

599

601

603

605

607

609

611

613

615

617

619

621

623

625

627

629

631

633

635

637

639

641

643

645

647

649

651

653

655

657

659

661

663

665

667

669

671

673

675

677

679

681

683

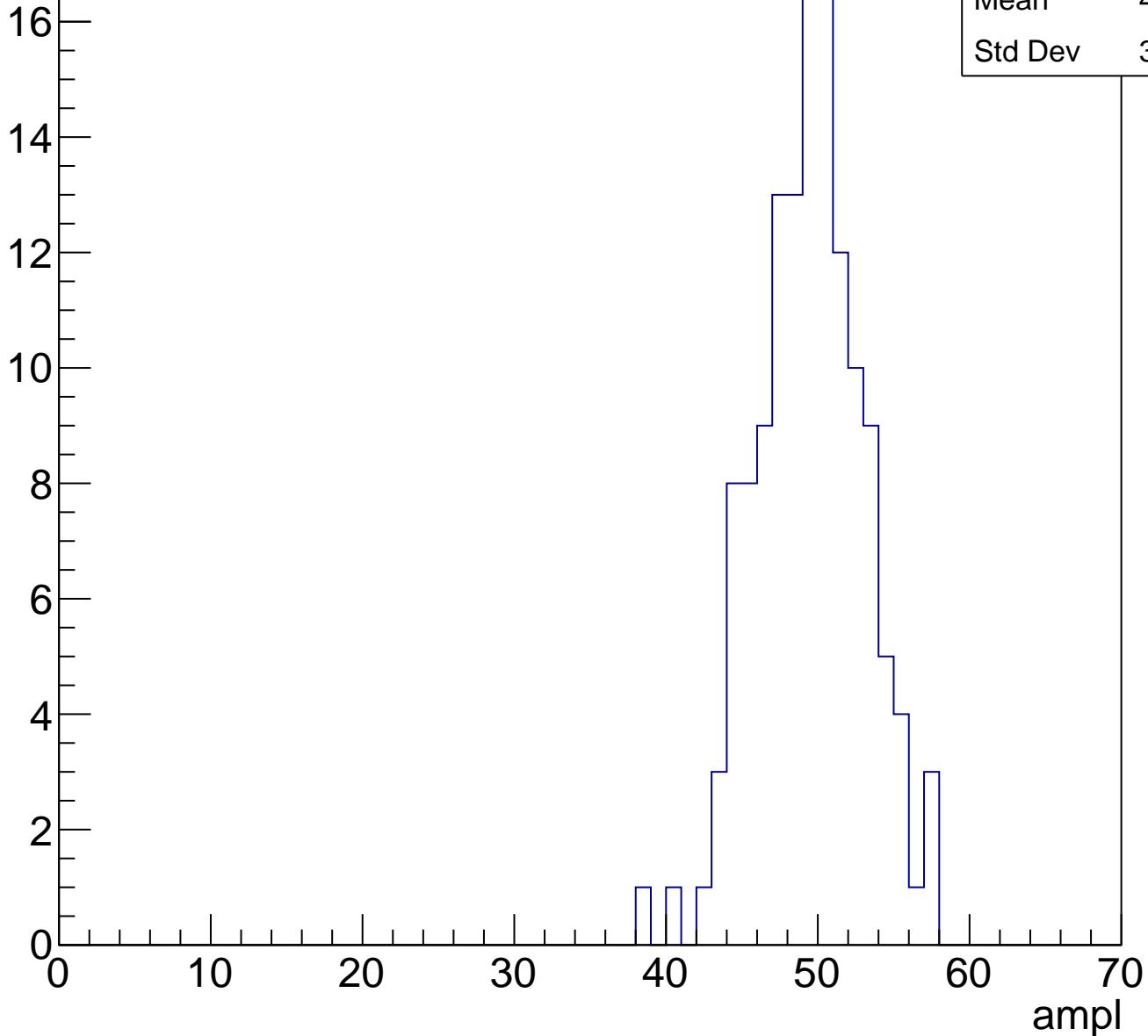
685

B1L001S, U25-ch120, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

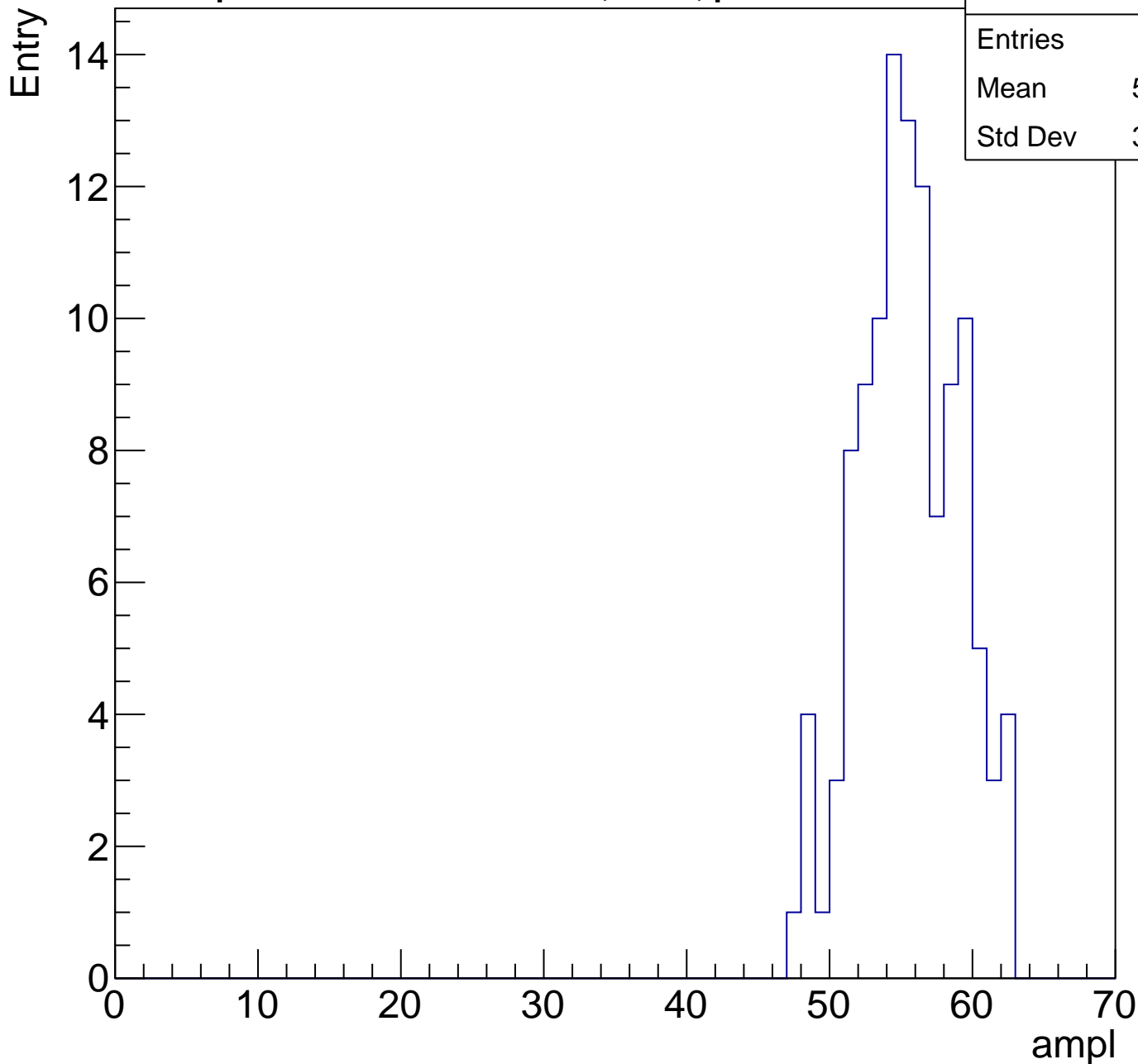
Entries	135
Mean	49.03
Std Dev	3.487



B1L001S, U25-ch120, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	55.12
Std Dev	3.447



B1L001S, U25-ch120, adc5

calib_packv5_042523_0143.root, FC#2, port C2

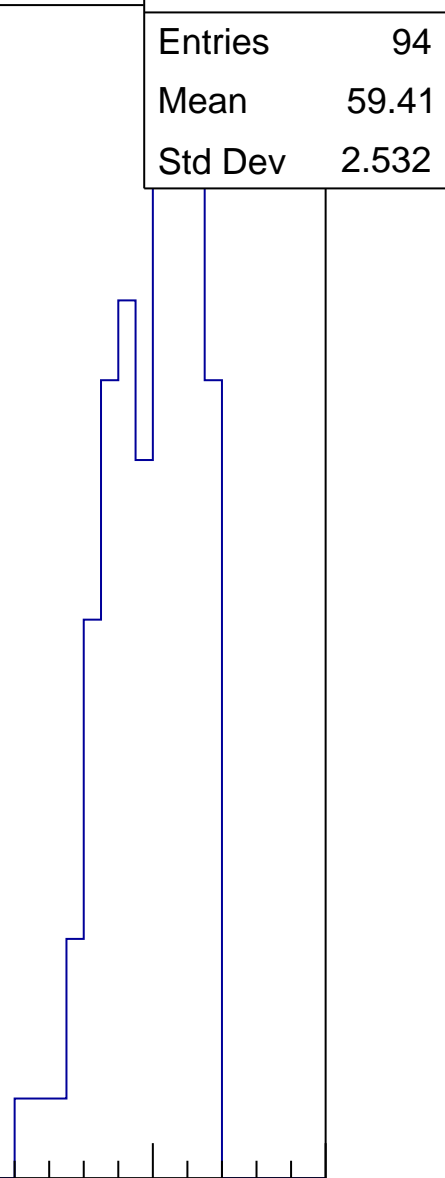
Entries	94
Mean	59.41
Std Dev	2.532

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch120, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	20
Mean	55.9
Std Dev	18.67

Entry

10

8

6

4

2

0

0

10

20

30

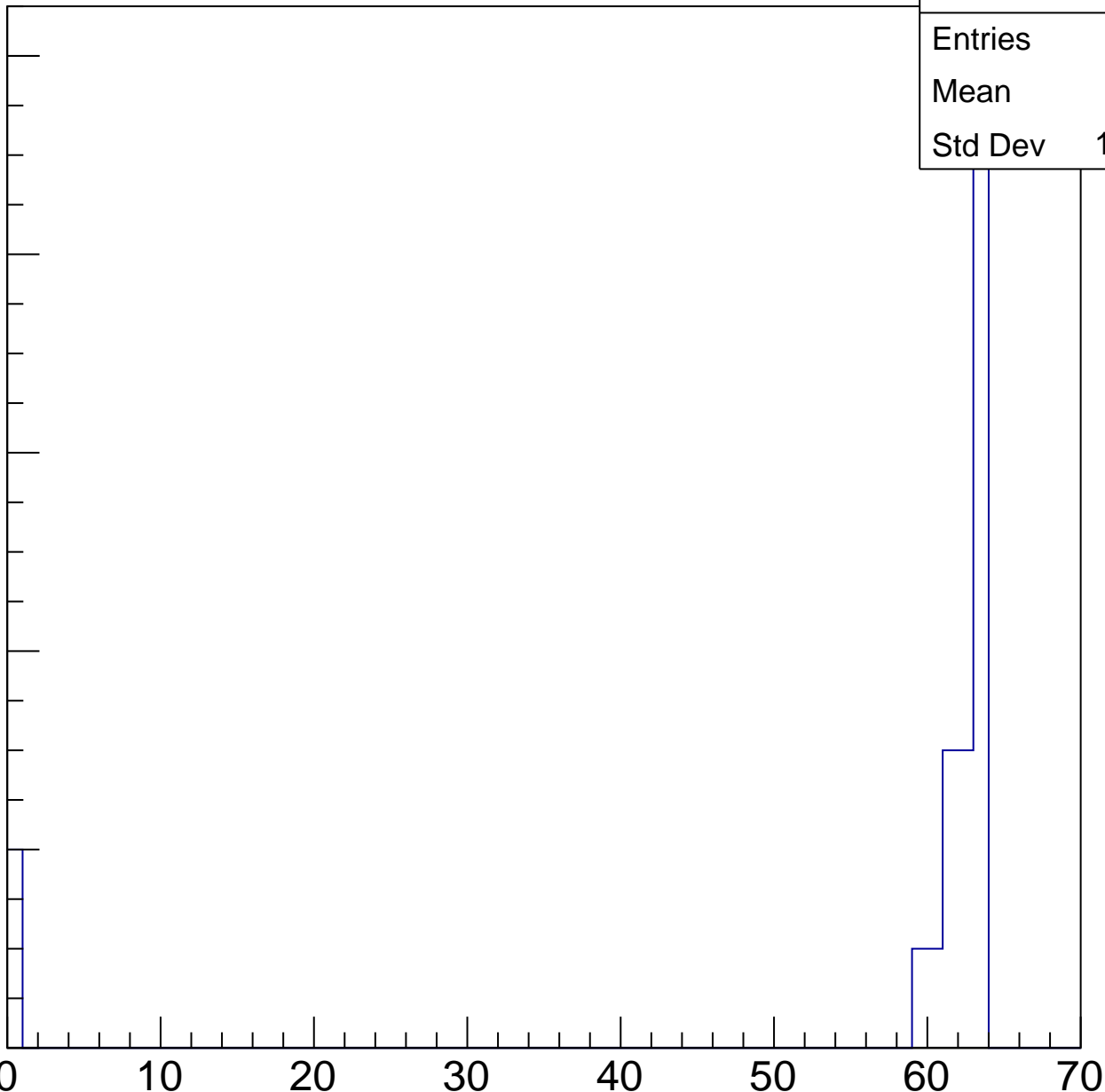
40

50

60

70

ampl



B1L001S, U25-ch120, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U25-ch121, adc0

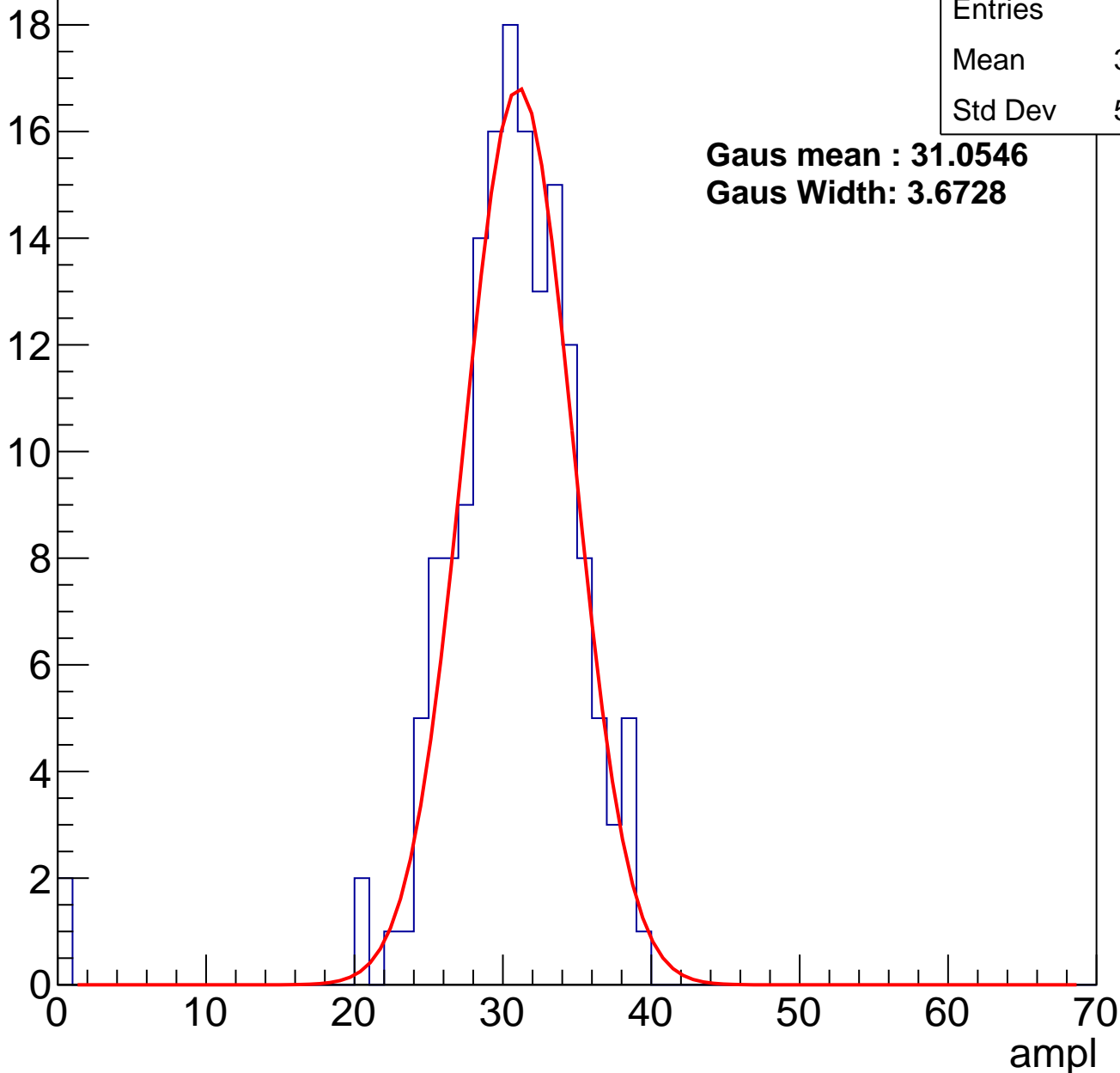
calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	30.04
Std Dev	5.029

Gaus mean : 31.0546

Gaus Width: 3.6728

Entry



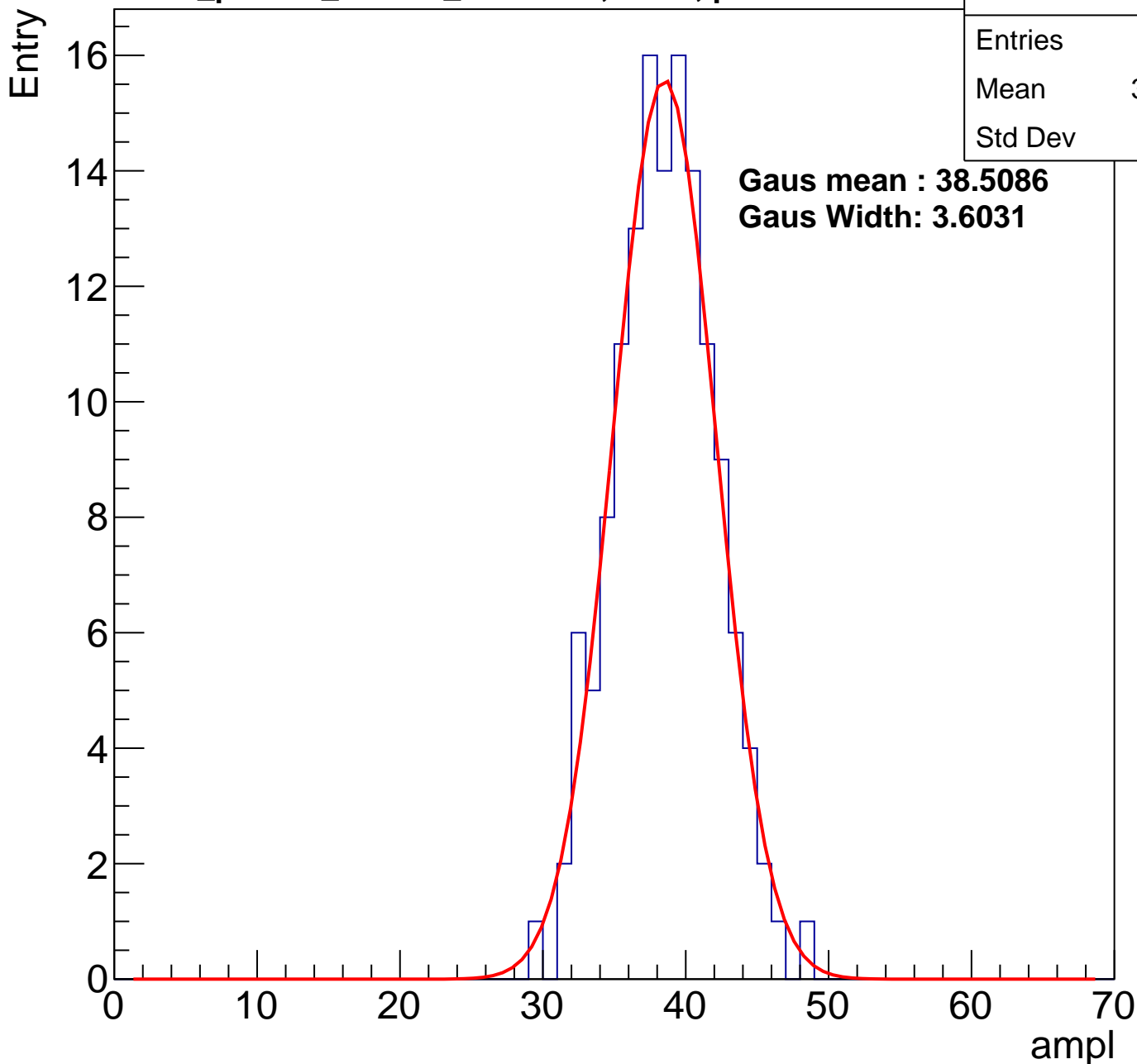
B1L001S, U25-ch121, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	38.06
Std Dev	3.47

Gaus mean : 38.5086

Gaus Width: 3.6031



B1L001S, U25-ch121, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

123

Mean

44.56

Std Dev

3.462

Gaus mean : 45.3342

Gaus Width: 3.4358

0

10

20

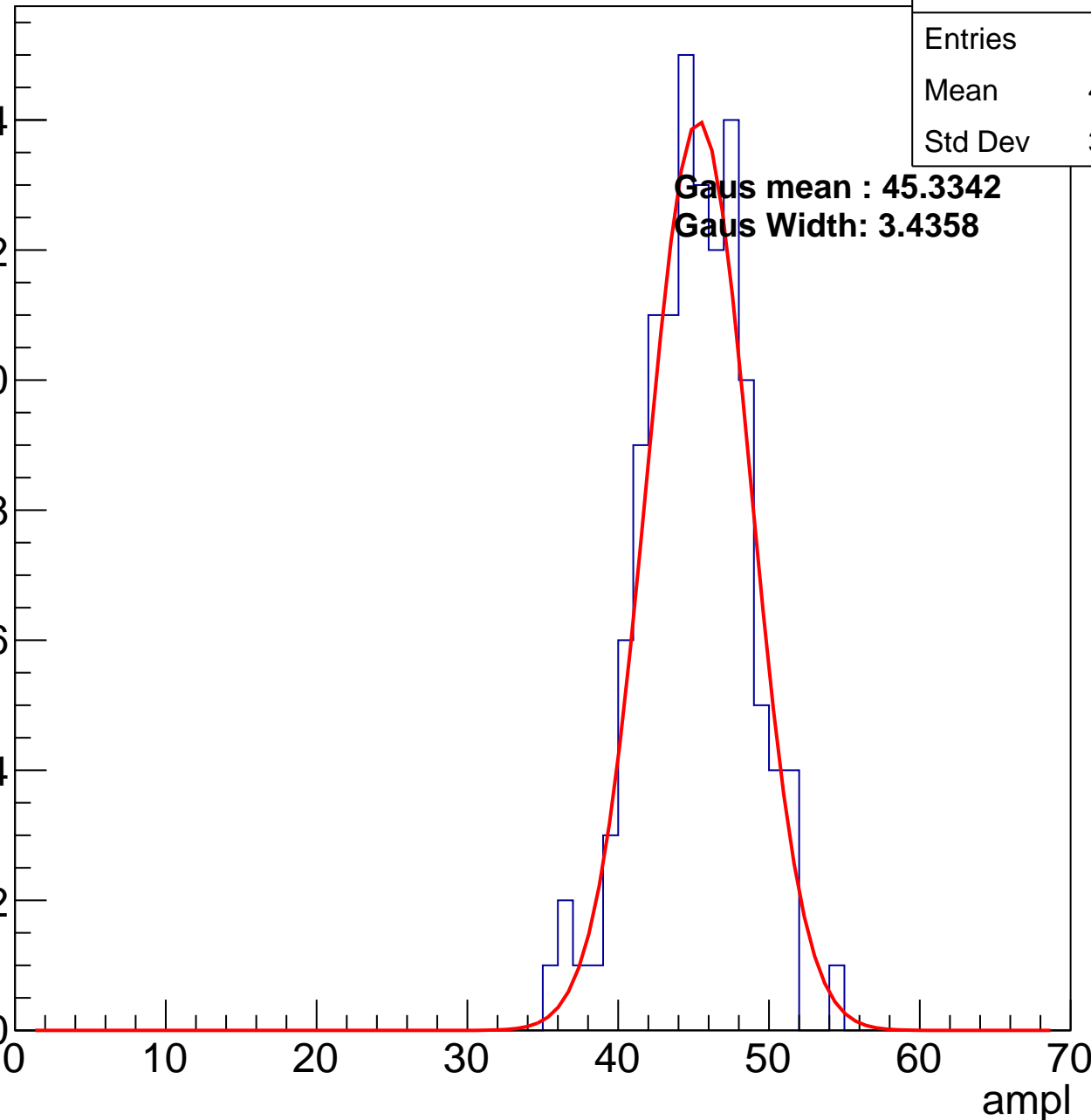
30

40

50

60

ampl



B1L001S, U25-ch121, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	132
Mean	50.86
Std Dev	3.467

16
14
12
10
8
6
4
2
0

0

10

20

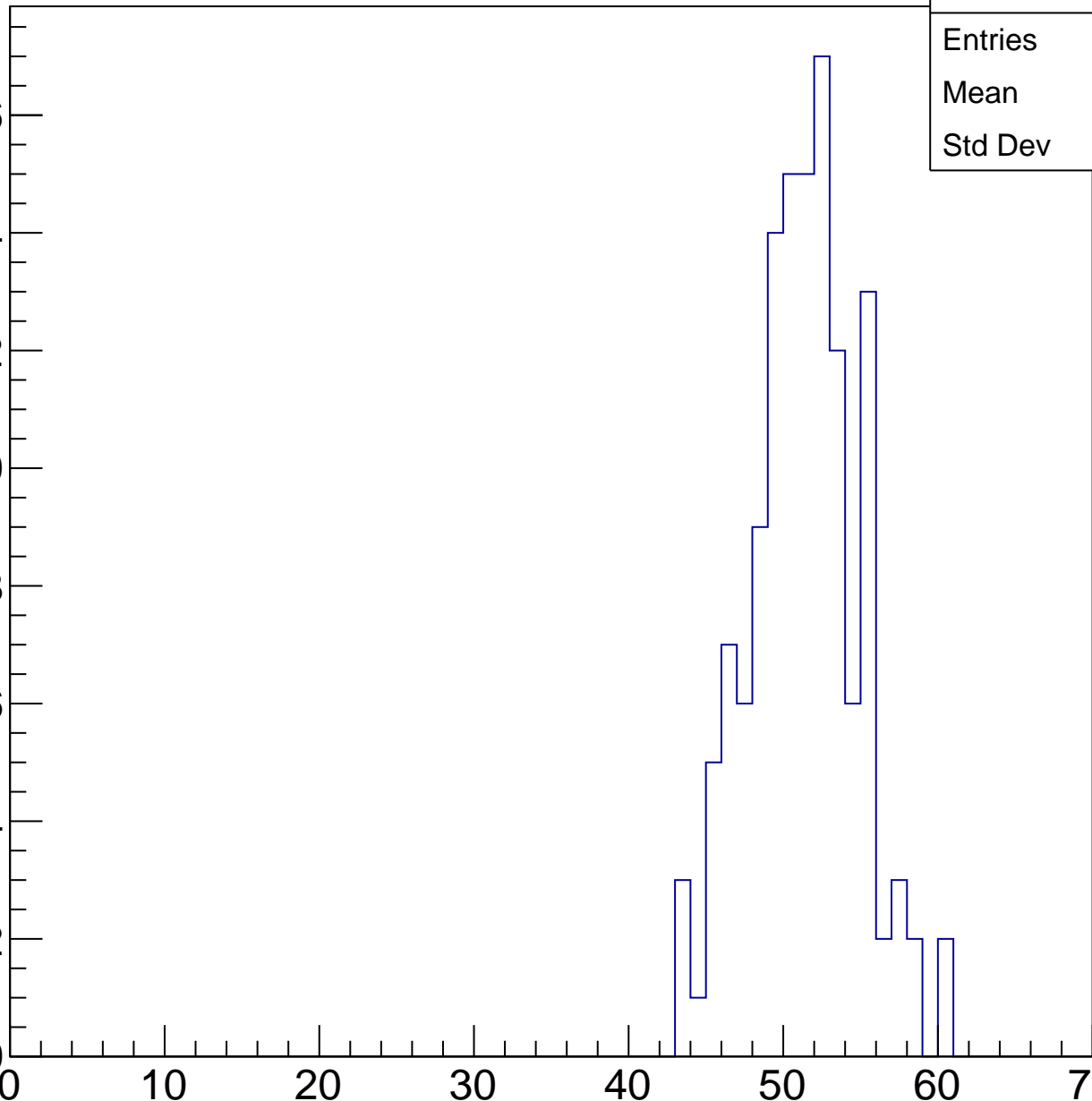
30

40

50

60

ampl



B1L001S, U25-ch121, adc4

calib_packv5_042523_0143.root, FC#2, port C2

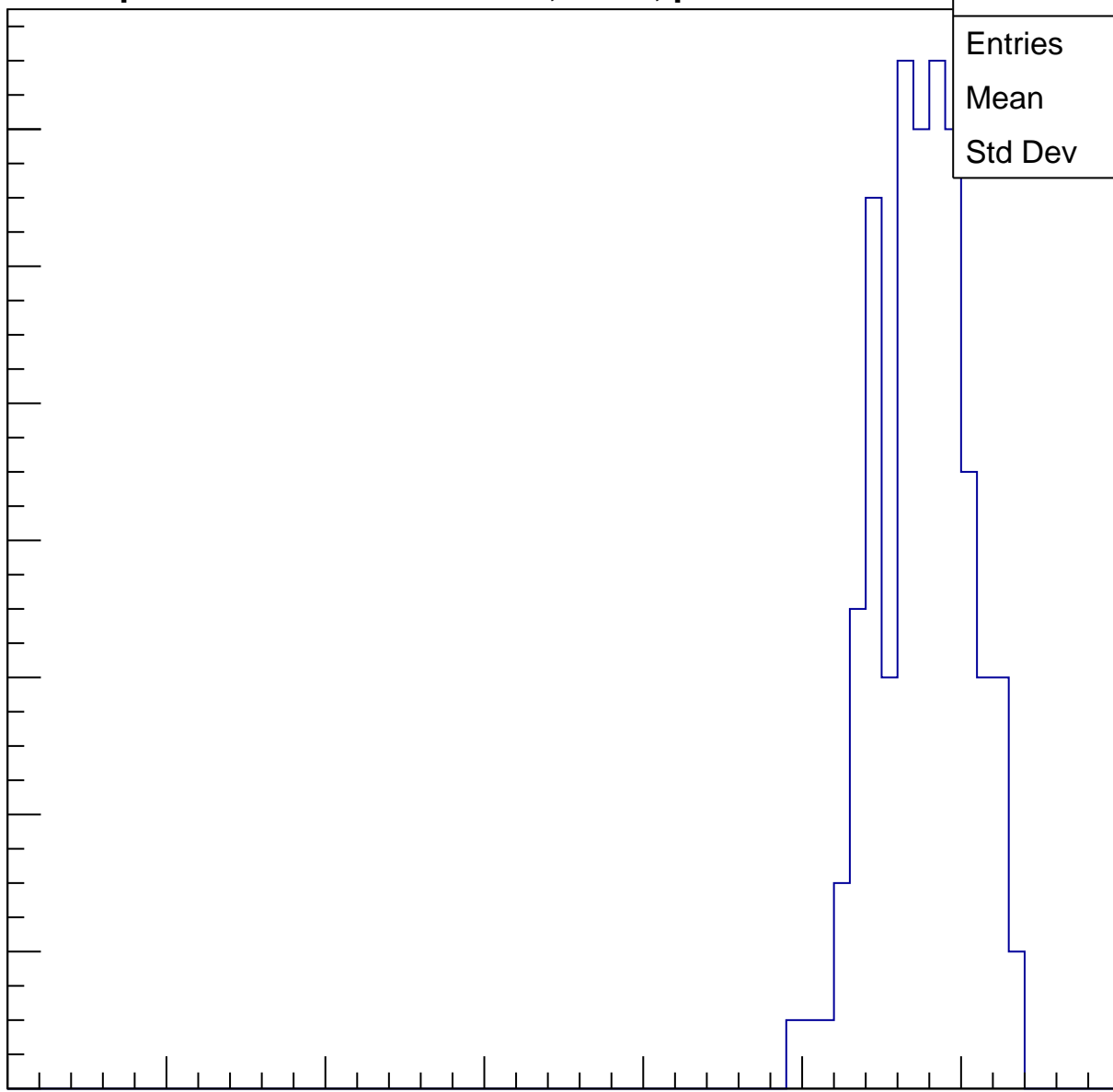
Entries	113
Mean	57.05
Std Dev	2.917

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch121, adc5

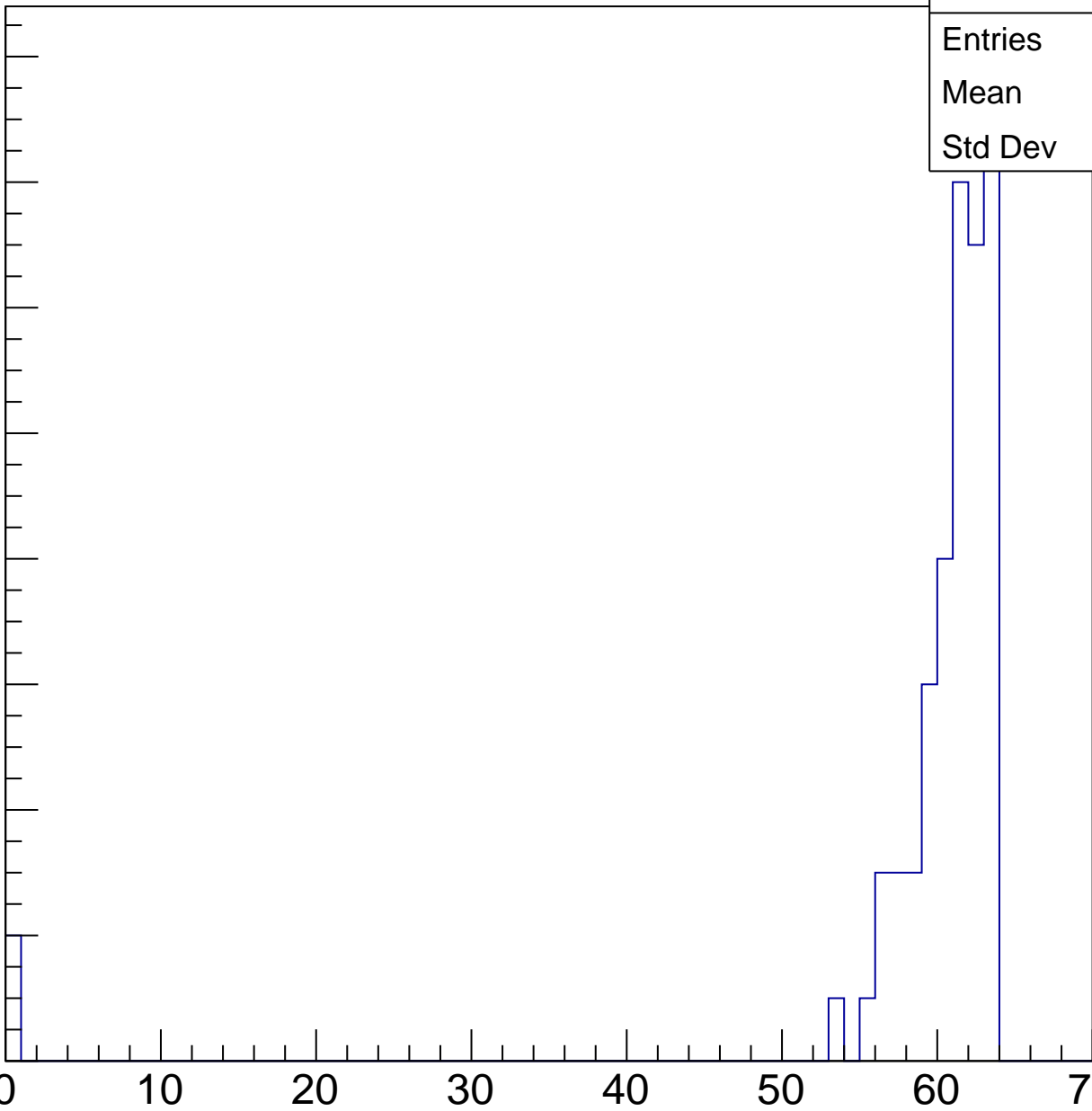
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	70
Mean	58.9
Std Dev	10.35

ampl



B1L001S, U25-ch121, adc6

calib_packv5_042523_0143.root, FC#2, port C2

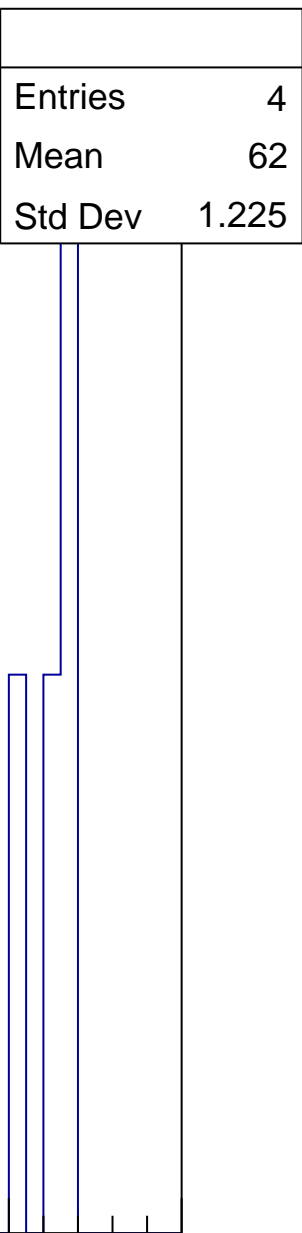
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch121, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



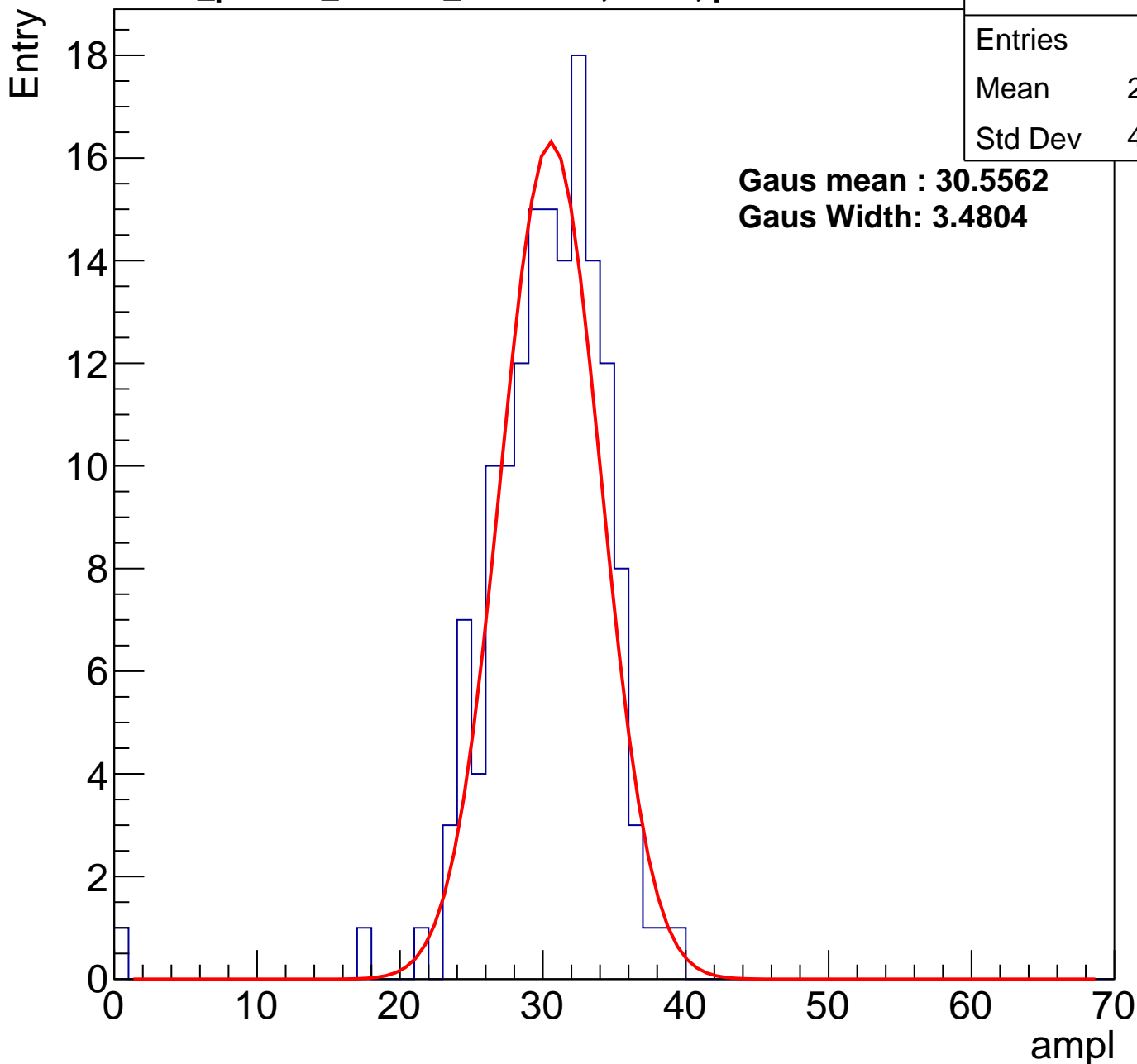
B1L001S, U25-ch122, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	29.85
Std Dev	4.356

Gaus mean : 30.5562

Gaus Width: 3.4804



B1L001S, U25-ch122, adc1

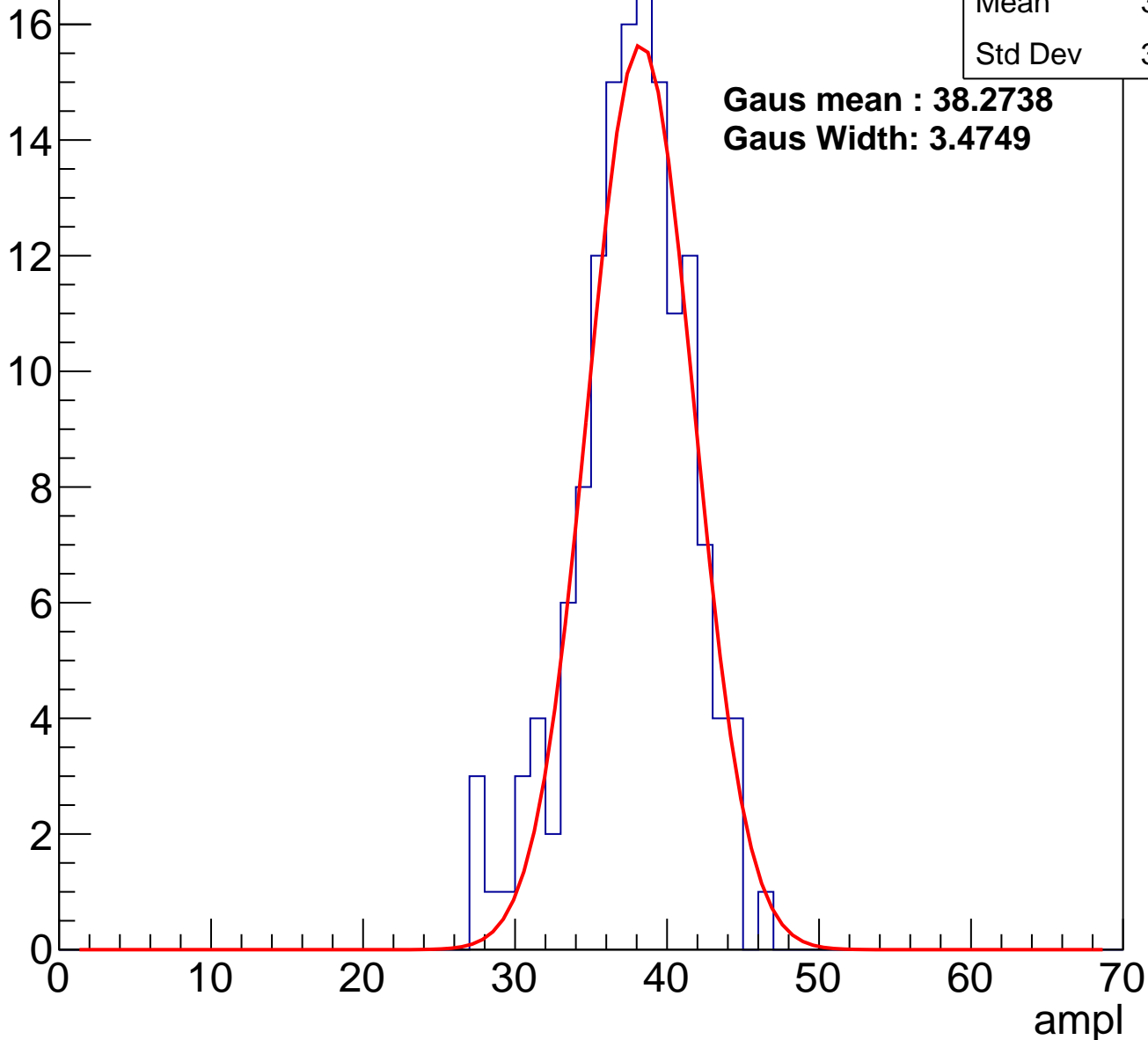
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	142
Mean	37.25
Std Dev	3.727

Gaus mean : 38.2738

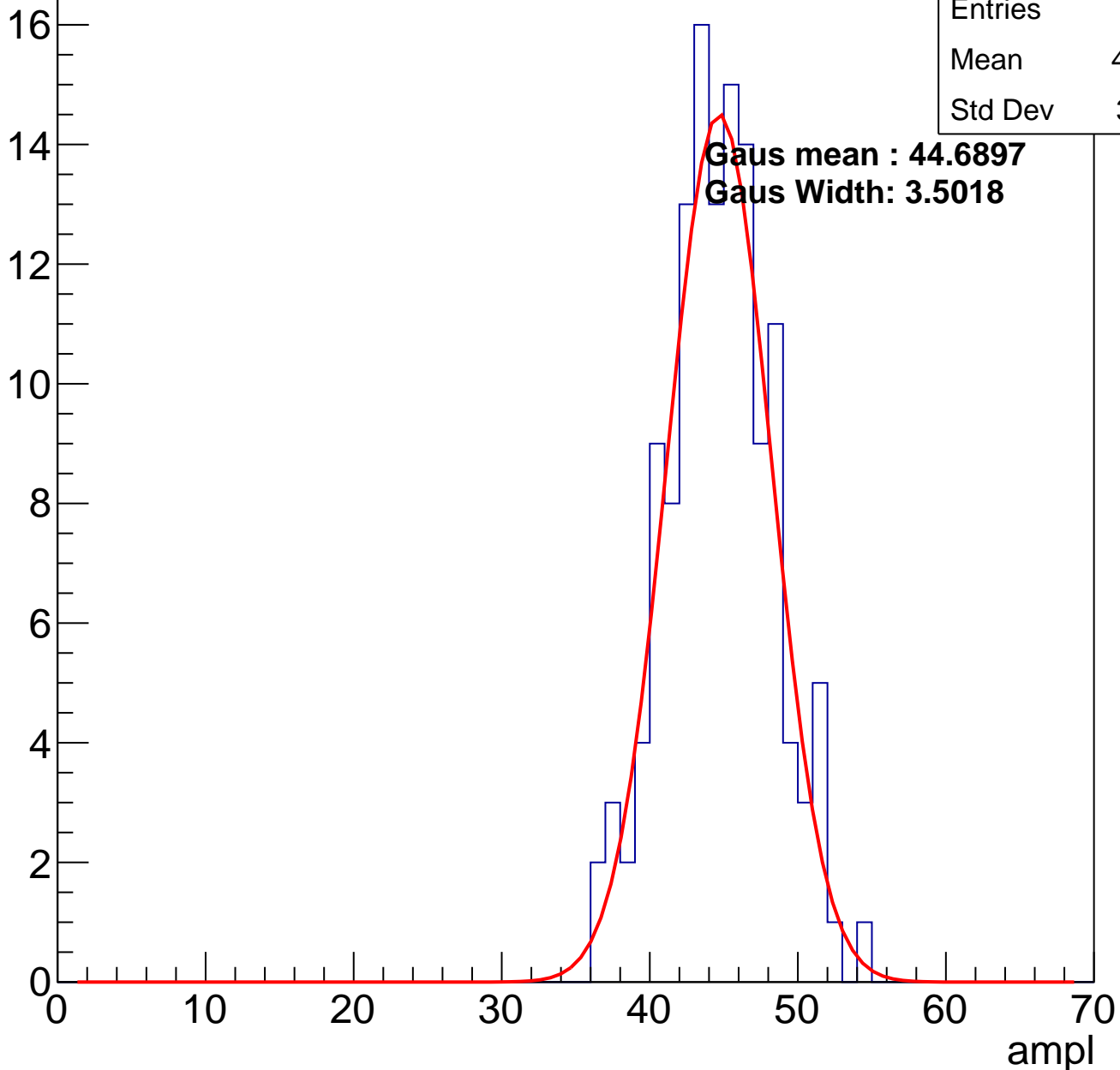
Gaus Width: 3.4749



B1L001S, U25-ch122, adc2

calib_packv5_042523_0143.root, FC#2, port C2

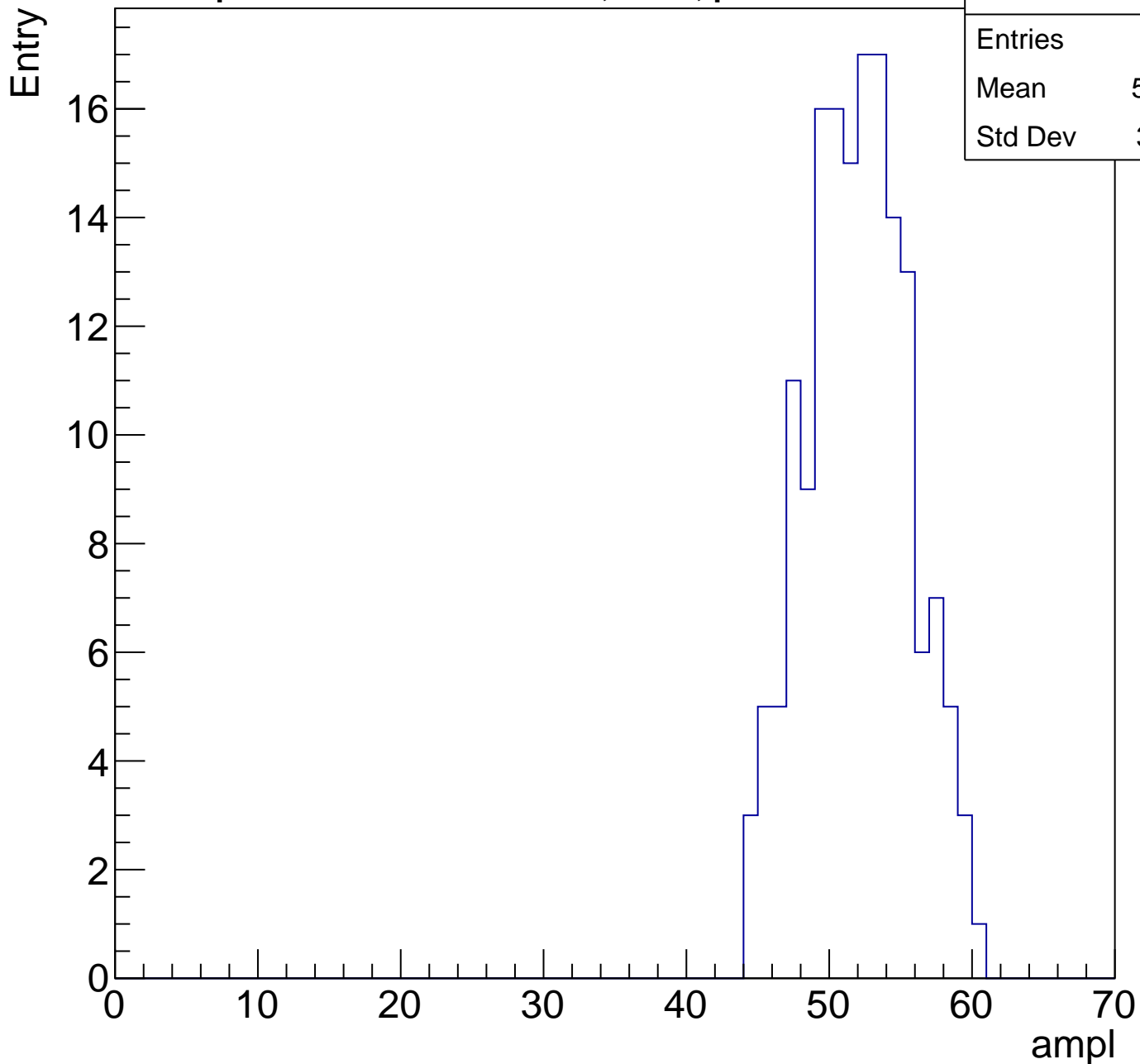
Entry



B1L001S, U25-ch122, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	51.55
Std Dev	3.571



B1L001S, U25-ch122, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	93
Mean	57.85
Std Dev	2.72

Entry

12

10

8

6

4

2

0

0

10

20

30

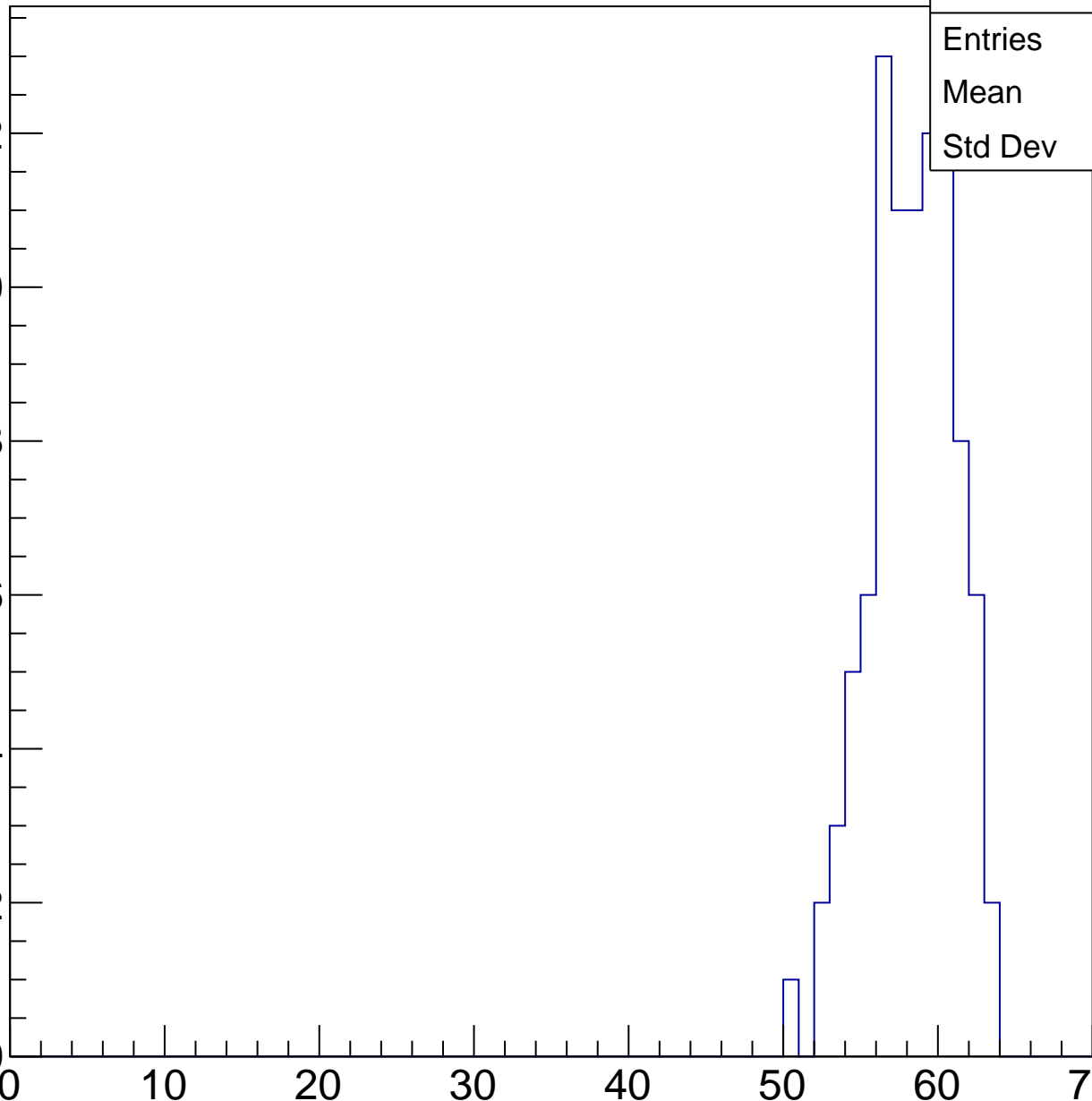
40

50

60

70

ampl



B1L001S, U25-ch122, adc5

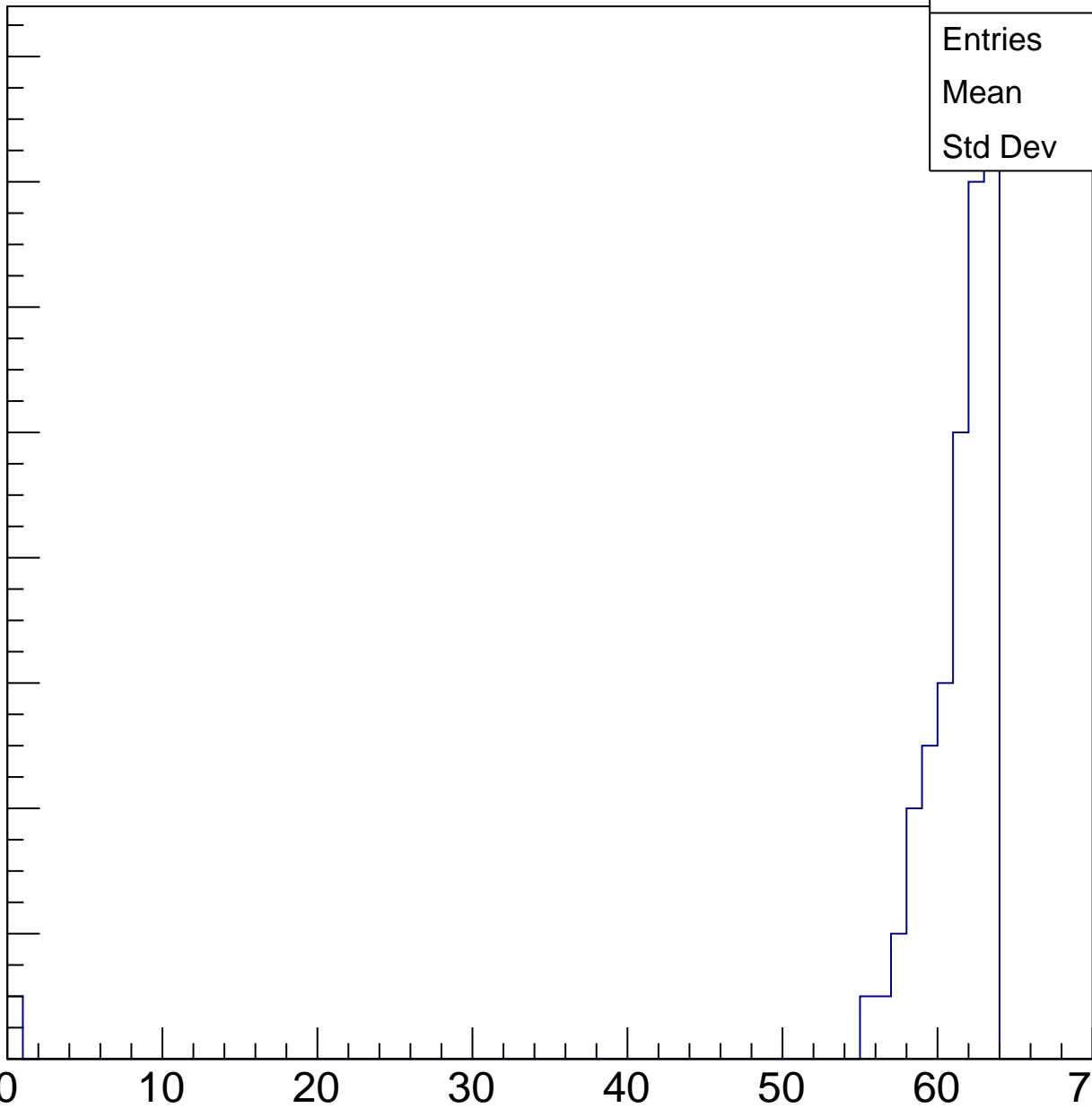
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	60
Mean	59.97
Std Dev	8.052

ampl



B1L001S, U25-ch122, adc6

calib_packv5_042523_0143.root, FC#2, port C2

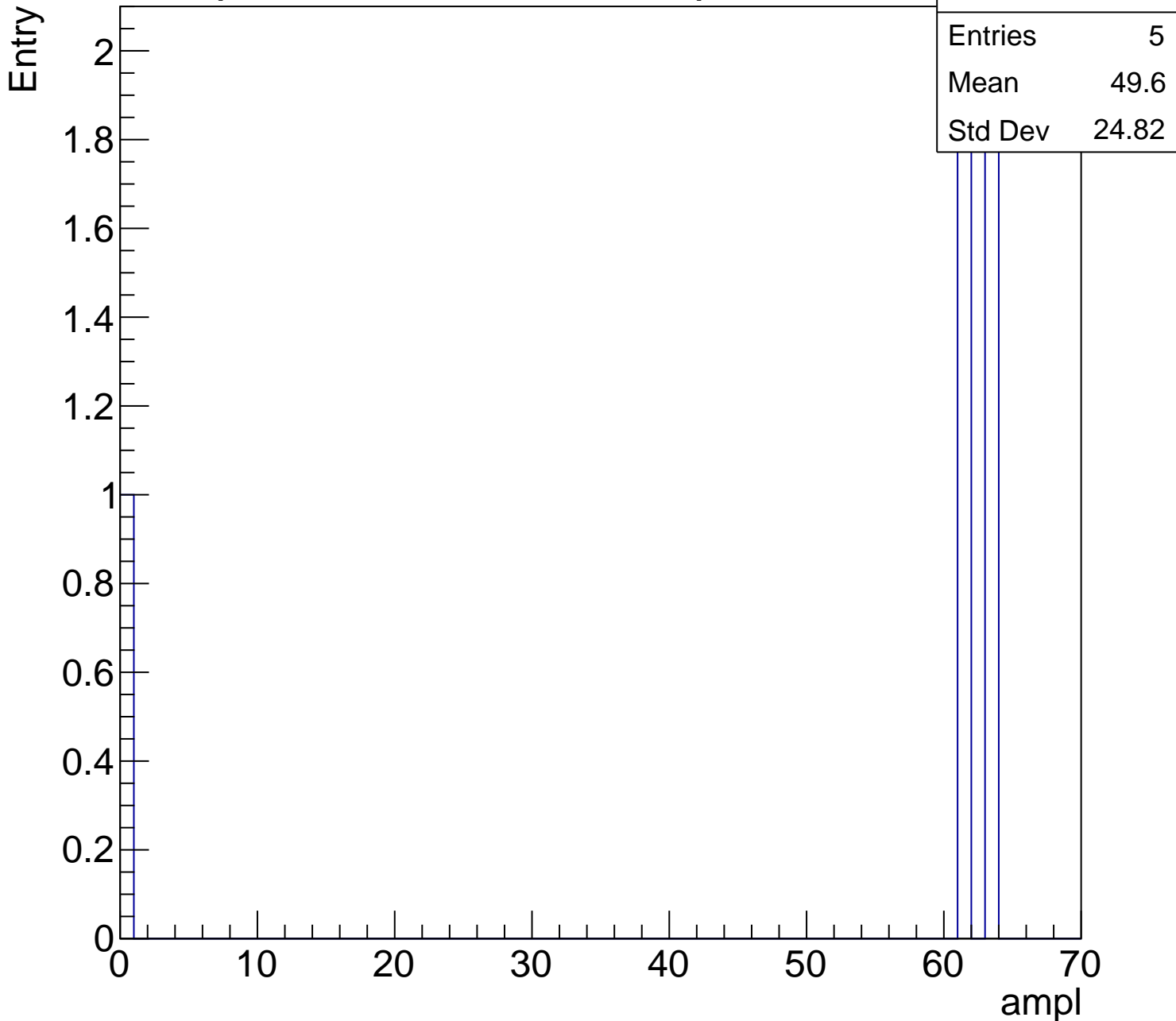
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.6
Std Dev	24.82

0 10 20 30 40 50 60 70

ampl



B1L001S, U25-ch122, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch123, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	29.14
Std Dev	3.179

Gaus mean : 29.7722

Gaus Width: 3.2300

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

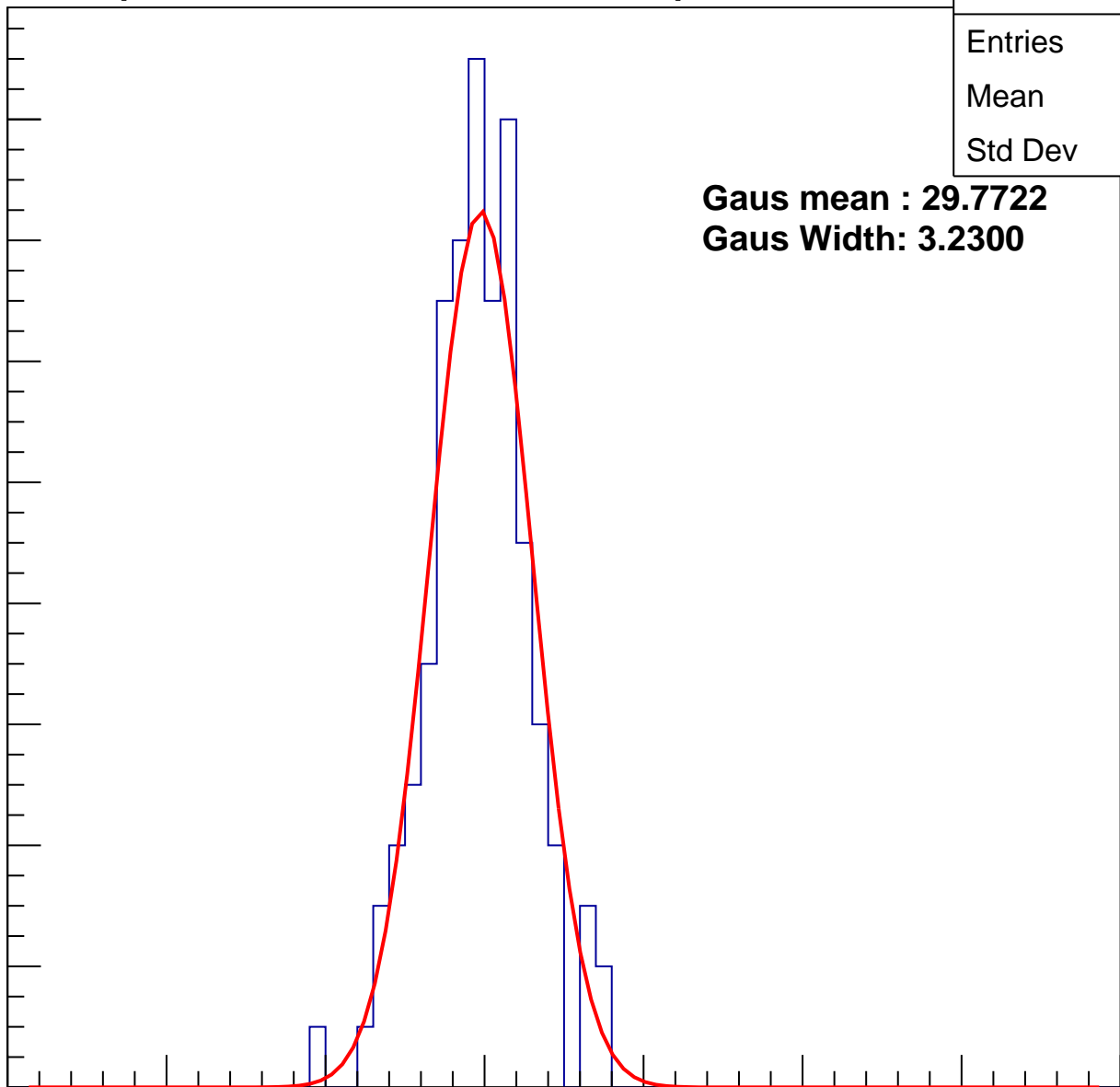
40

50

60

70

ampl



B1L001S, U25-ch123, adc1

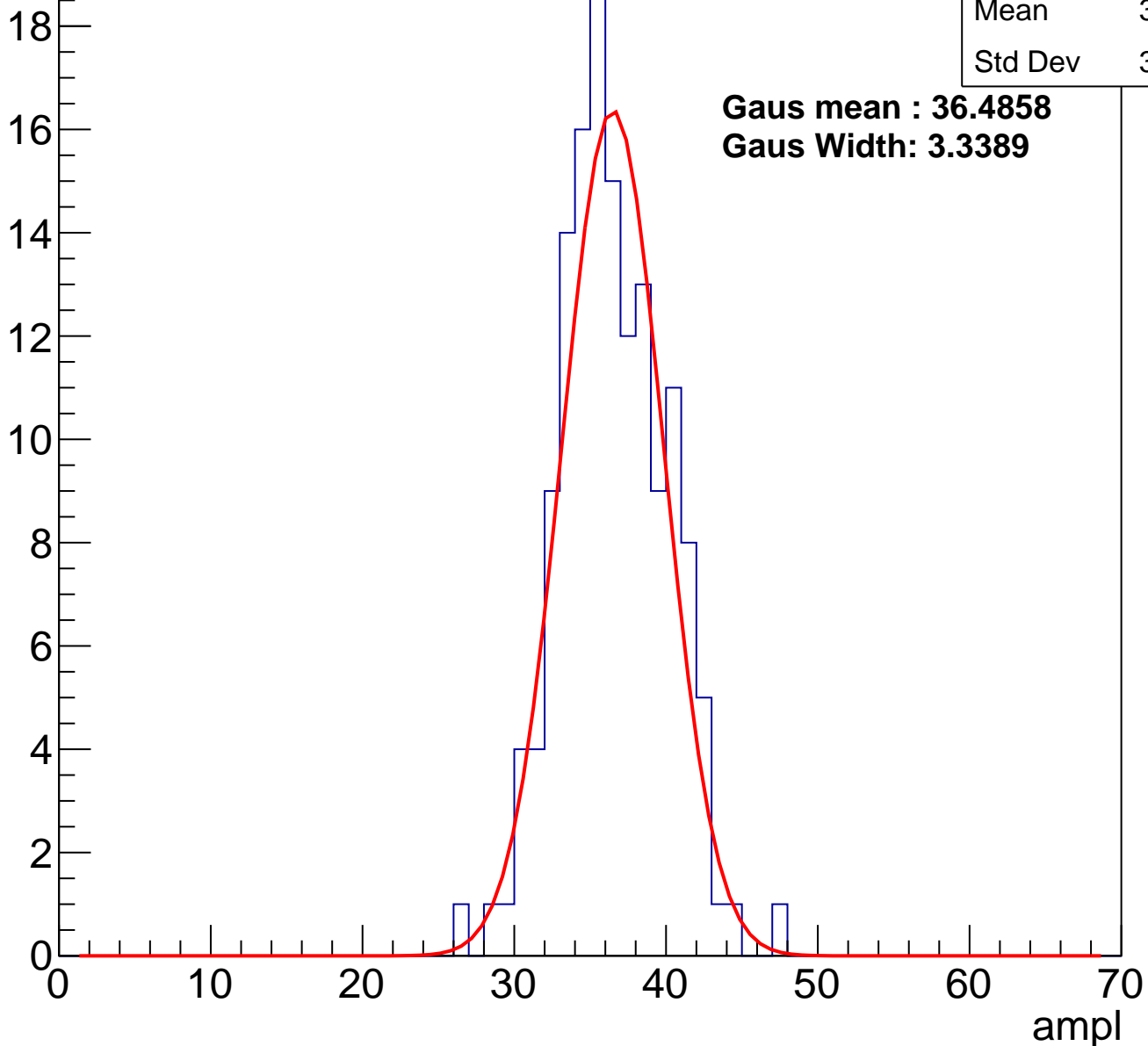
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	36.05
Std Dev	3.469

Gaus mean : 36.4858

Gaus Width: 3.3389

Entry



B1L001S, U25-ch123, adc2

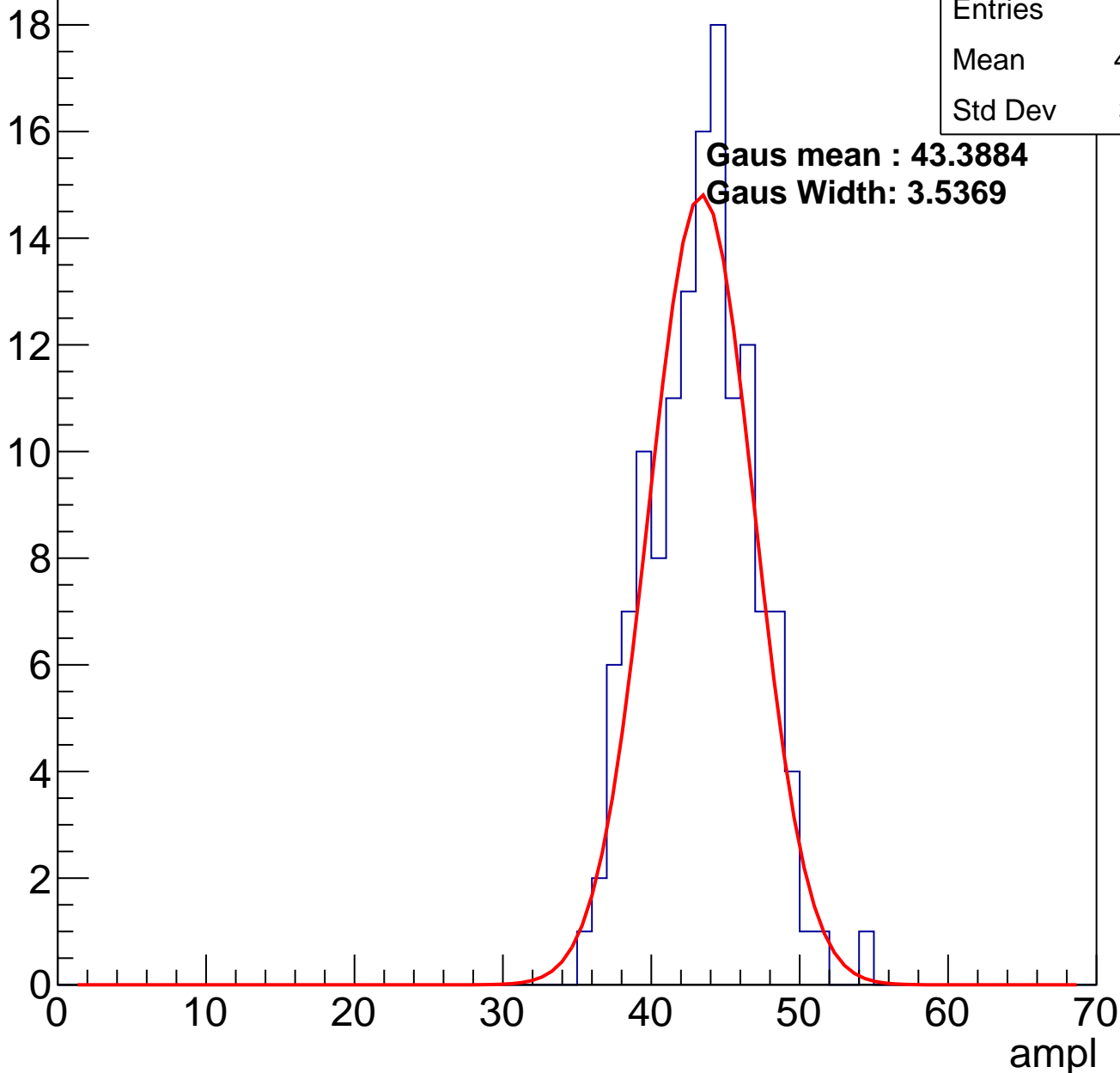
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	136
Mean	42.98
Std Dev	3.511

Gaus mean : 43.3884

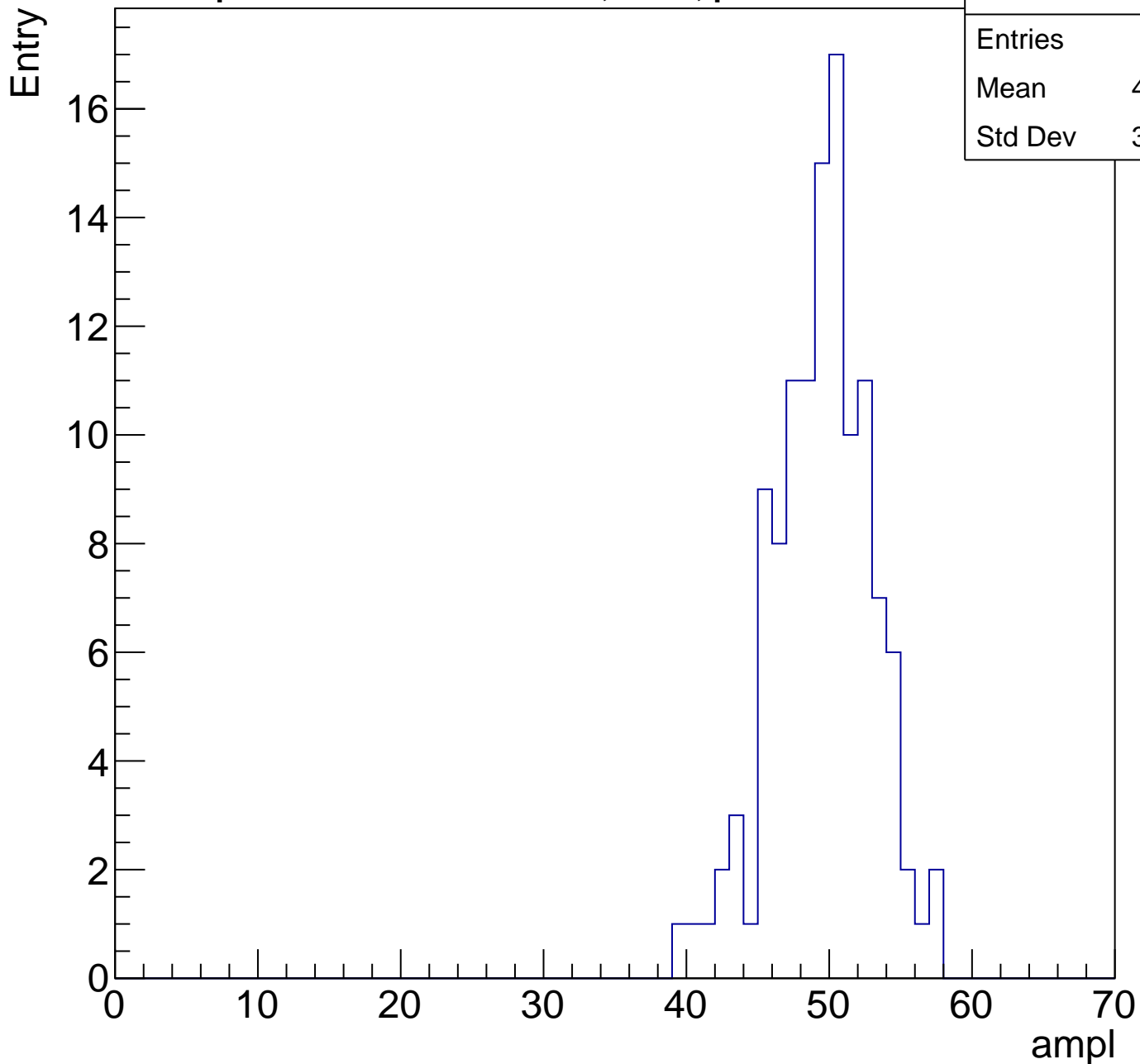
Gaus Width: 3.5369



B1L001S, U25-ch123, adc3

calib_packv5_042523_0143.root, FC#2, port C2

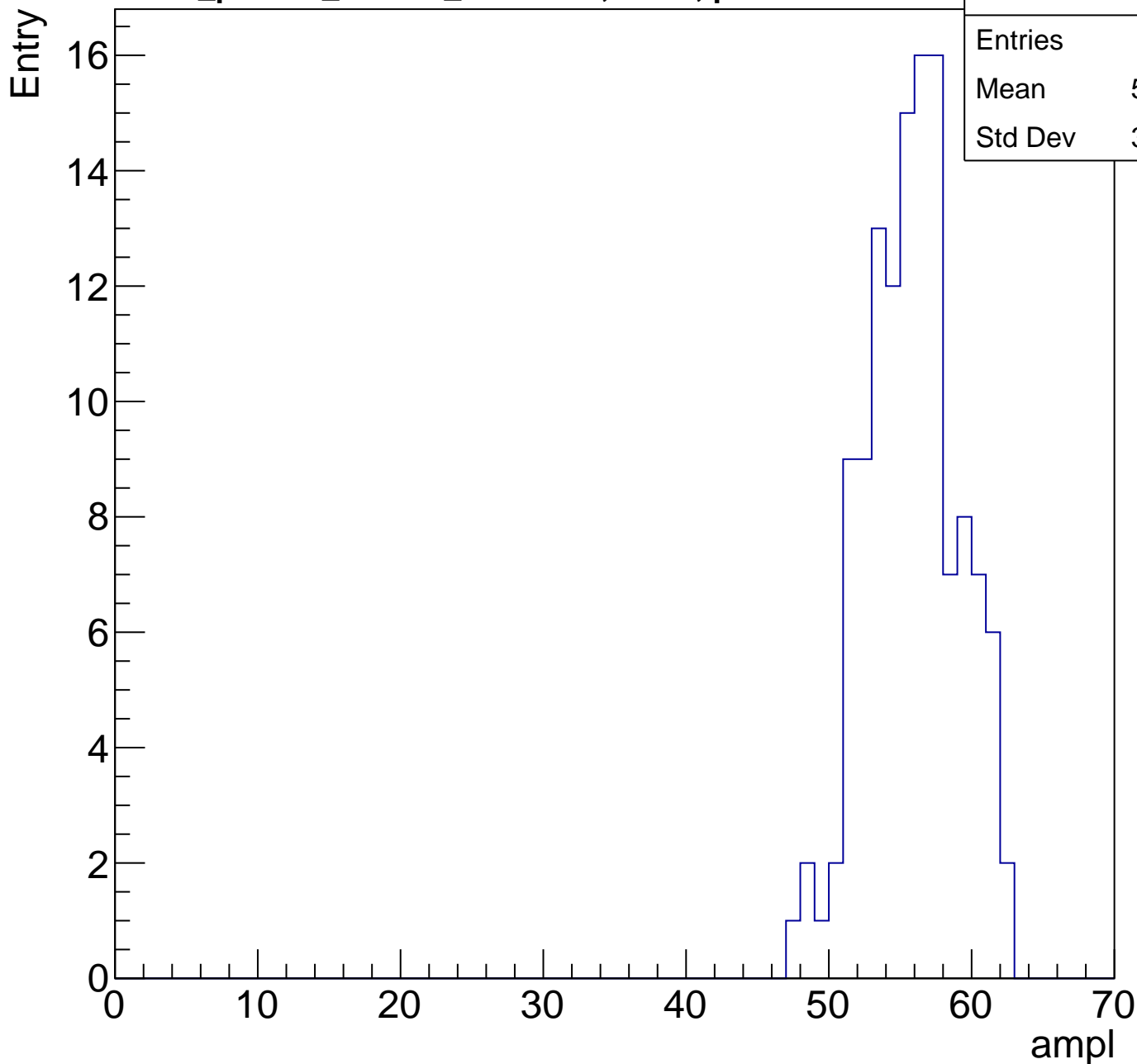
Entries	119
Mean	49.05
Std Dev	3.447



B1L001S, U25-ch123, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	55.37
Std Dev	3.187



B1L001S, U25-ch123, adc5

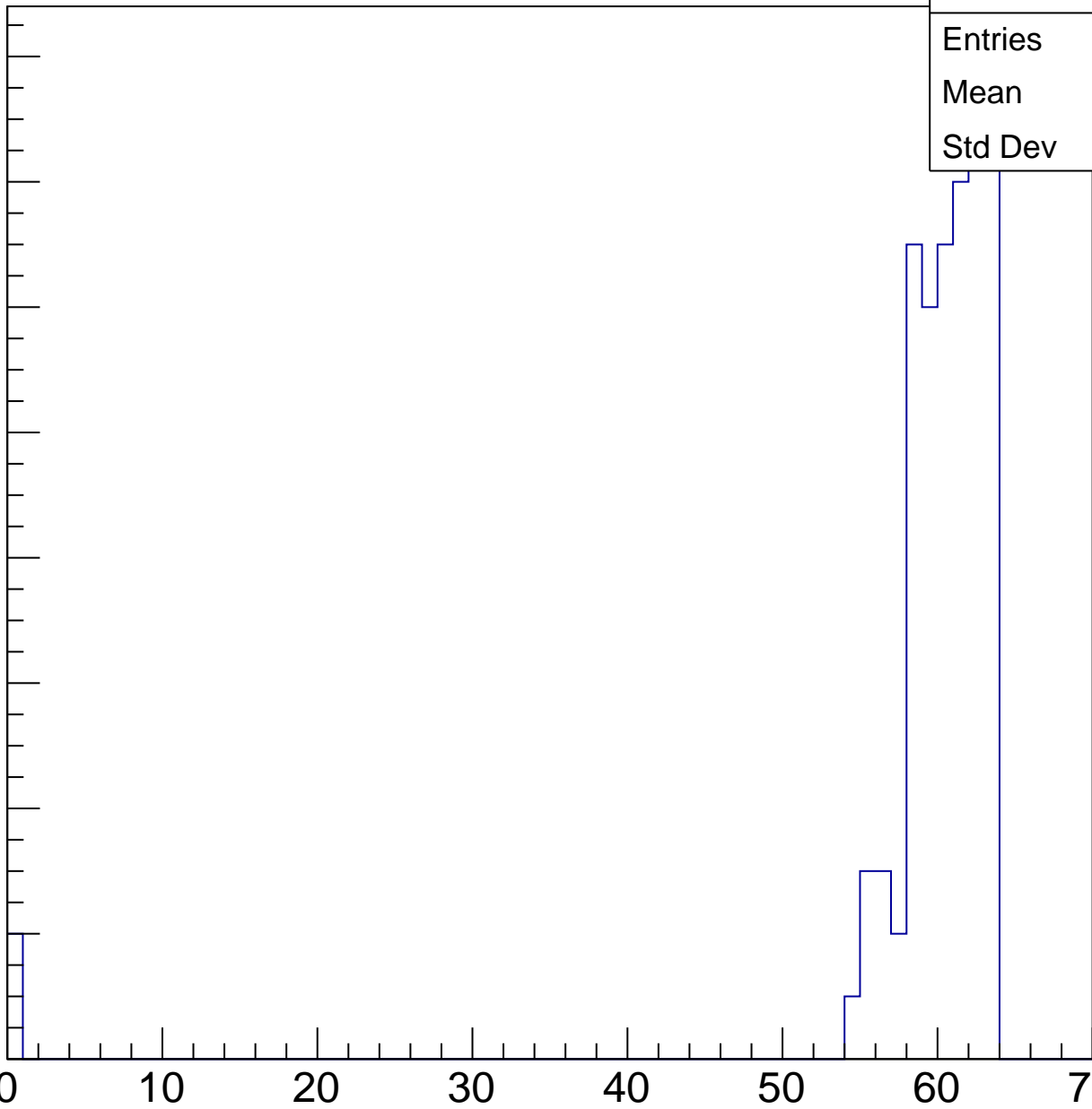
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	58.87
Std Dev	8.953

ampl

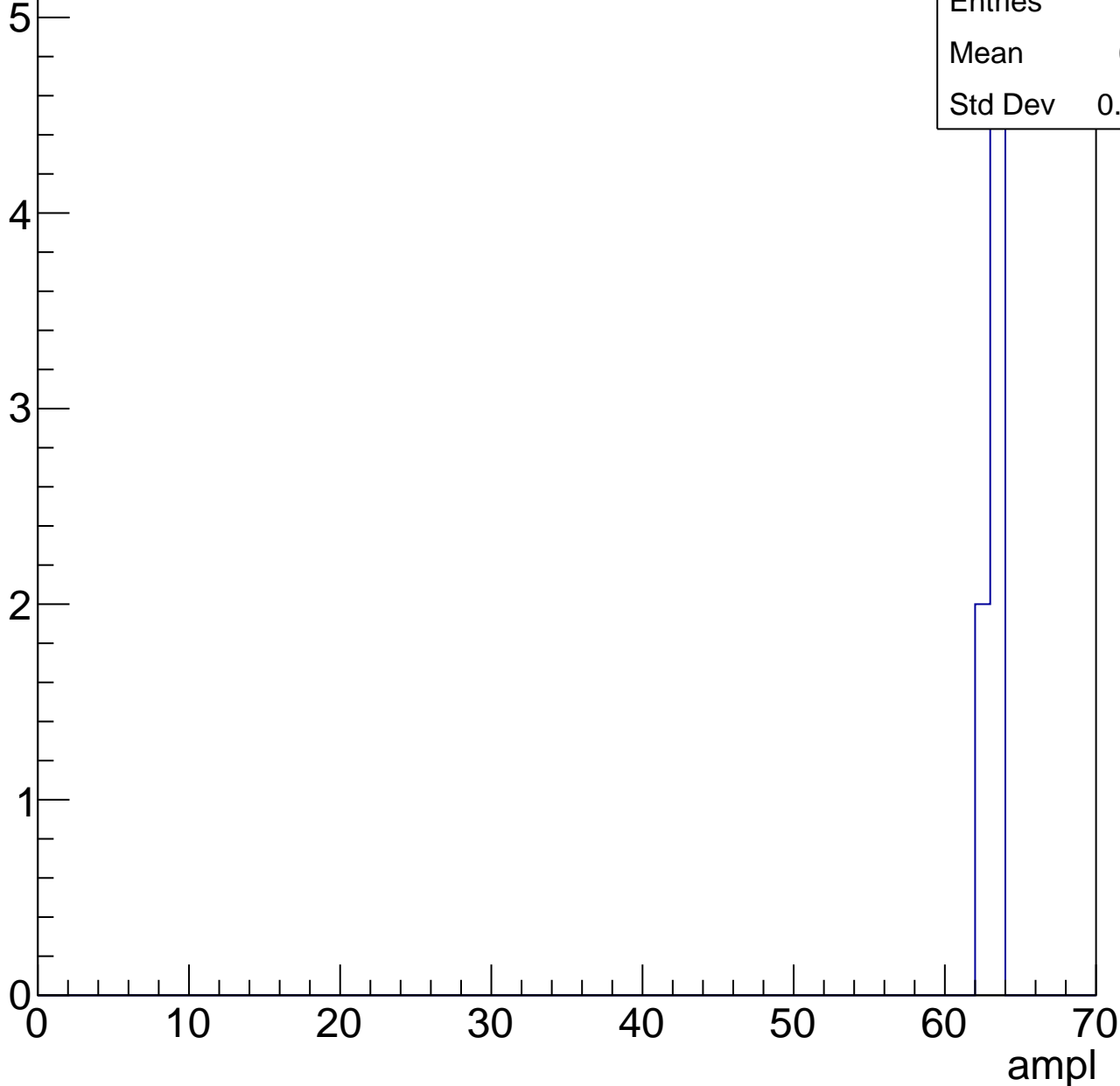


B1L001S, U25-ch123, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	62.71
Std Dev	0.4518



B1L001S, U25-ch123, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch124, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	28.52
Std Dev	5.444

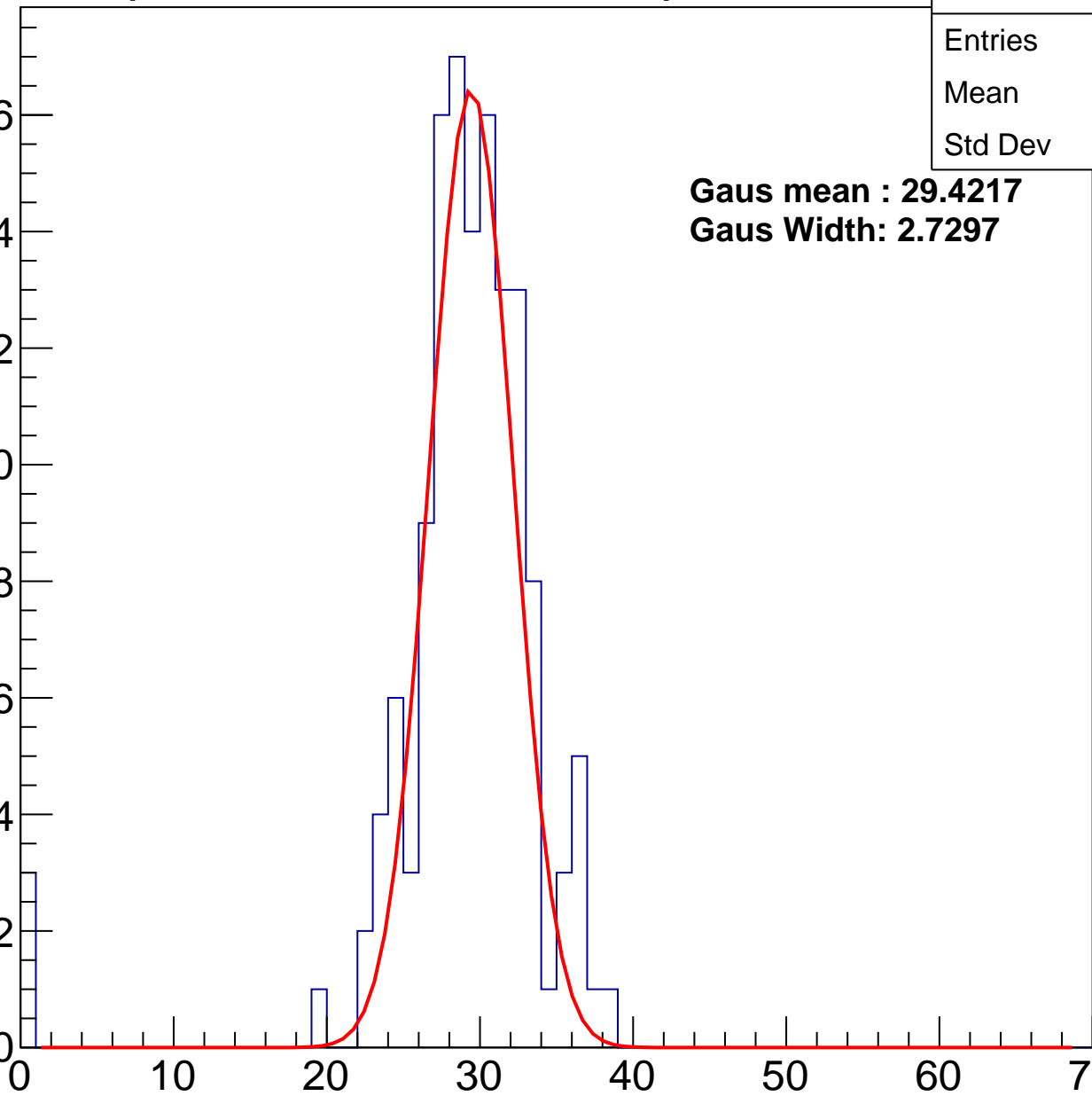
Gaus mean : 29.4217

Gaus Width: 2.7297

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch124, adc1

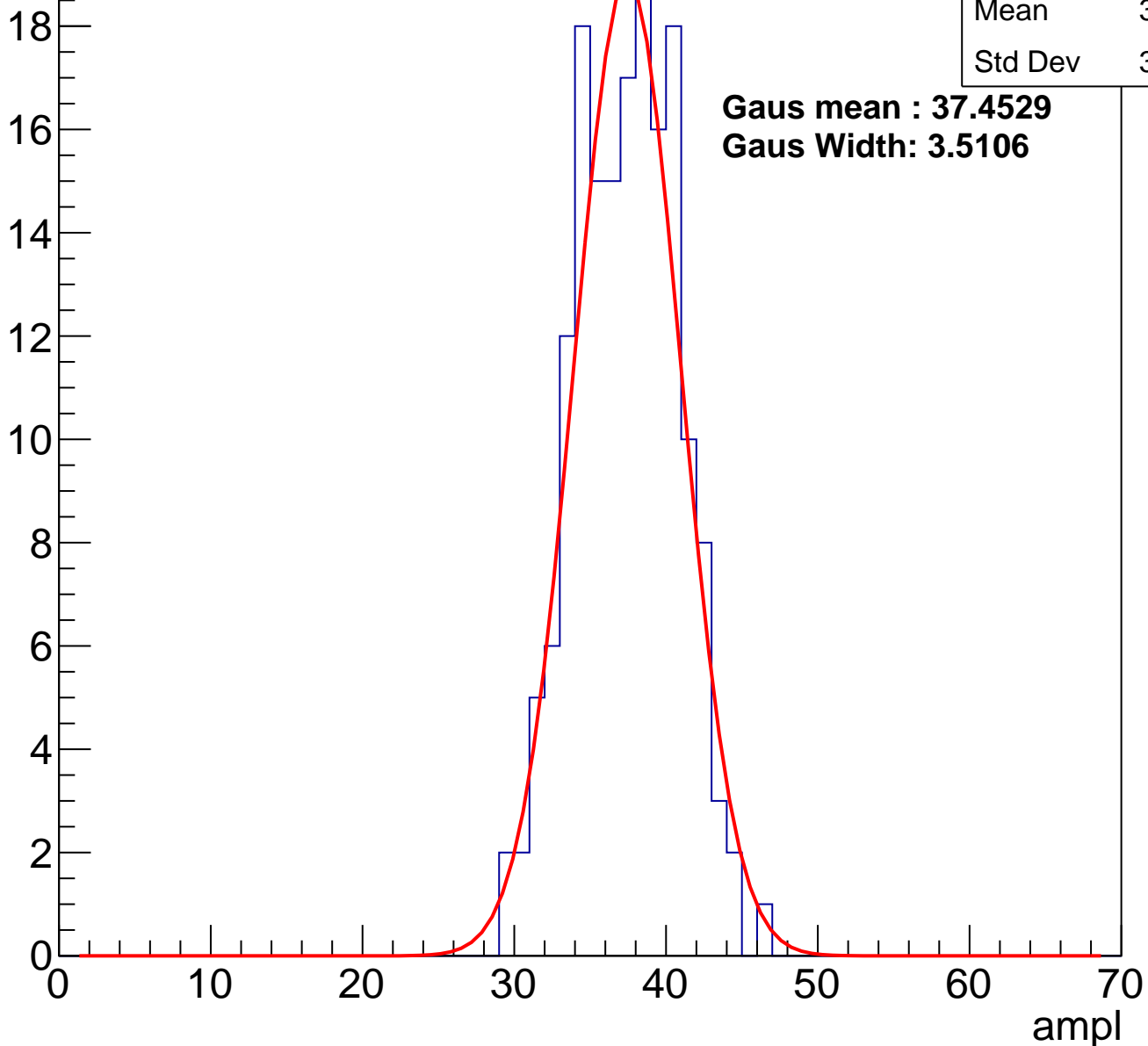
calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	36.93
Std Dev	3.332

Gaus mean : 37.4529

Gaus Width: 3.5106

Entry



B1L001S, U25-ch124, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	44.58
Std Dev	3.683

Gaus mean : 45.1371

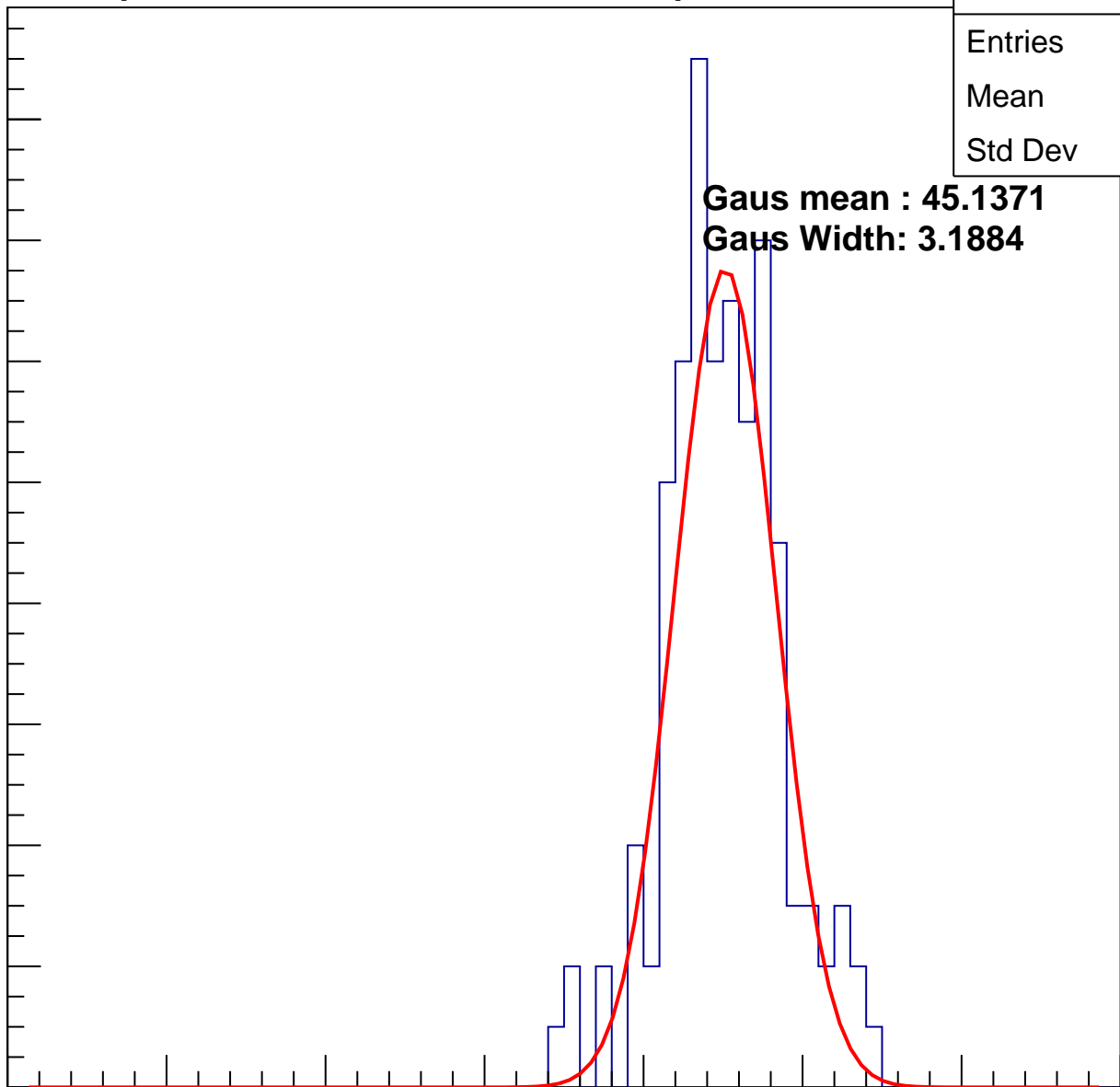
Gaus Width: 3.1884

Entry

16
14
12
10
8
6
4
2
0

ampl

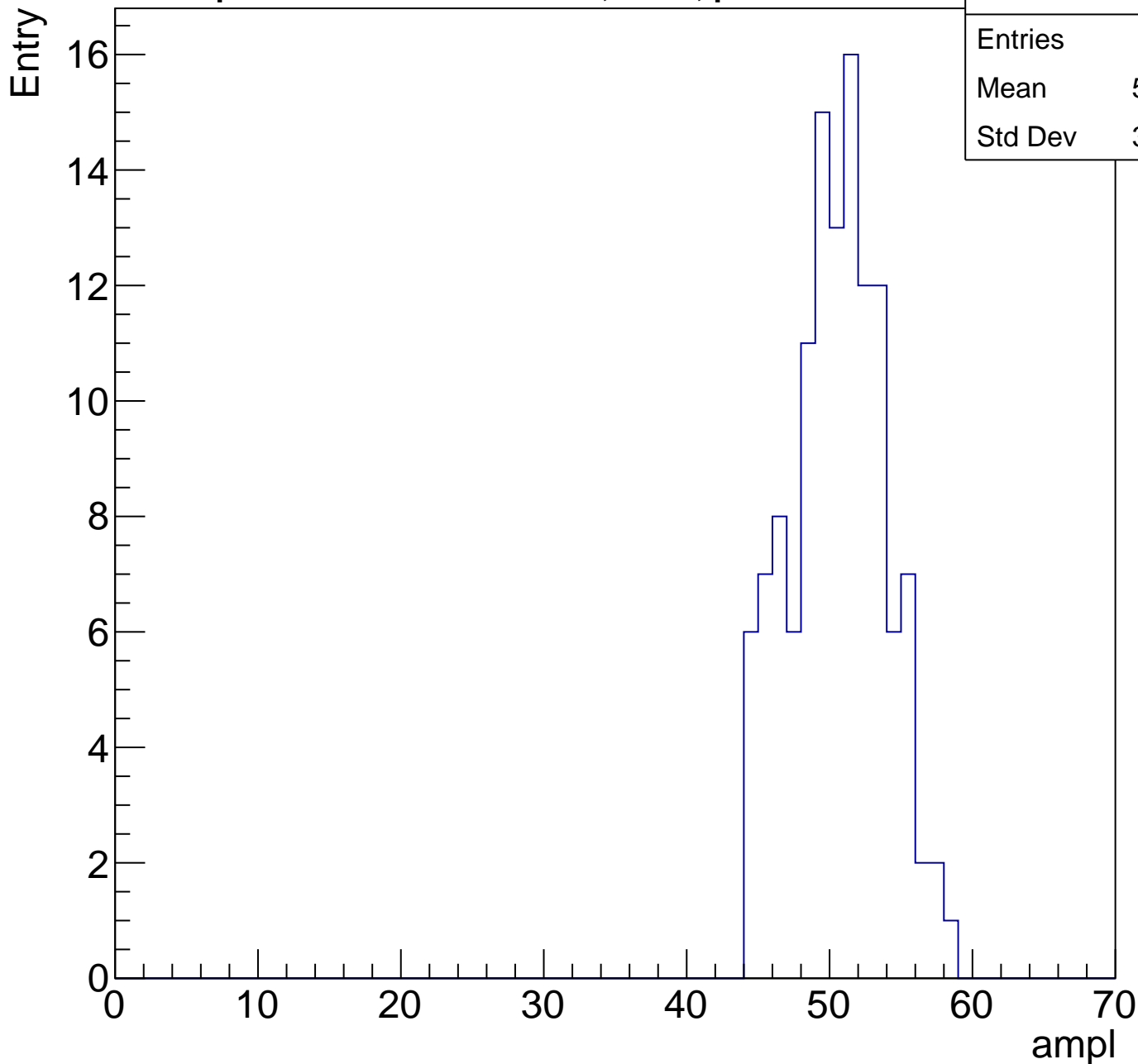
0 10 20 30 40 50 60 70



B1L001S, U25-ch124, adc3

calib_packv5_042523_0143.root, FC#2, port C2

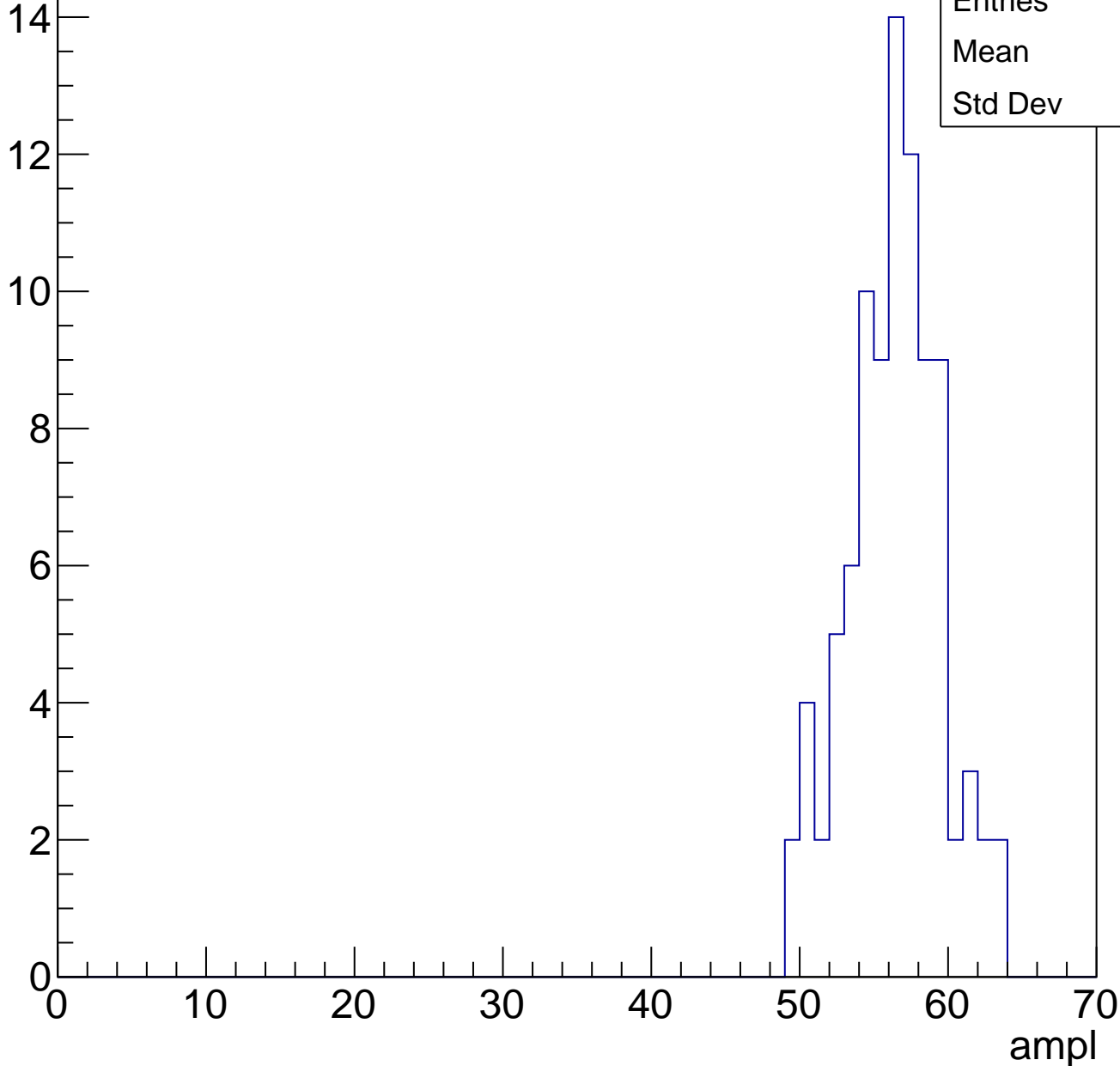
Entries	124
Mean	50.09
Std Dev	3.248



B1L001S, U25-ch124, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch124, adc5

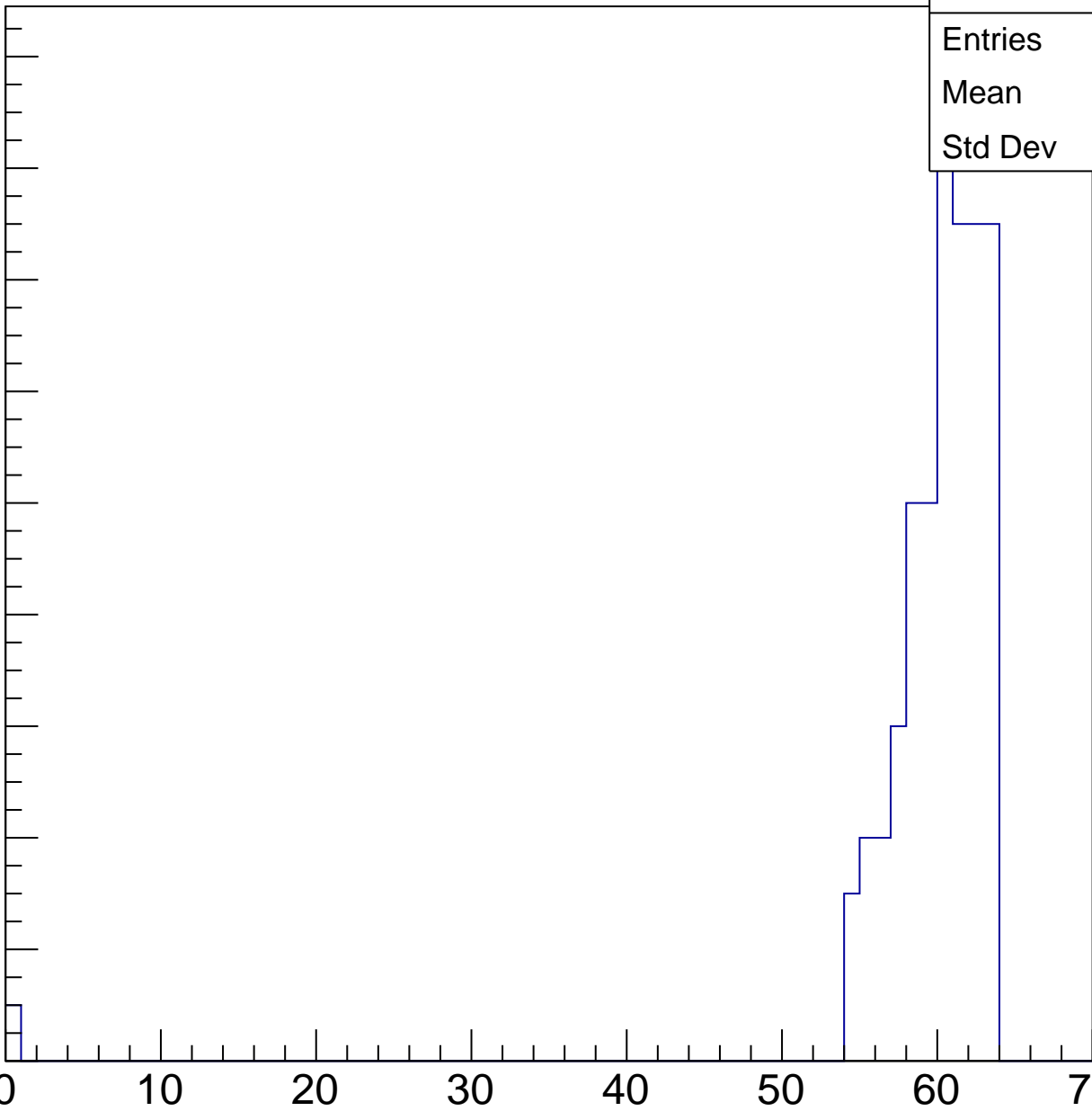
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	101
Mean	59.29
Std Dev	6.398

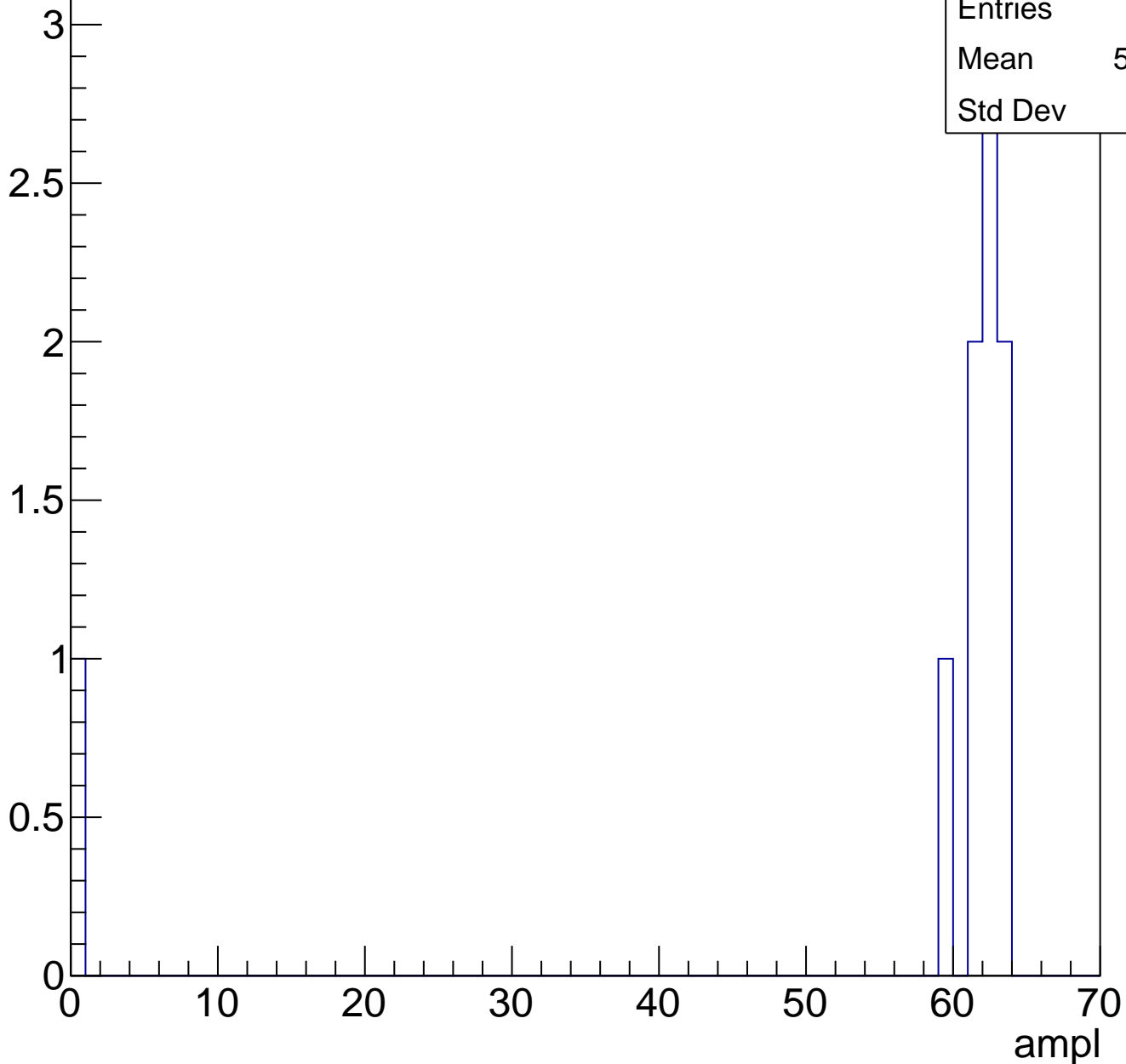
ampl



B1L001S, U25-ch124, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch124, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch125, adc0

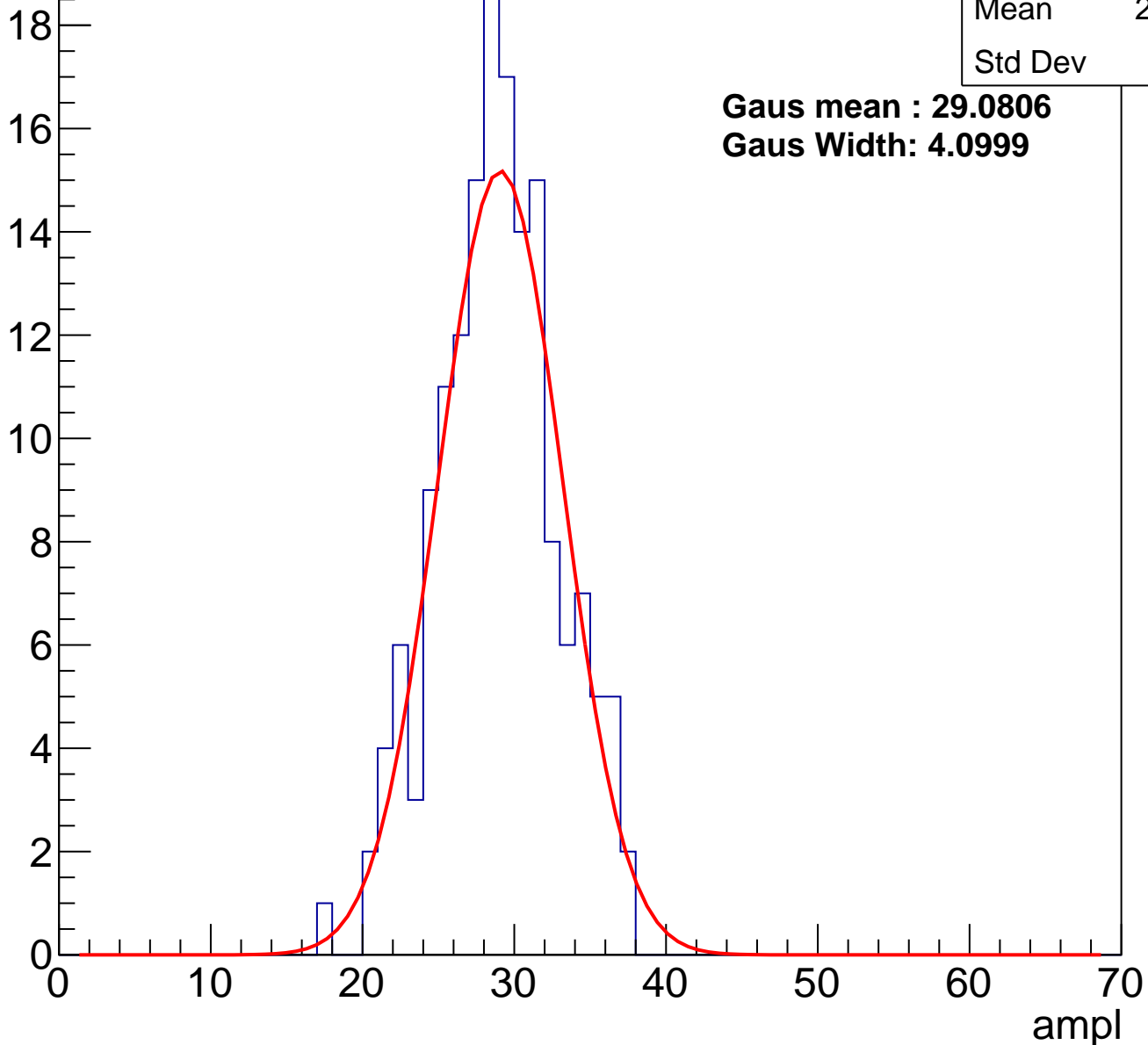
calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	28.45
Std Dev	3.92

Gaus mean : 29.0806

Gaus Width: 4.0999

Entry



B1L001S, U25-ch125, adc1

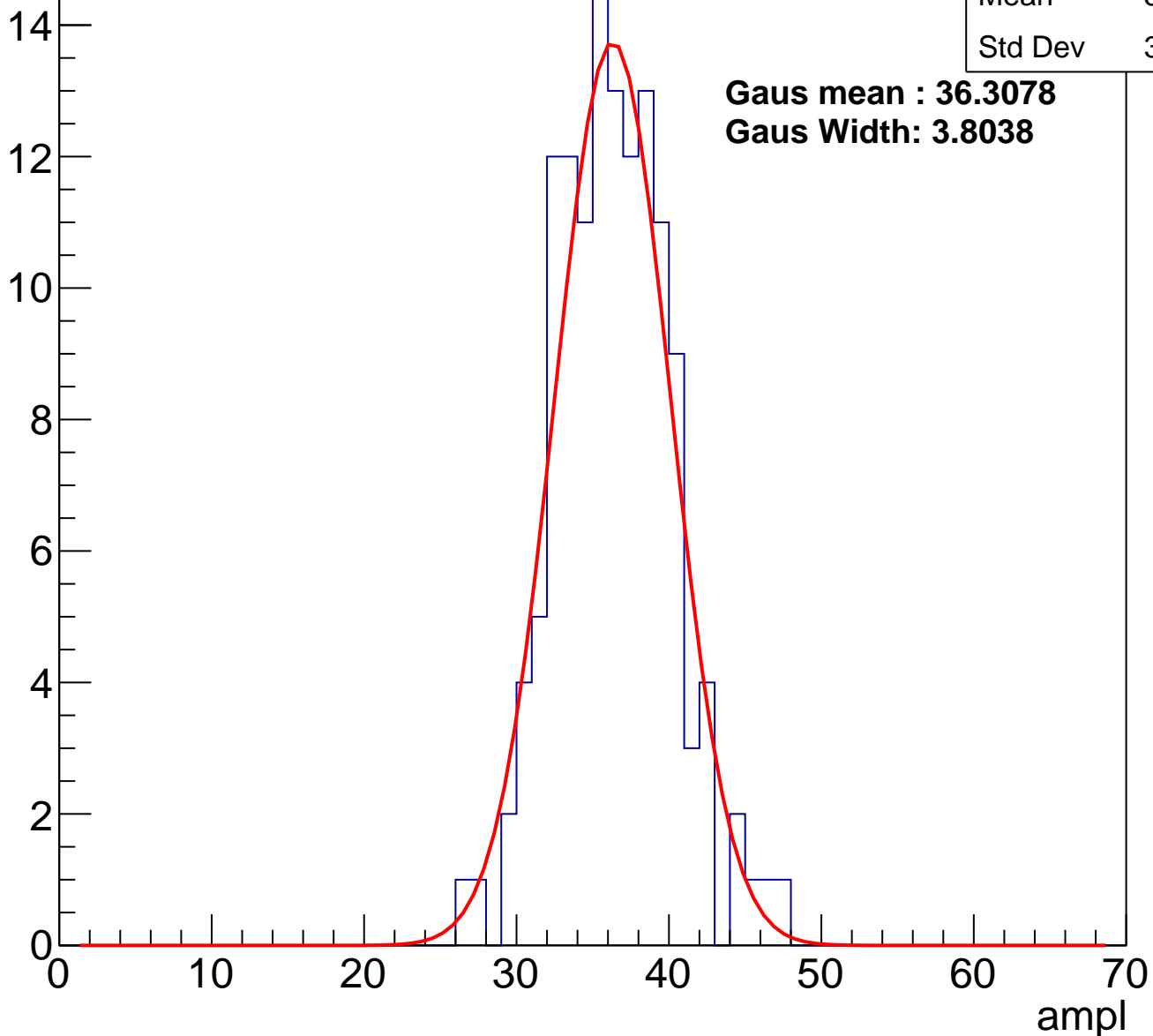
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	35.92
Std Dev	3.742

Gaus mean : 36.3078

Gaus Width: 3.8038

Entry



B1L001S, U25-ch125, adc2

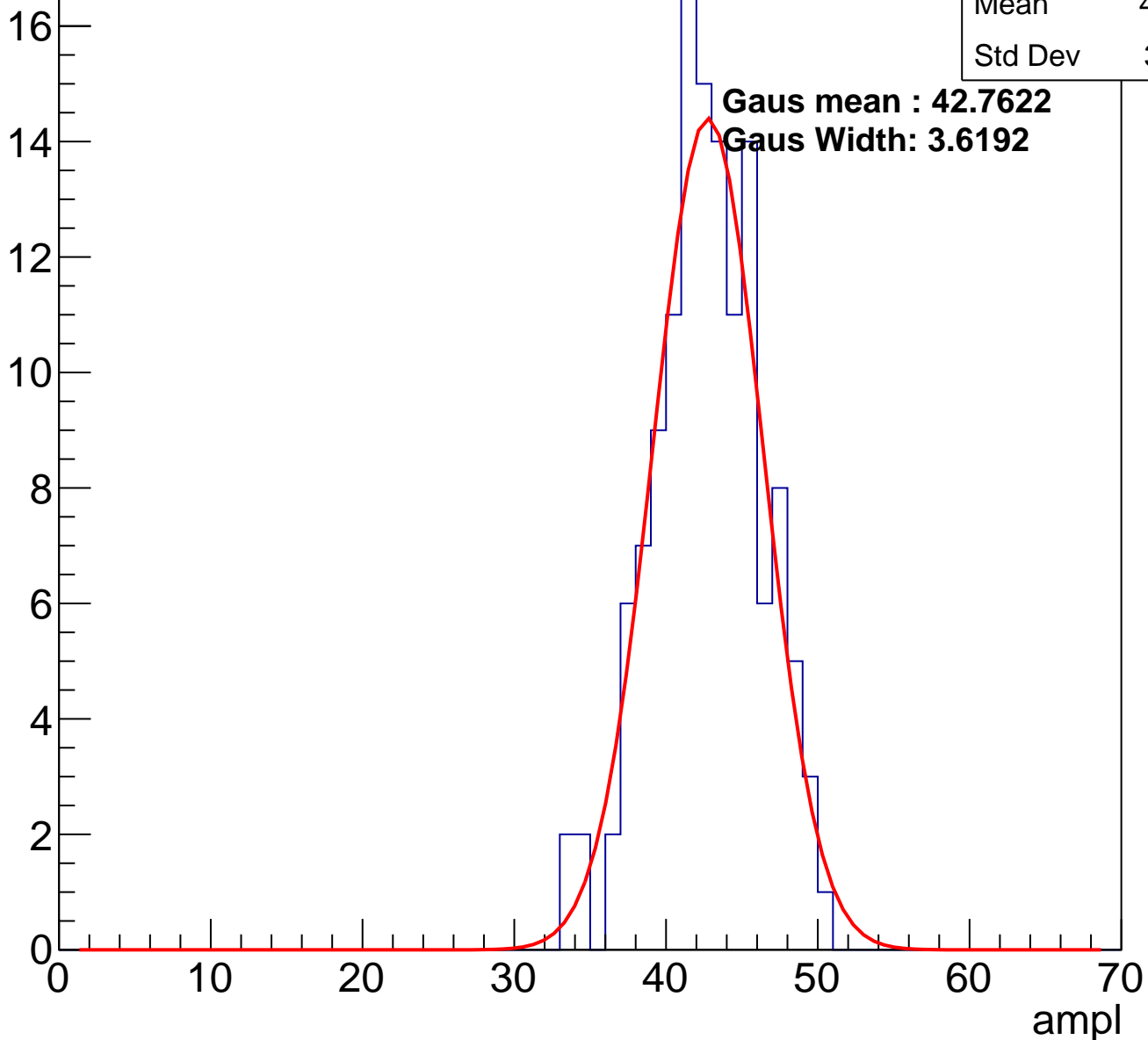
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	133
Mean	42.23
Std Dev	3.501

Gaus mean : 42.7622

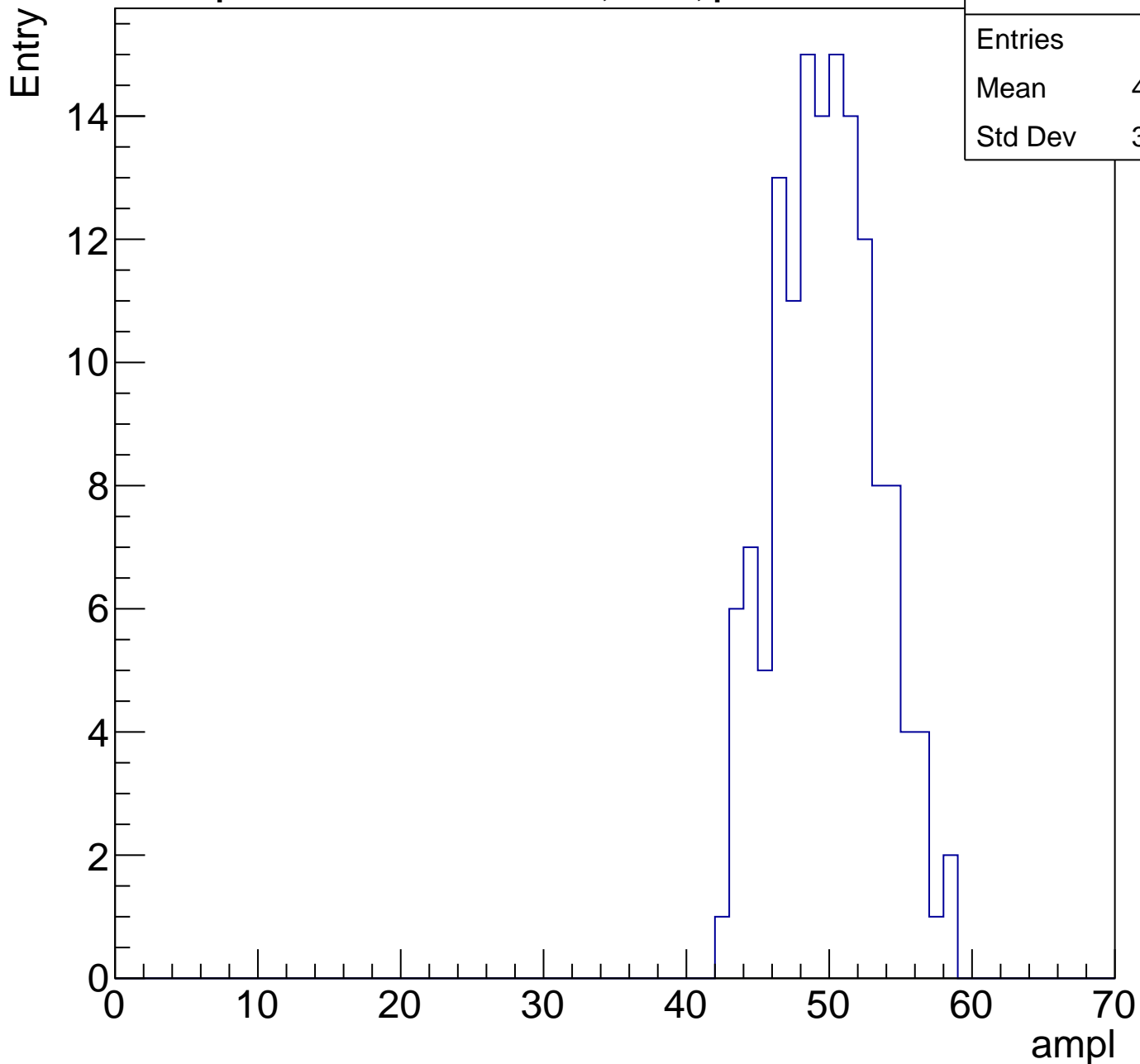
Gaus Width: 3.6192



B1L001S, U25-ch125, adc3

calib_packv5_042523_0143.root, FC#2, port C2

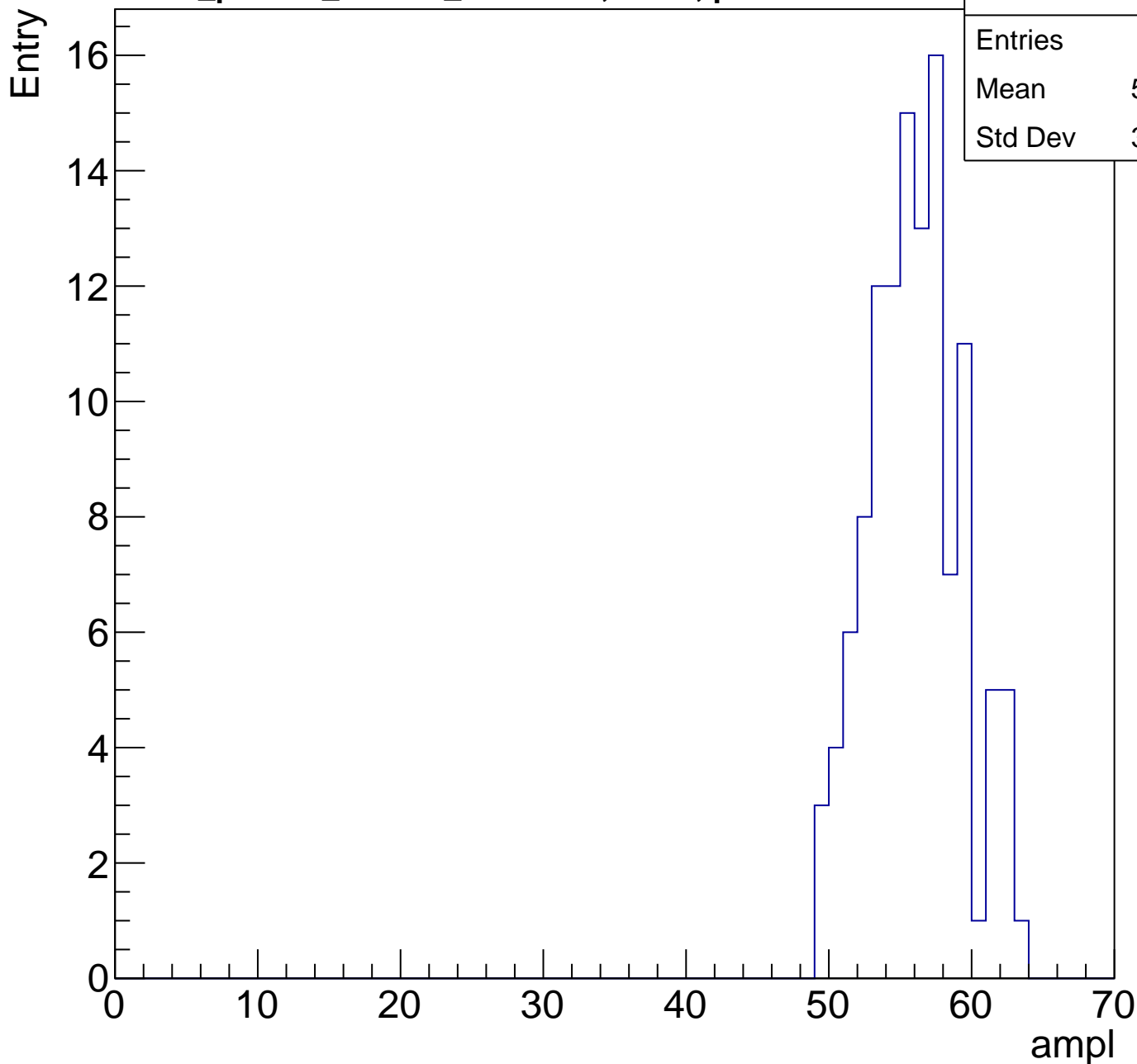
Entries	140
Mean	49.39
Std Dev	3.533



B1L001S, U25-ch125, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	55.55
Std Dev	3.214



B1L001S, U25-ch125, adc5

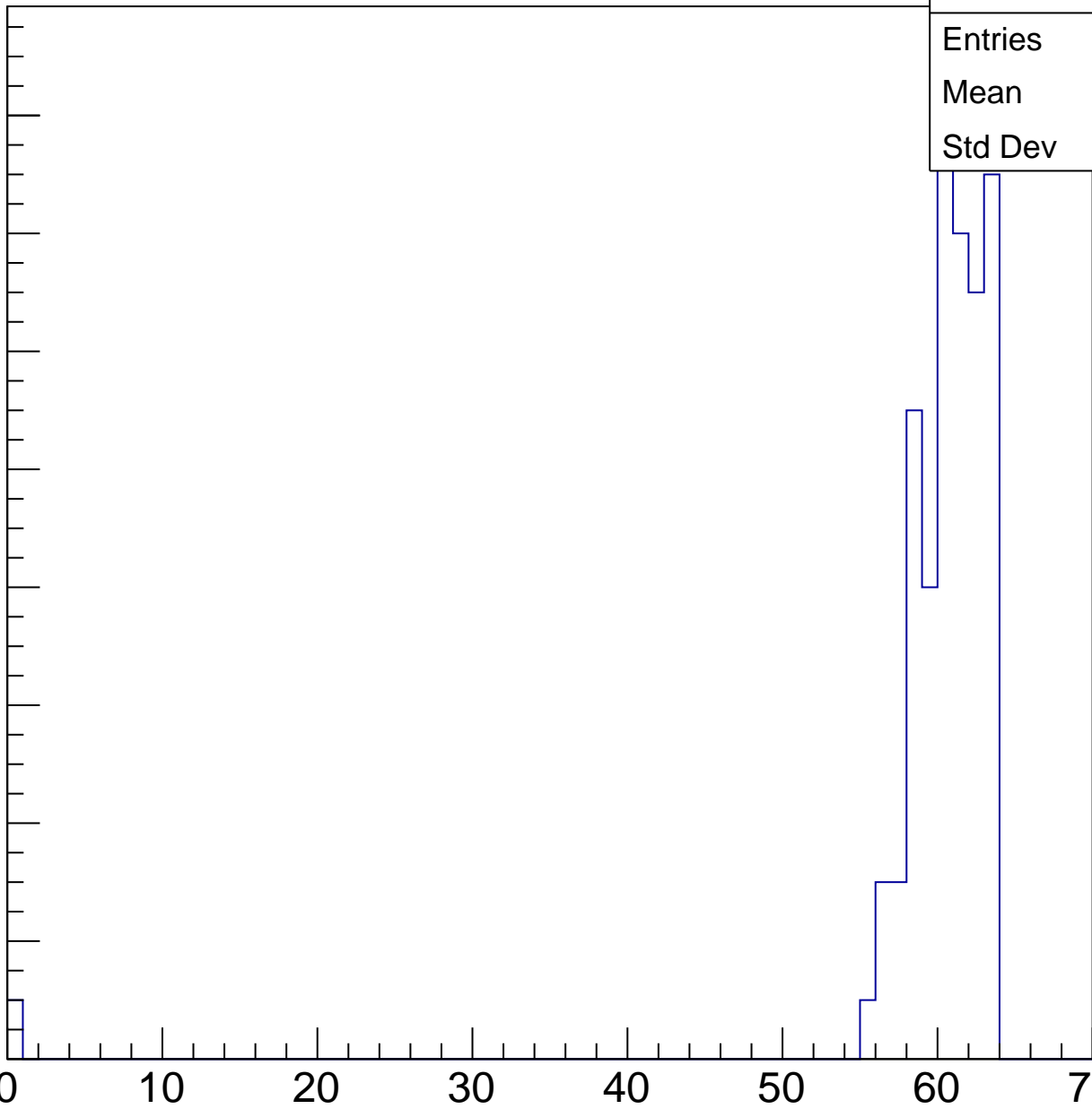
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.64
Std Dev	6.77

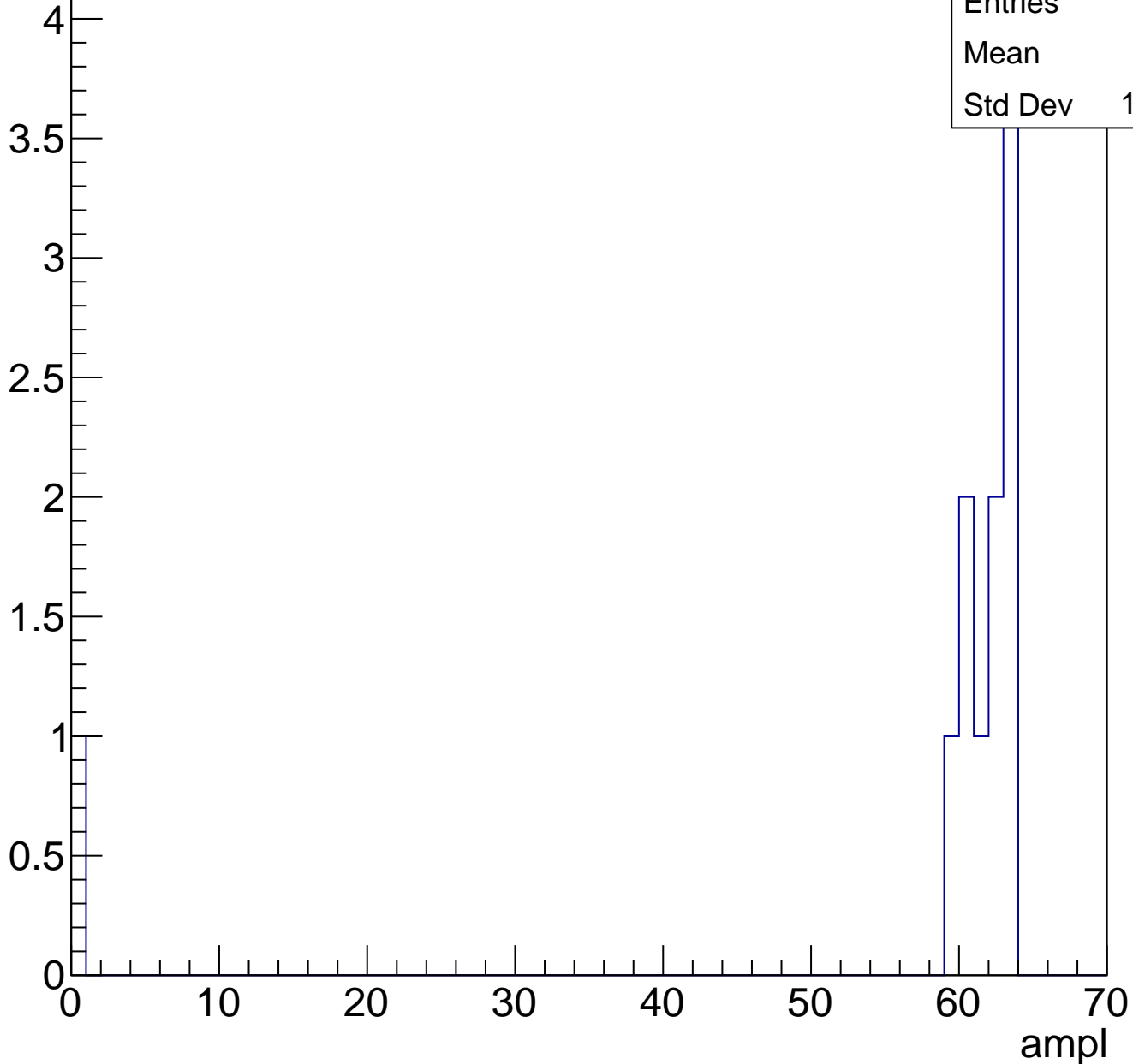
ampl



B1L001S, U25-ch125, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch125, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	0
Std Dev	0

B1L001S, U25-ch126, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	30.43
Std Dev	4.19

Gaus mean : 31.1116

Gaus Width: 3.5942

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

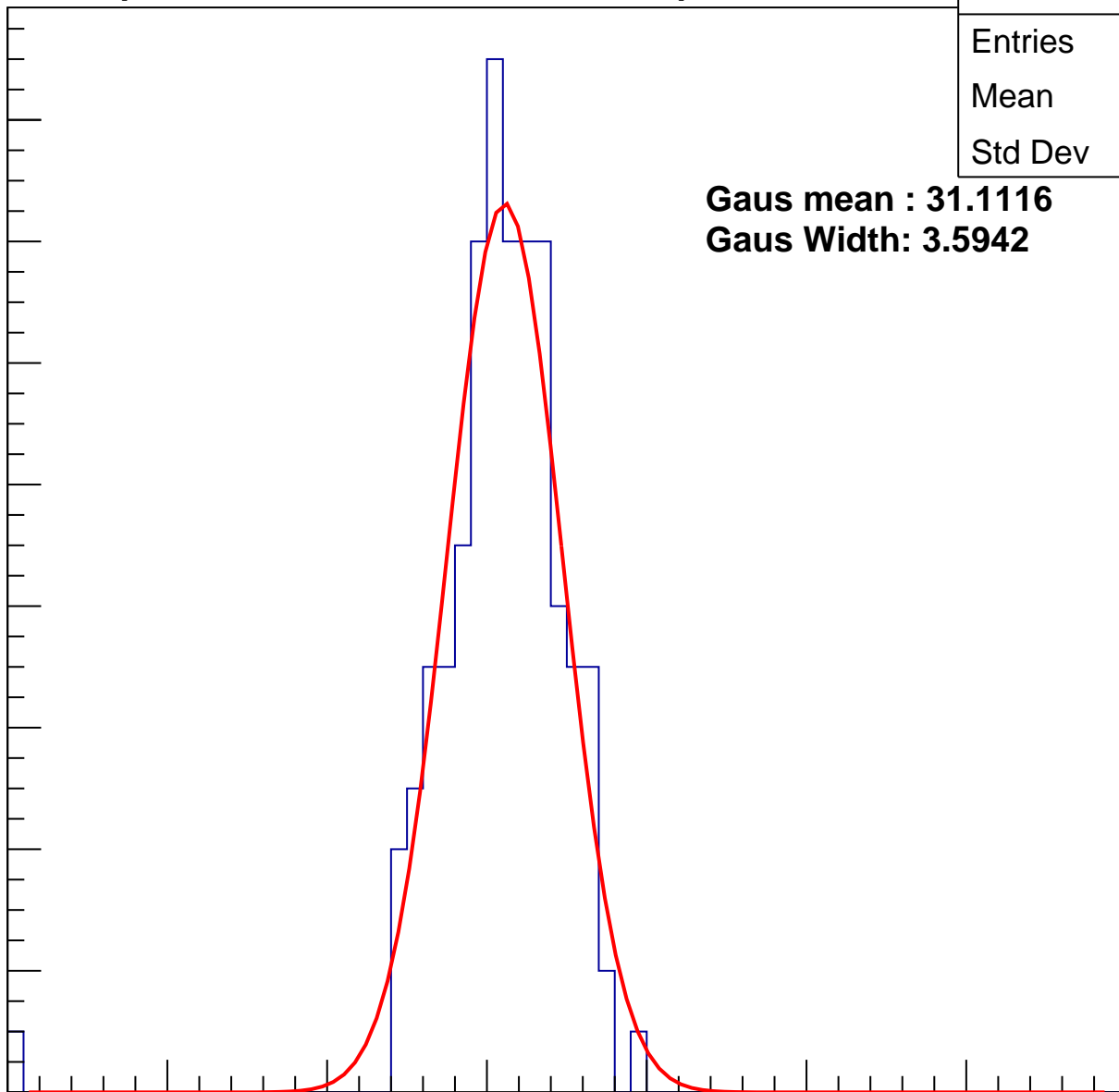
40

50

60

70

ampl



B1L001S, U25-ch126, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	37.31
Std Dev	3.331

Gaus mean : 37.5253

Gaus Width: 3.2422

Entry

14

12

10

8

6

4

2

0

0

10

20

30

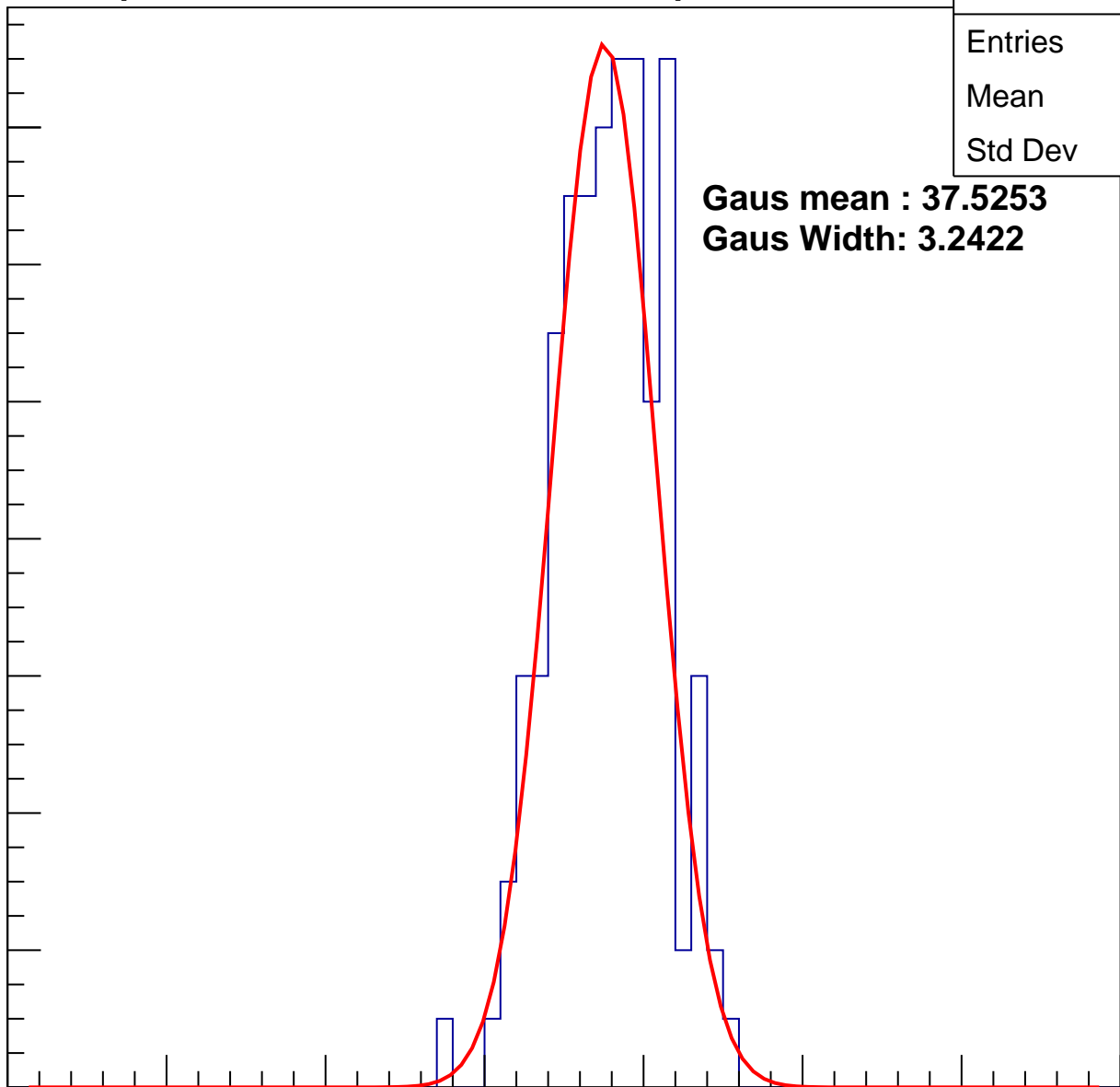
40

50

60

70

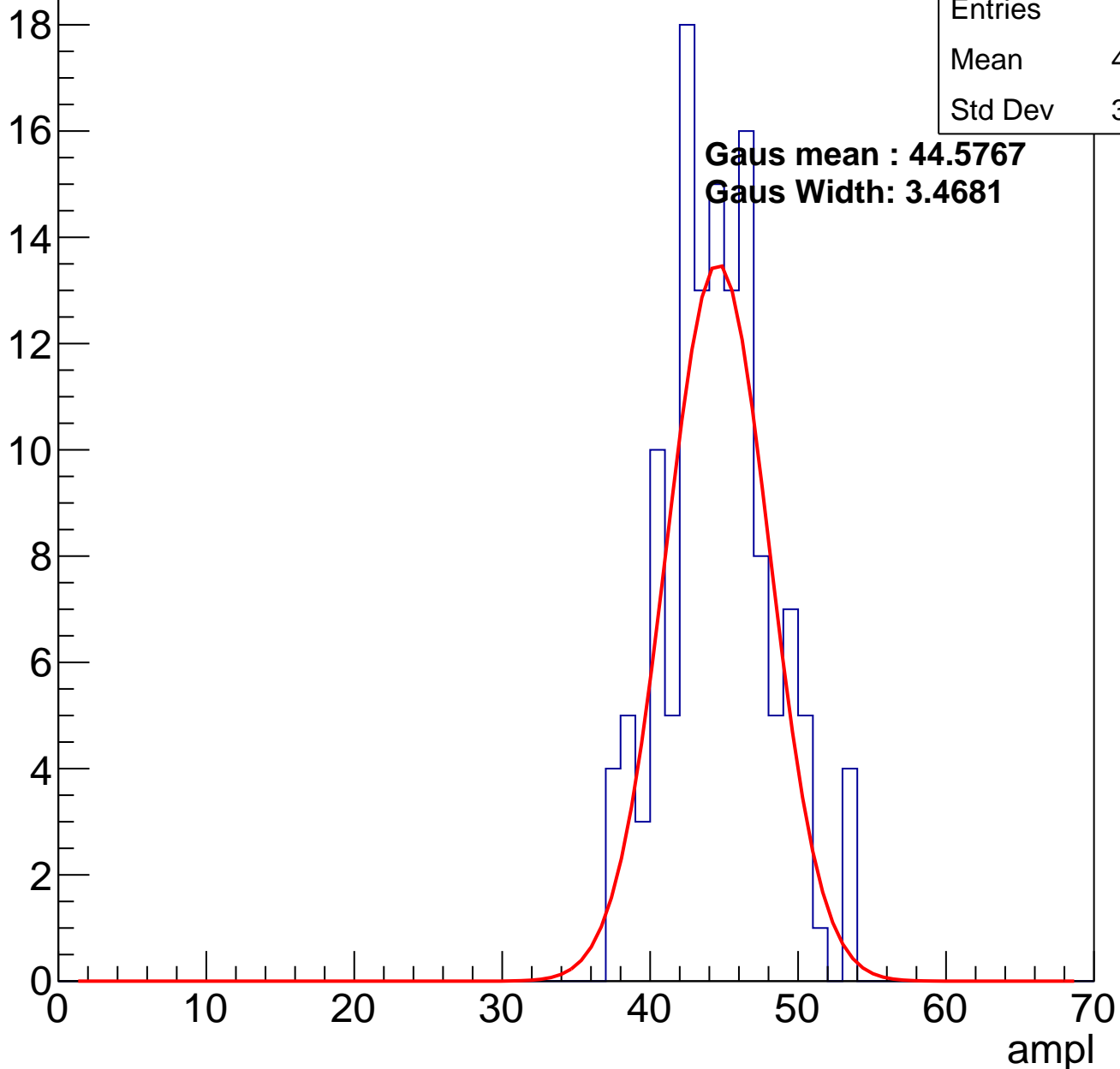
ampl



B1L001S, U25-ch126, adc2

calib_packv5_042523_0143.root, FC#2, port C2

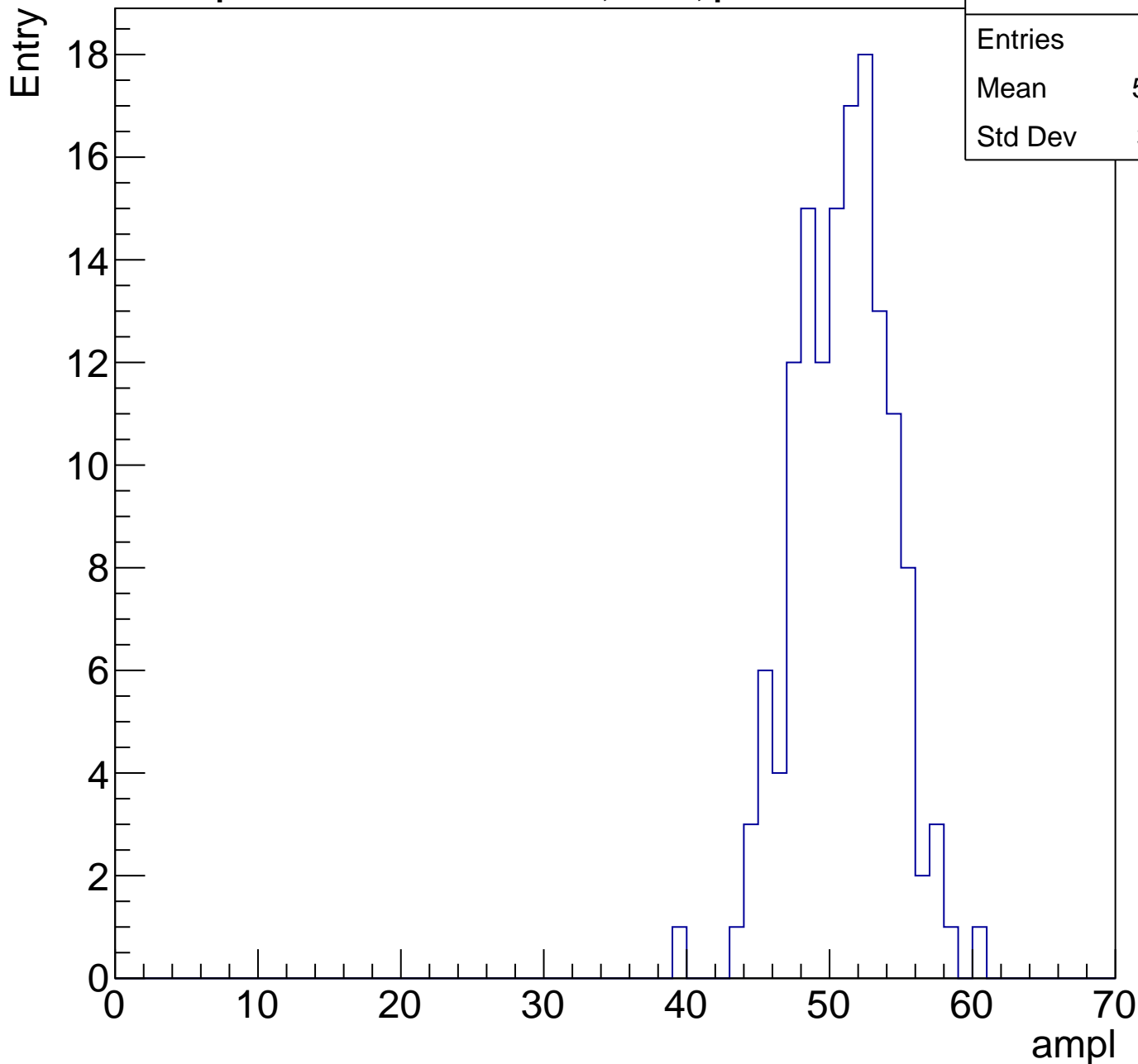
Entry



B1L001S, U25-ch126, adc3

calib_packv5_042523_0143.root, FC#2, port C2

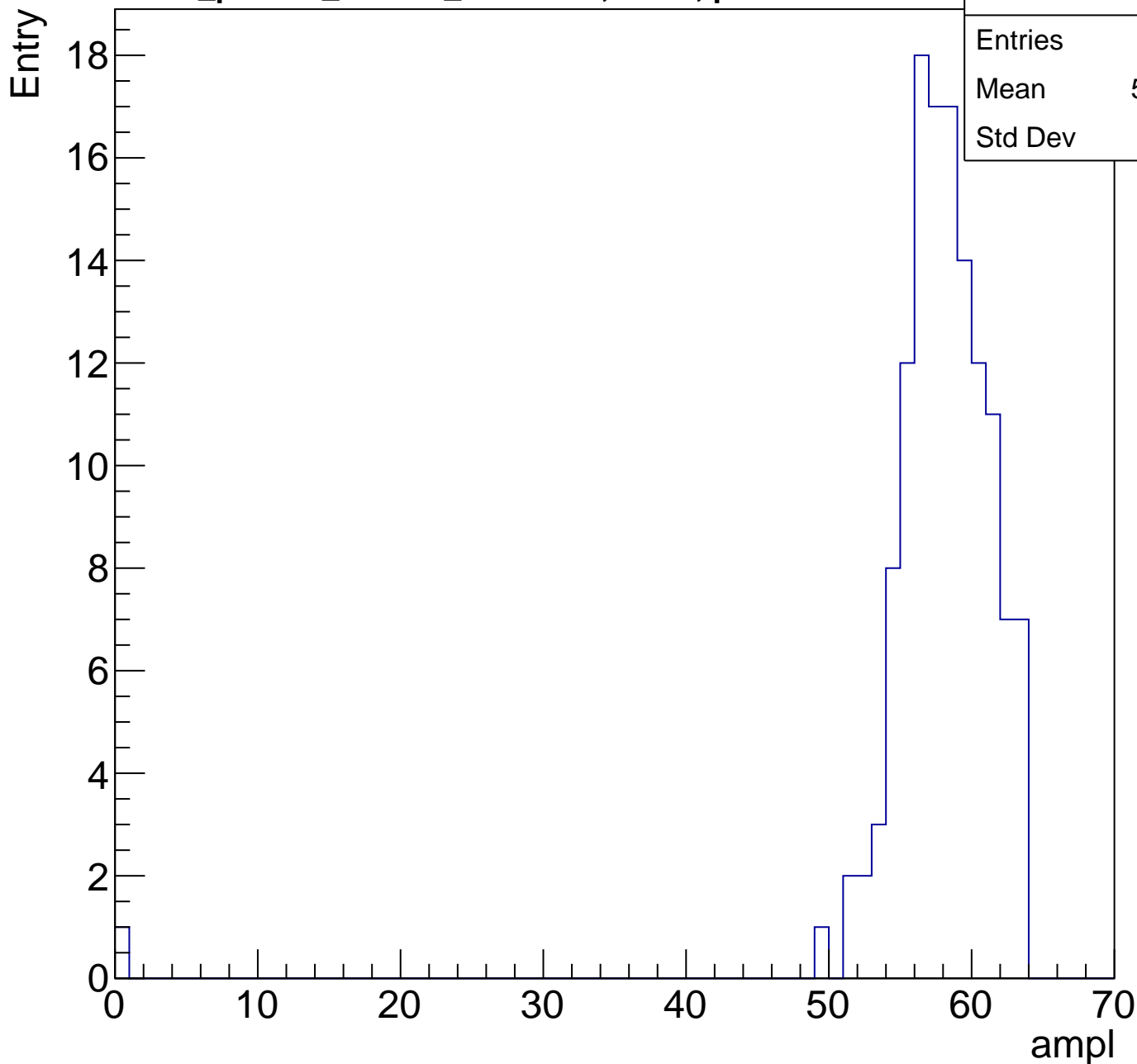
Entries	143
Mean	50.47
Std Dev	3.351



B1L001S, U25-ch126, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	57.28
Std Dev	5.77



B1L001S, U25-ch126, adc5

calib_packv5_042523_0143.root, FC#2, port C2

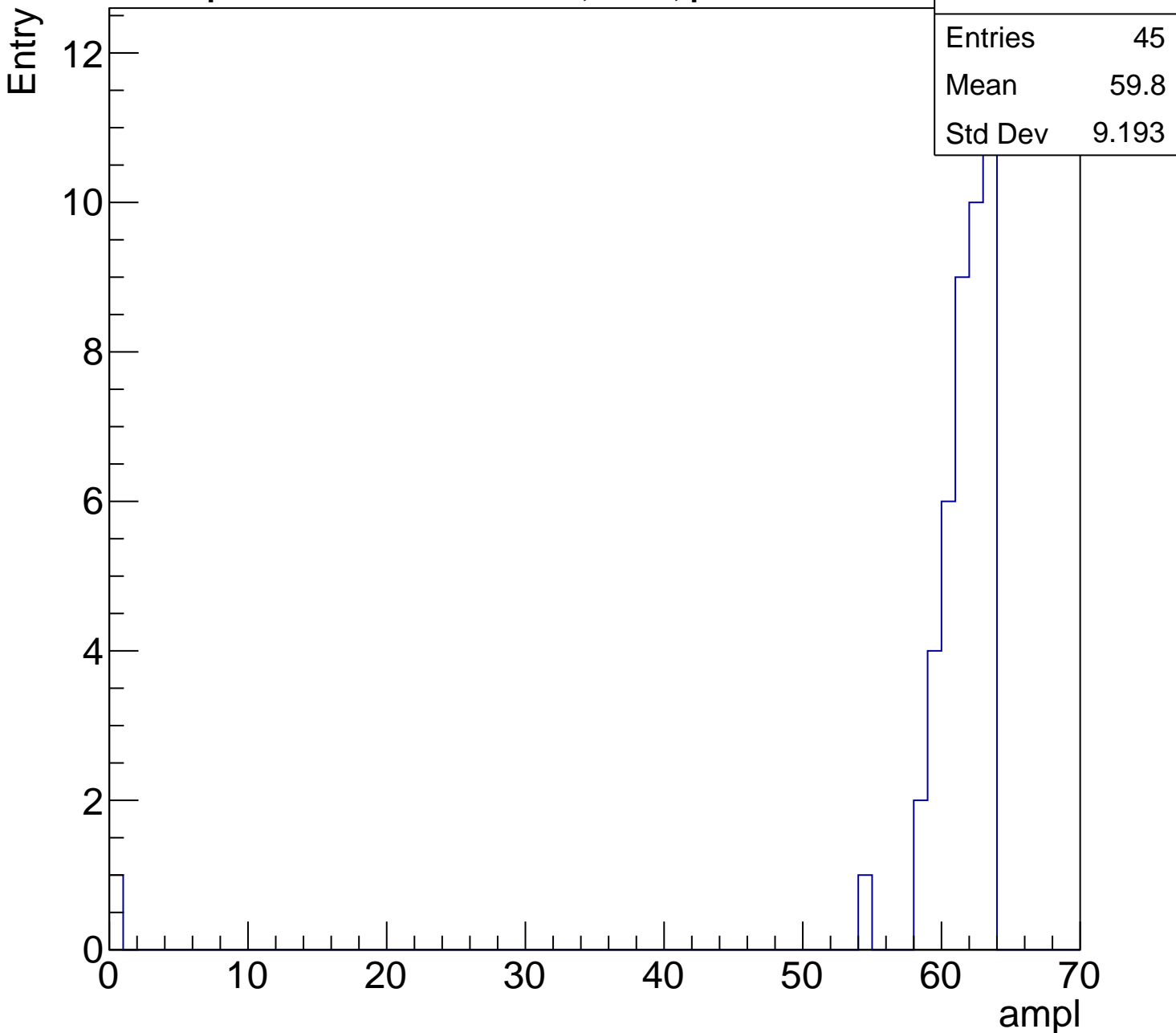
Entry

12
10
8
6
4
2
0

Entries	45
Mean	59.8
Std Dev	9.193

ampl

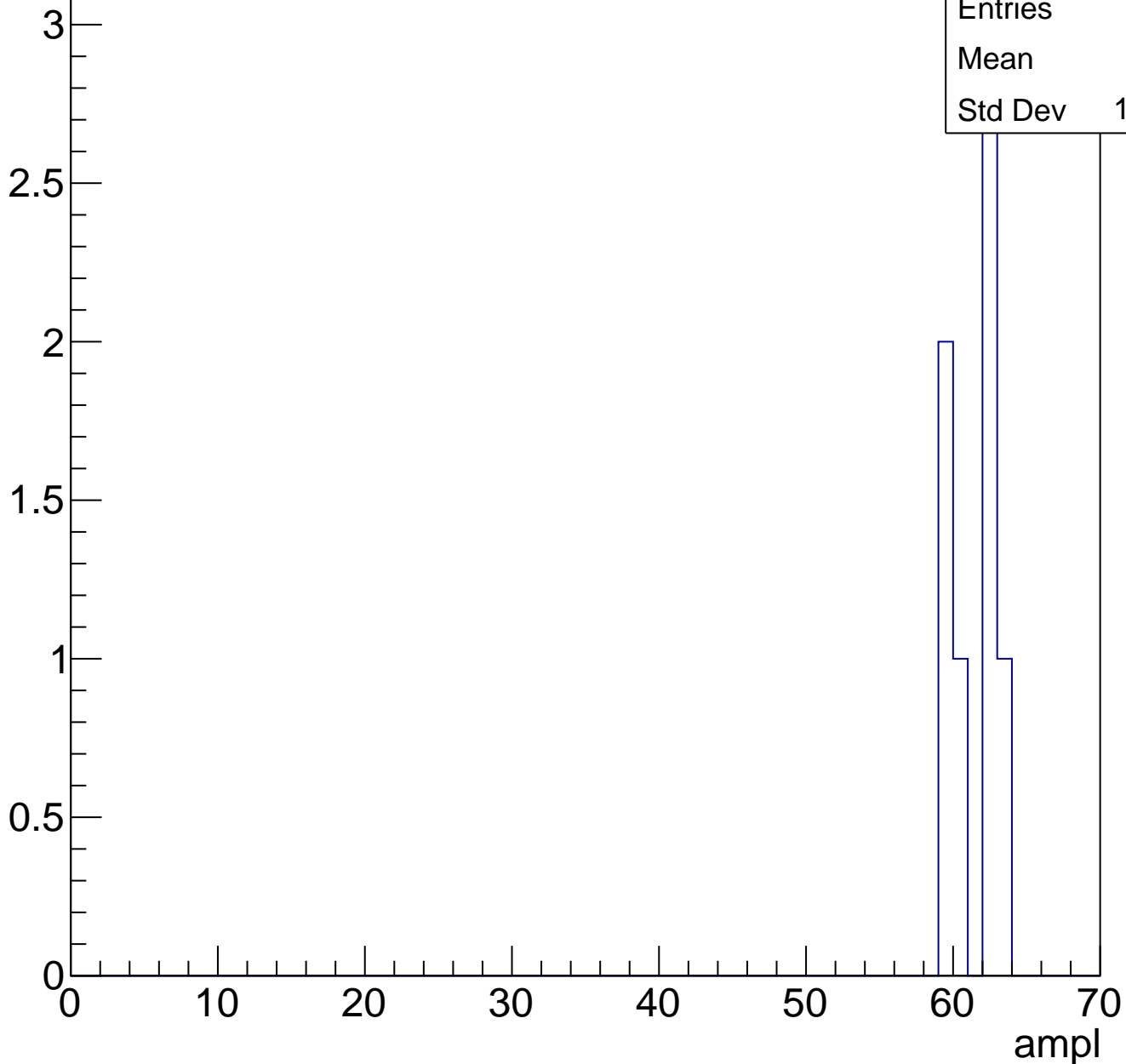
0 10 20 30 40 50 60 70



B1L001S, U25-ch126, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch126, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U25-ch127, adc0

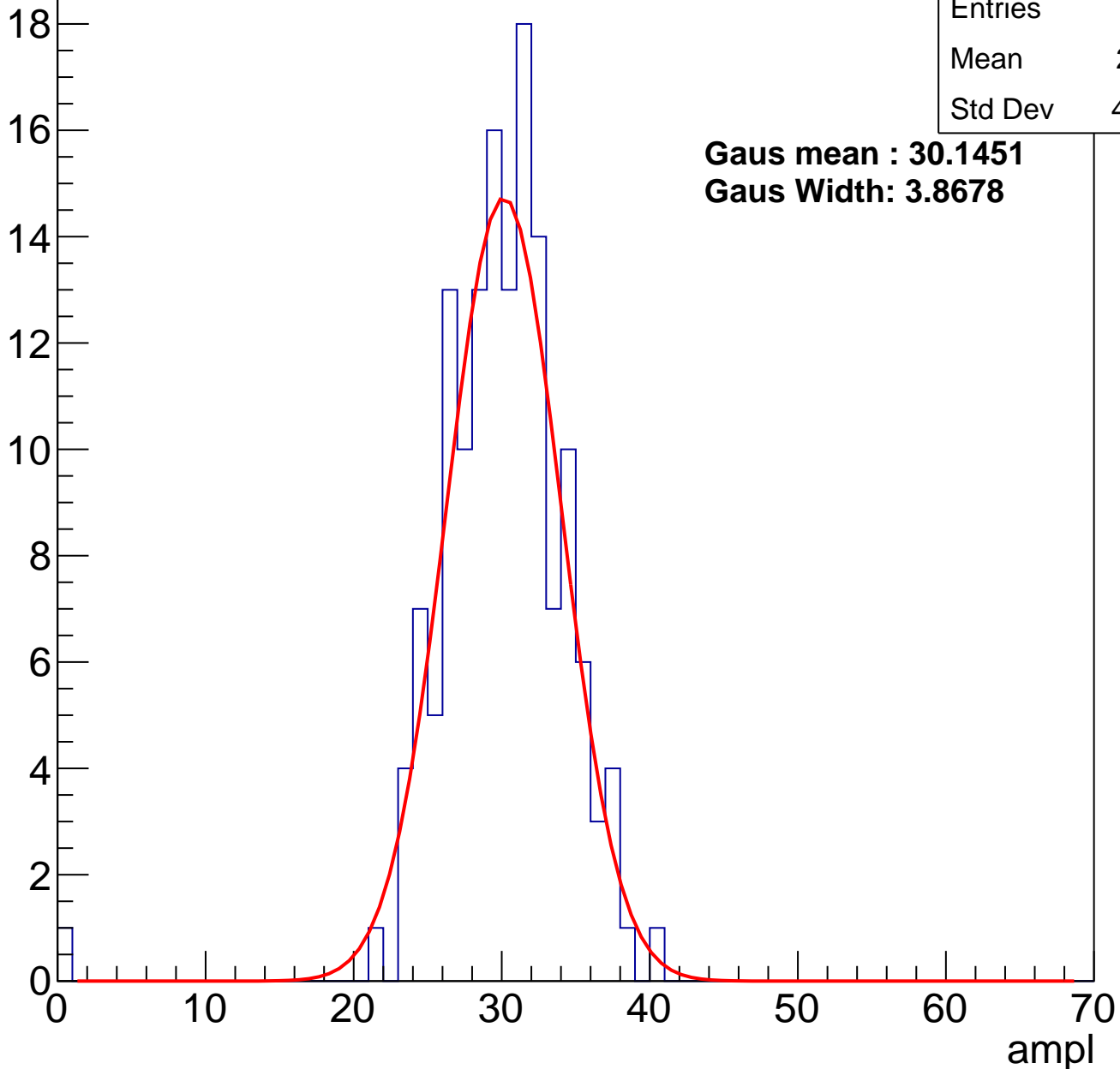
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	29.61
Std Dev	4.374

Gaus mean : 30.1451

Gaus Width: 3.8678

Entry



B1L001S, U25-ch127, adc1

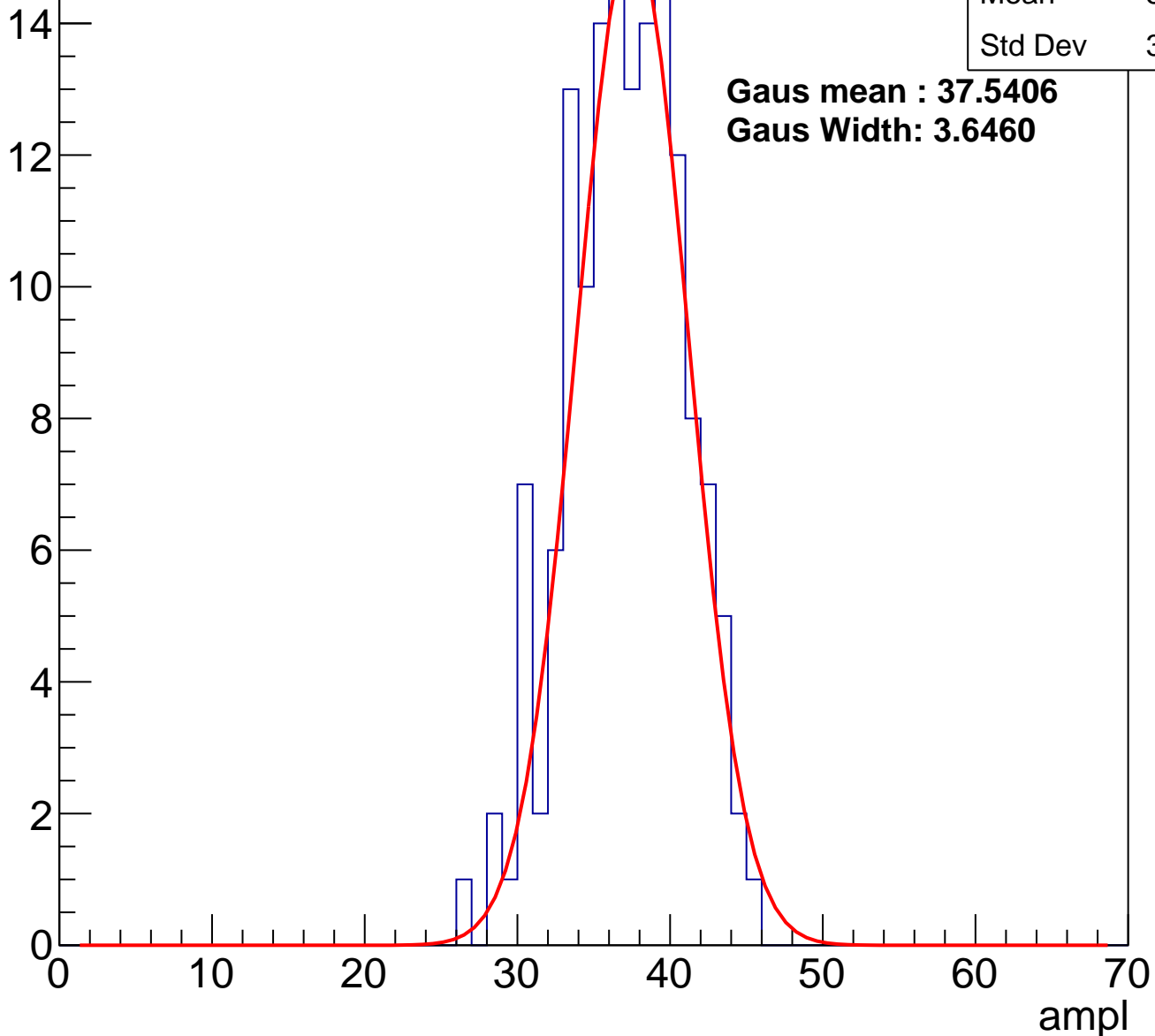
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.64
Std Dev	3.756

Gaus mean : 37.5406

Gaus Width: 3.6460

Entry



B1L001S, U25-ch127, adc2

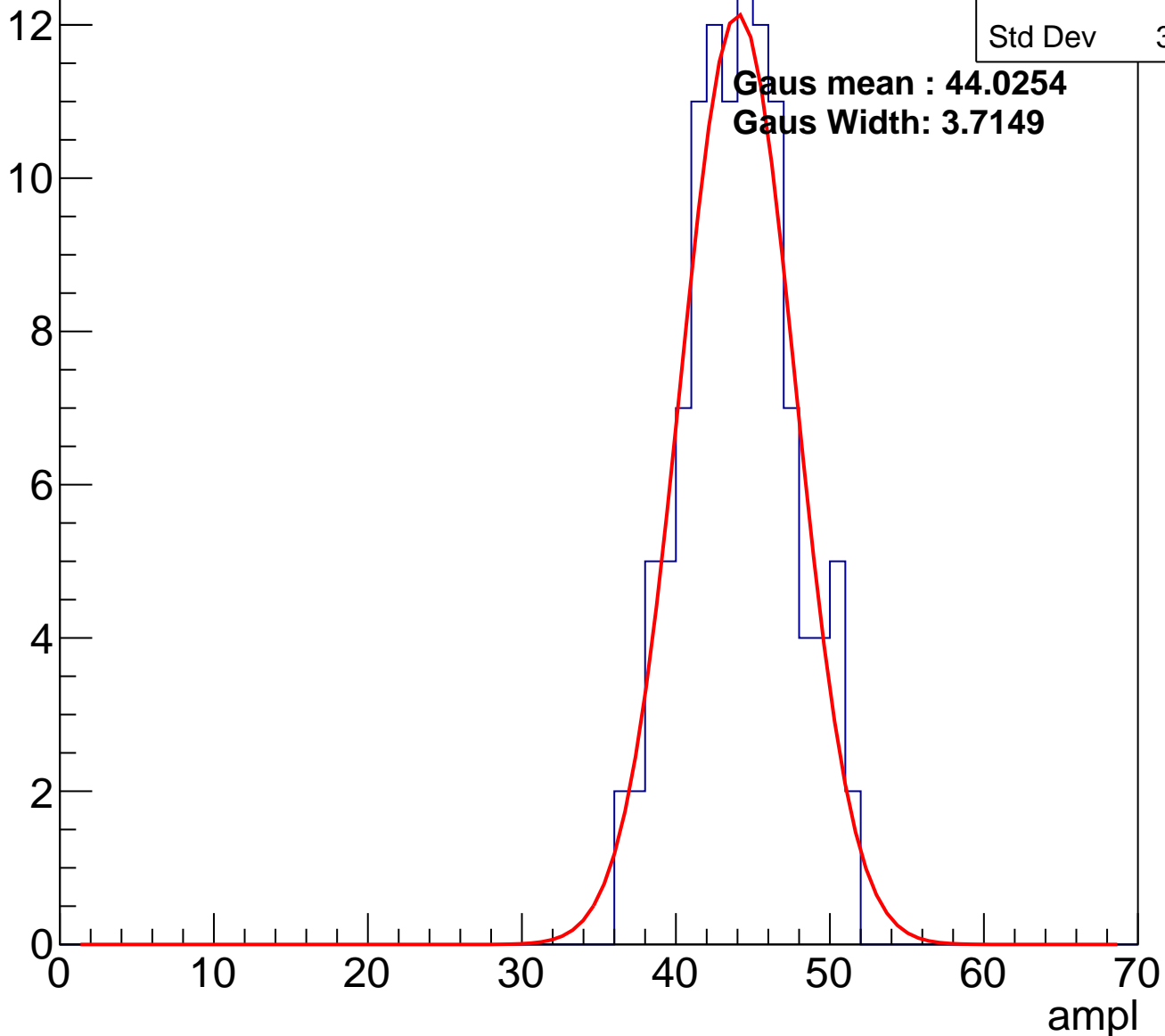
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	43.59
Std Dev	3.448

Gaus mean : 44.0254

Gaus Width: 3.7149

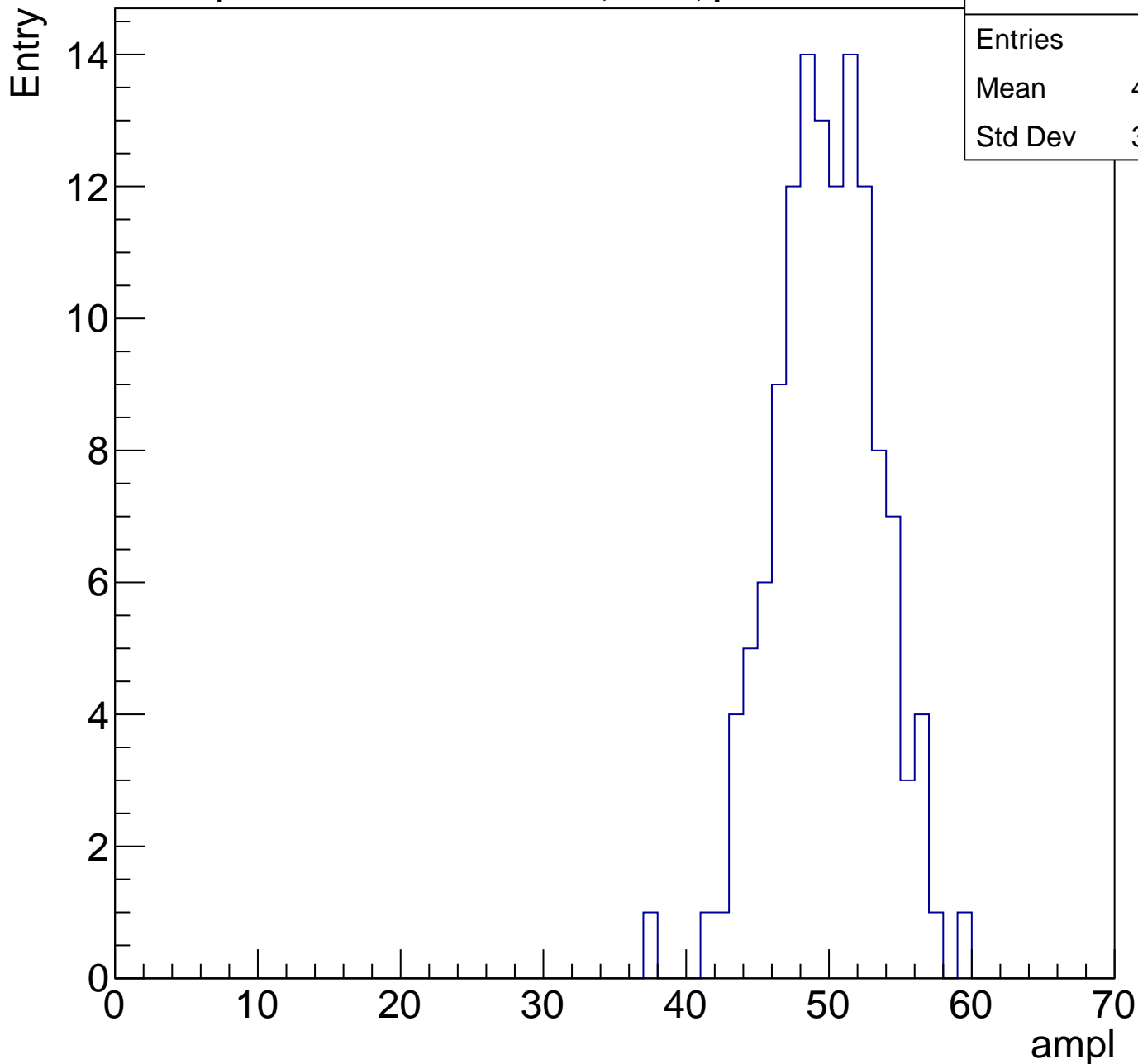
Entry



B1L001S, U25-ch127, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	49.33
Std Dev	3.642



B1L001S, U25-ch127, adc4

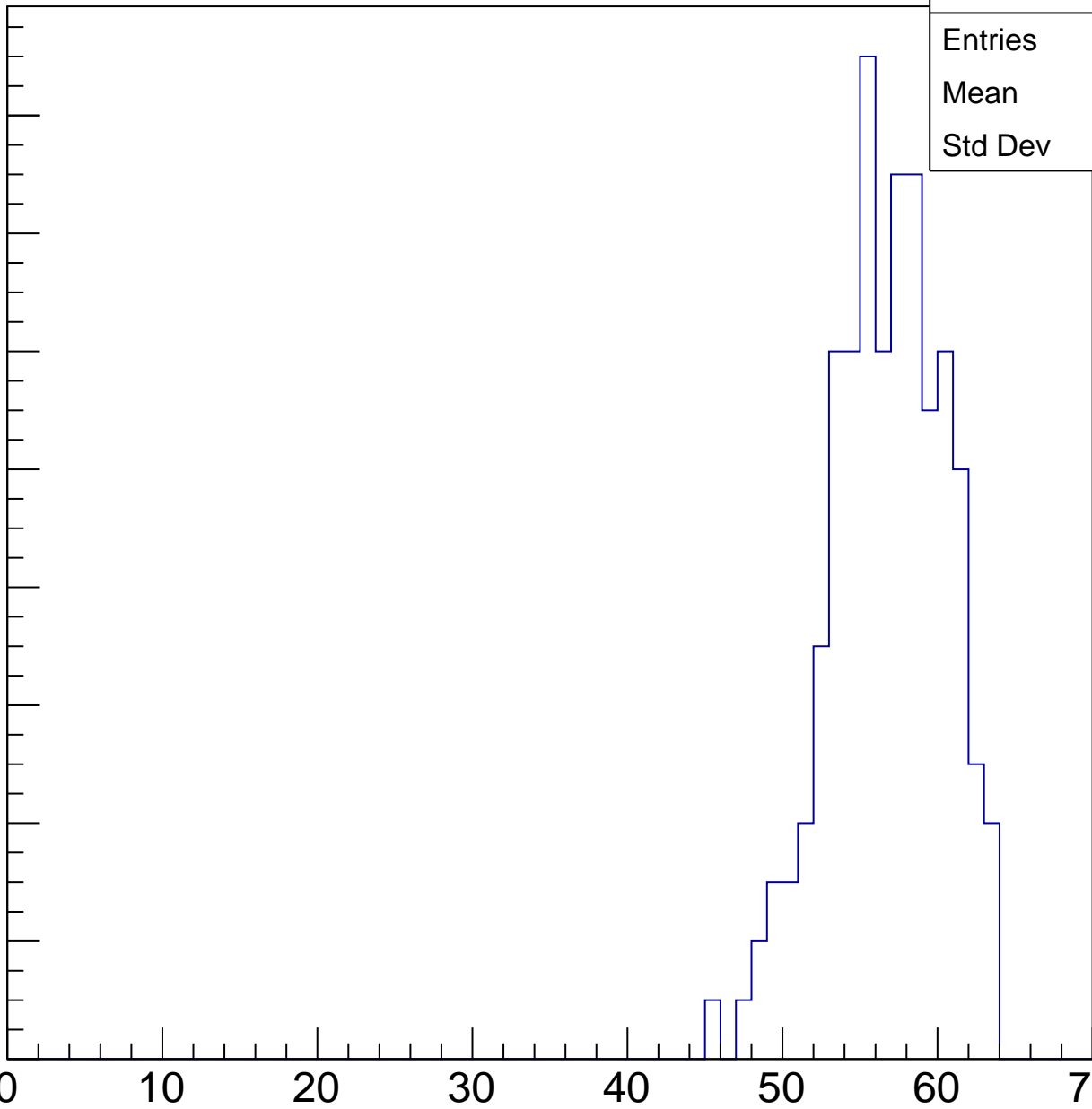
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	56.23
Std Dev	3.658

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U25-ch127, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

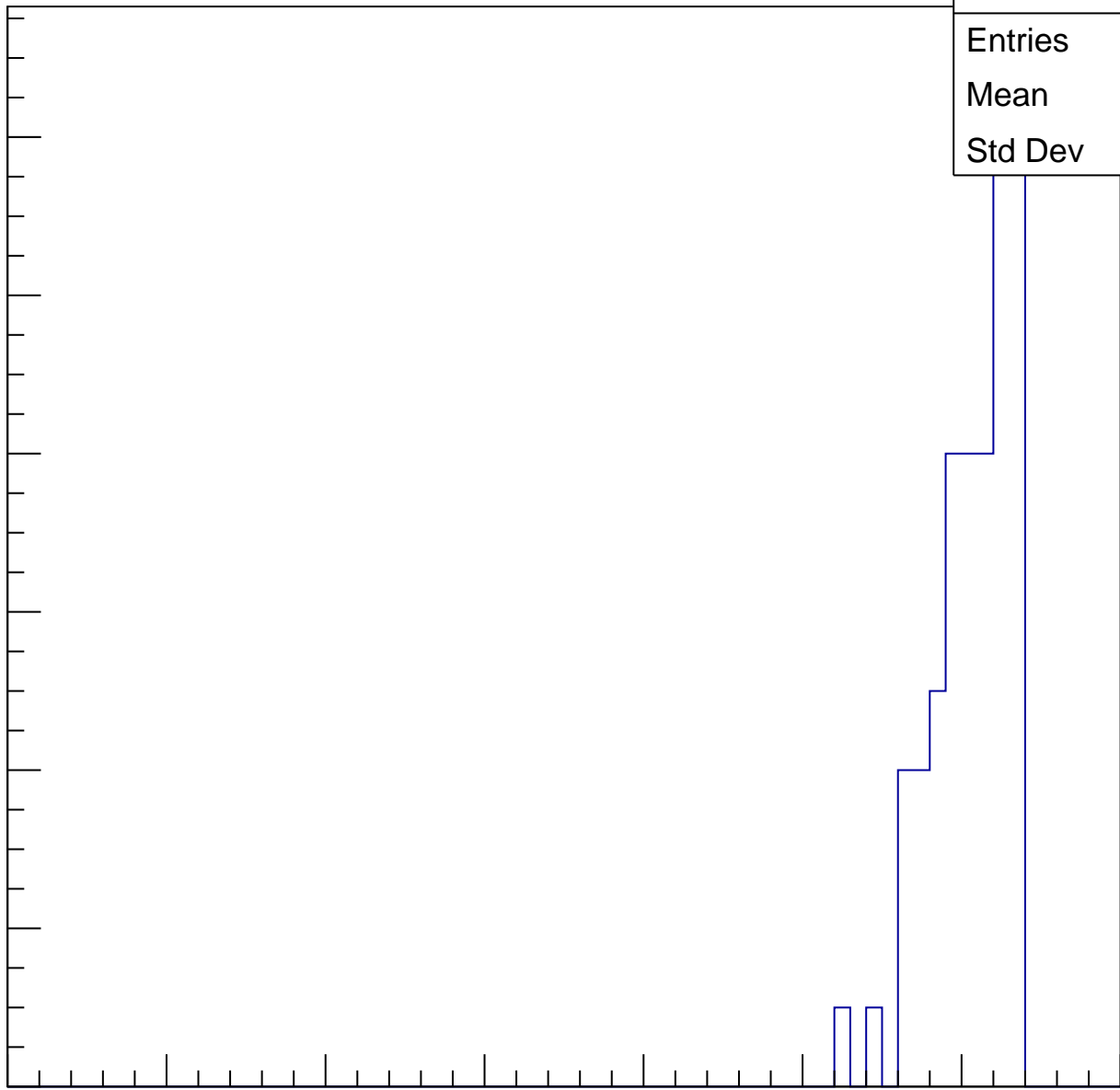
2

0

Entries	64
Mean	60.17
Std Dev	2.497

ampl

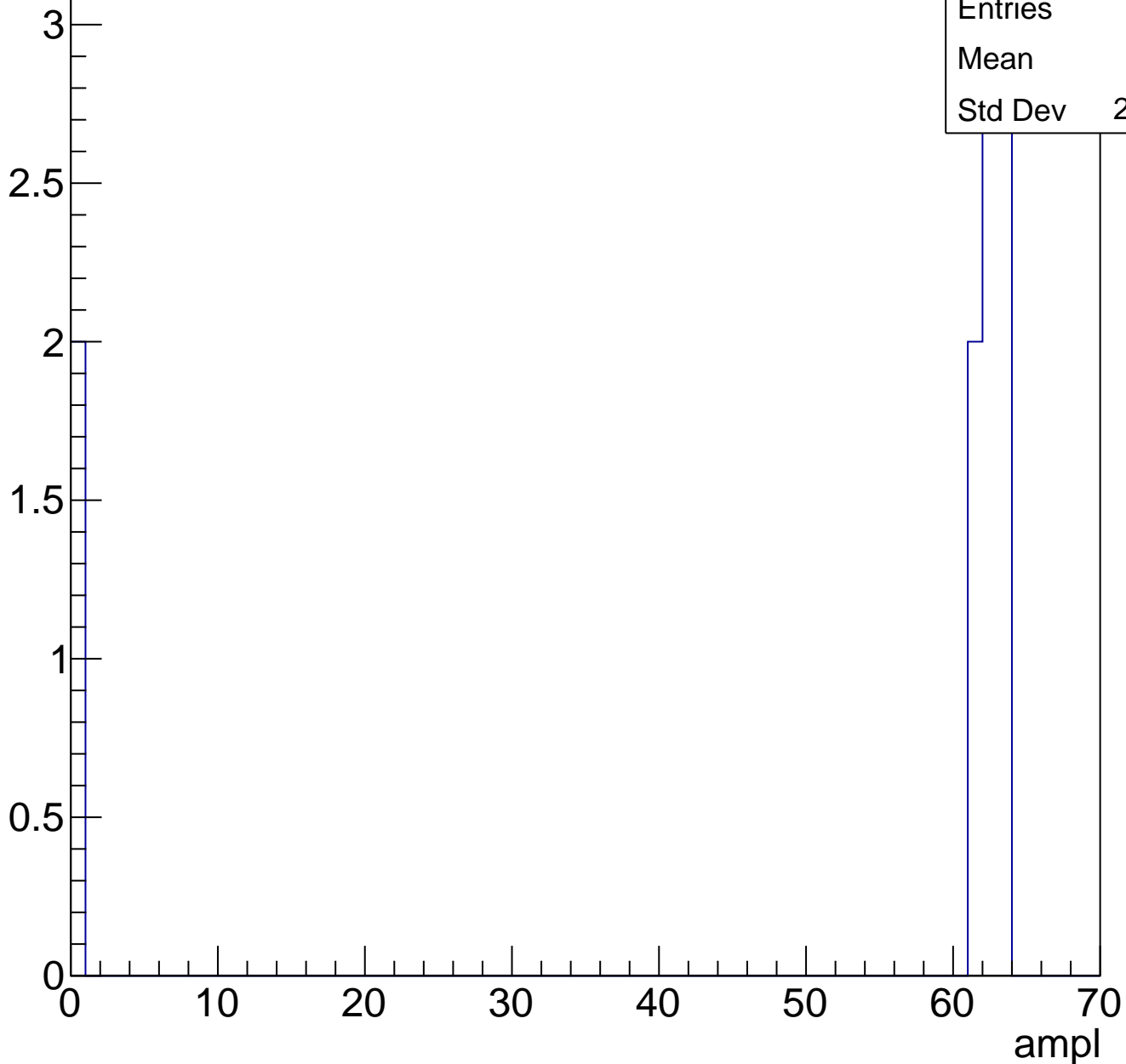
0 10 20 30 40 50 60 70



B1L001S, U25-ch127, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U25-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

