

B1L103S, U14-ch0, adc0

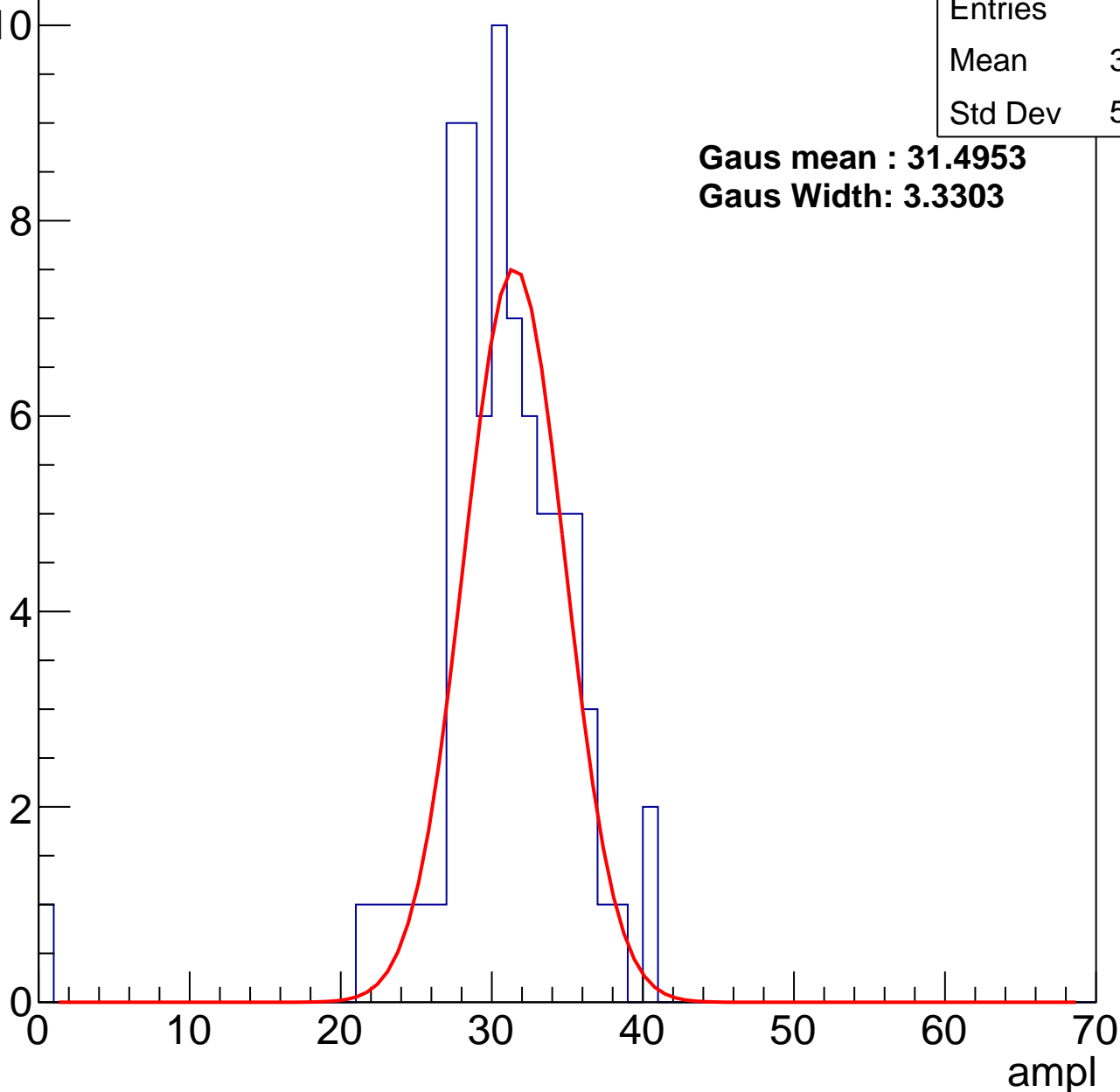
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	30.16
Std Dev	5.122

Gaus mean : 31.4953

Gaus Width: 3.3303



B1L103S, U14-ch0, adc1

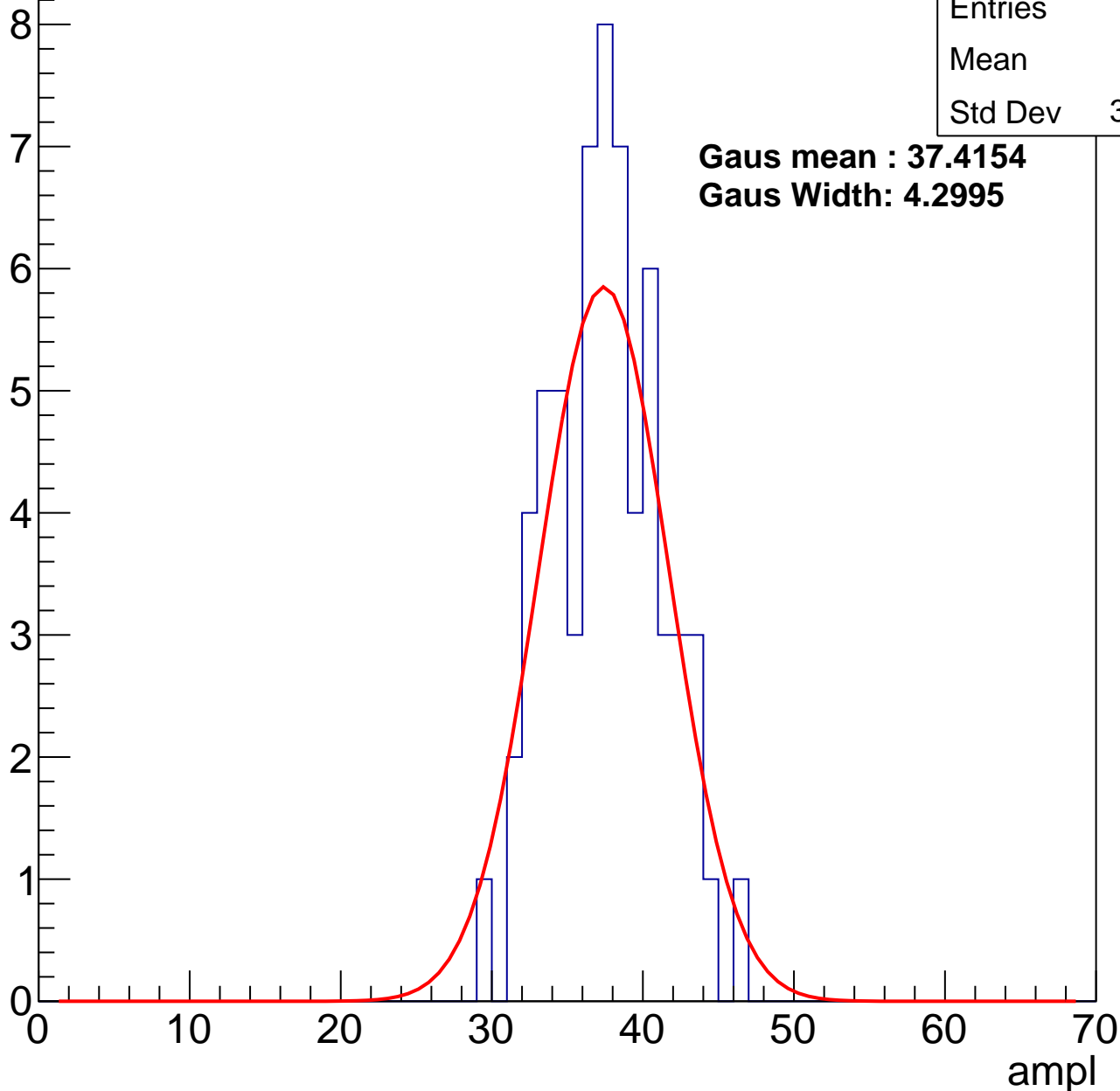
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.1
Std Dev	3.602

Gaus mean : 37.4154

Gaus Width: 4.2995



B1L103S, U14-ch0, adc2

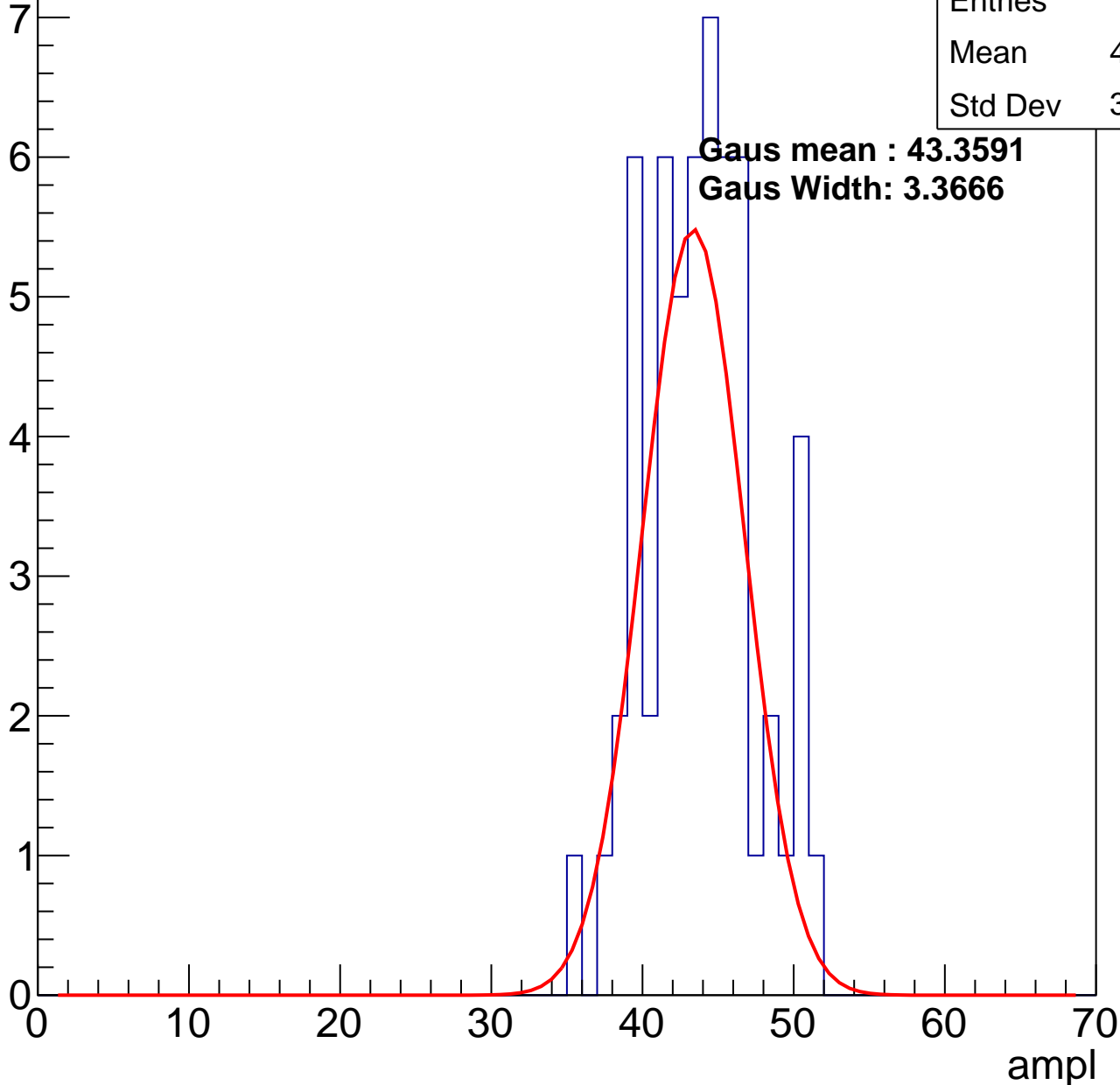
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	43.39
Std Dev	3.582

Gaus mean : 43.3591

Gaus Width: 3.3666

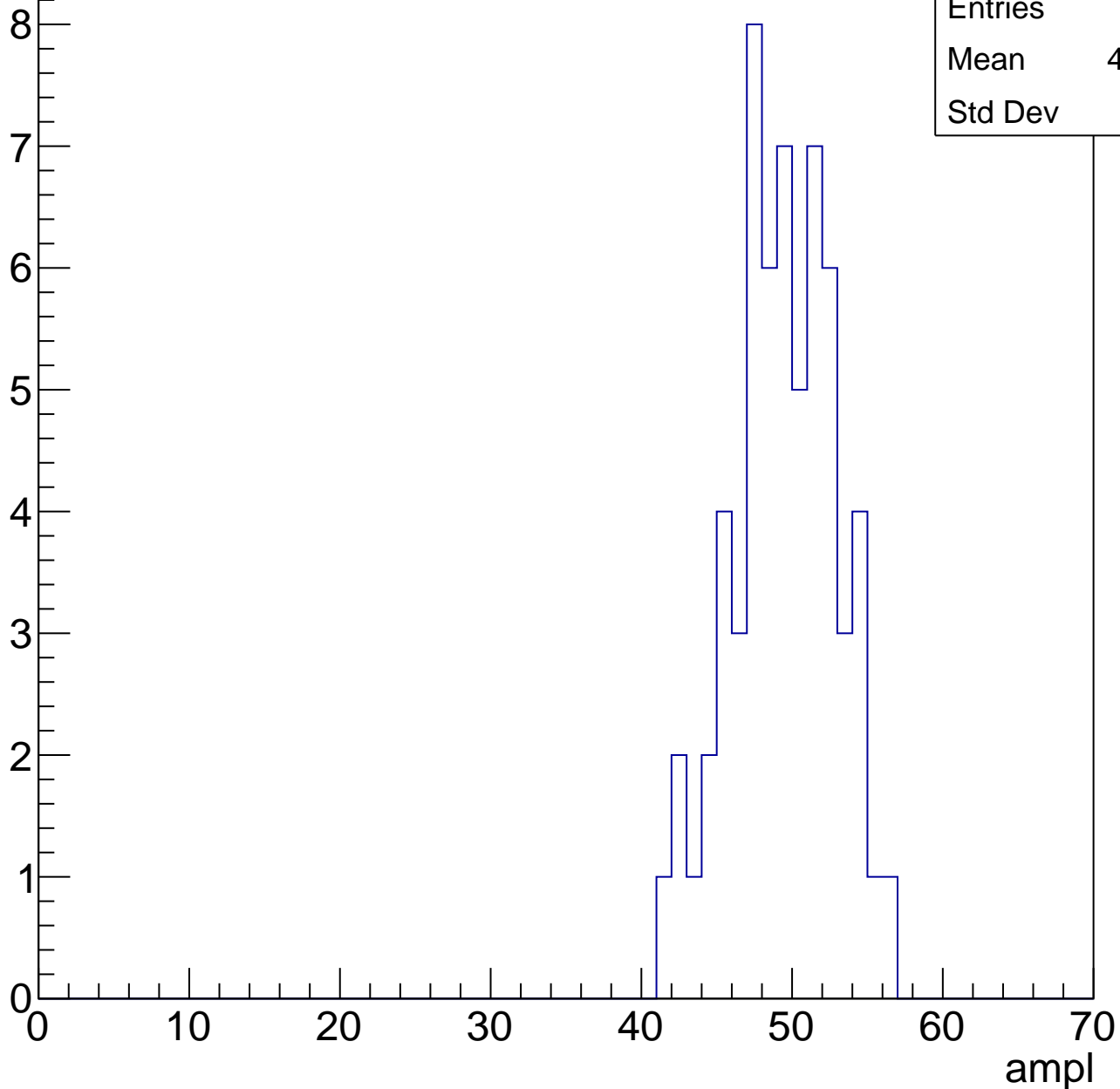


B1L103S, U14-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	48.95
Std Dev	3.38

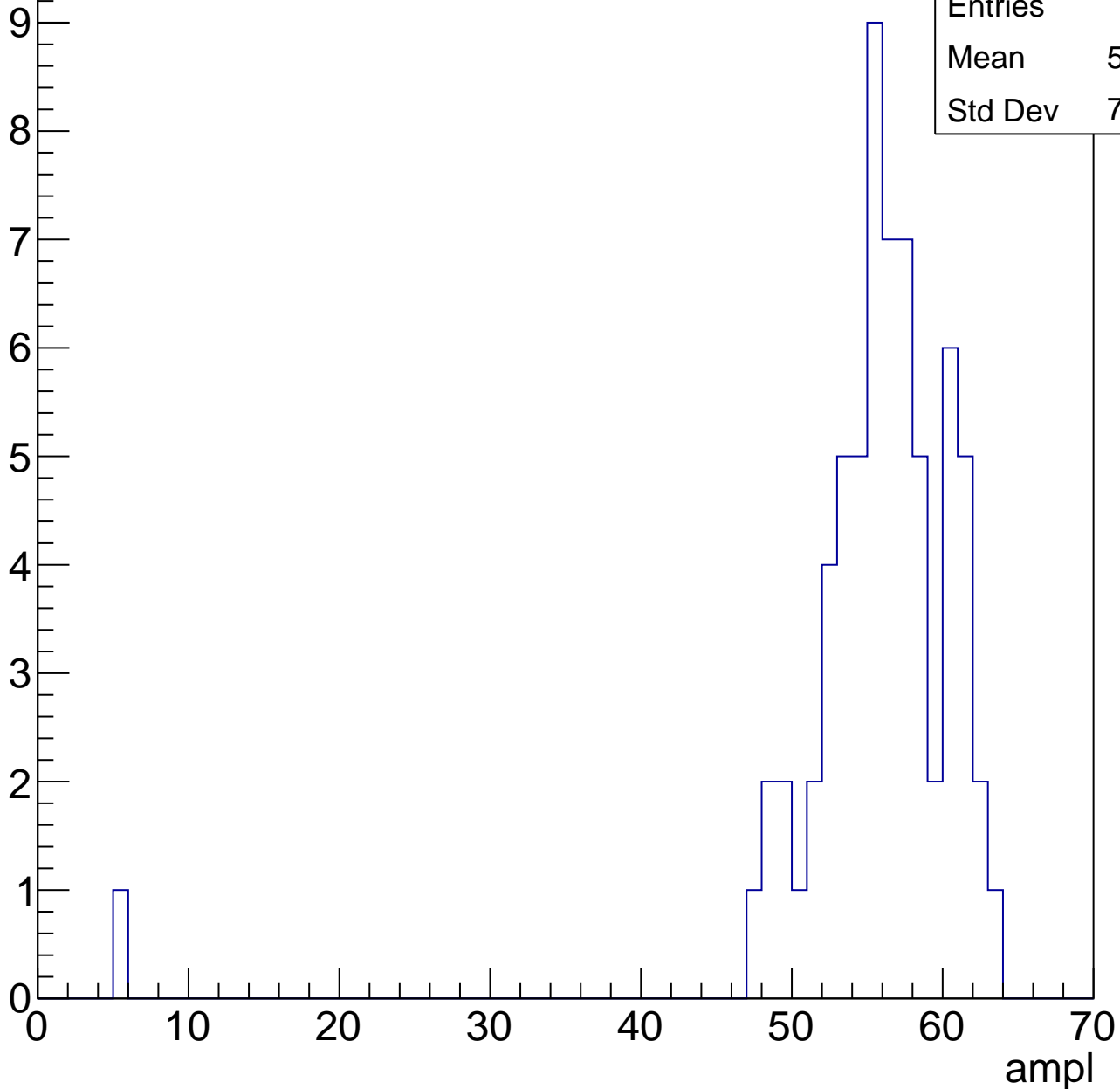


B1L103S, U14-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	55.03
Std Dev	7.182

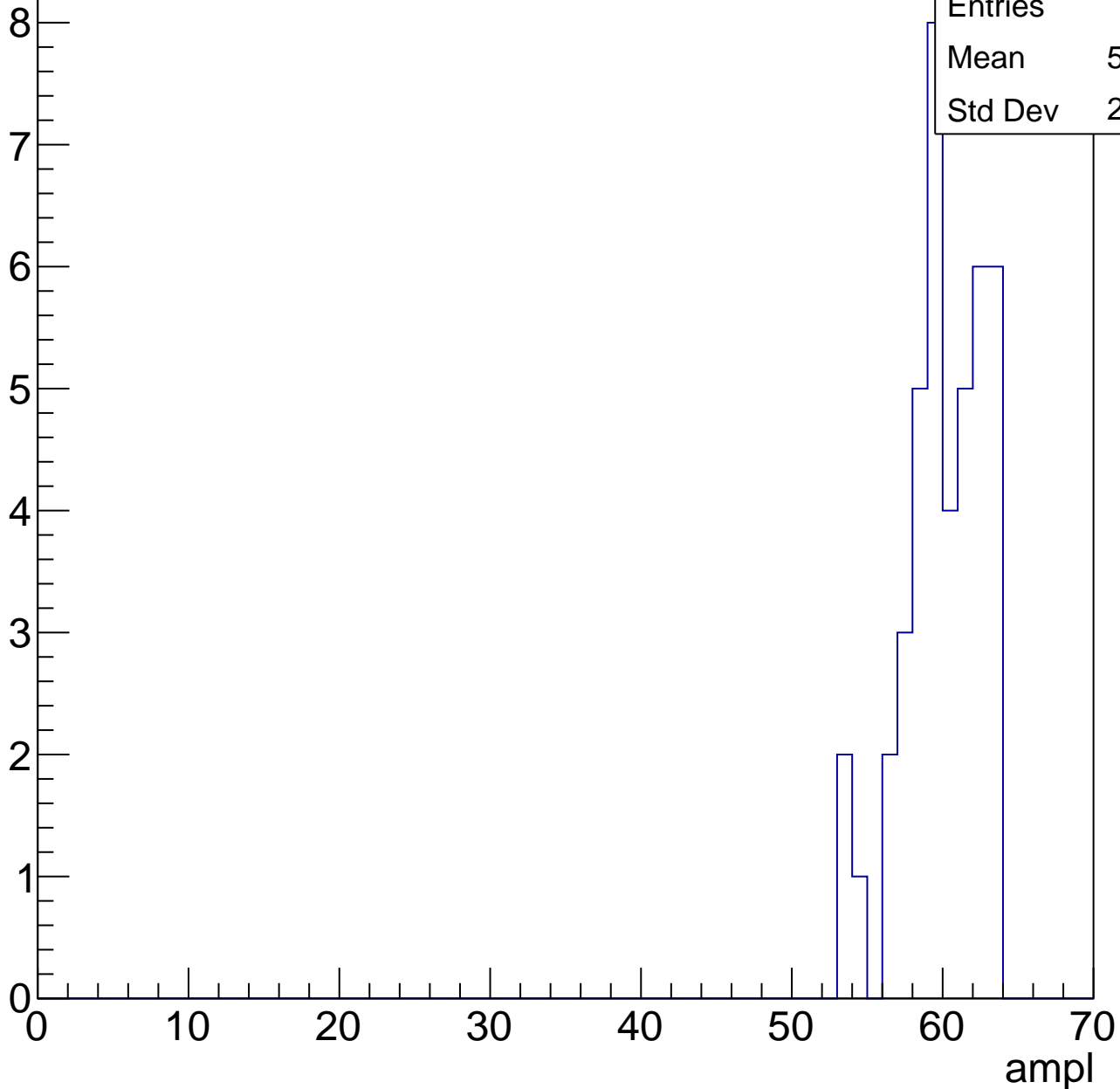


B1L103S, U14-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

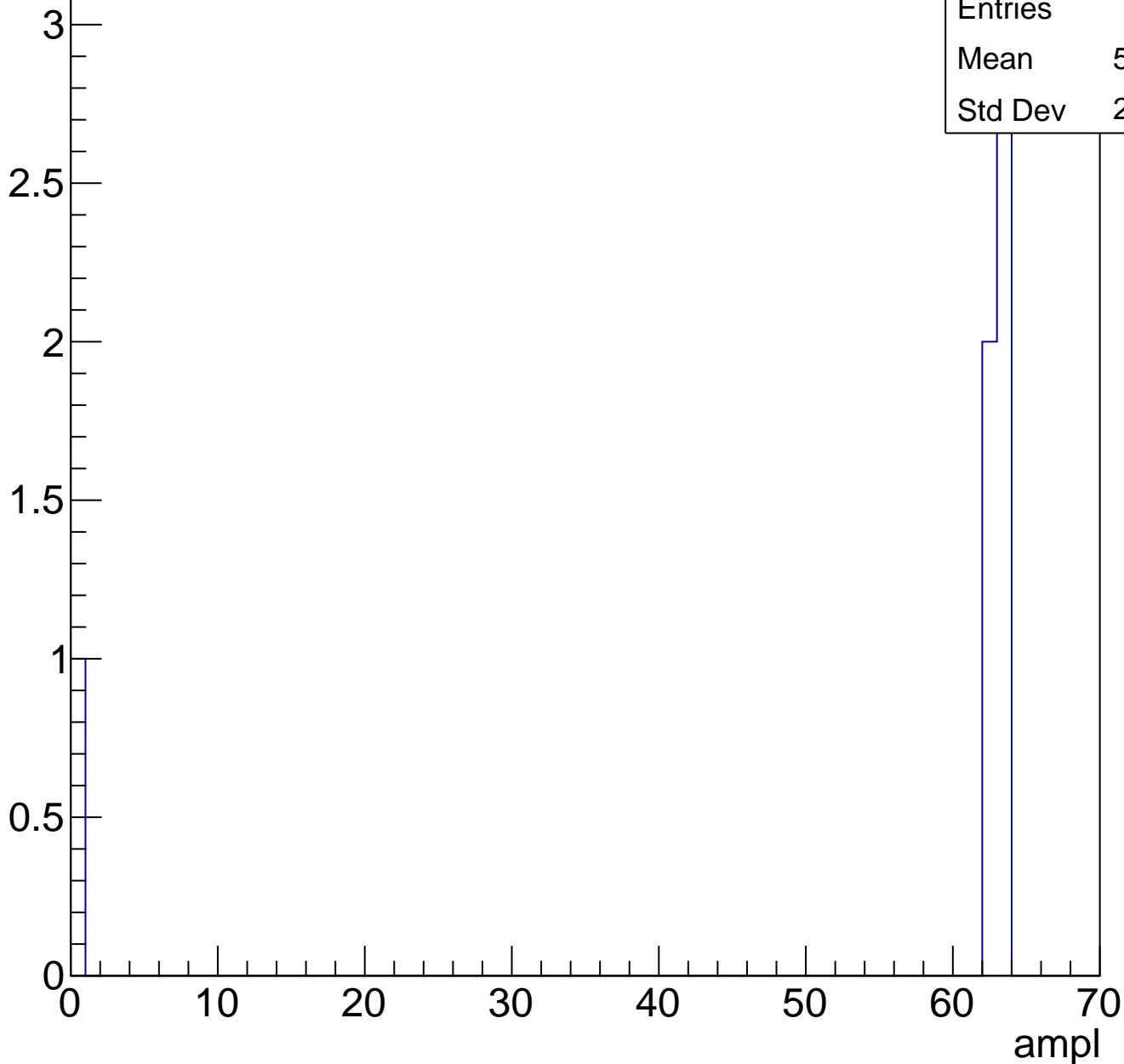
Entries	42
Mean	59.52
Std Dev	2.648



B1L103S, U14-ch0, adc6

calib_packv5_042523_0143.root, FC#7, port C2

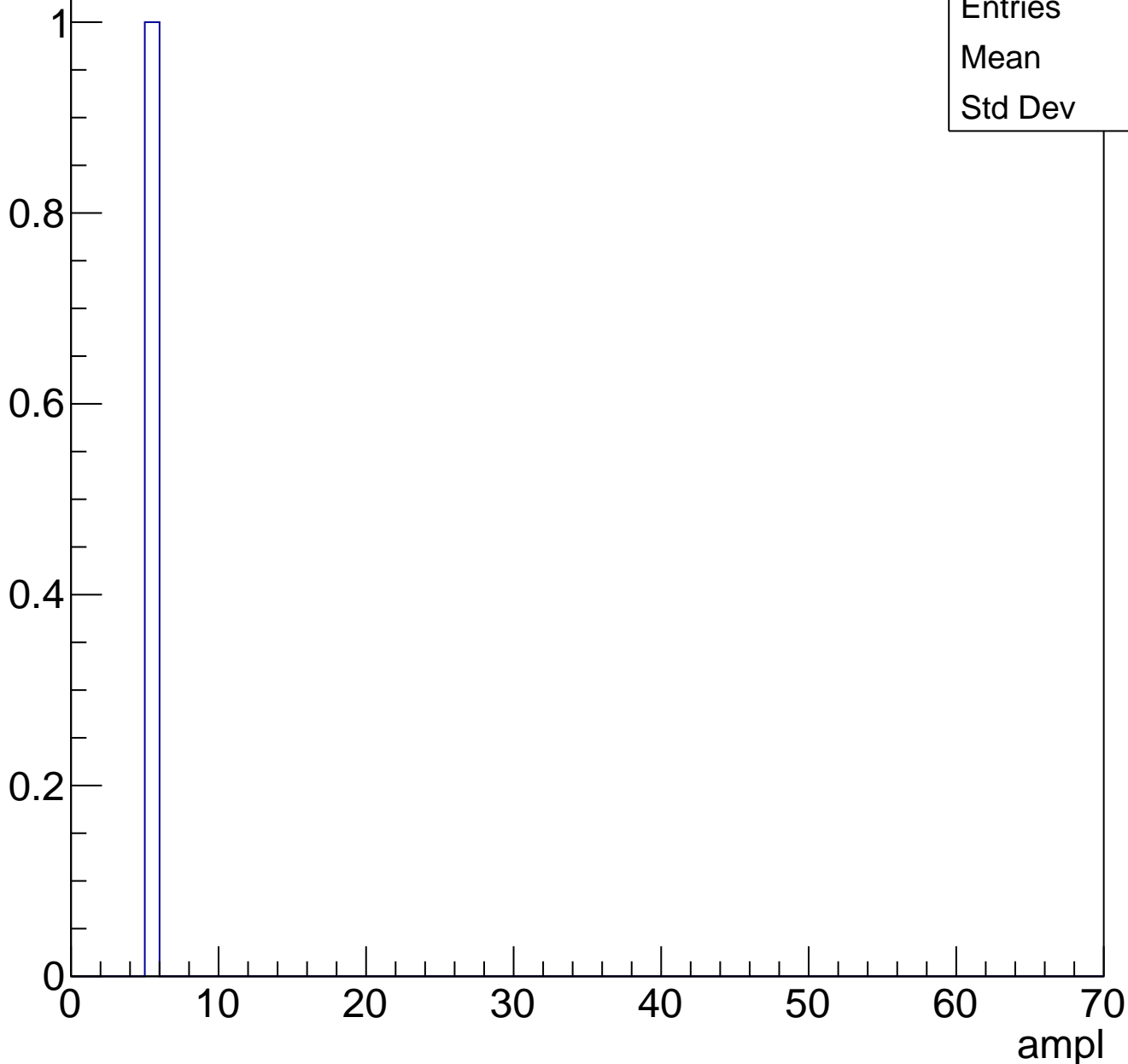
Entry



B1L103S, U14-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch1, adc0

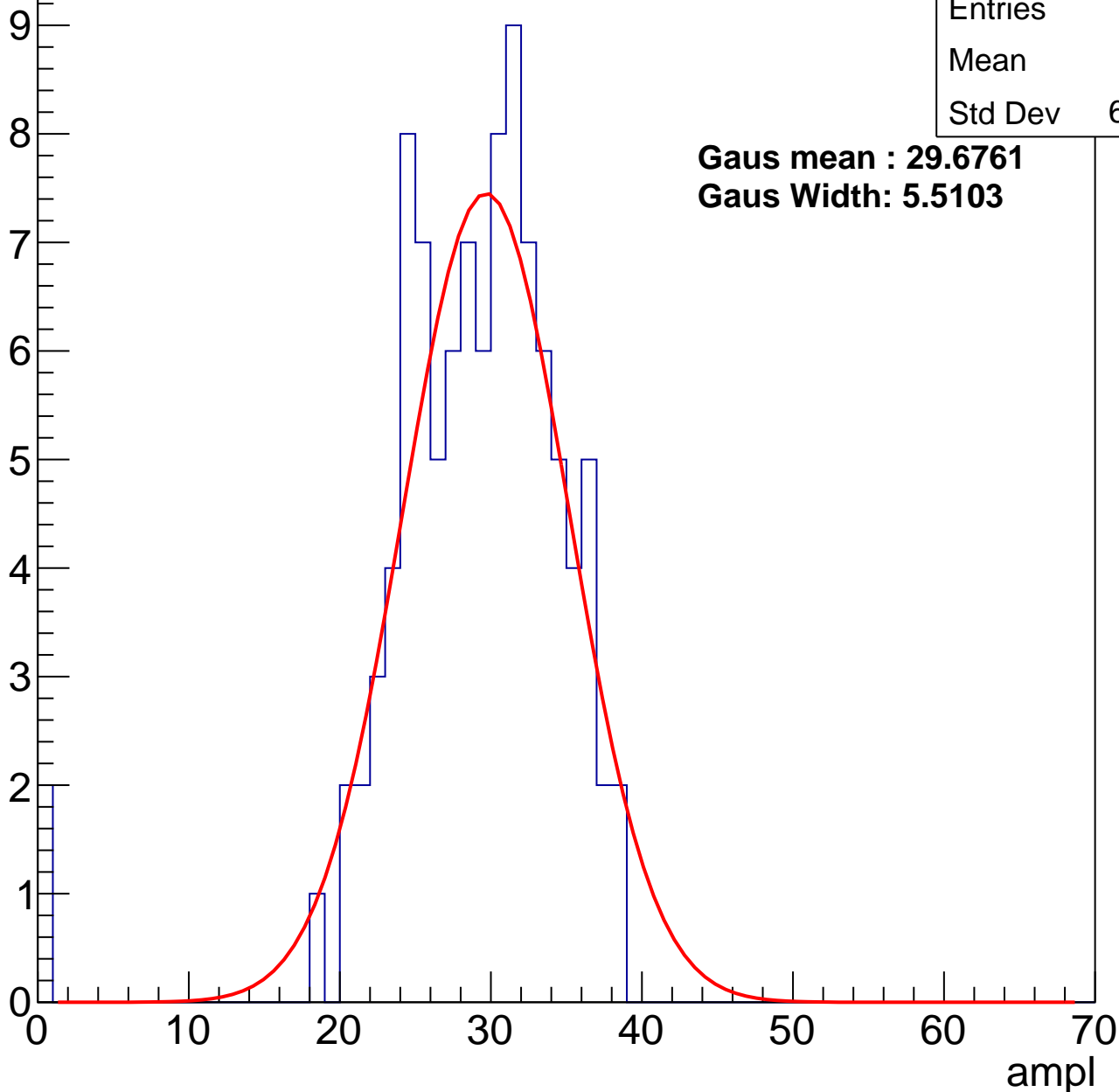
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	101
Mean	28.4
Std Dev	6.087

Gaus mean : 29.6761

Gaus Width: 5.5103



B1L103S, U14-ch1, adc1

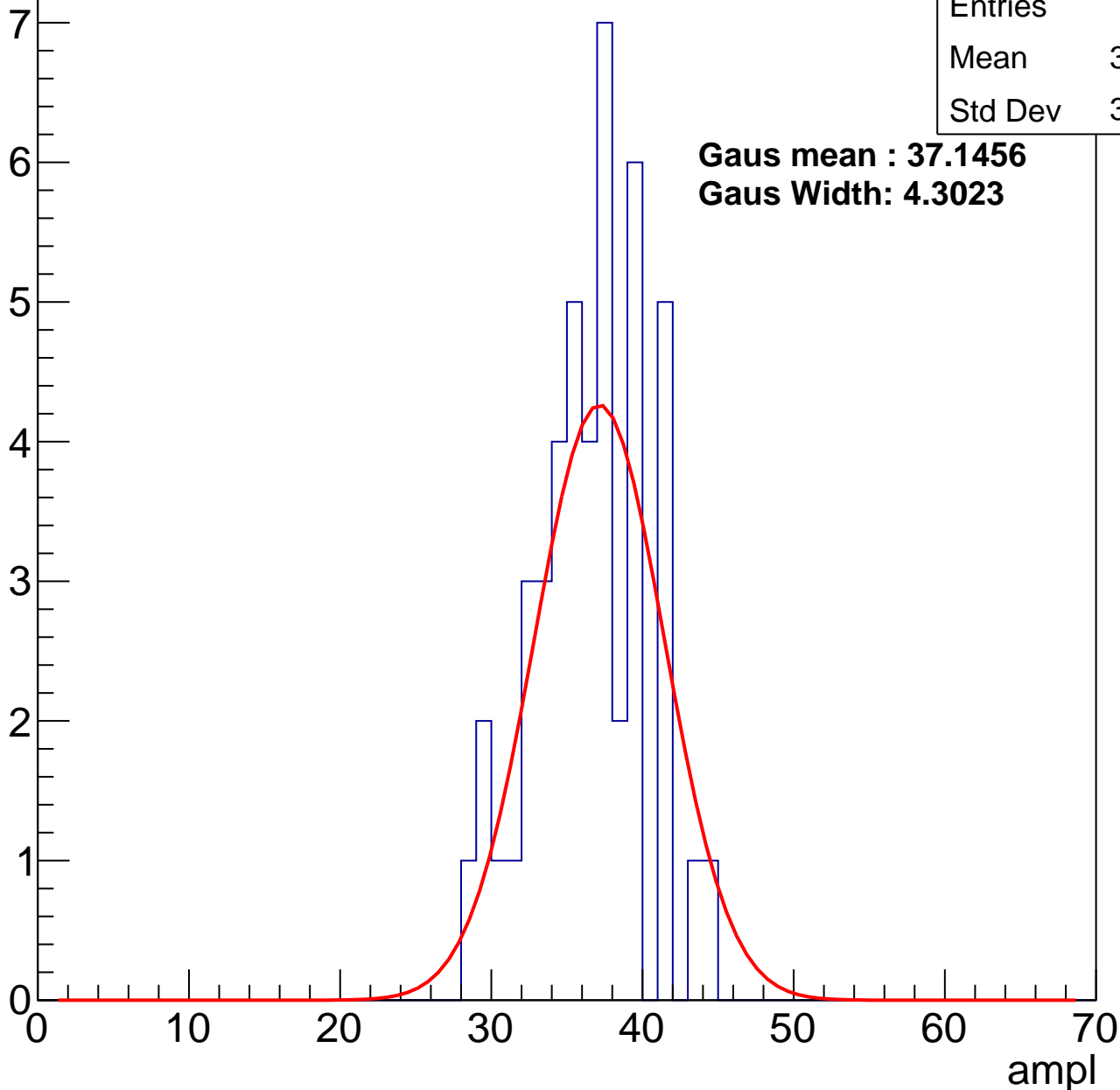
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	36.04
Std Dev	3.689

Gaus mean : 37.1456

Gaus Width: 4.3023



B1L103S, U14-ch1, adc2

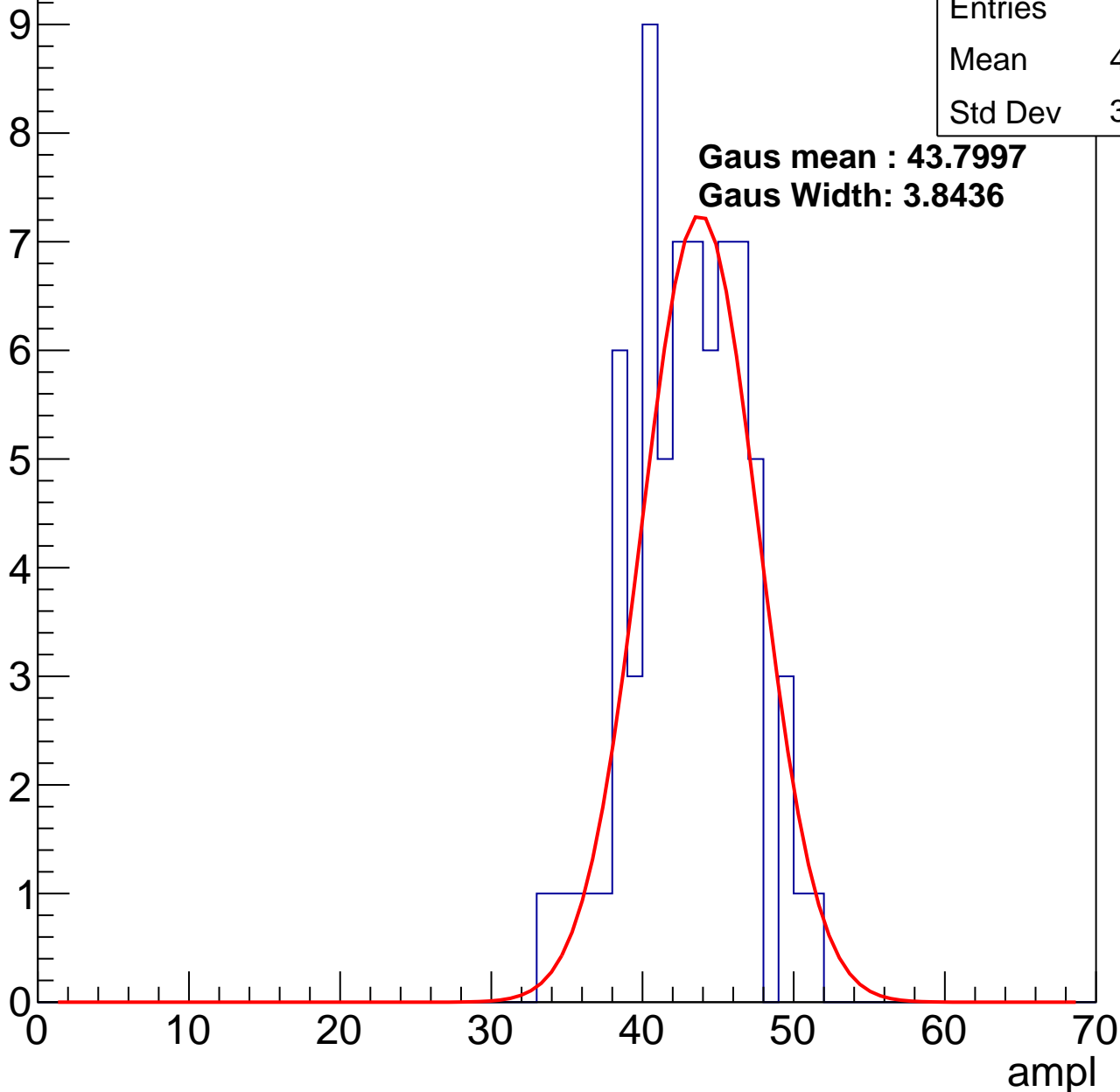
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.56
Std Dev	3.763

Gaus mean : 43.7997

Gaus Width: 3.8436

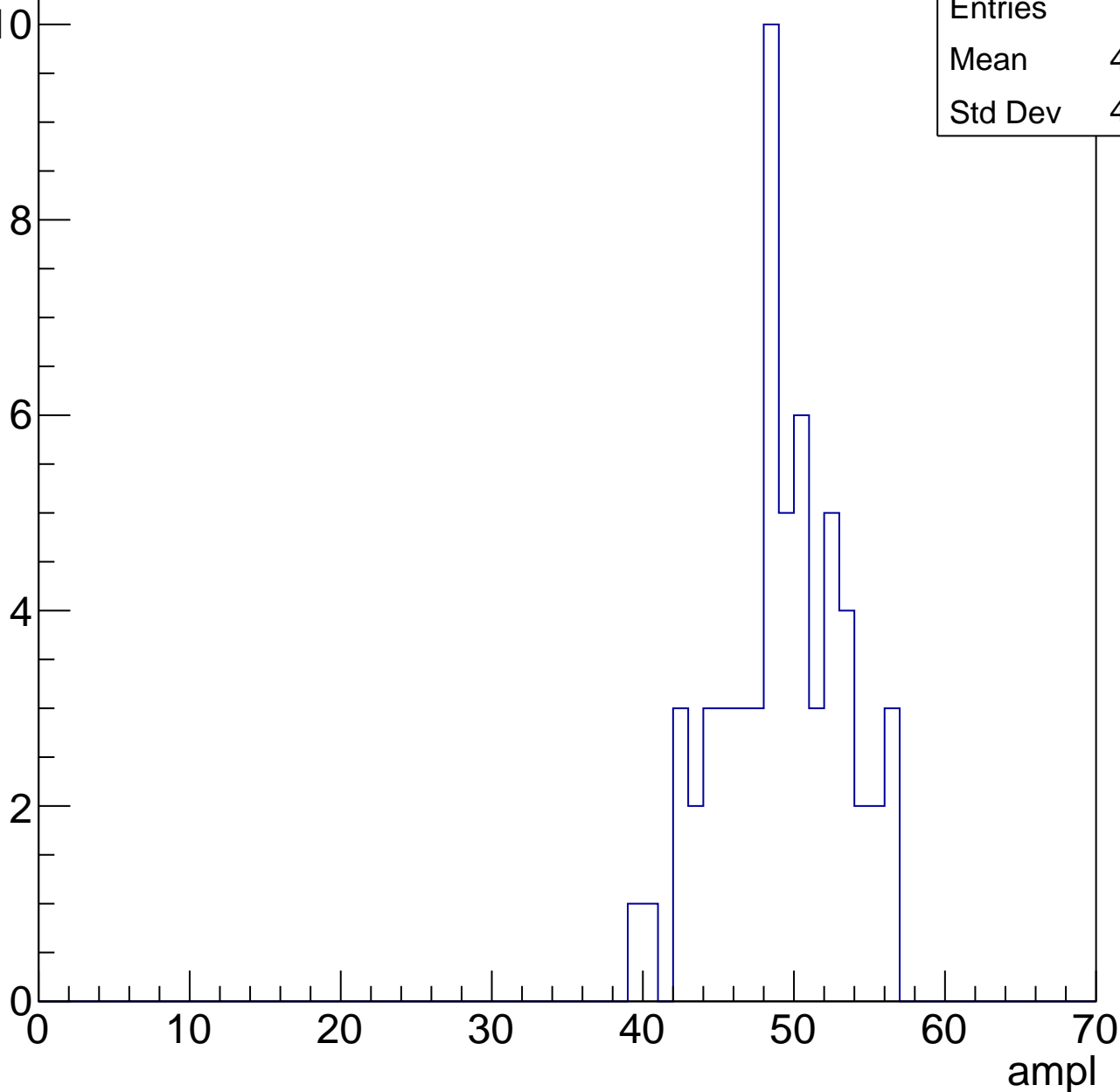


B1L103S, U14-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	48.69
Std Dev	4.039

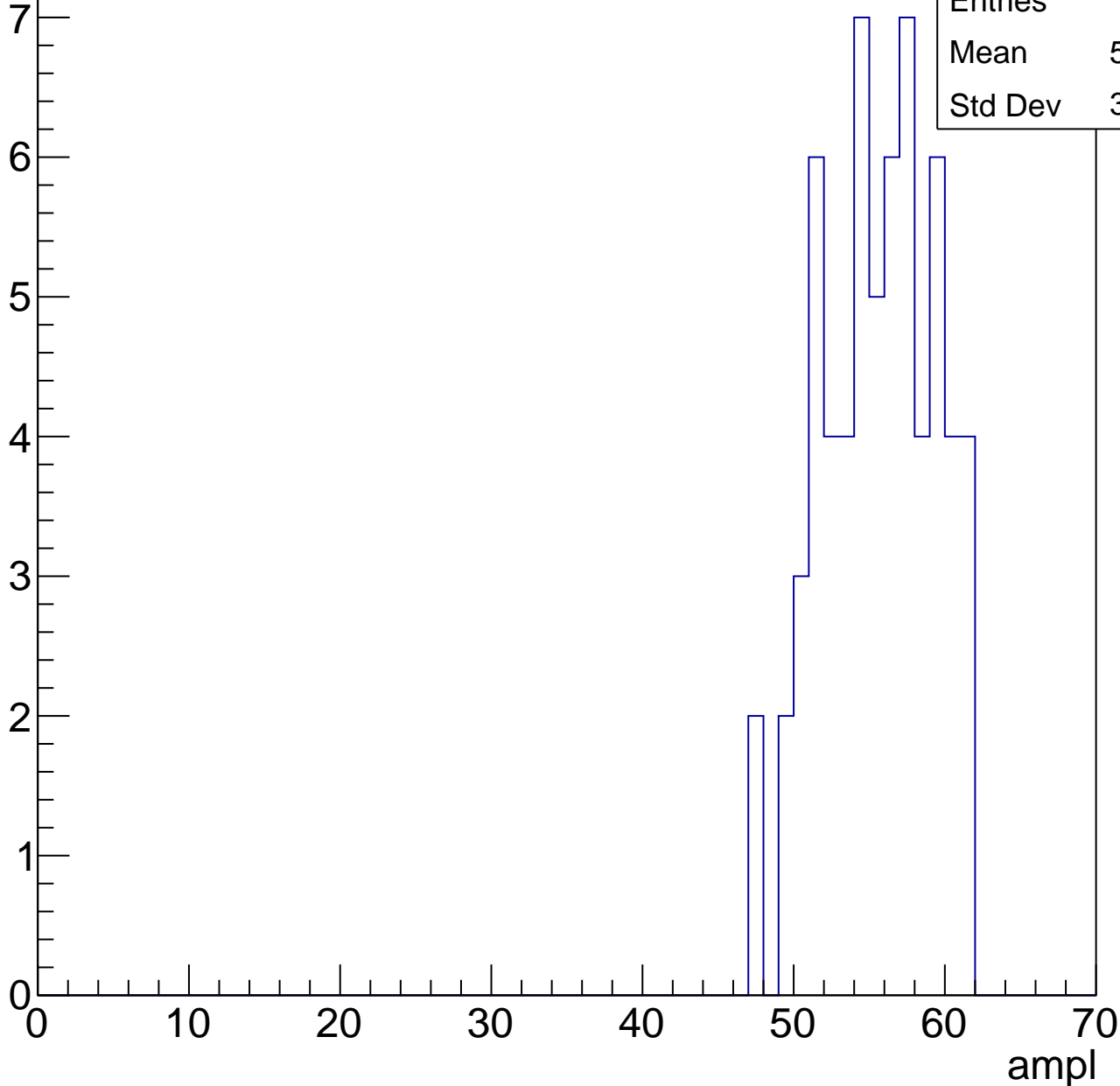


B1L103S, U14-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.09
Std Dev	3.622

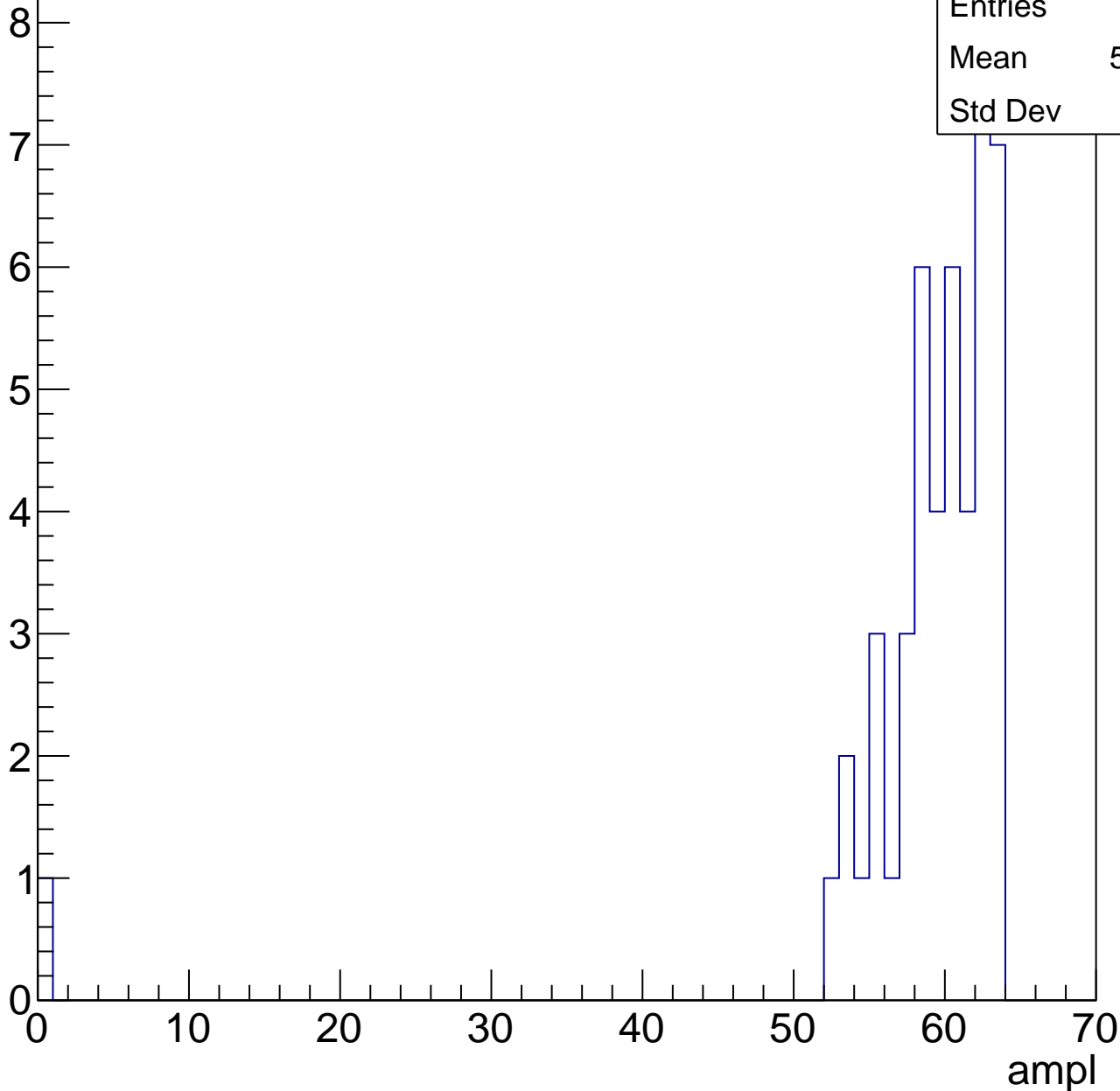


B1L103S, U14-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

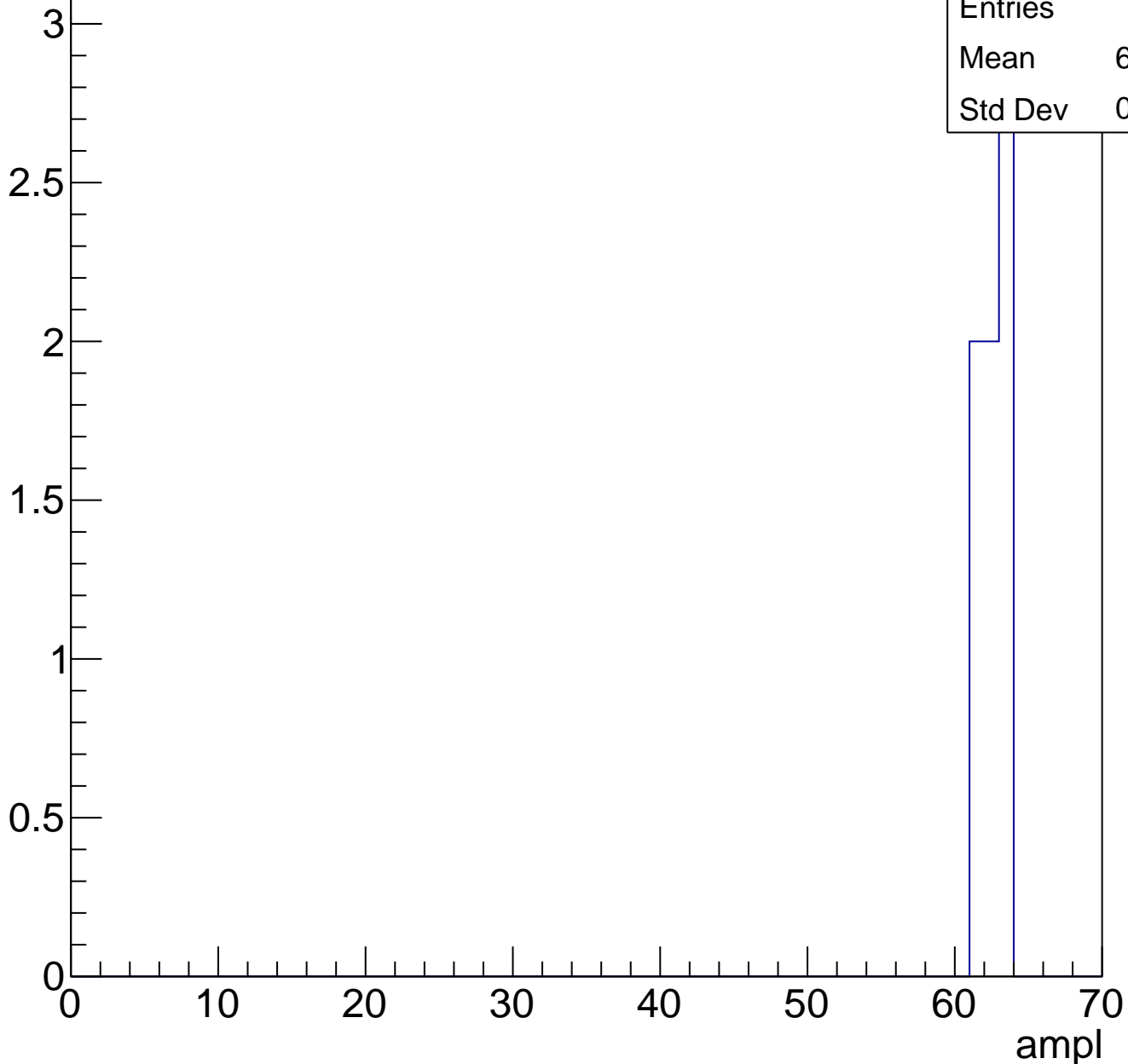
Entries	47
Mean	58.06
Std Dev	9.07



B1L103S, U14-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	7
Mean	62.14
Std Dev	0.833

B1L103S, U14-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch2, adc0

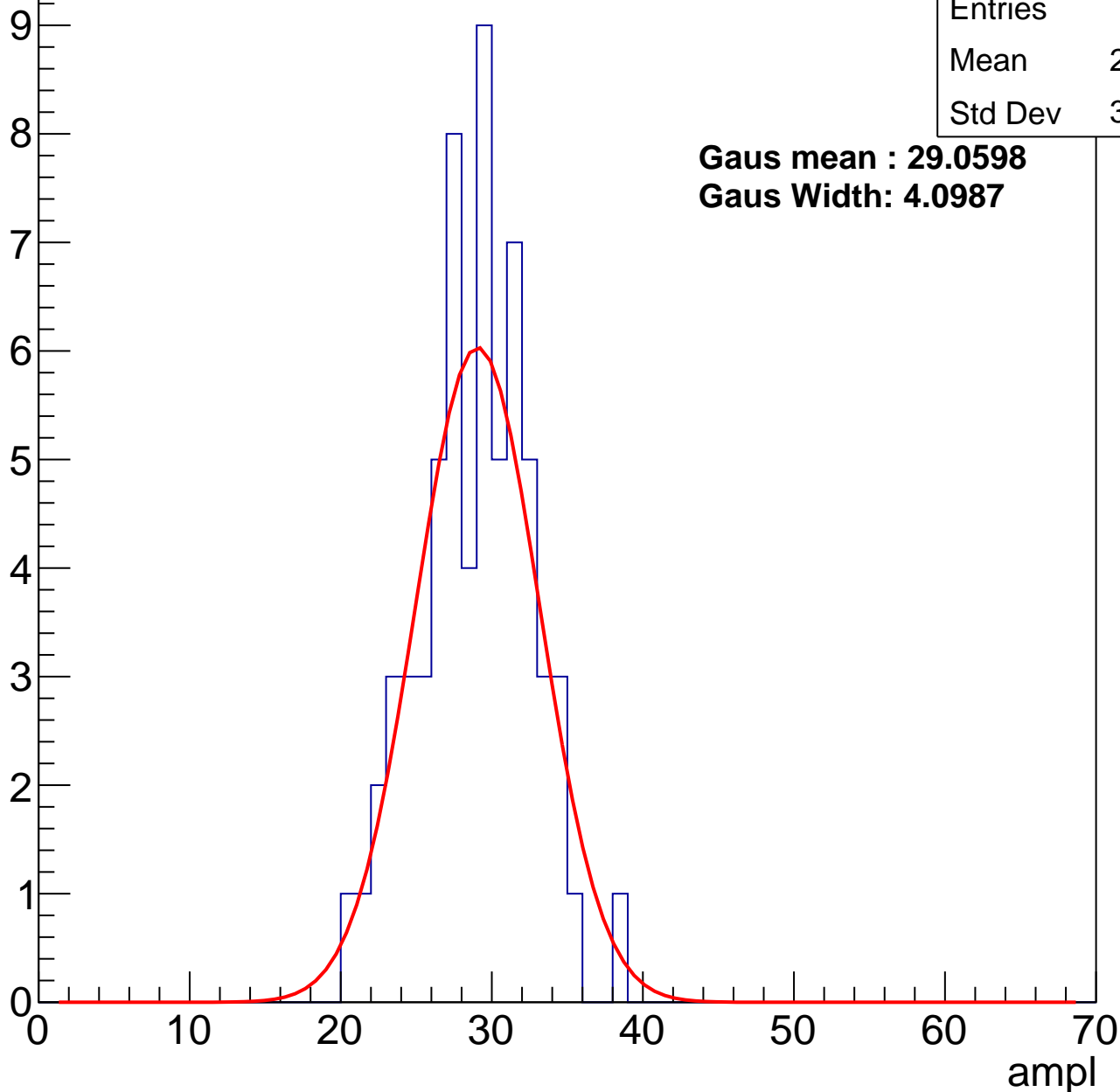
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.45
Std Dev	3.653

Gaus mean : 29.0598

Gaus Width: 4.0987



B1L103S, U14-ch2, adc1

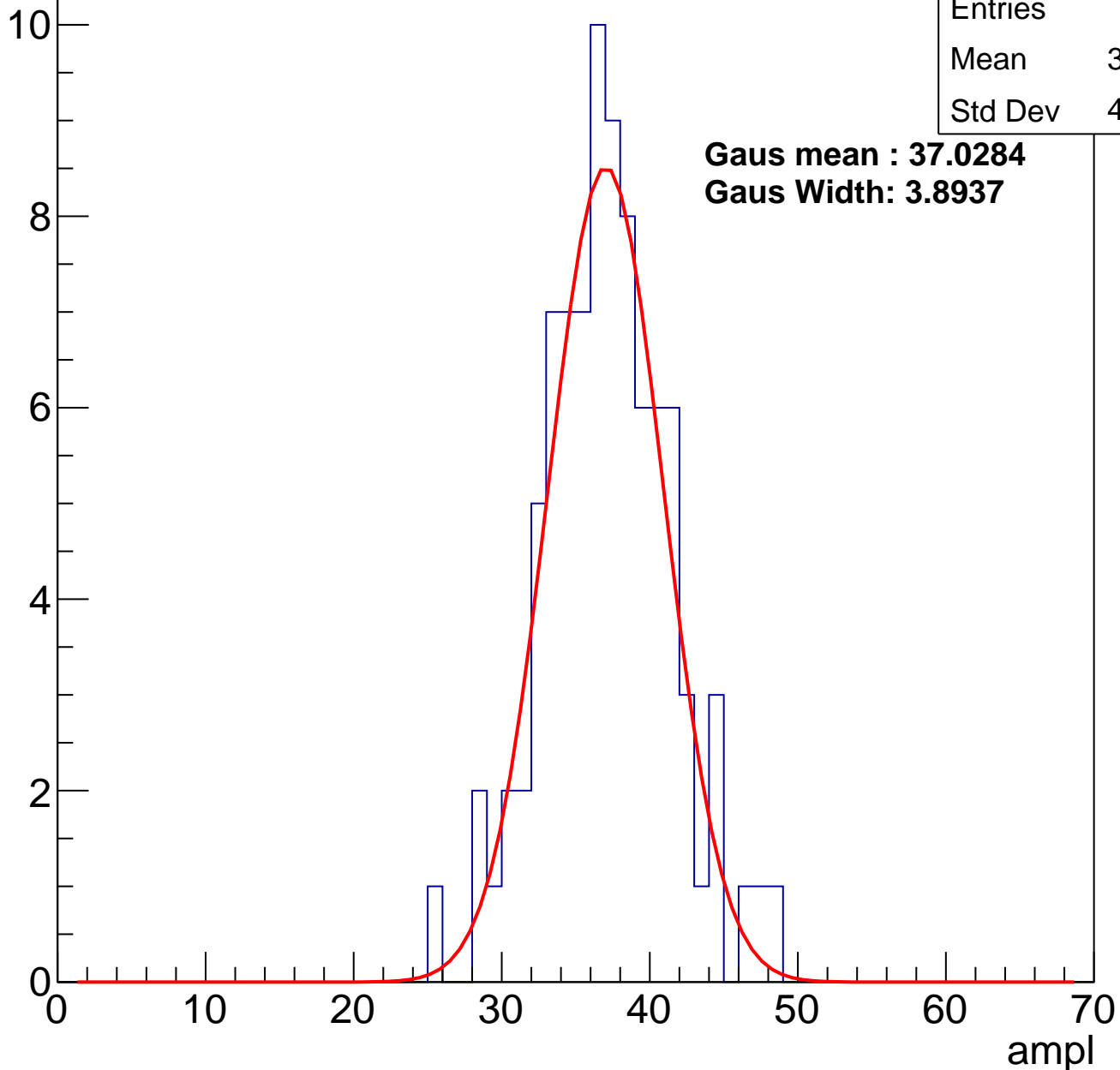
calib_packv5_042523_0143.root, FC#7, port C2

Entries	89
Mean	36.69
Std Dev	4.255

Gaus mean : 37.0284

Gaus Width: 3.8937

Entry



B1L103S, U14-ch2, adc2

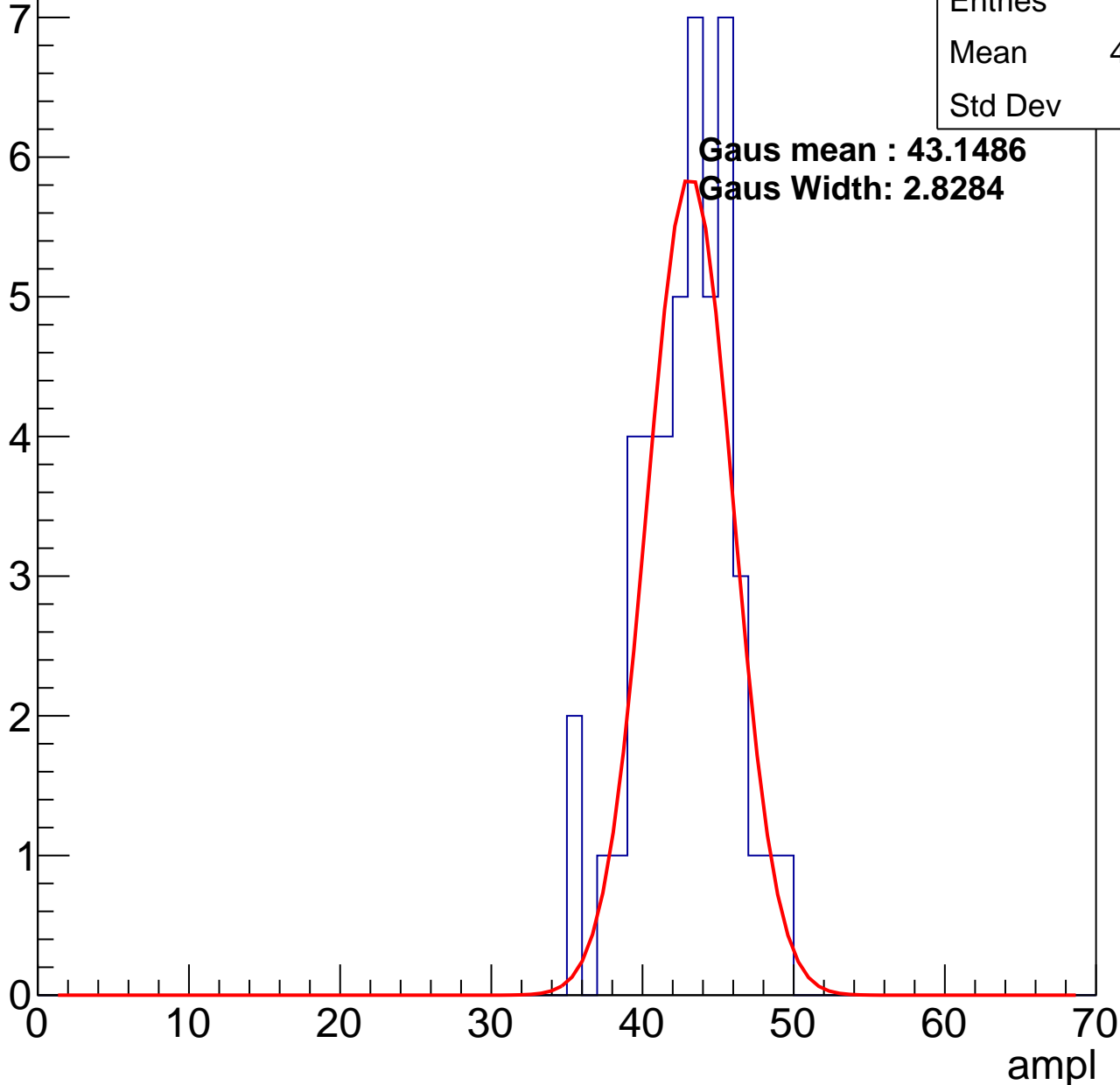
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	42.46
Std Dev	3.07

Gaus mean : 43.1486

Gaus Width: 2.8284

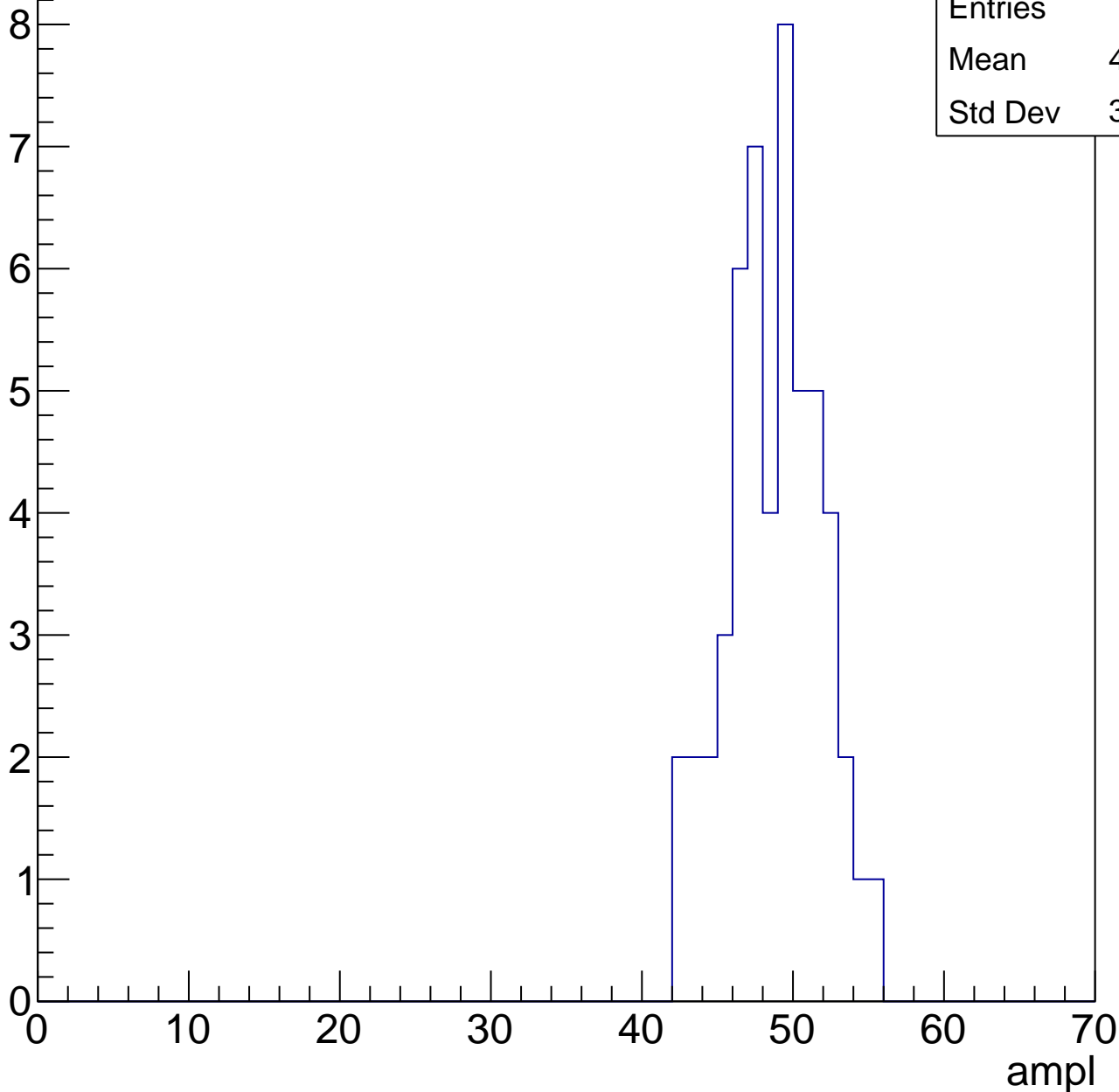


B1L103S, U14-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	48.27
Std Dev	3.039

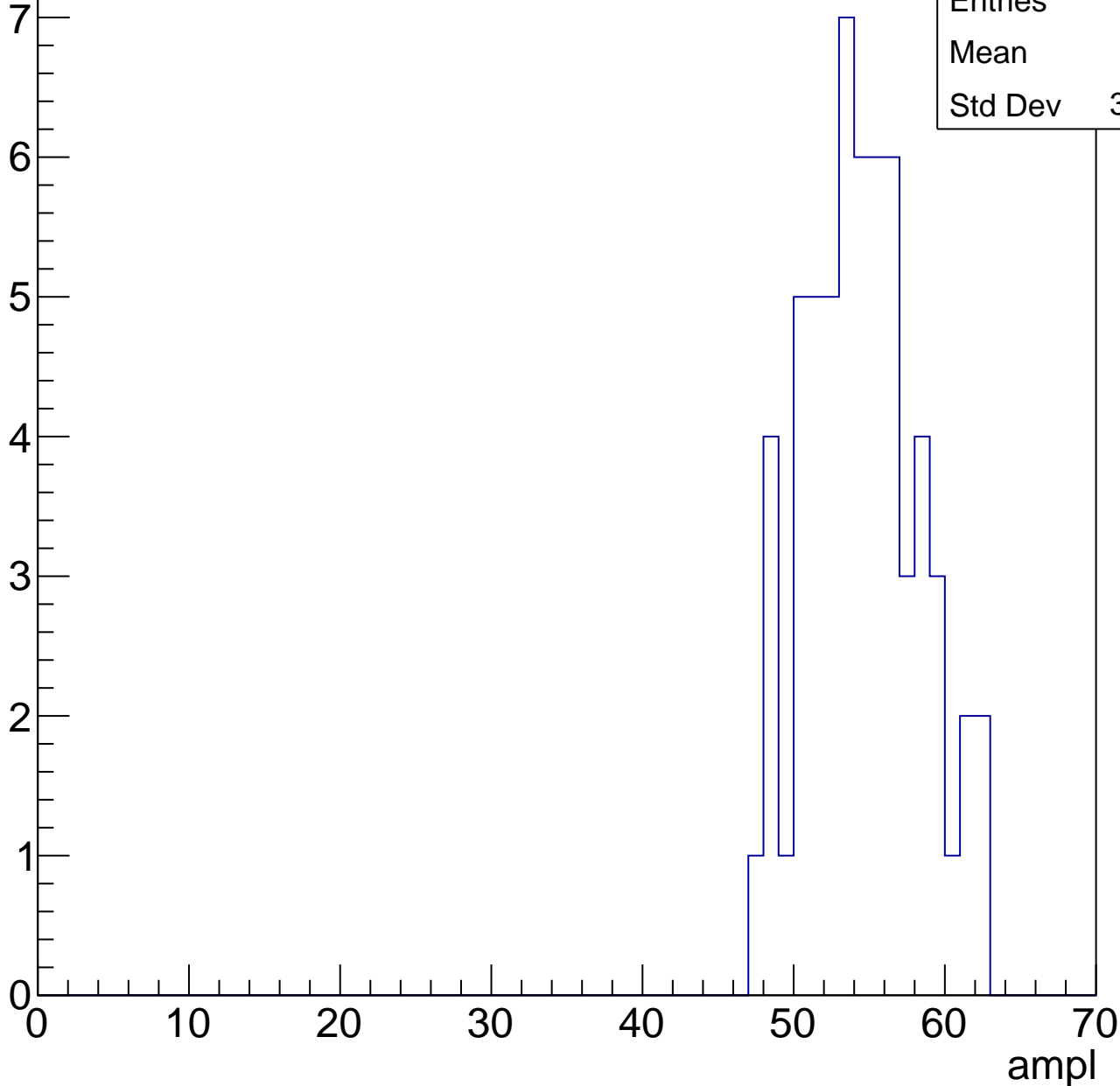


B1L103S, U14-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	54.1
Std Dev	3.683

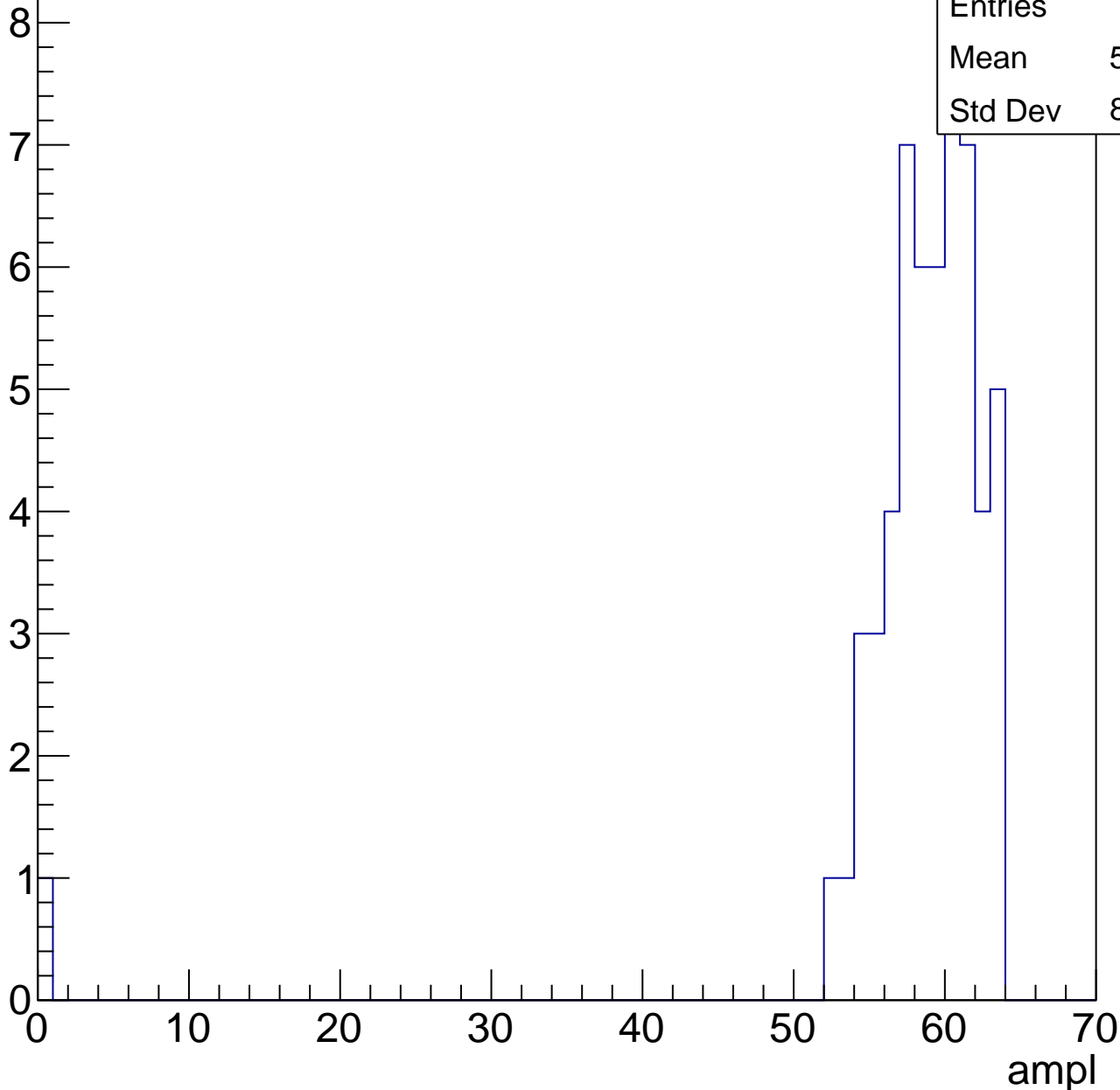


B1L103S, U14-ch2, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

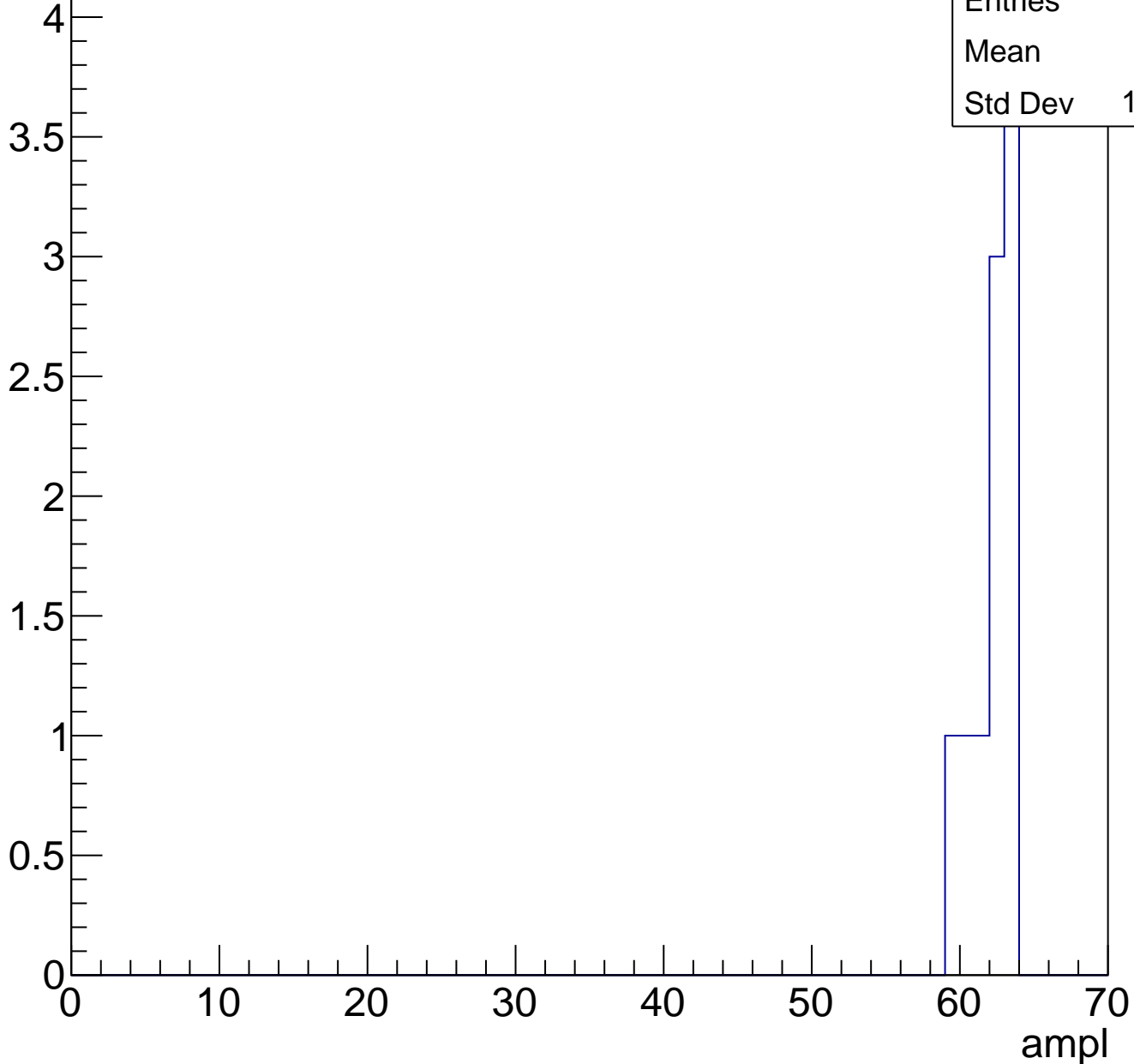
Entries	56
Mean	57.62
Std Dev	8.243



B1L103S, U14-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

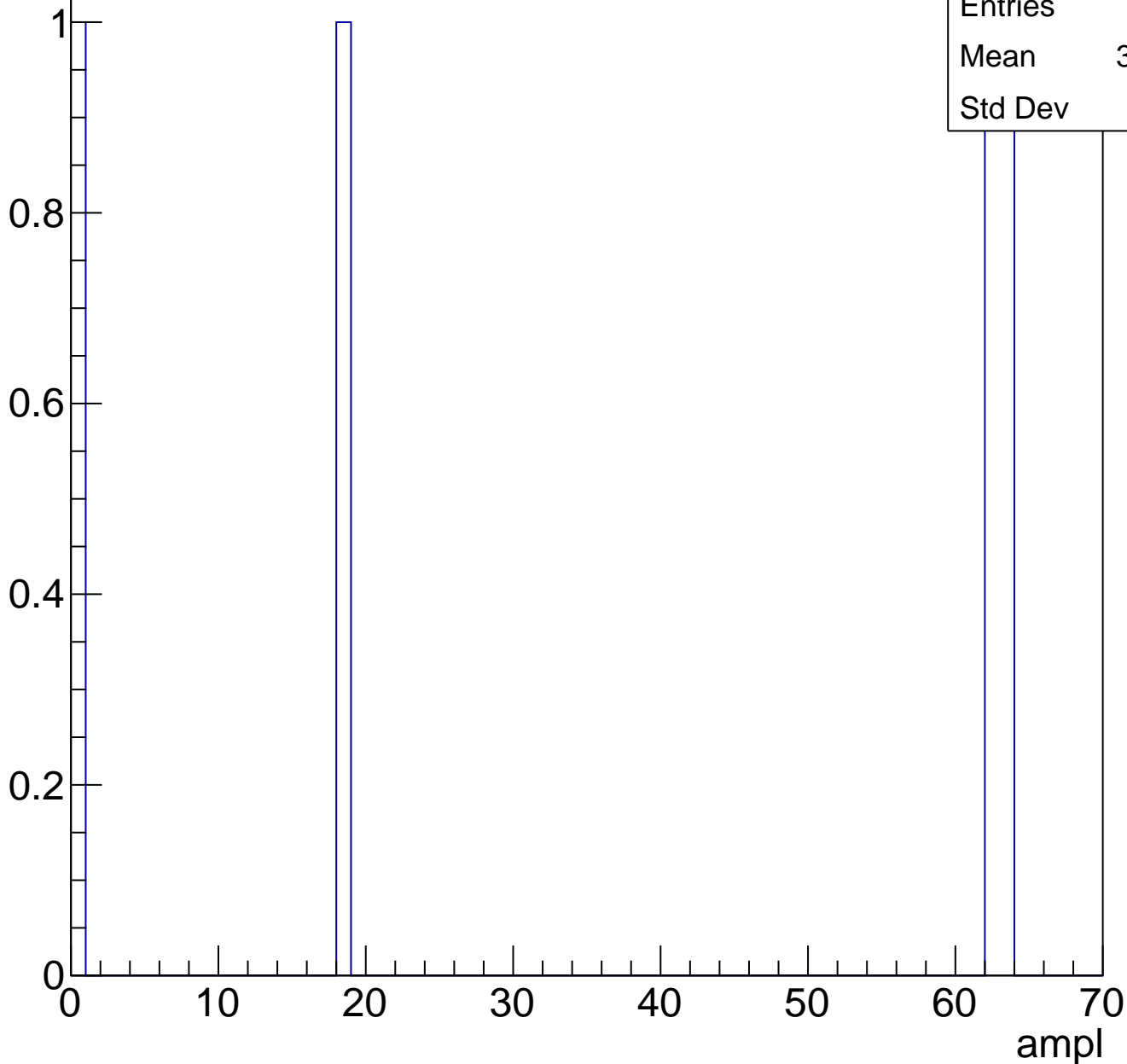
Entry



B1L103S, U14-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch3, adc0

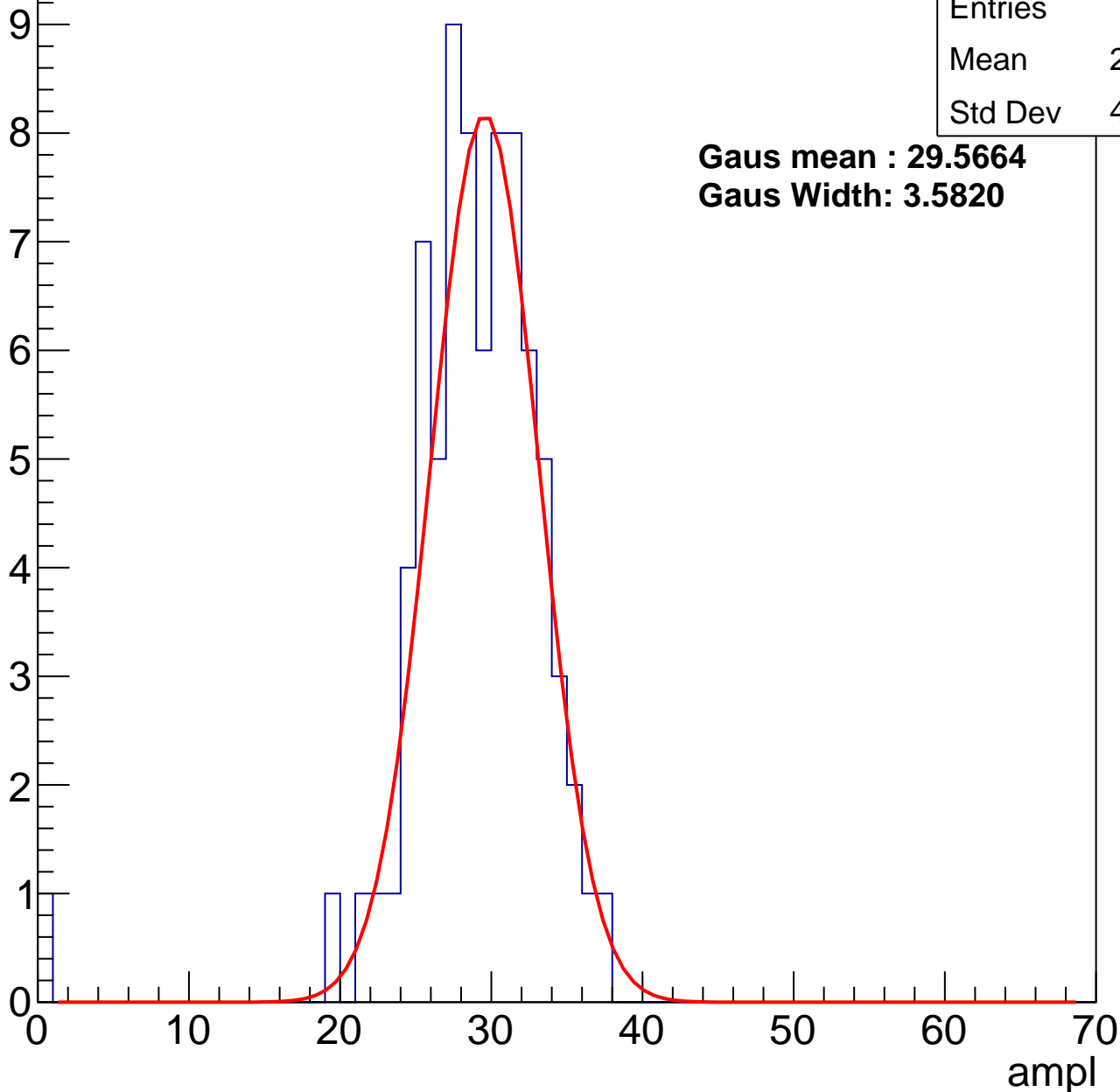
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	28.42
Std Dev	4.803

Gaus mean : 29.5664

Gaus Width: 3.5820



B1L103S, U14-ch3, adc1

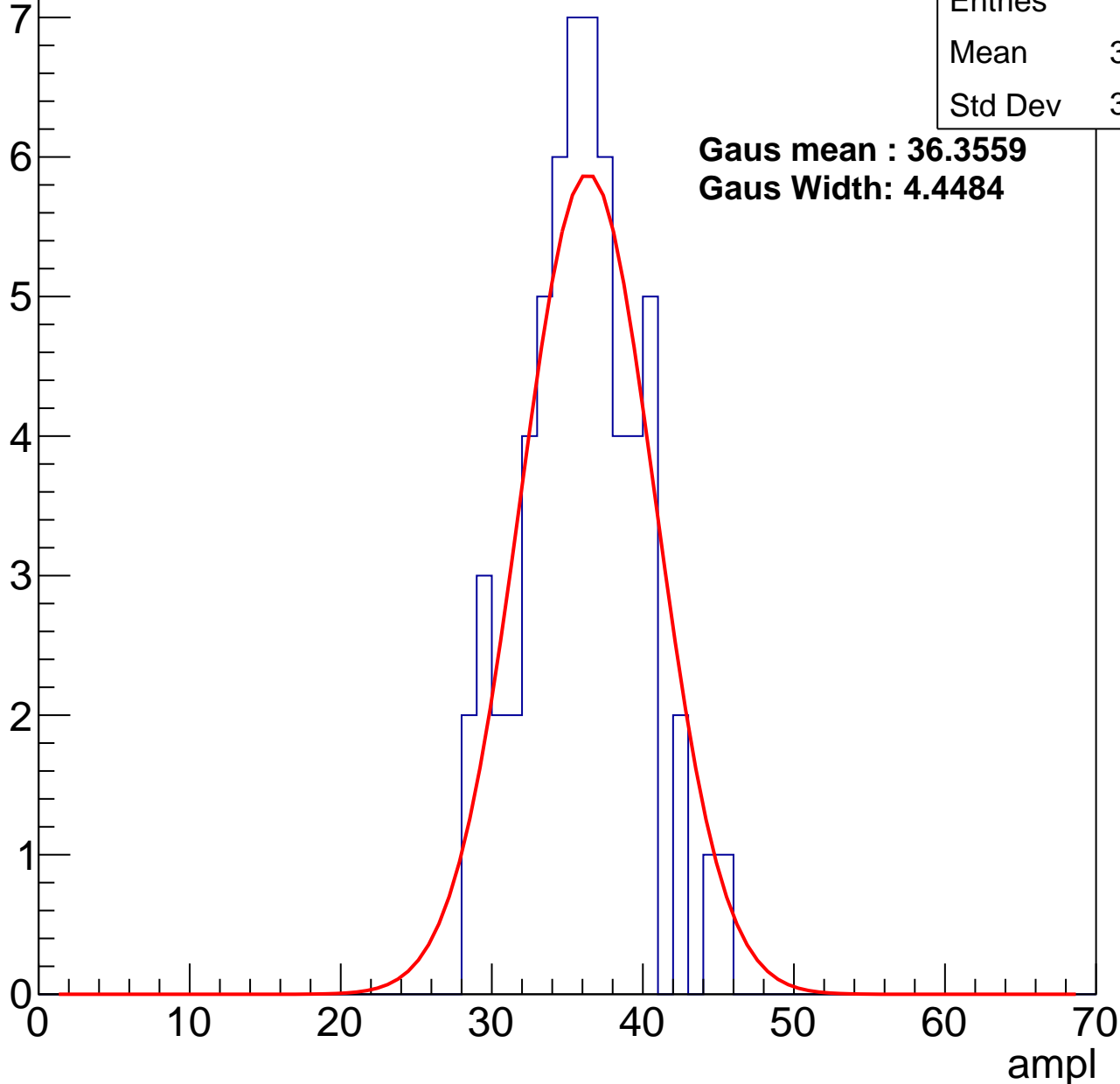
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.44
Std Dev	3.783

Gaus mean : 36.3559

Gaus Width: 4.4484



B1L103S, U14-ch3, adc2

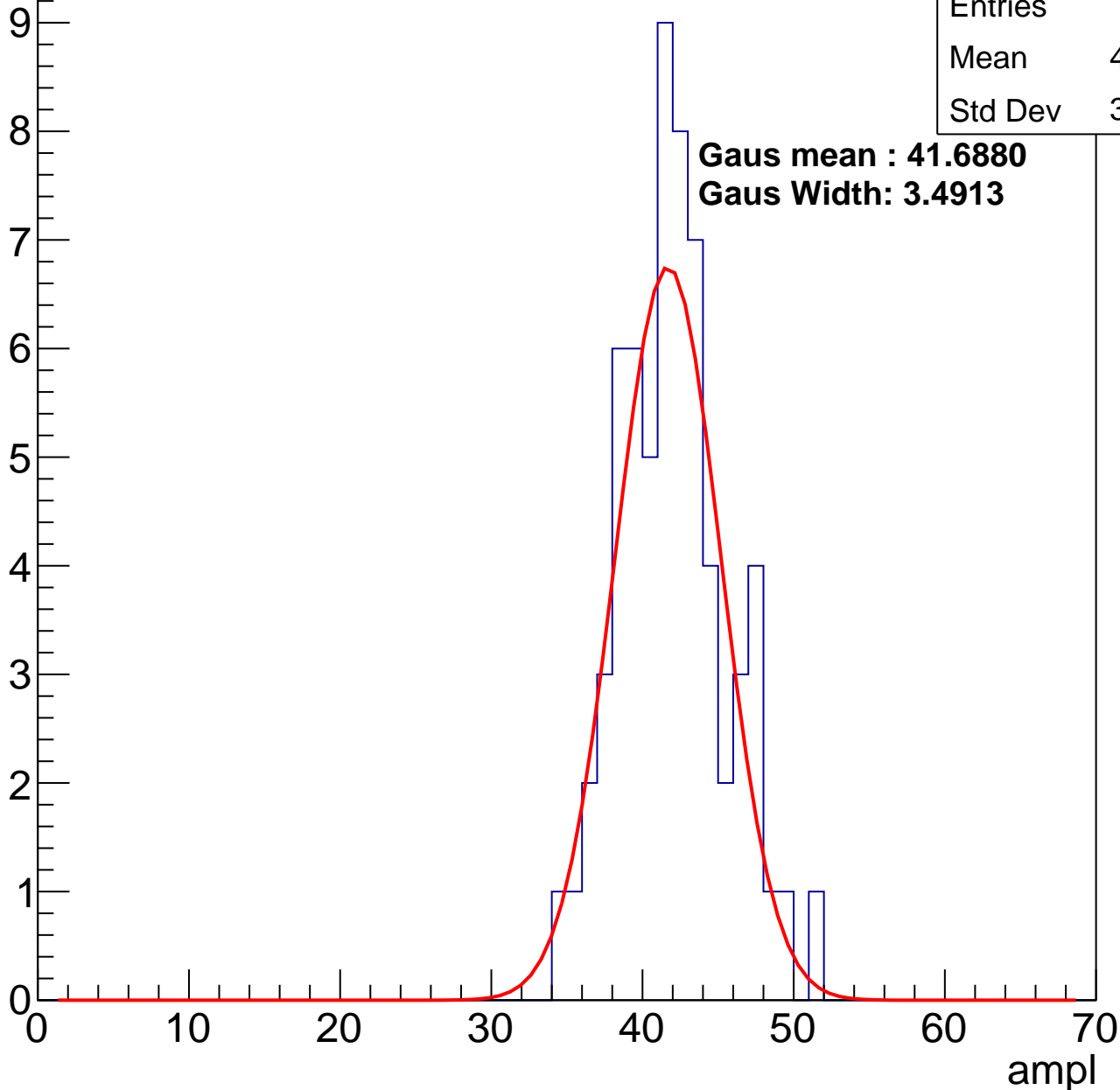
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	41.56
Std Dev	3.495

Gaus mean : 41.6880

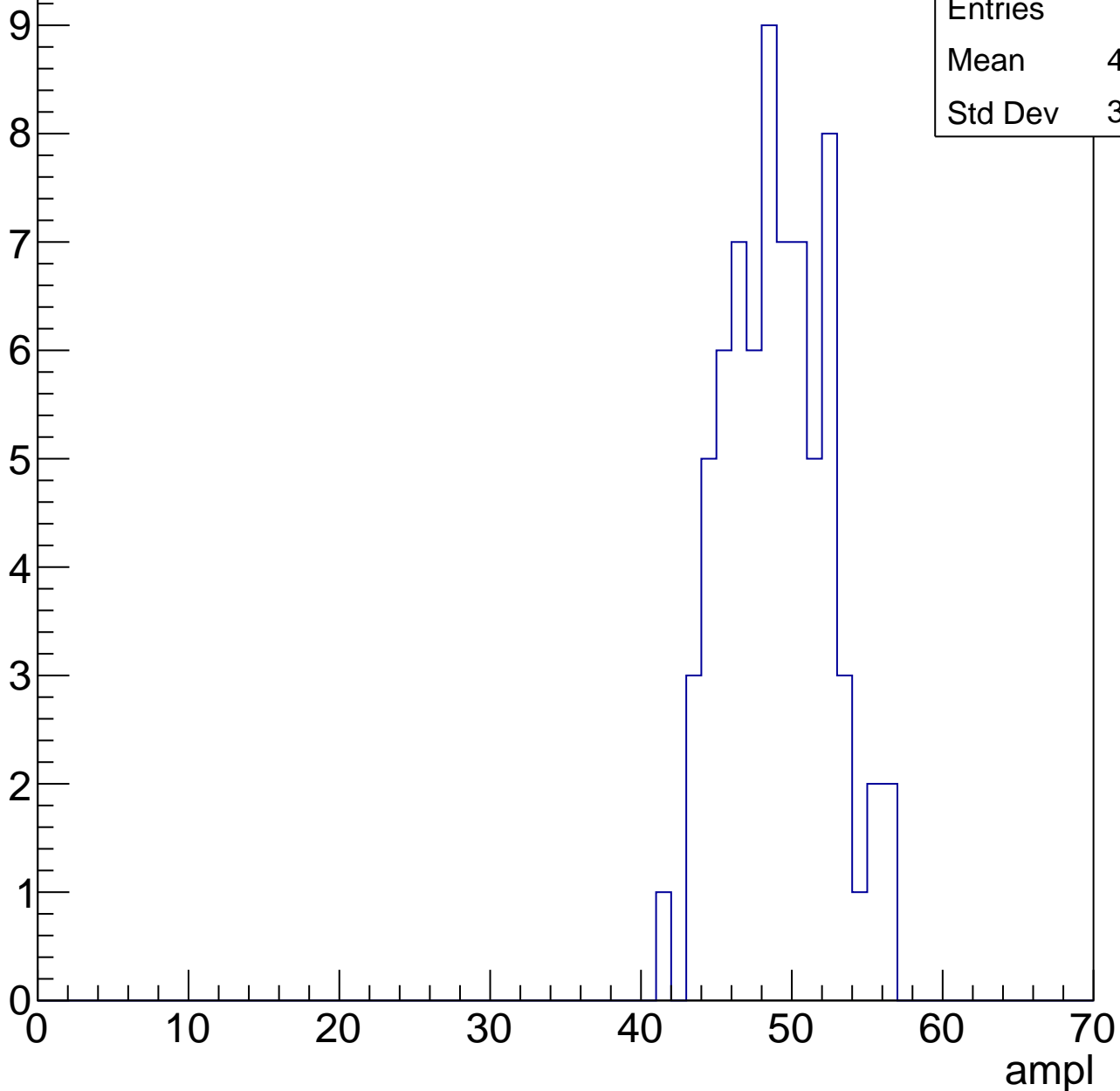
Gaus Width: 3.4913



B1L103S, U14-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

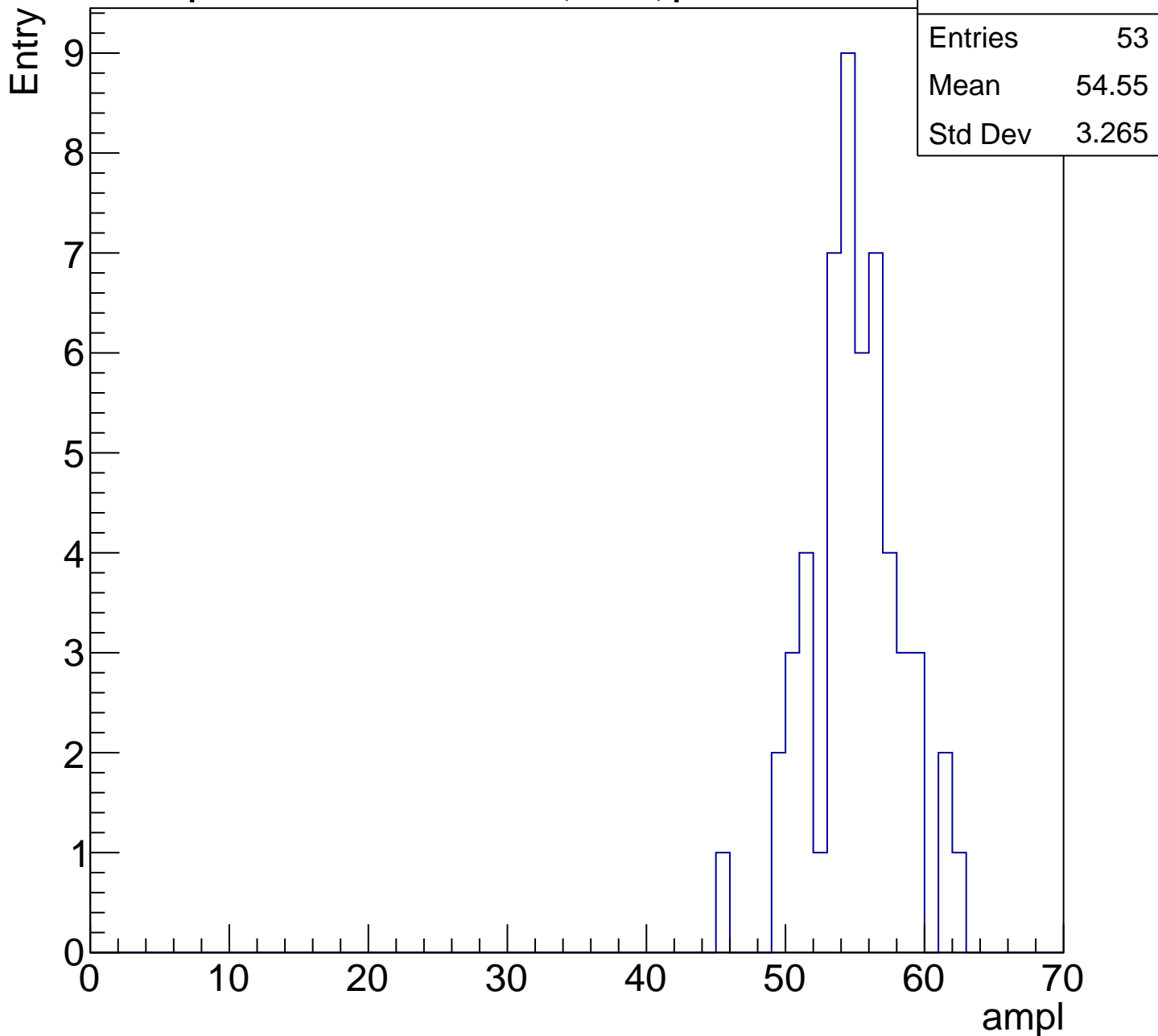
Entry



Entries	72
Mean	48.54
Std Dev	3.366

B1L103S, U14-ch3, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U14-ch3, adc5

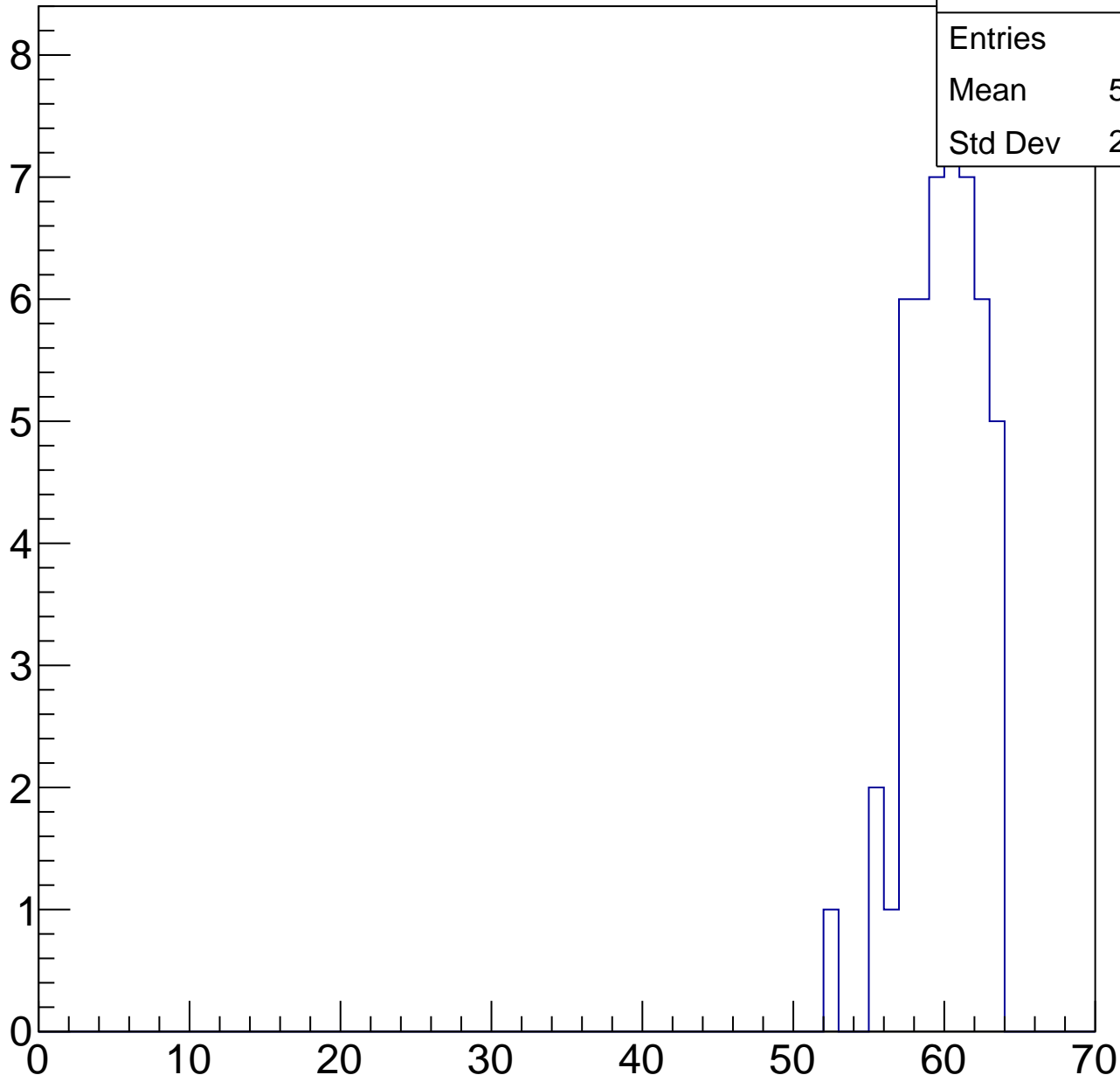
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.49
Std Dev	2.383

ampl

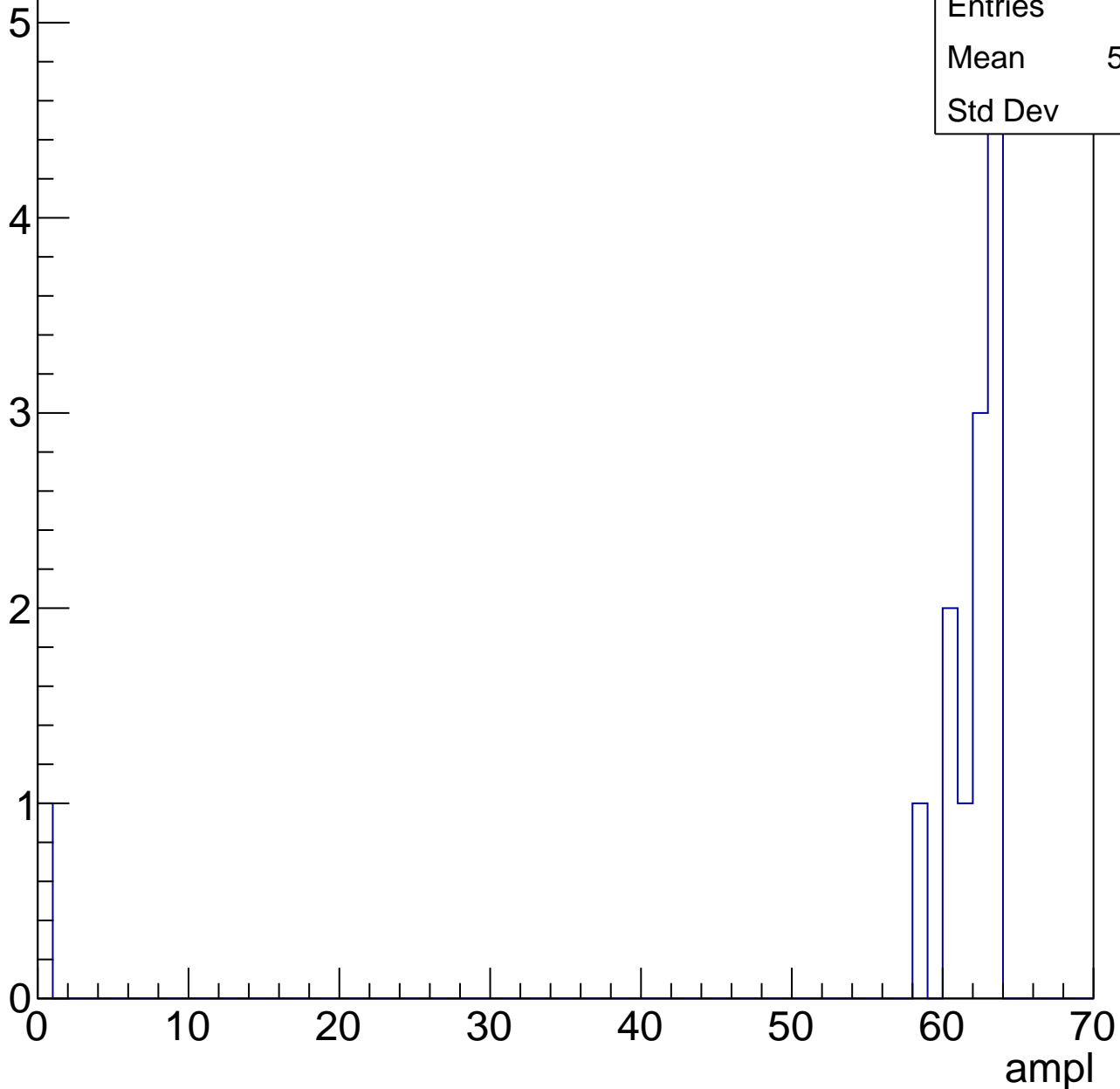


B1L103S, U14-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	56.92
Std Dev	16.5



B1L103S, U14-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch4, adc0

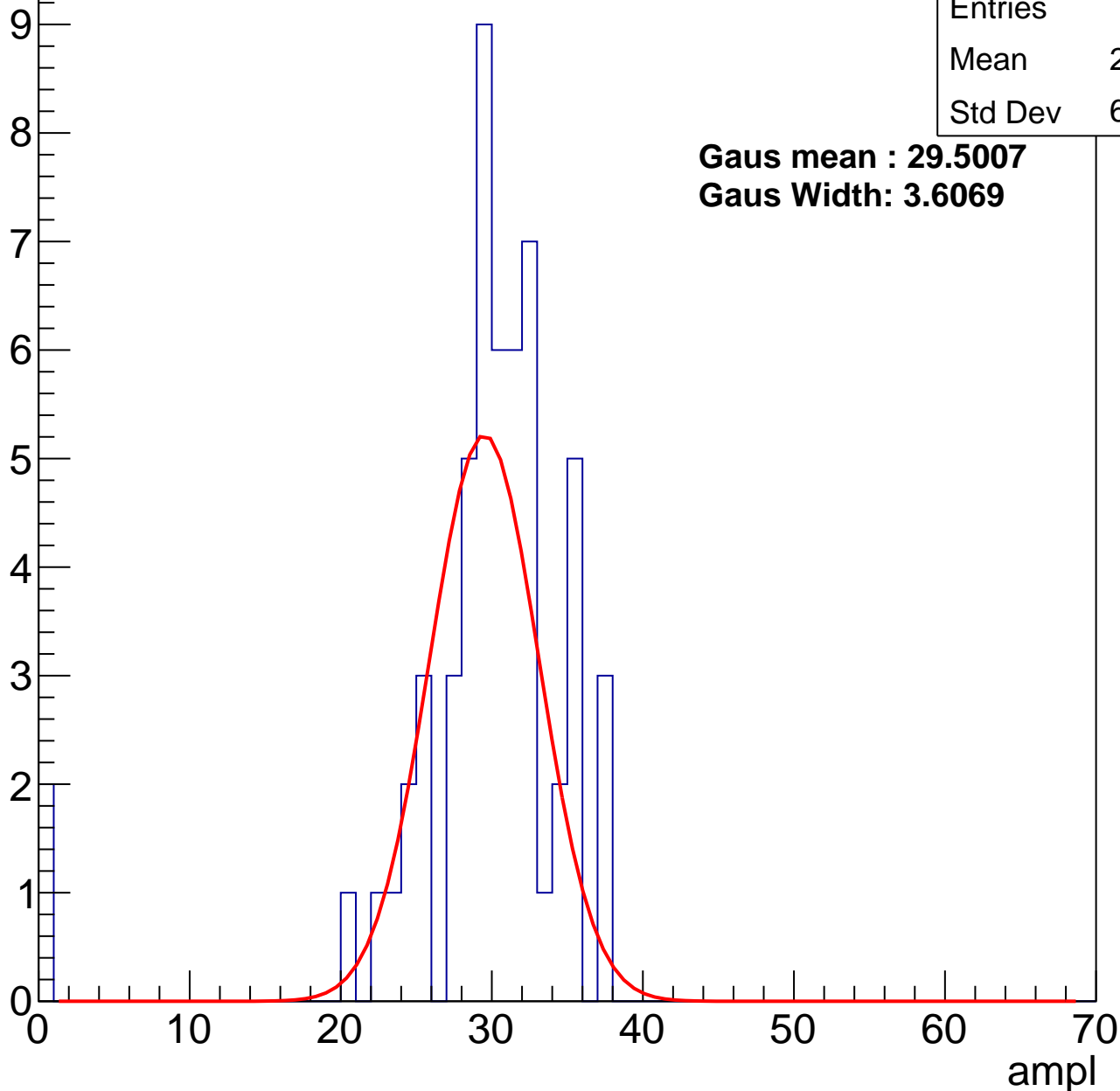
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	28.89
Std Dev	6.614

Gaus mean : 29.5007

Gaus Width: 3.6069



B1L103S, U14-ch4, adc1

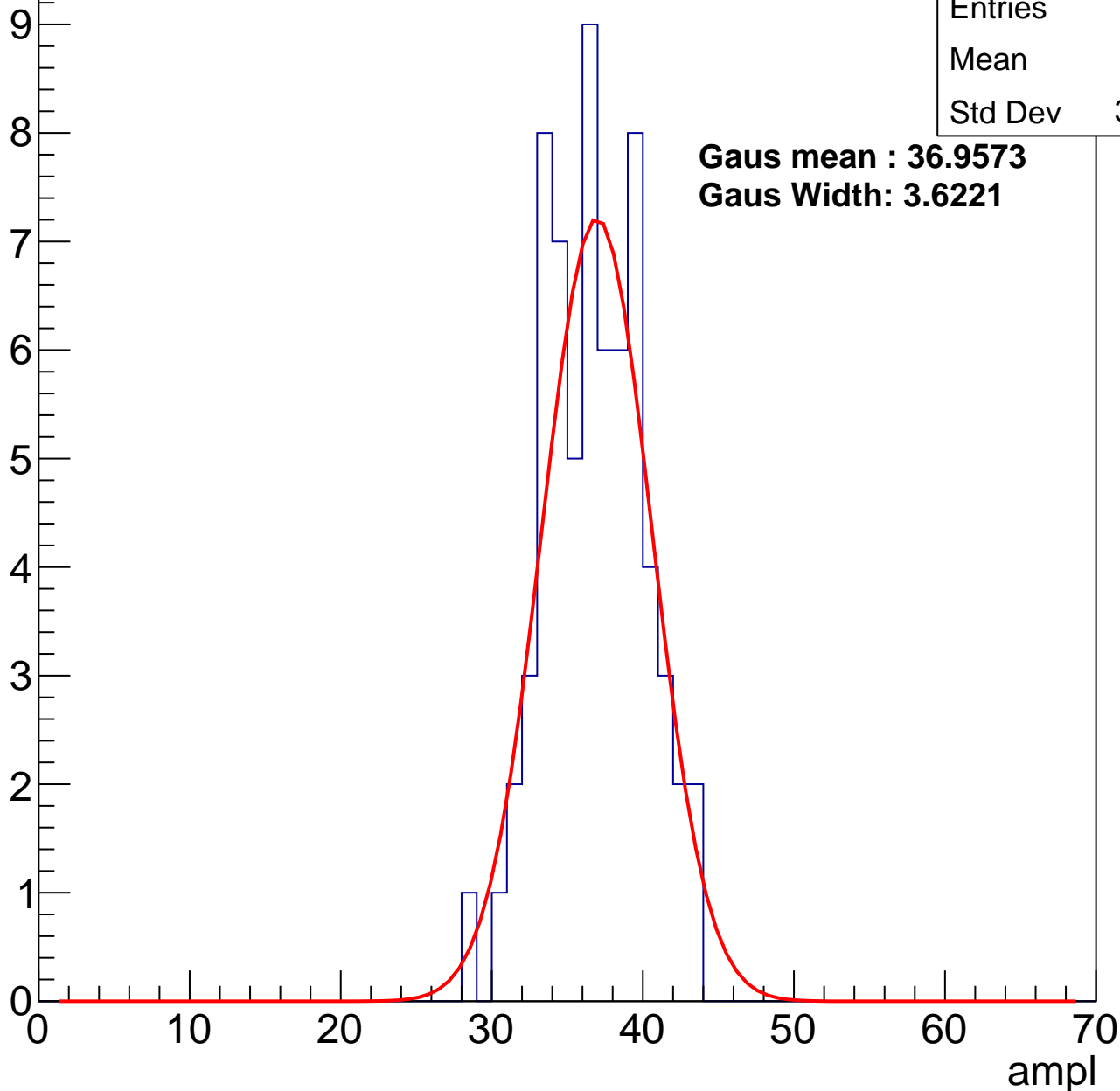
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	36.3
Std Dev	3.251

Gaus mean : 36.9573

Gaus Width: 3.6221



B1L103S, U14-ch4, adc2

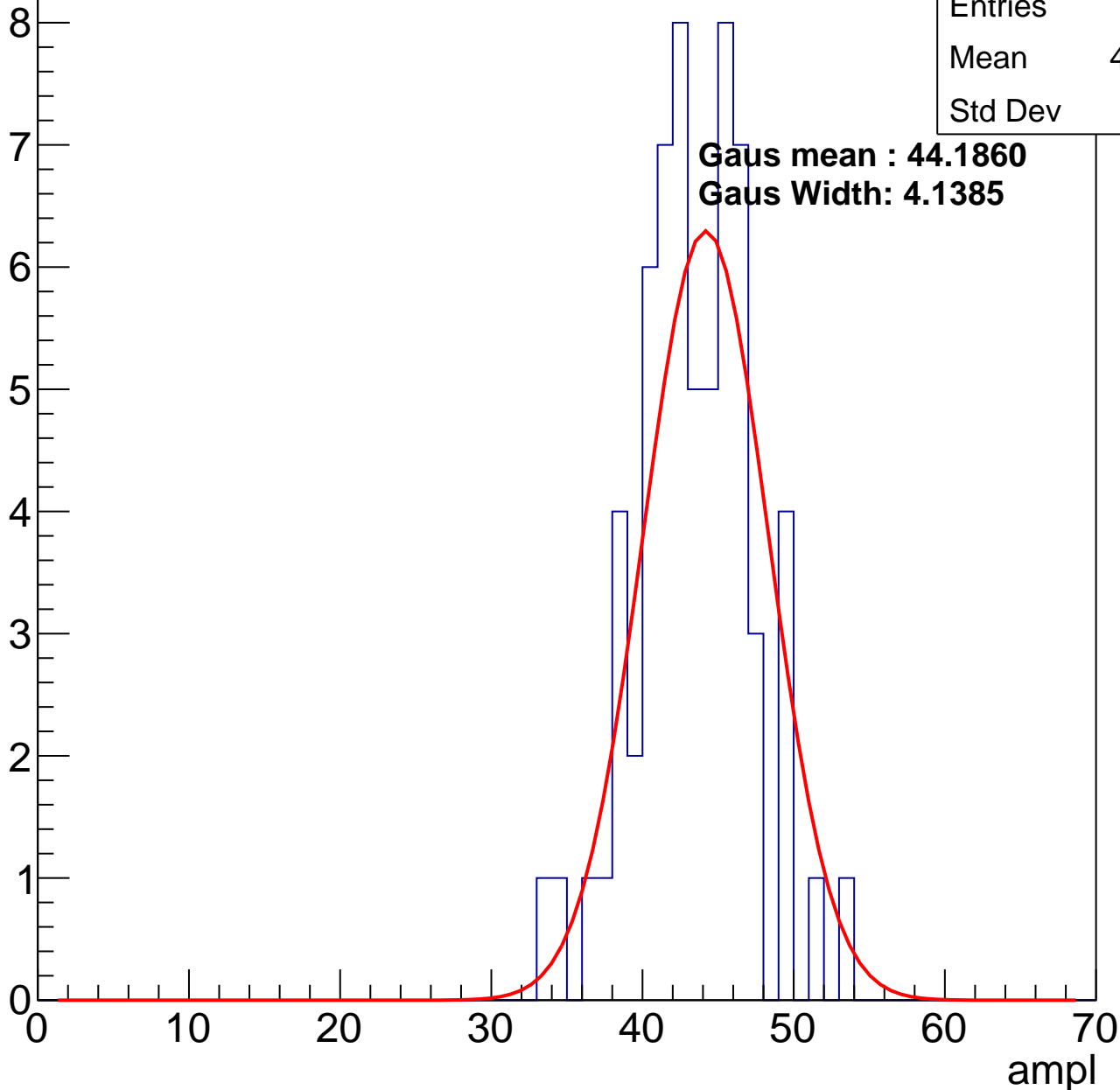
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.94
Std Dev	3.81

Gaus mean : 44.1860

Gaus Width: 4.1385

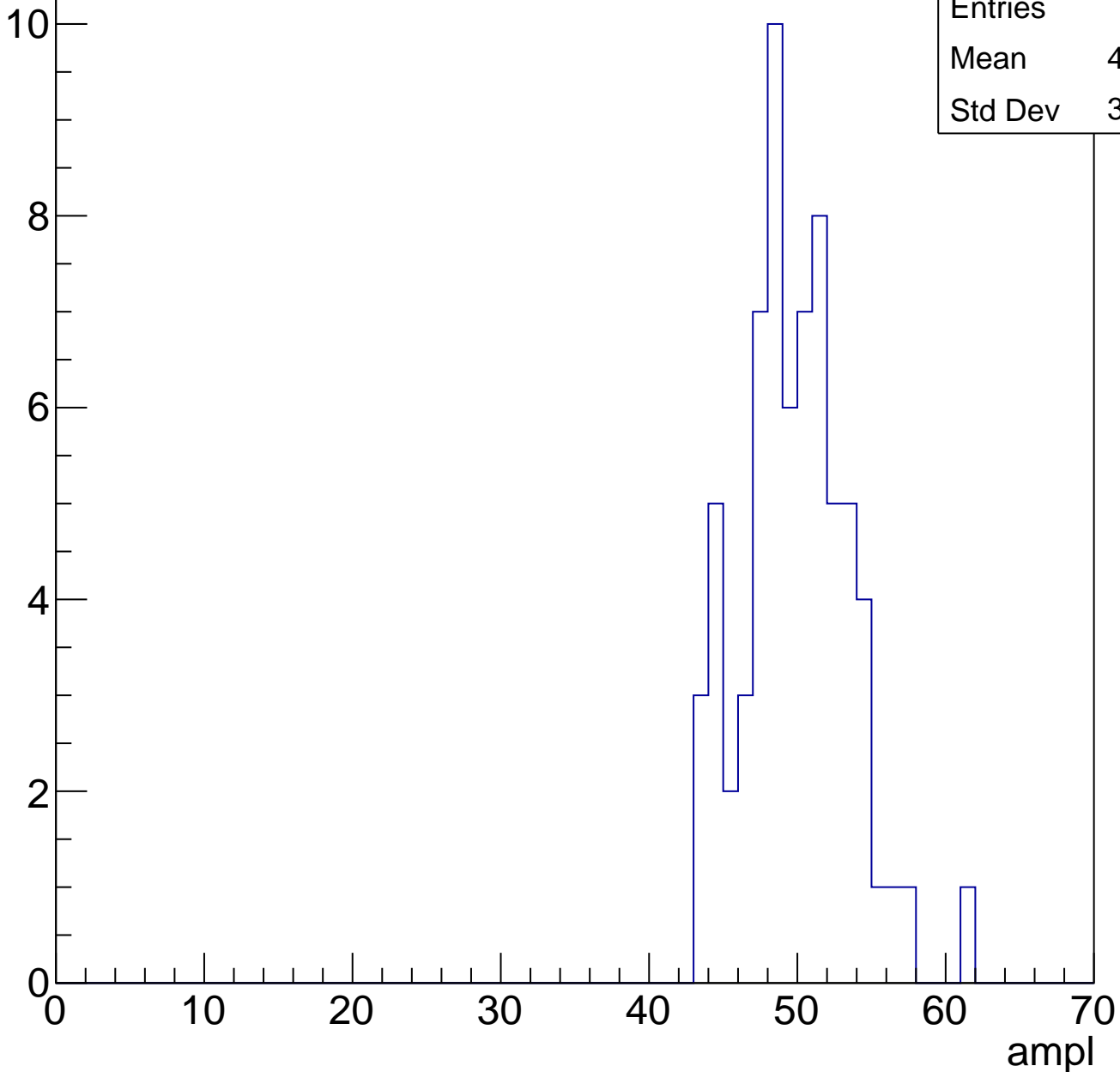


B1L103S, U14-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	49.39
Std Dev	3.564

Entry

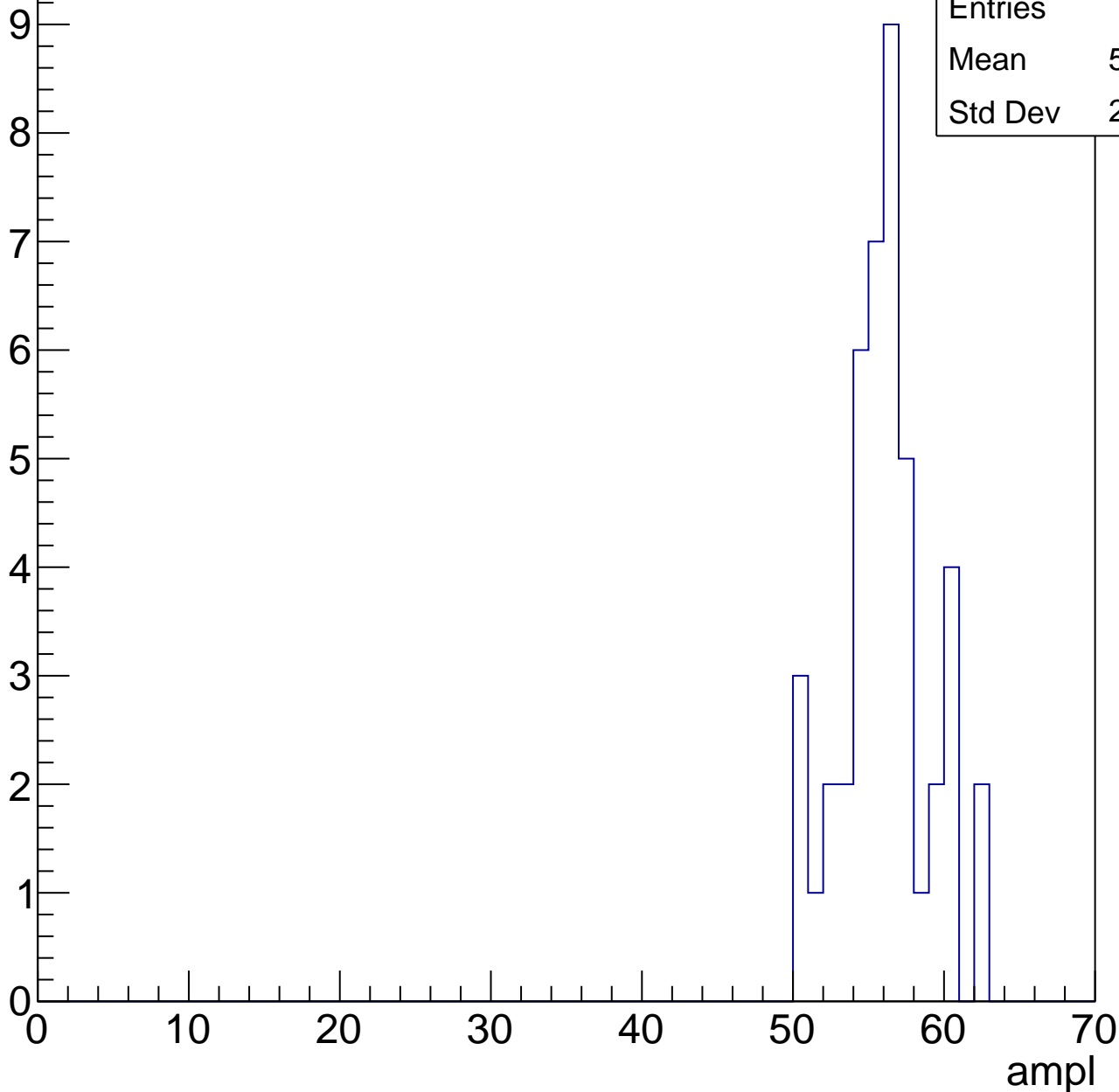


B1L103S, U14-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	55.66
Std Dev	2.907



B1L103S, U14-ch4, adc5

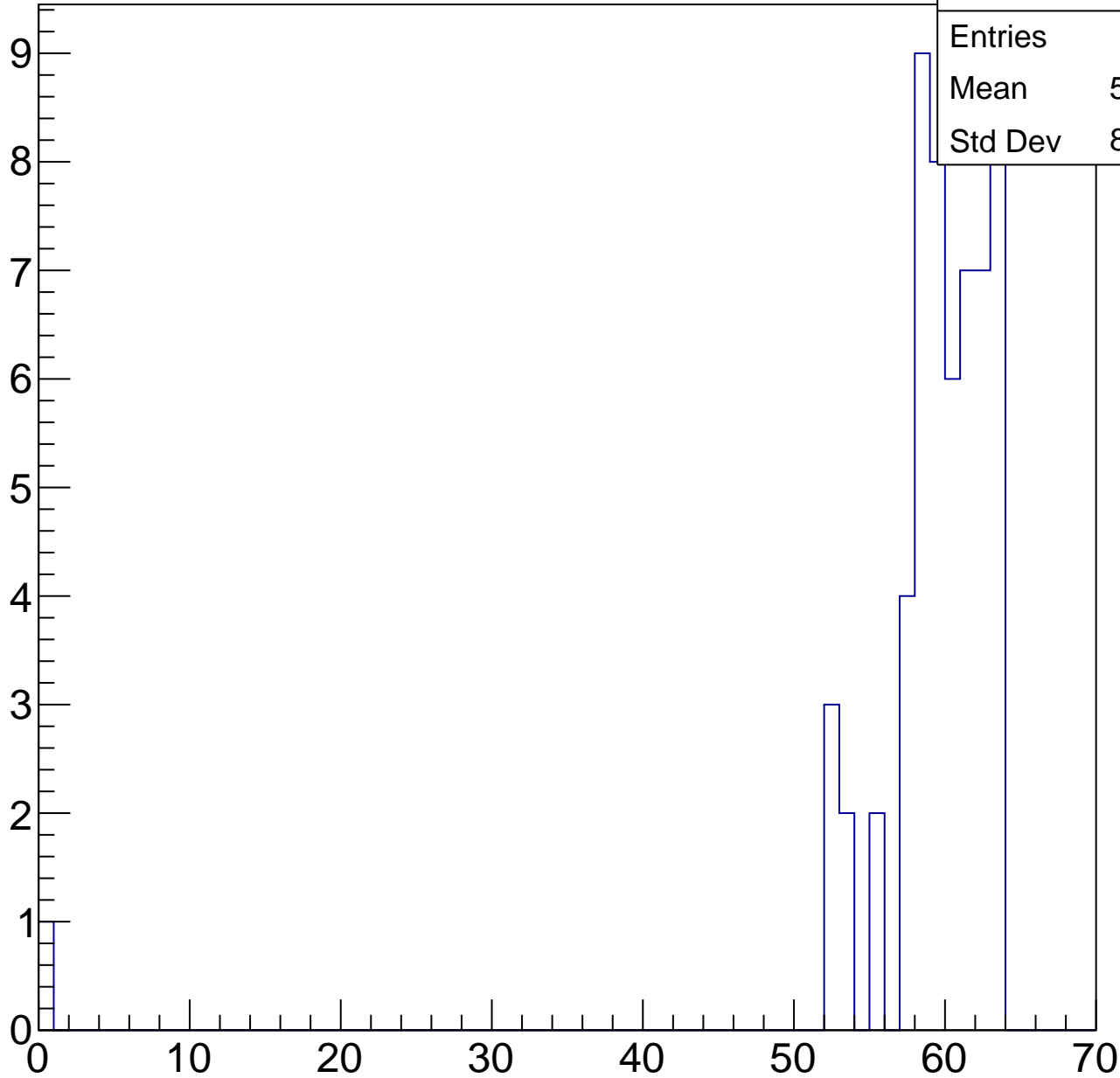
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	58.23
Std Dev	8.322

ampl



B1L103S, U14-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

B1L103S, U14-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch5, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	28.37
Std Dev	5.937

Gaus mean : 29.2035

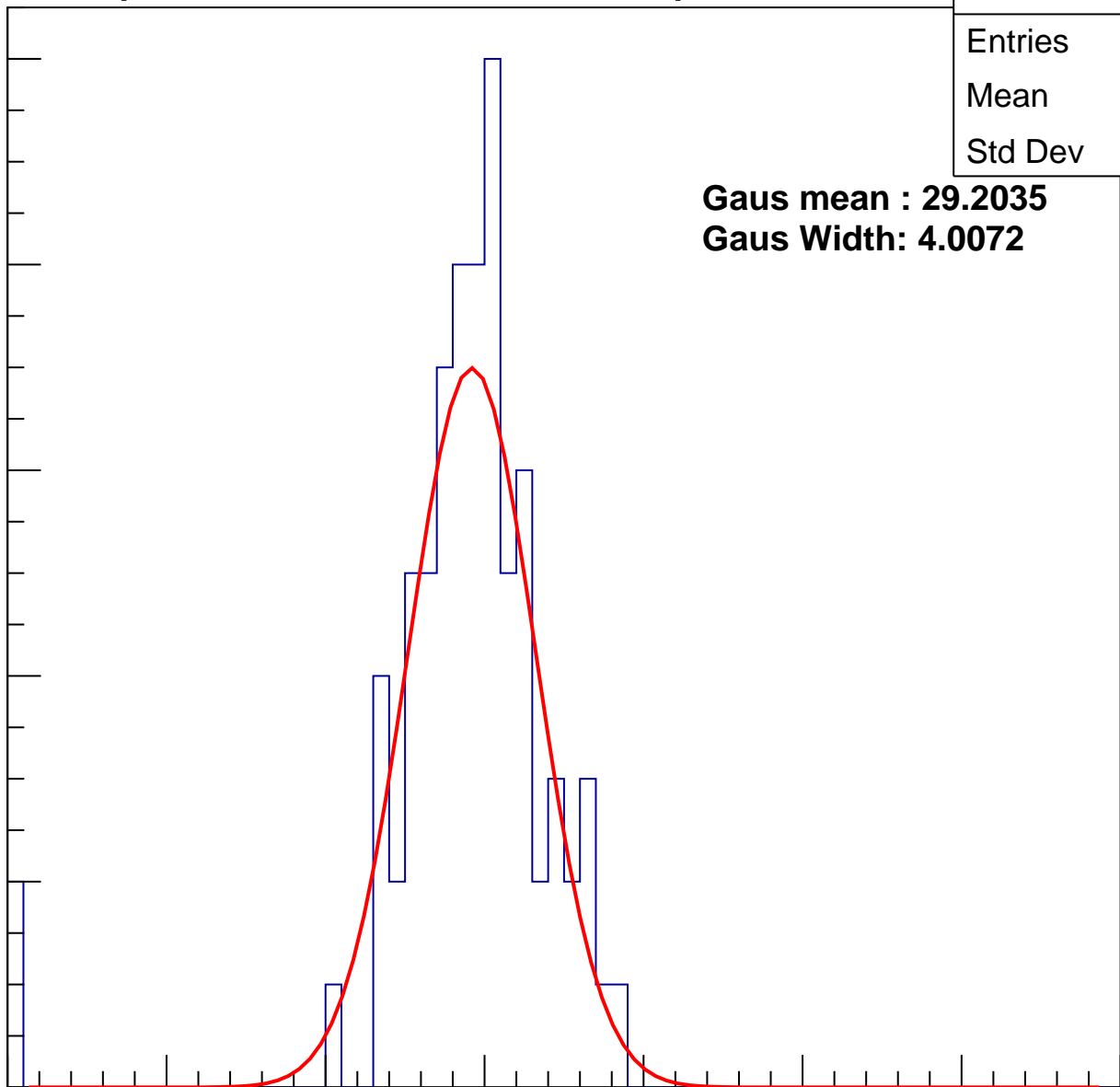
Gaus Width: 4.0072

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch5, adc1

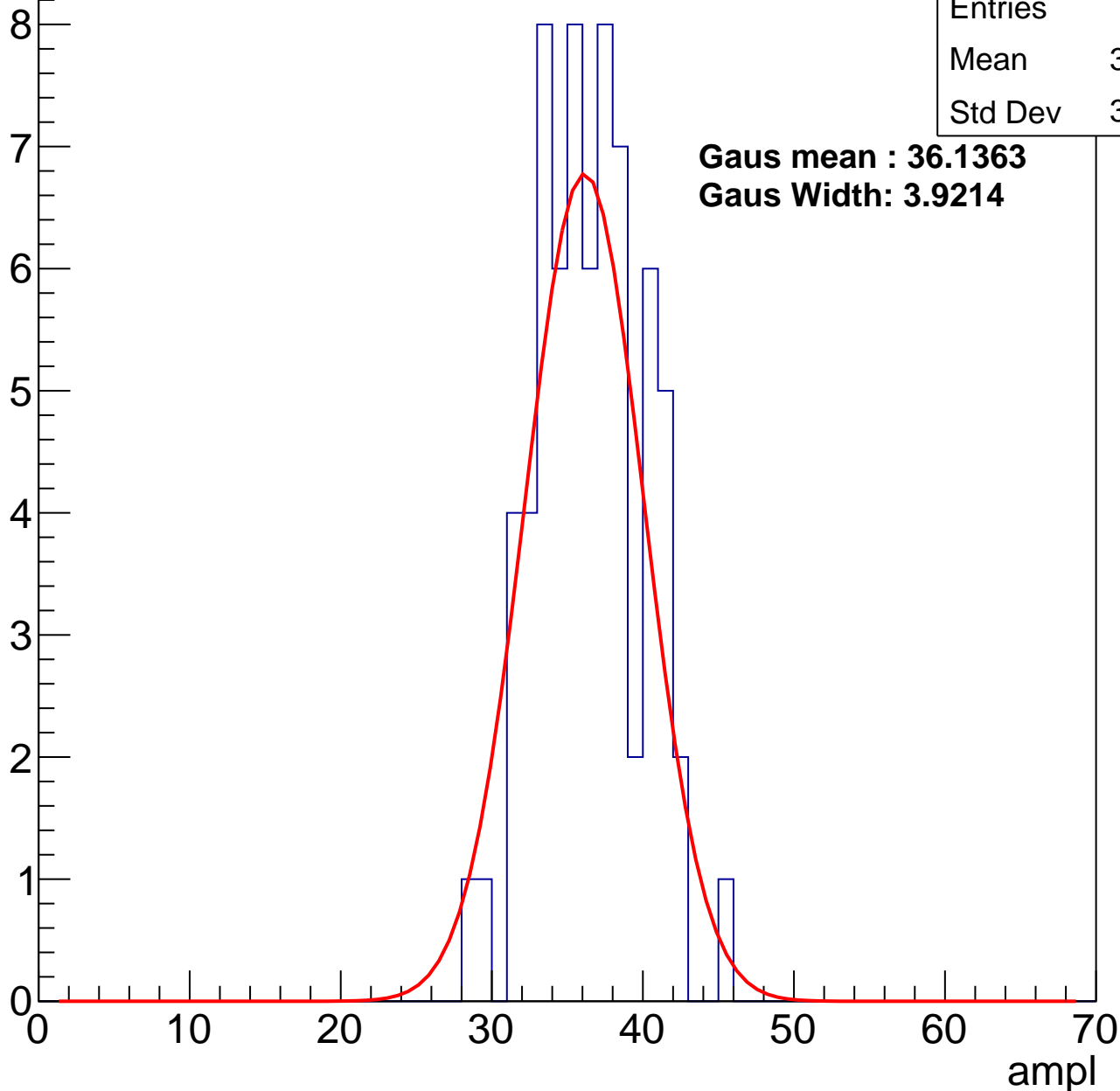
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.04
Std Dev	3.424

Gaus mean : 36.1363

Gaus Width: 3.9214

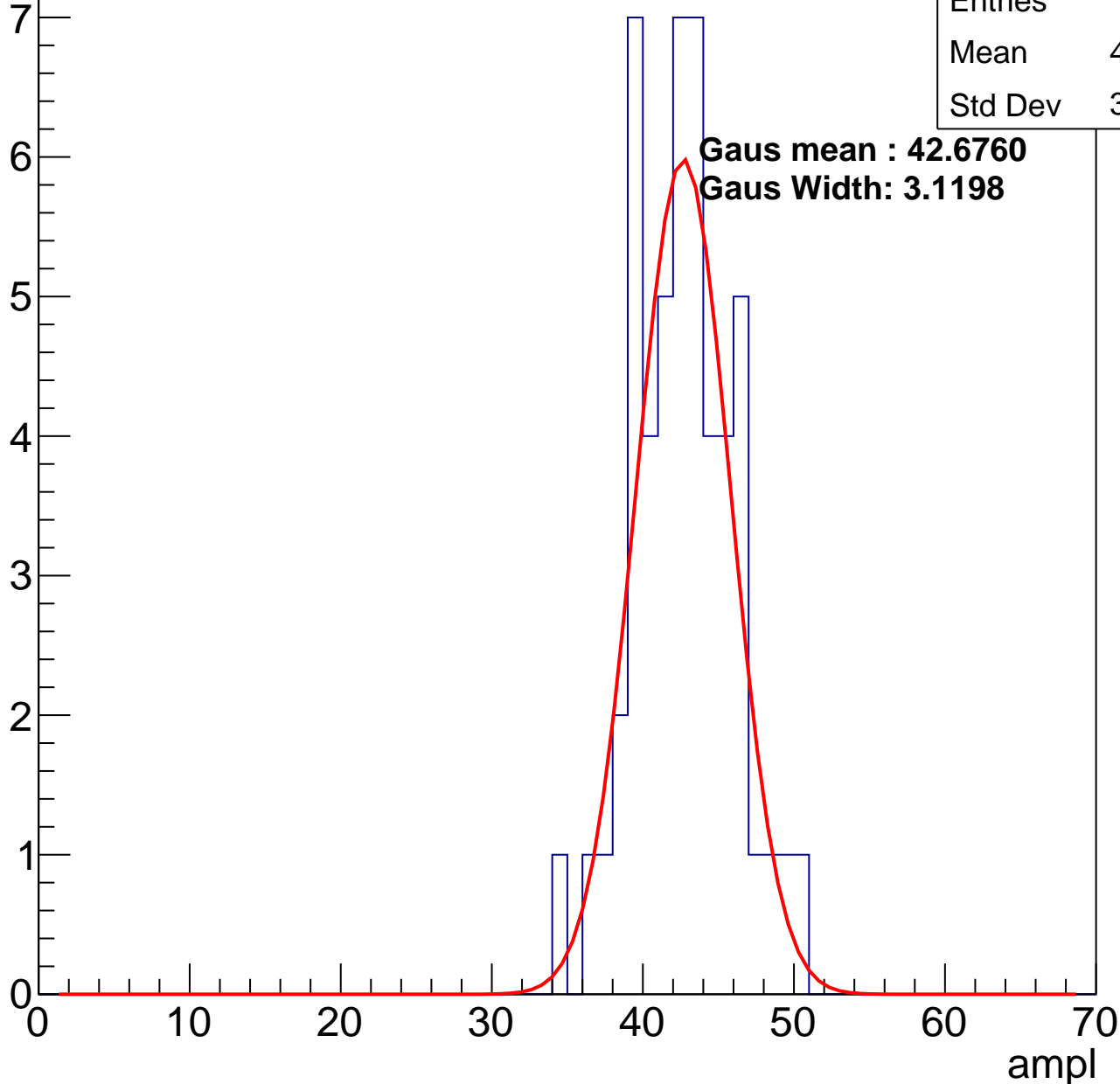


B1L103S, U14-ch5, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	42.23
Std Dev	3.256

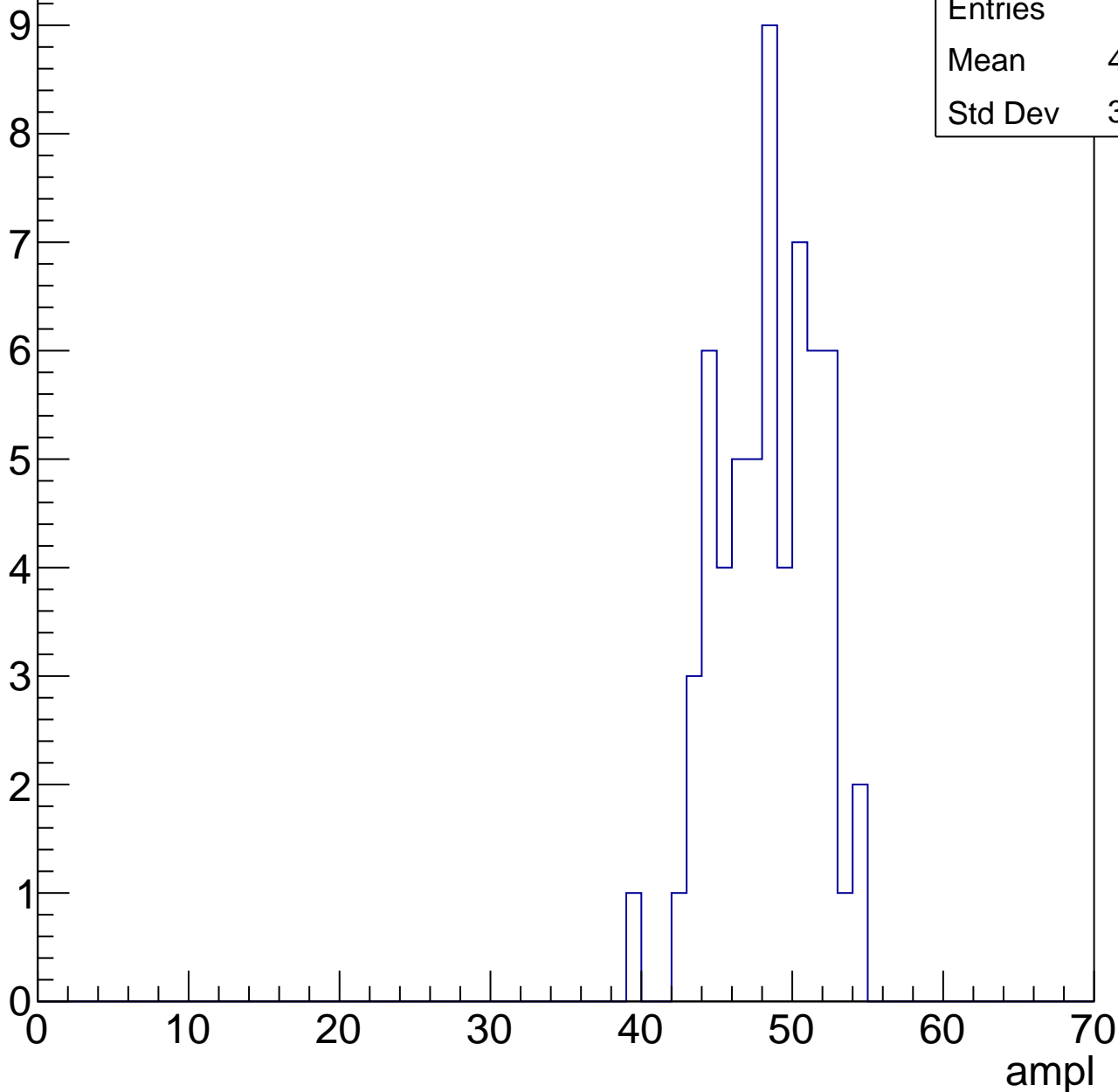


B1L103S, U14-ch5, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	47.93
Std Dev	3.235

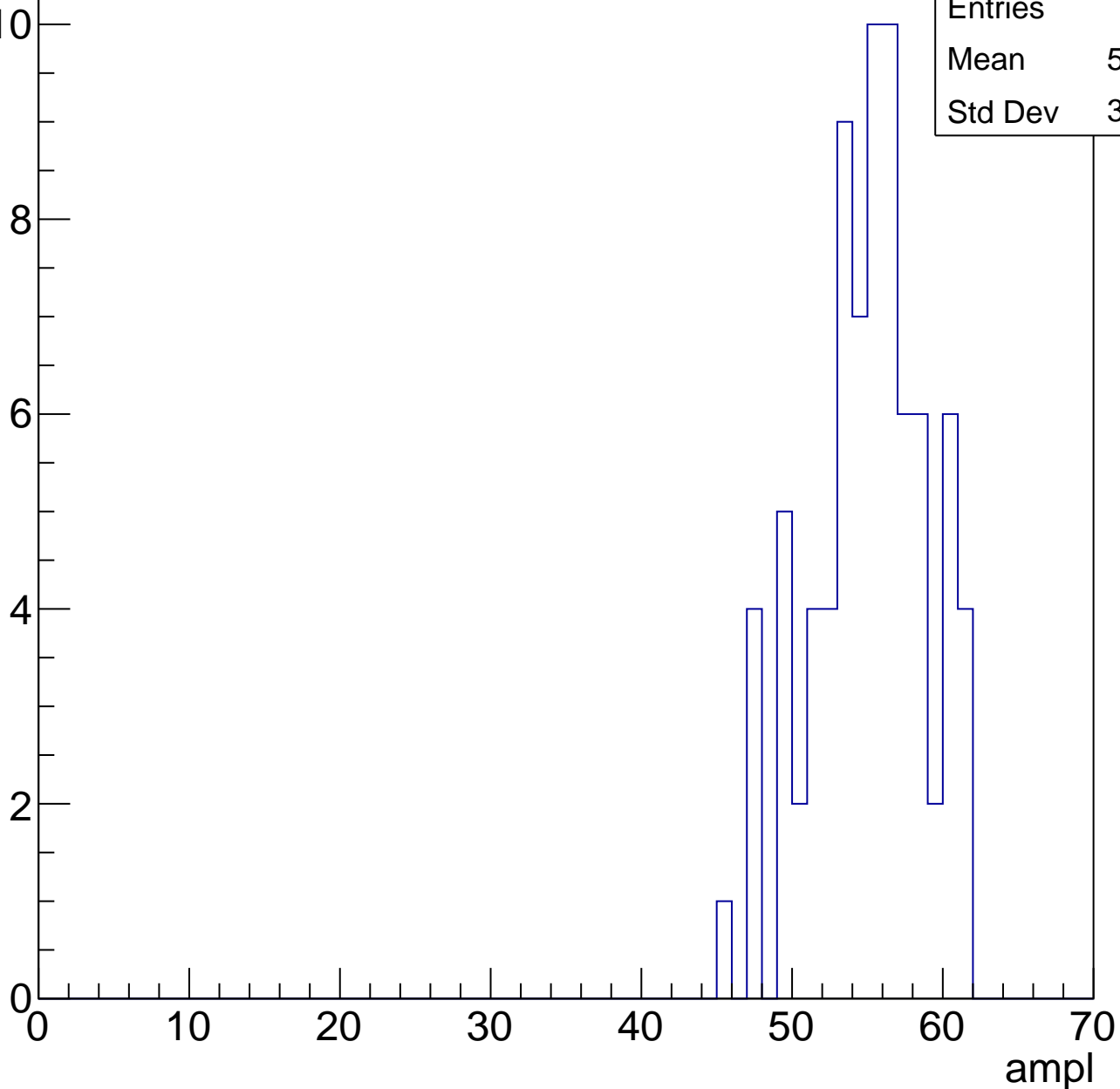


B1L103S, U14-ch5, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	54.59
Std Dev	3.757

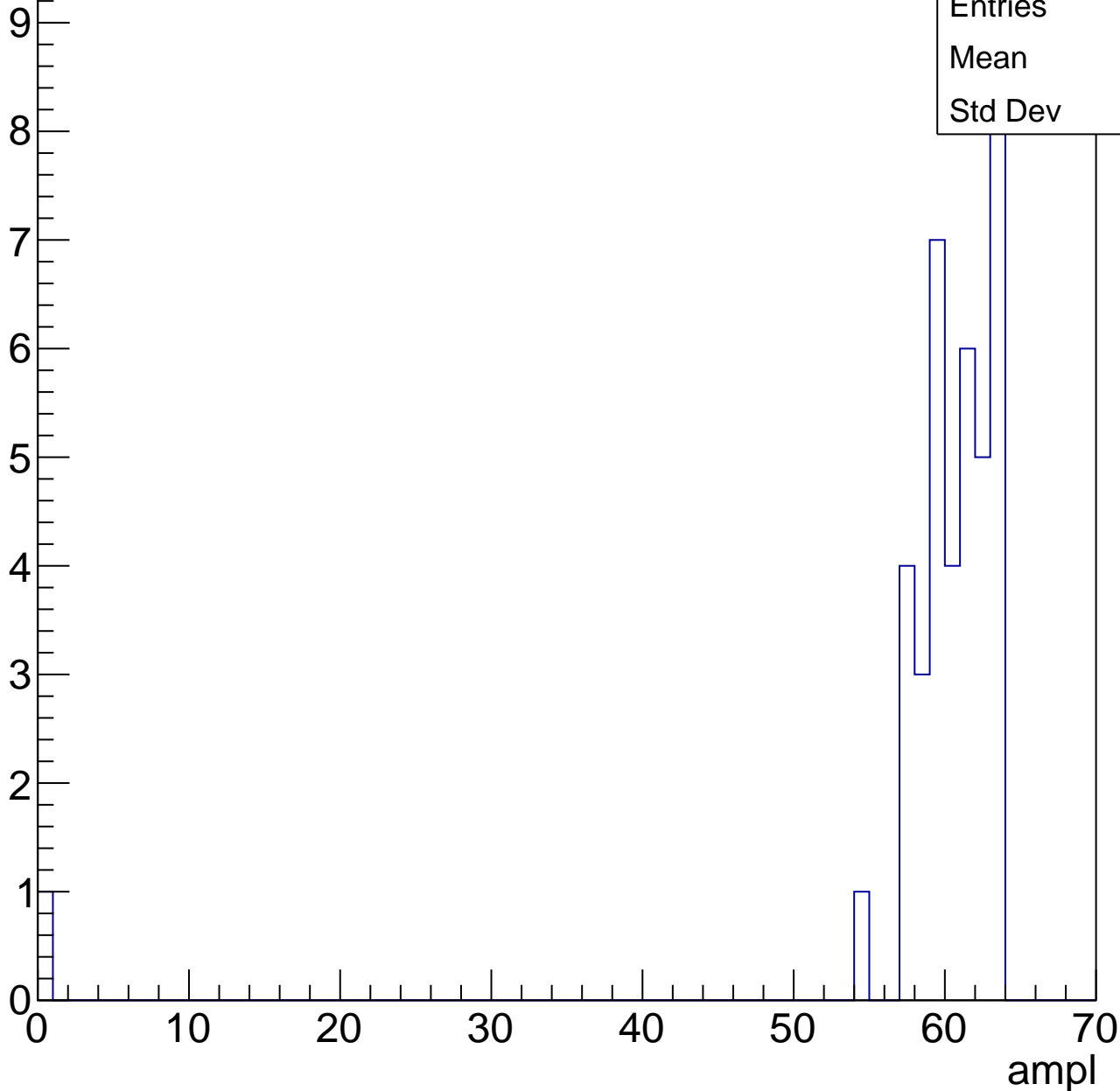


B1L103S, U14-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.8
Std Dev	9.67

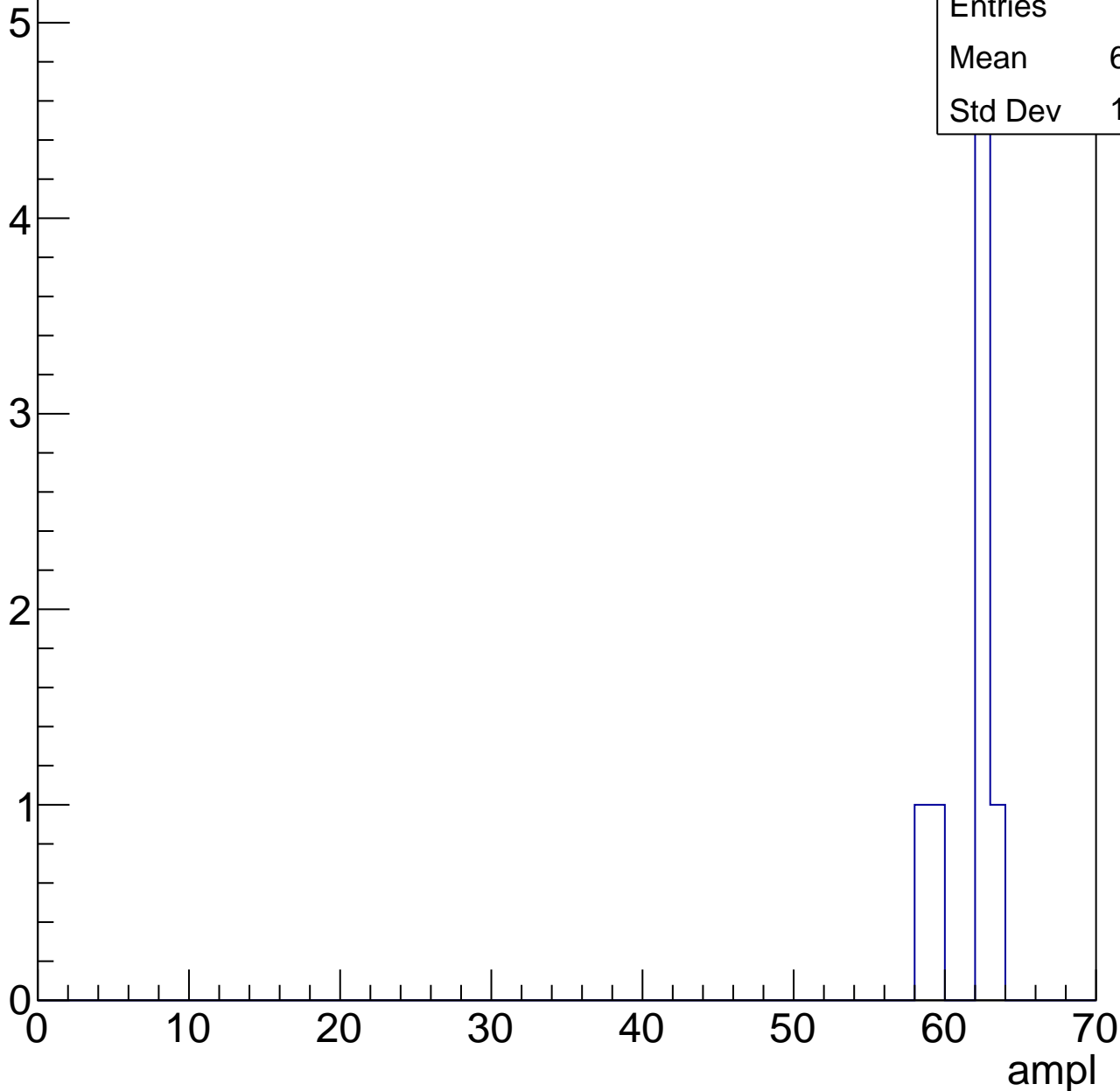


B1L103S, U14-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

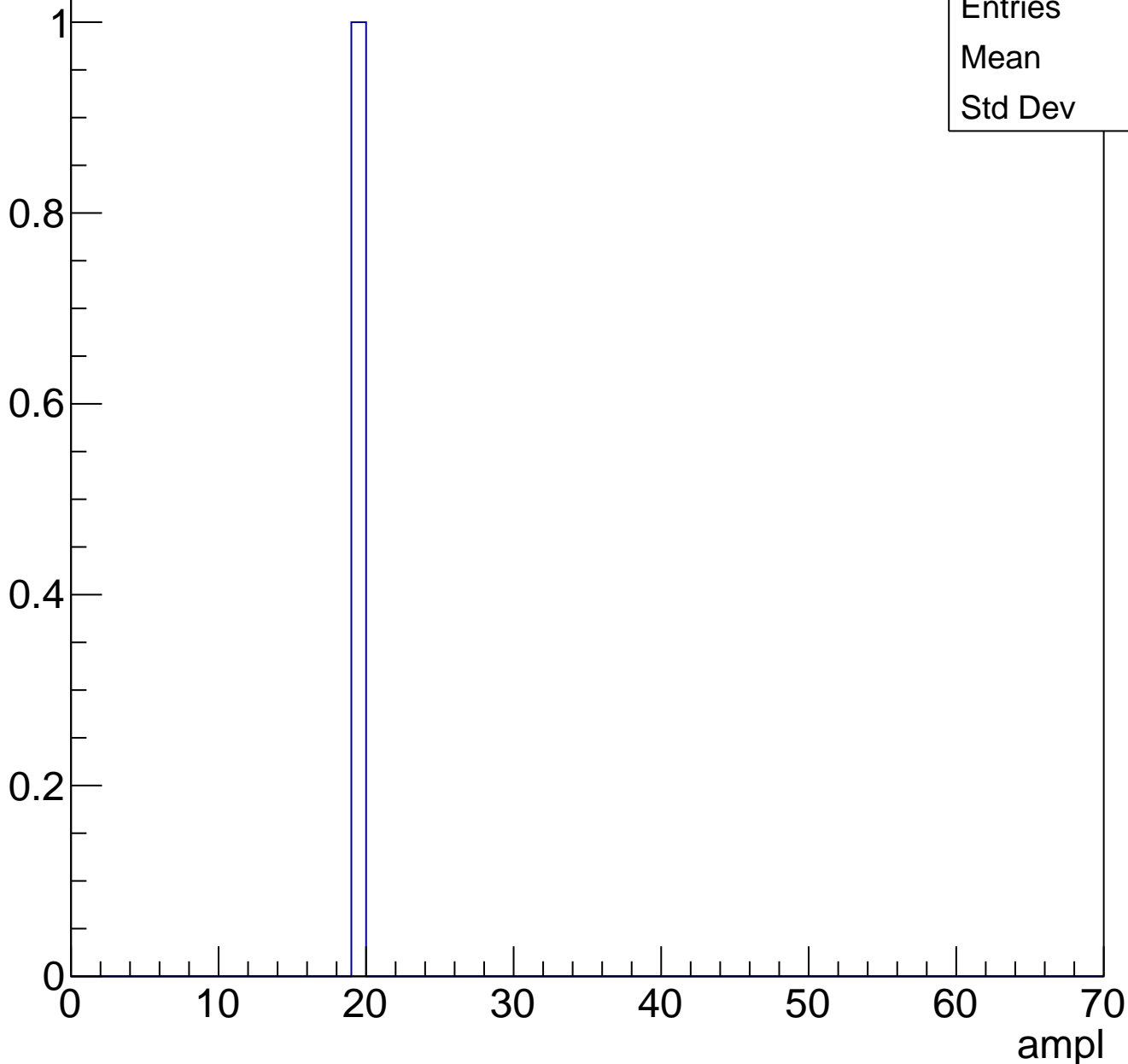
Entries	8
Mean	61.25
Std Dev	1.639



B1L103S, U14-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	19
Std Dev	0

B1L103S, U14-ch6, adc0

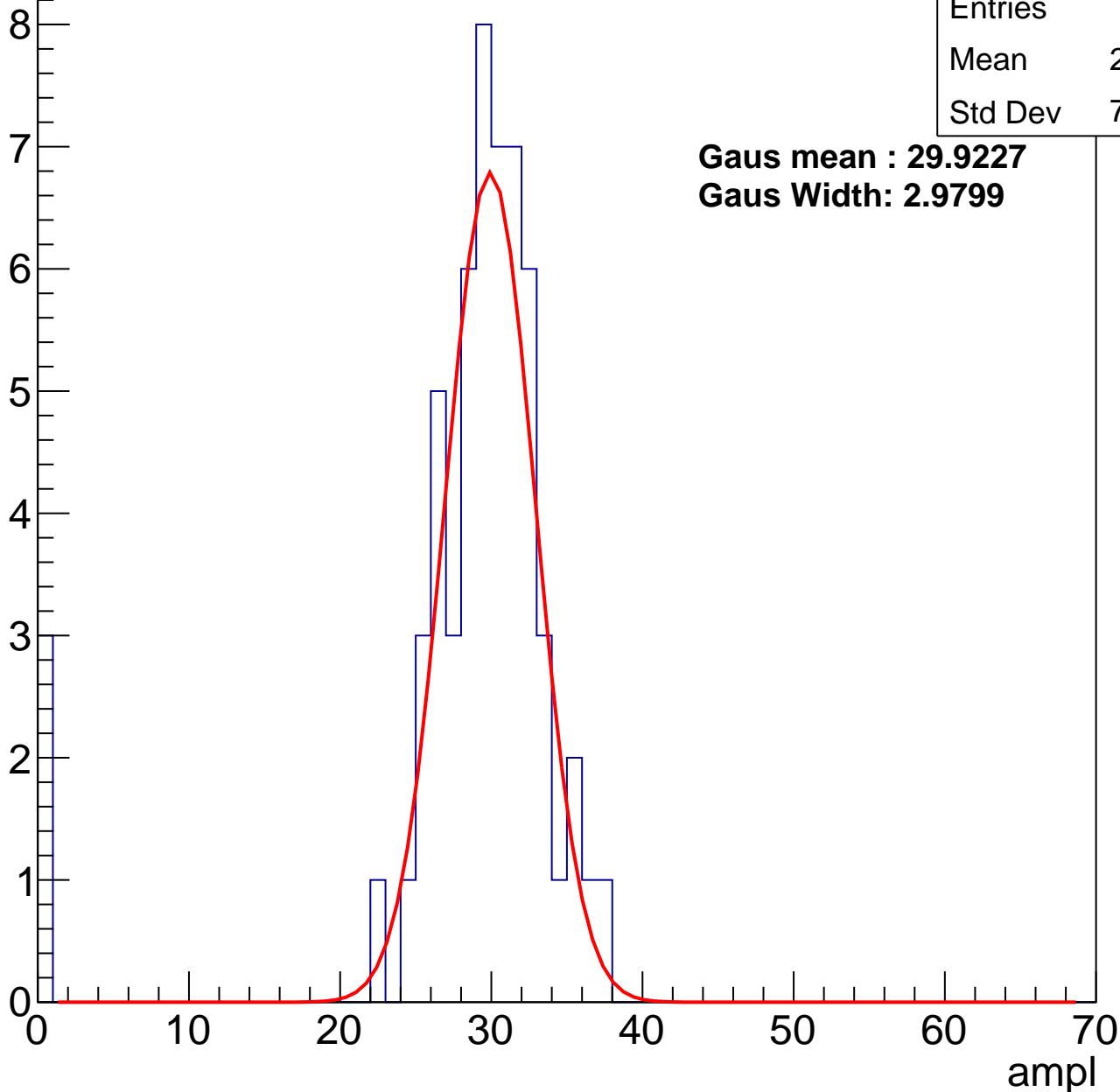
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	28.05
Std Dev	7.195

Gaus mean : 29.9227

Gaus Width: 2.9799



B1L103S, U14-ch6, adc1

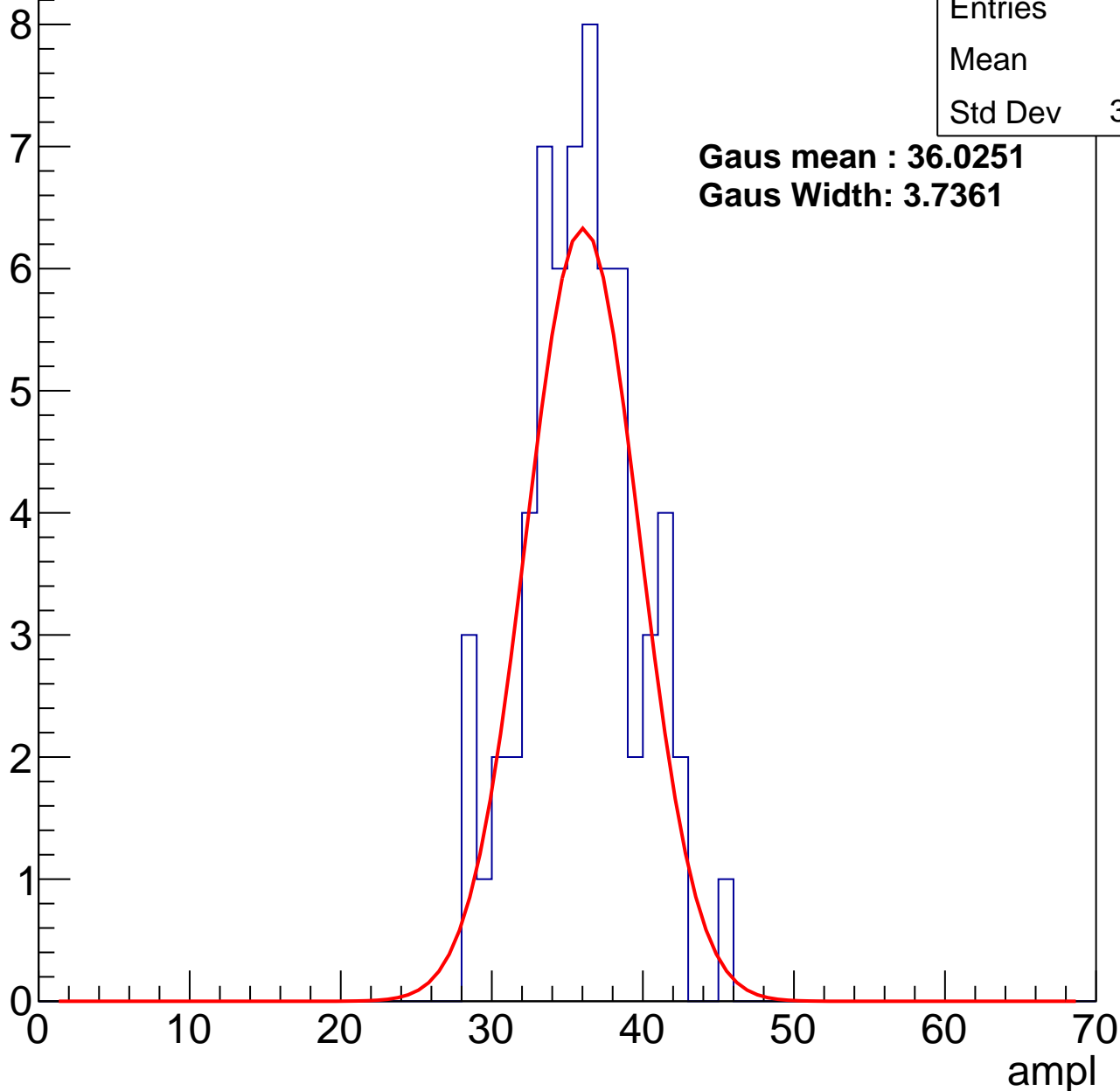
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.5
Std Dev	3.653

Gaus mean : 36.0251

Gaus Width: 3.7361



B1L103S, U14-ch6, adc2

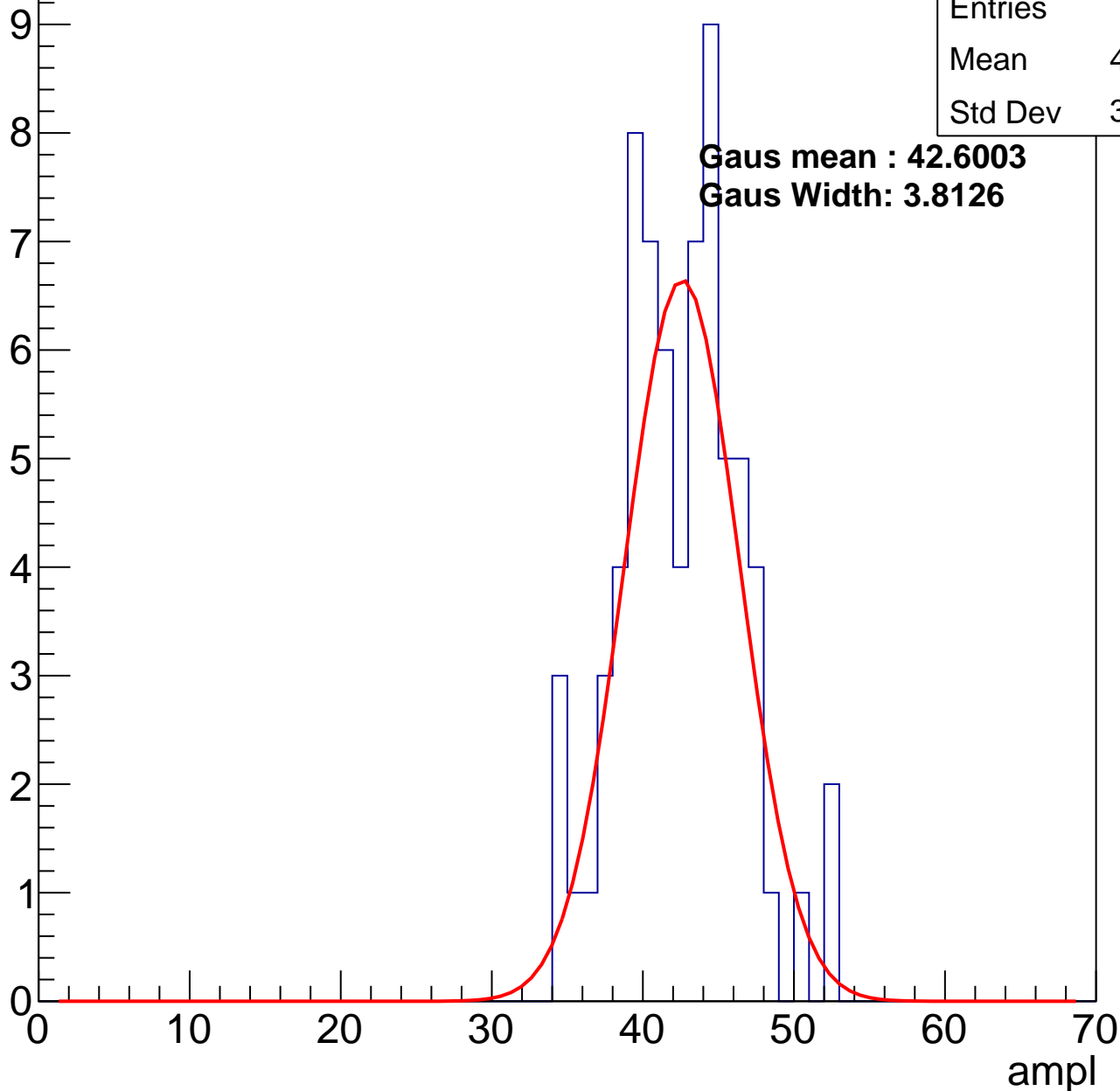
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.03
Std Dev	3.918

Gaus mean : 42.6003

Gaus Width: 3.8126

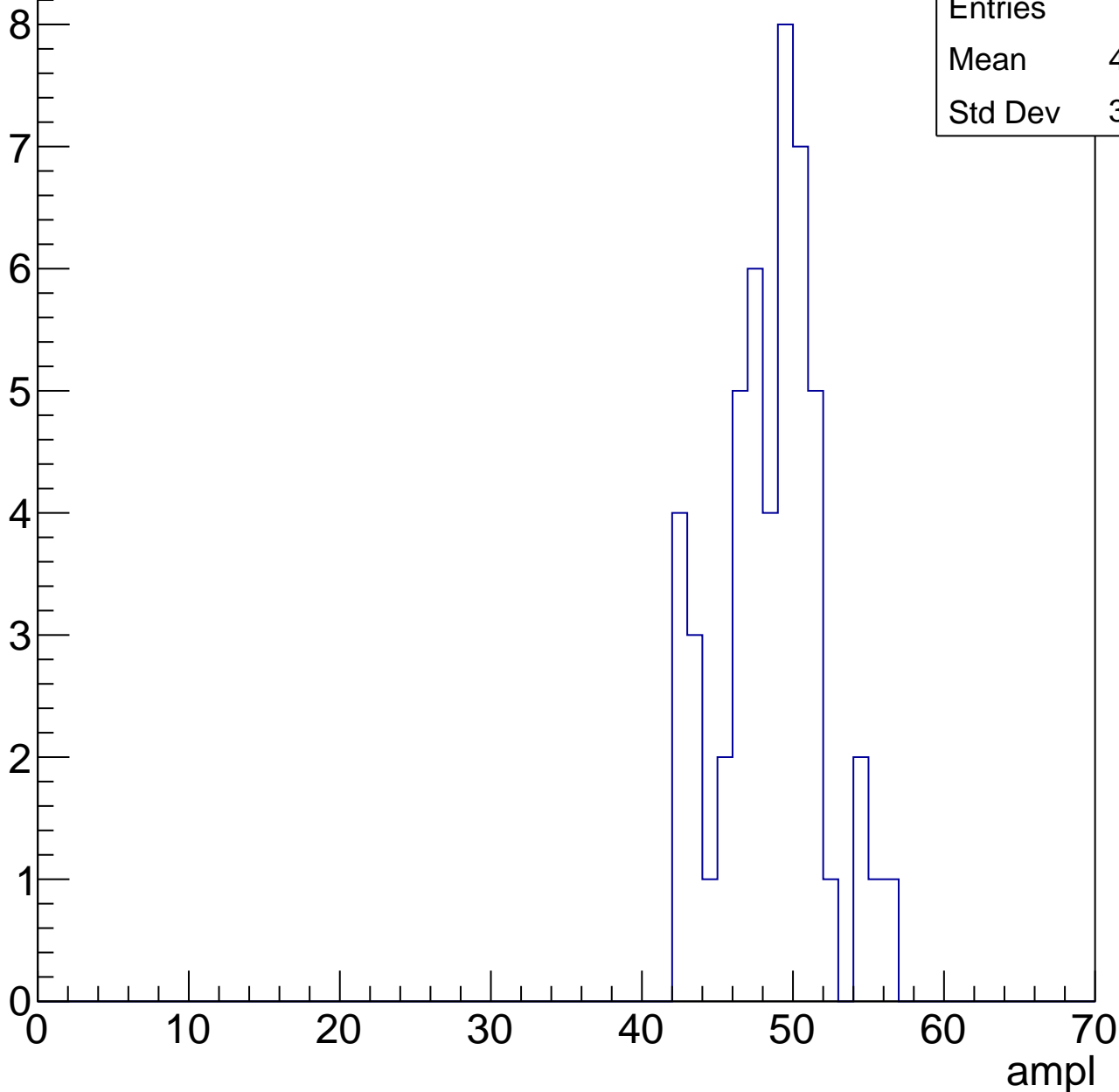


B1L103S, U14-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	48.06
Std Dev	3.349

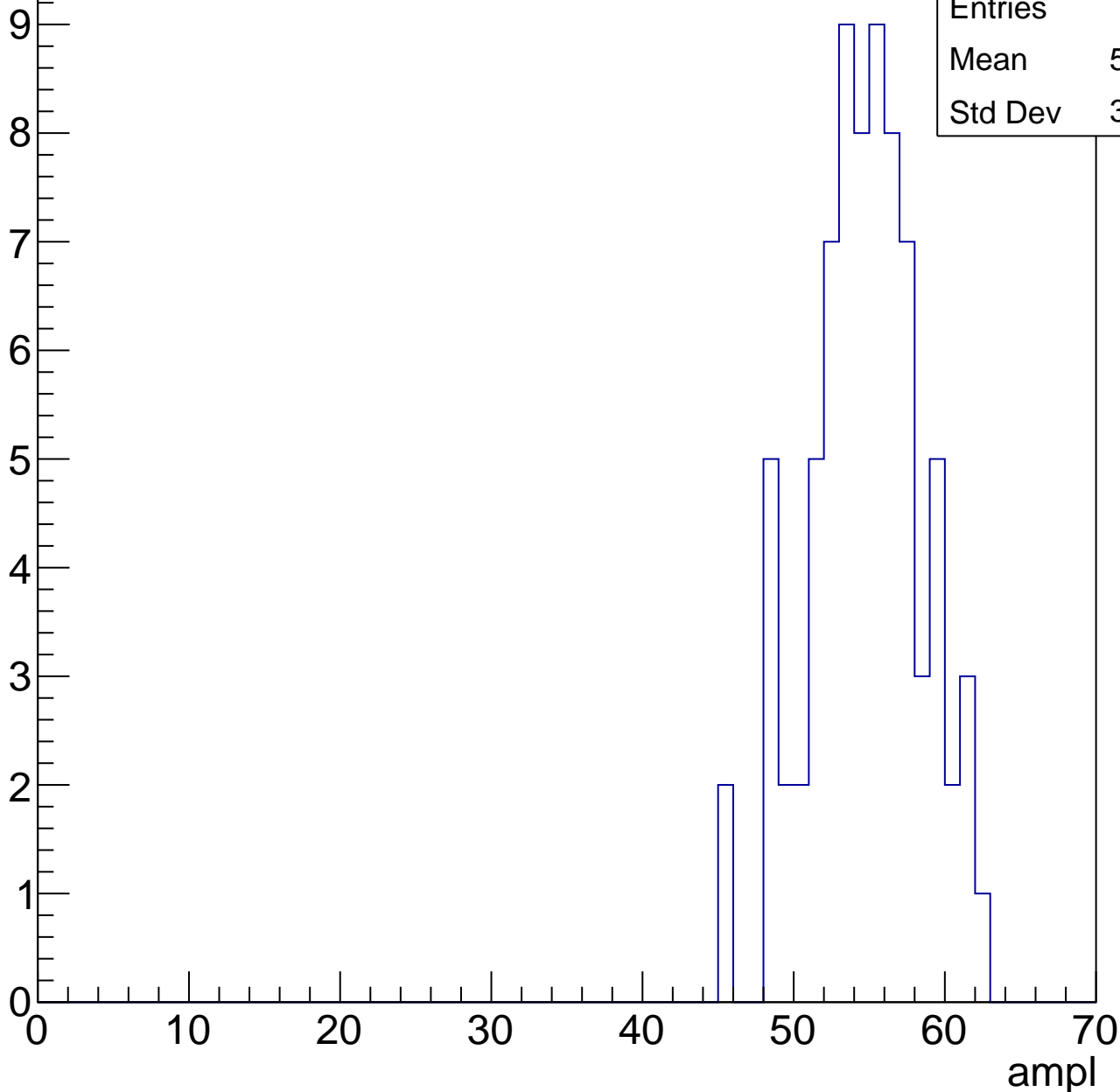


B1L103S, U14-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	54.26
Std Dev	3.688

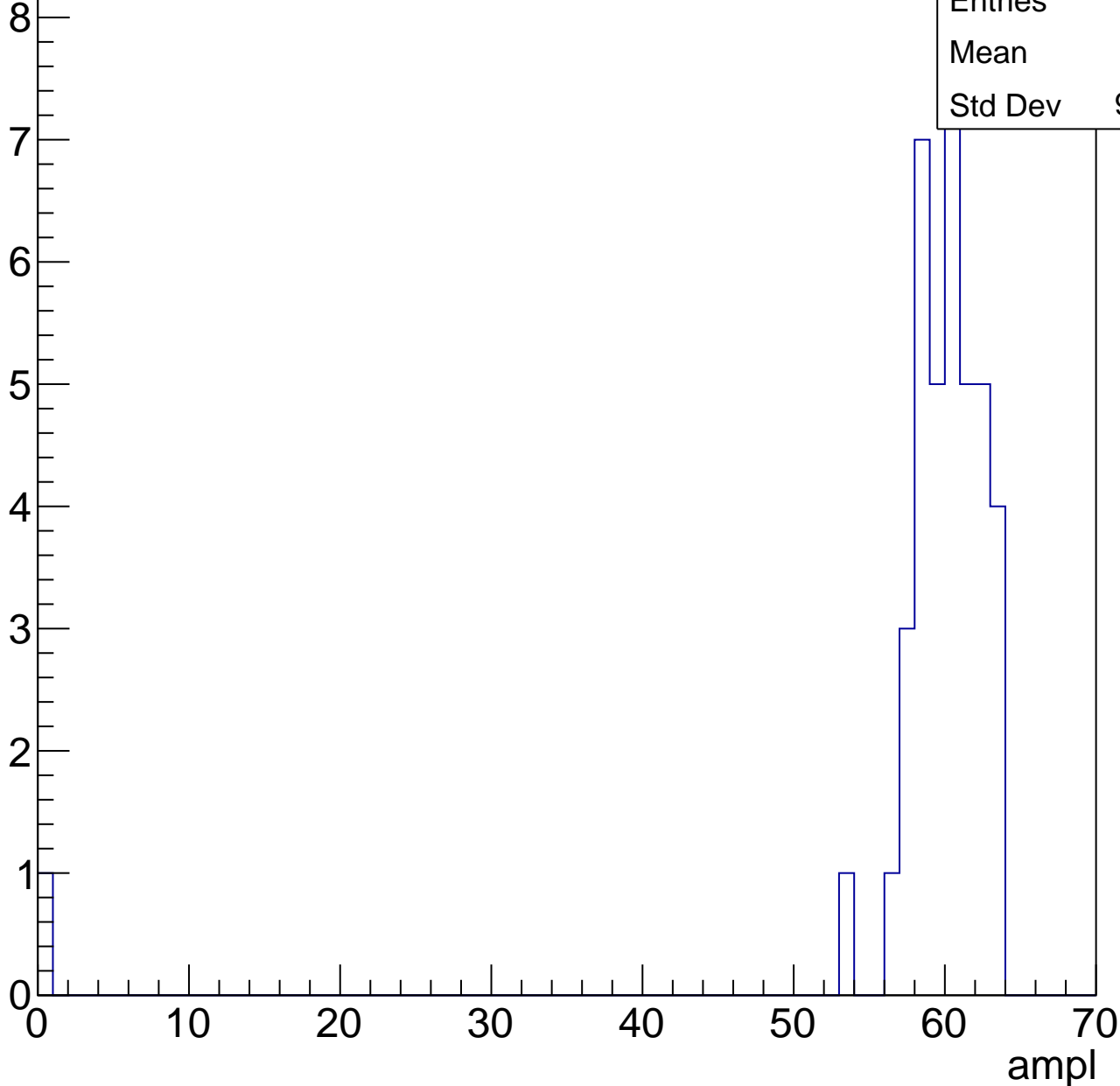


B1L103S, U14-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

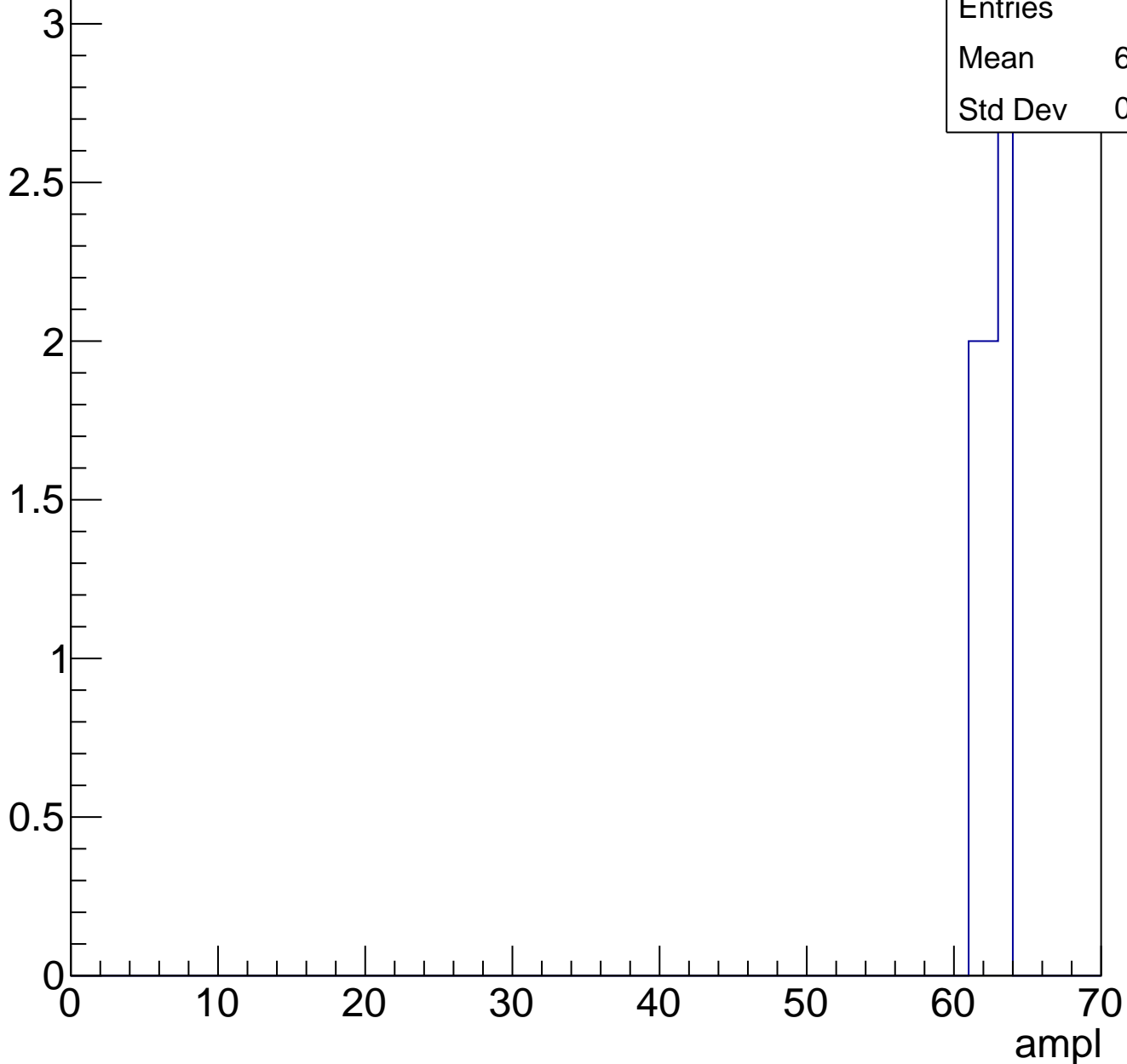
Entries	40
Mean	58.2
Std Dev	9.561



B1L103S, U14-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

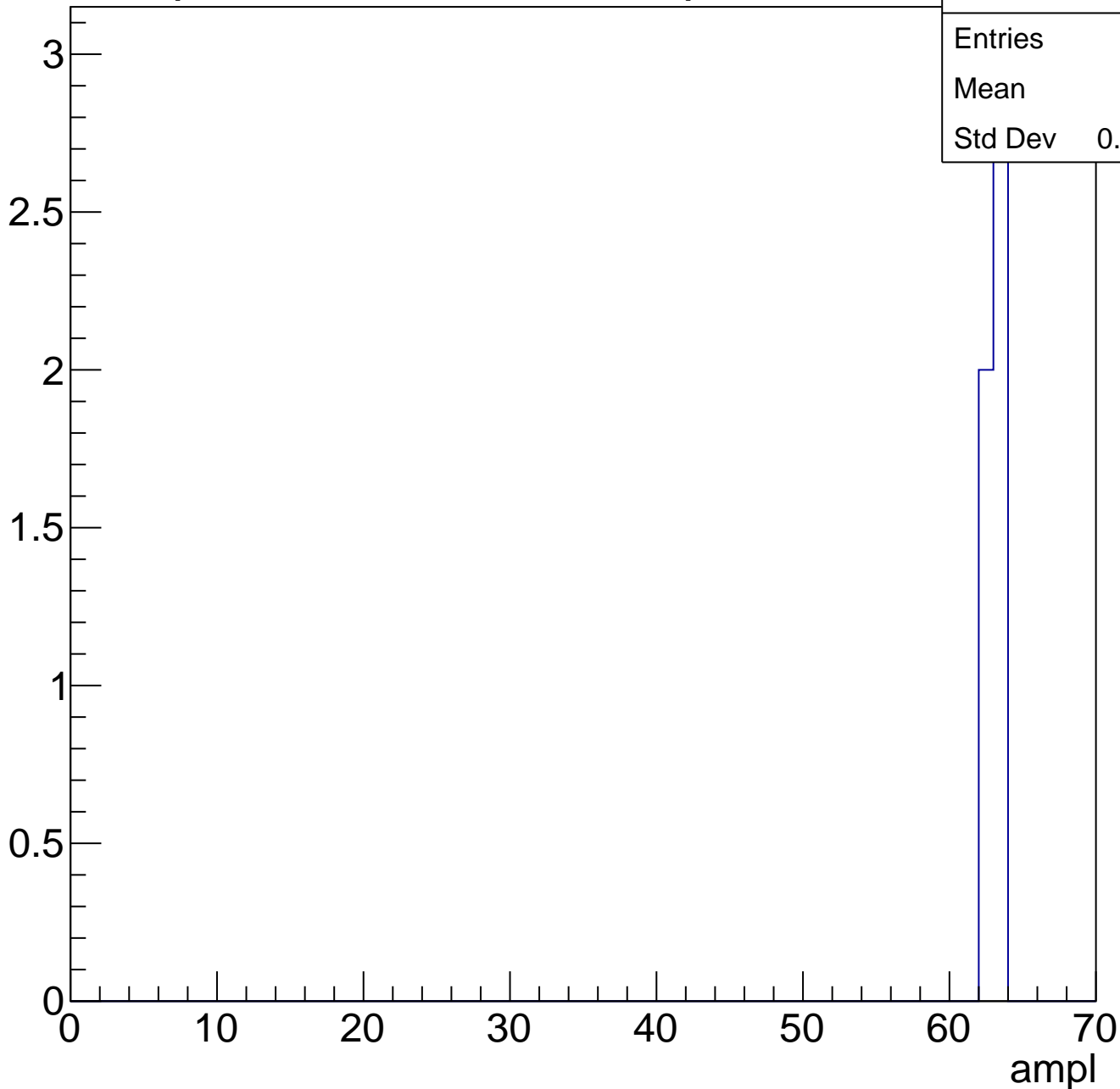
Entry



B1L103S, U14-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch7, adc0

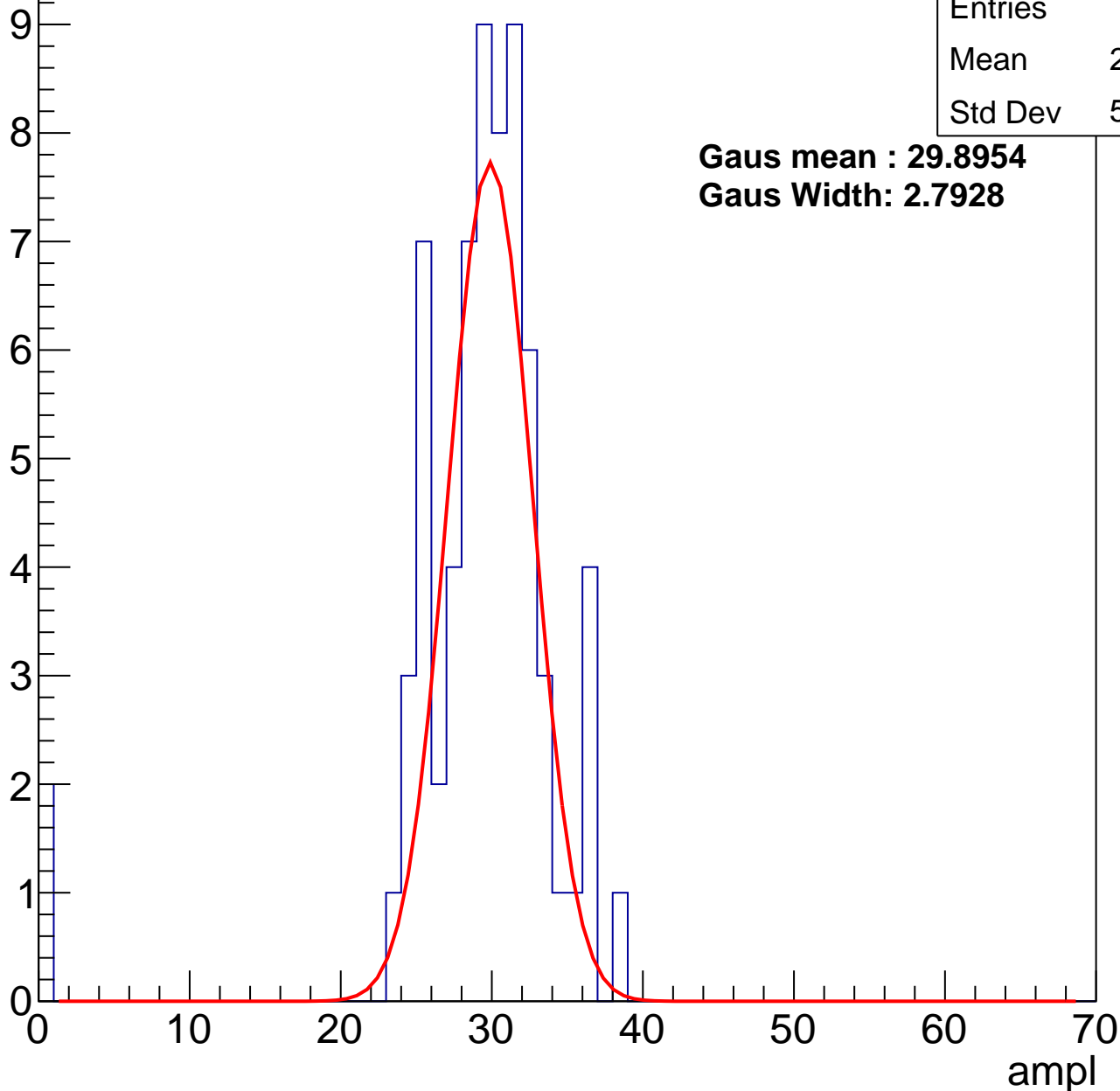
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.65
Std Dev	5.967

Gaus mean : 29.8954

Gaus Width: 2.7928



B1L103S, U14-ch7, adc1

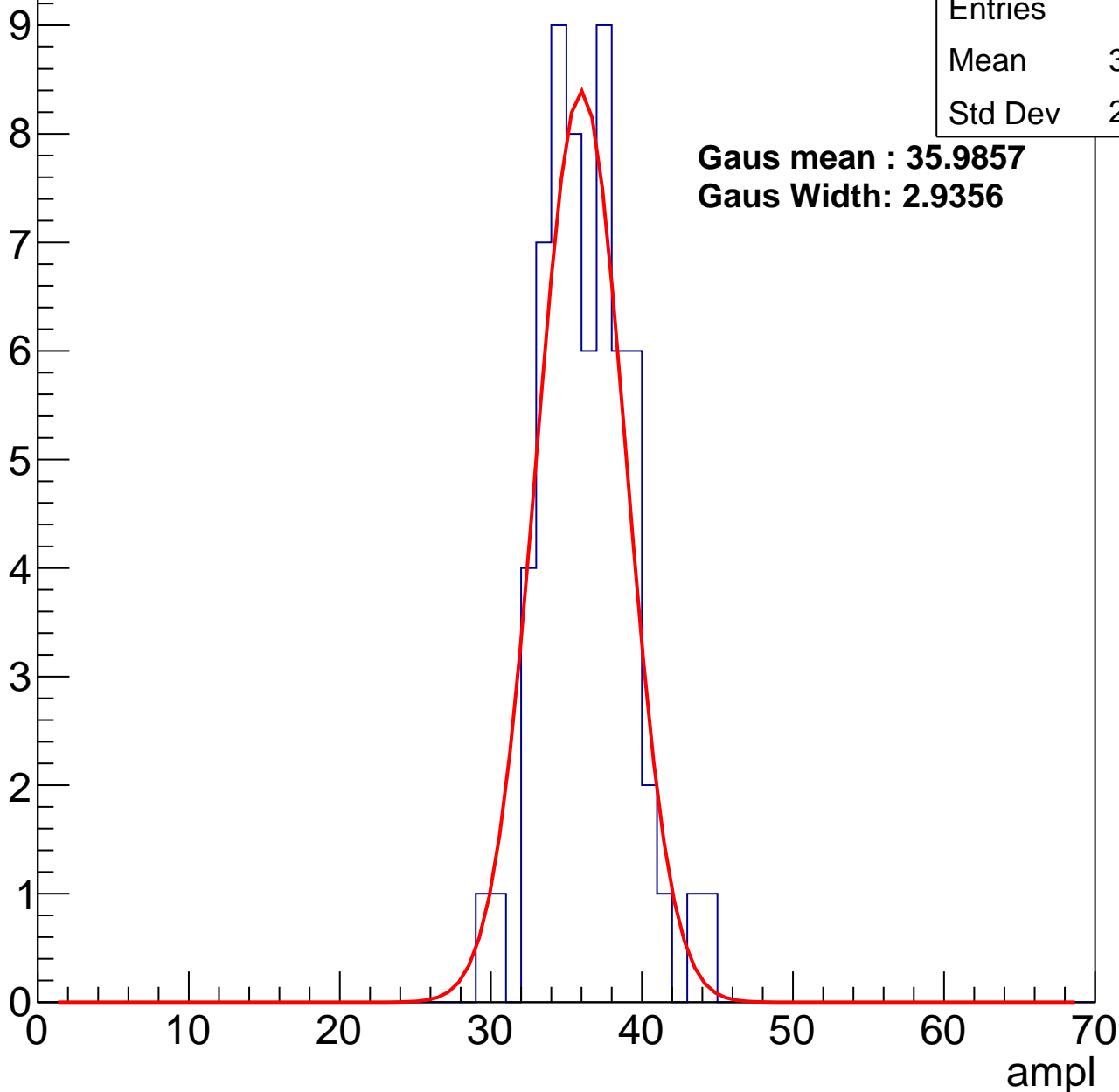
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.85
Std Dev	2.873

Gaus mean : 35.9857

Gaus Width: 2.9356



B1L103S, U14-ch7, adc2

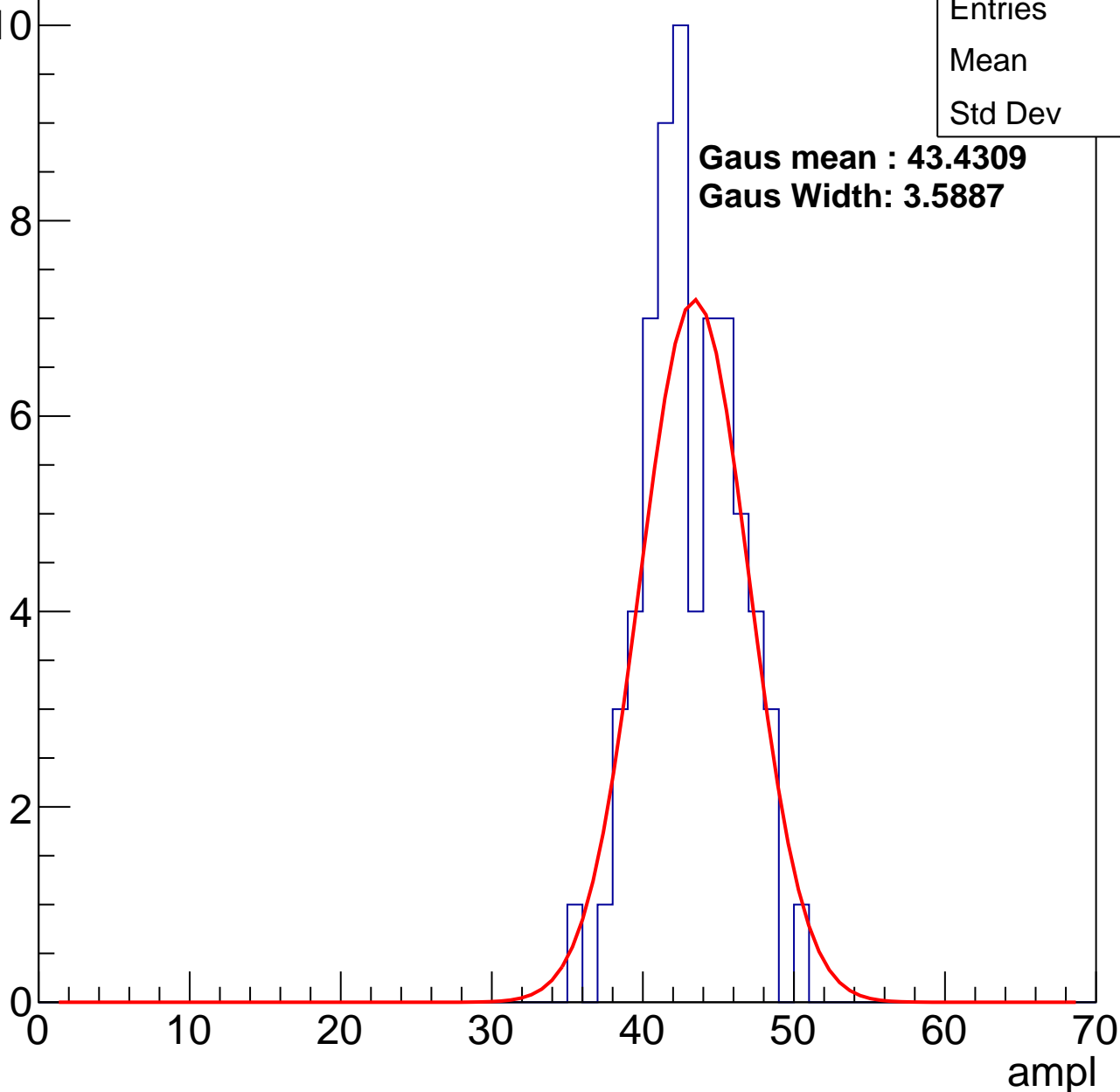
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.7
Std Dev	3.04

Gaus mean : 43.4309

Gaus Width: 3.5887

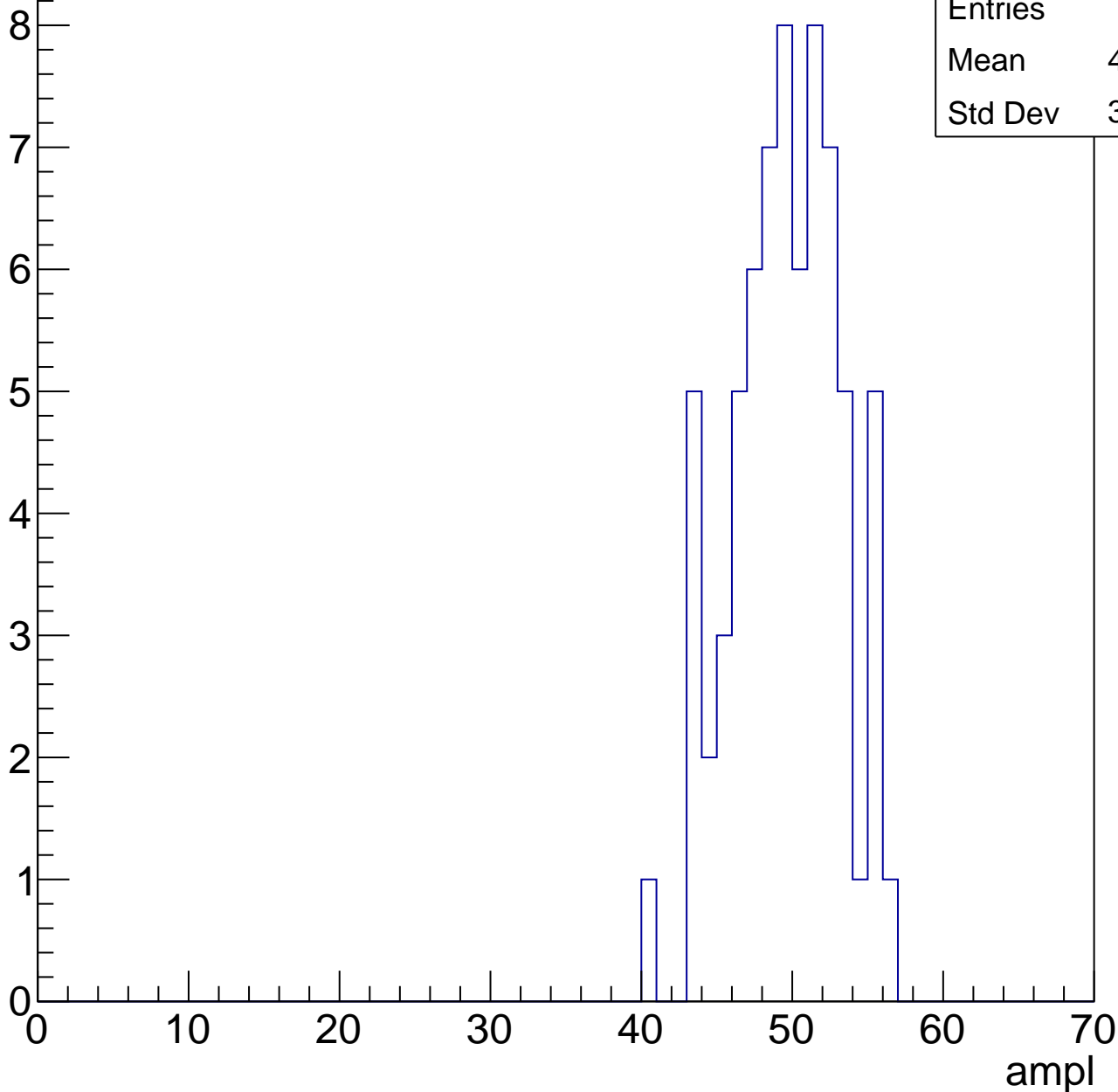


B1L103S, U14-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	49.14
Std Dev	3.523

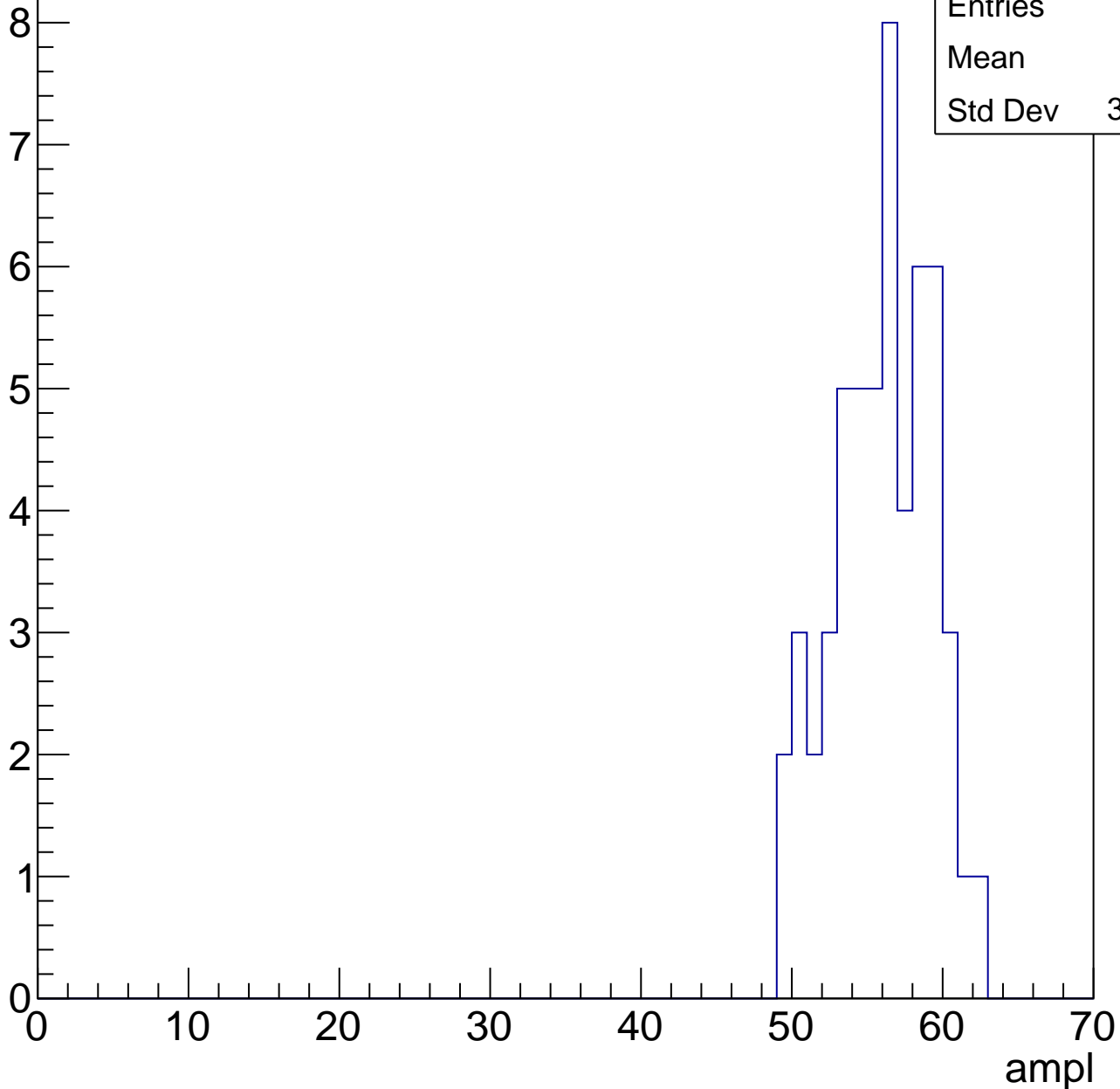


B1L103S, U14-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.5
Std Dev	3.196

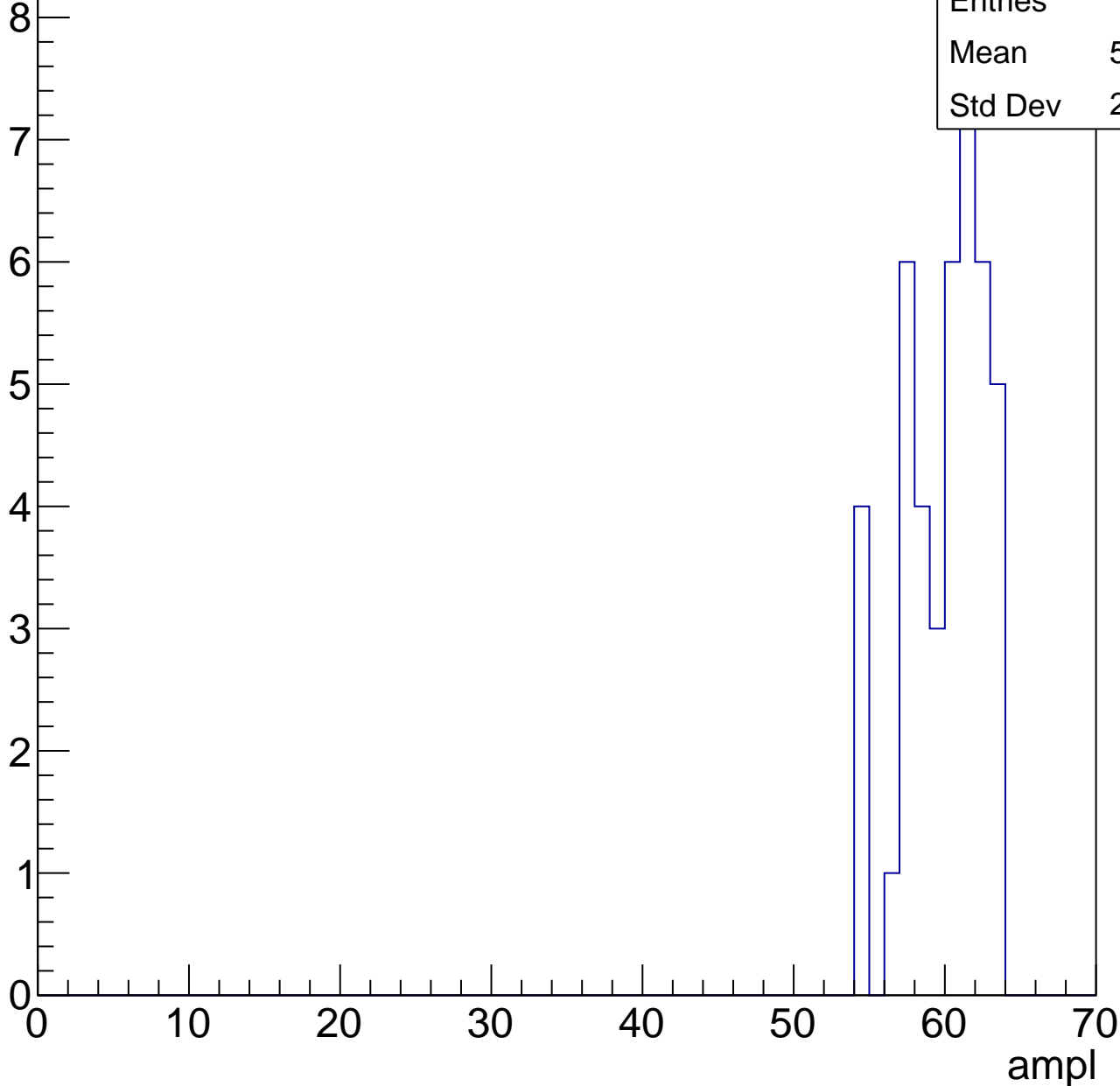


B1L103S, U14-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	59.49
Std Dev	2.636

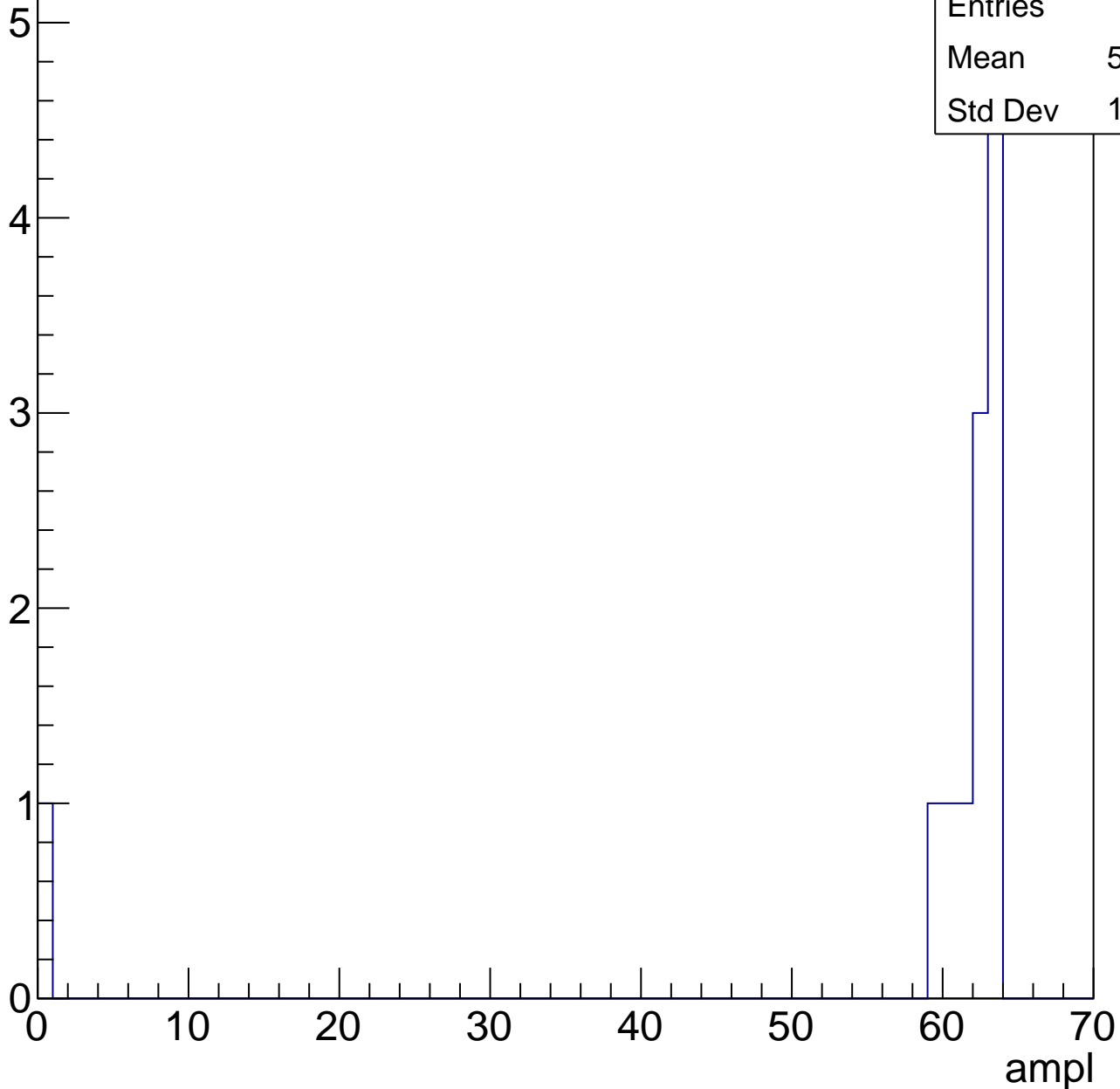


B1L103S, U14-ch7, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	56.75
Std Dev	17.16



B1L103S, U14-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch8, adc0

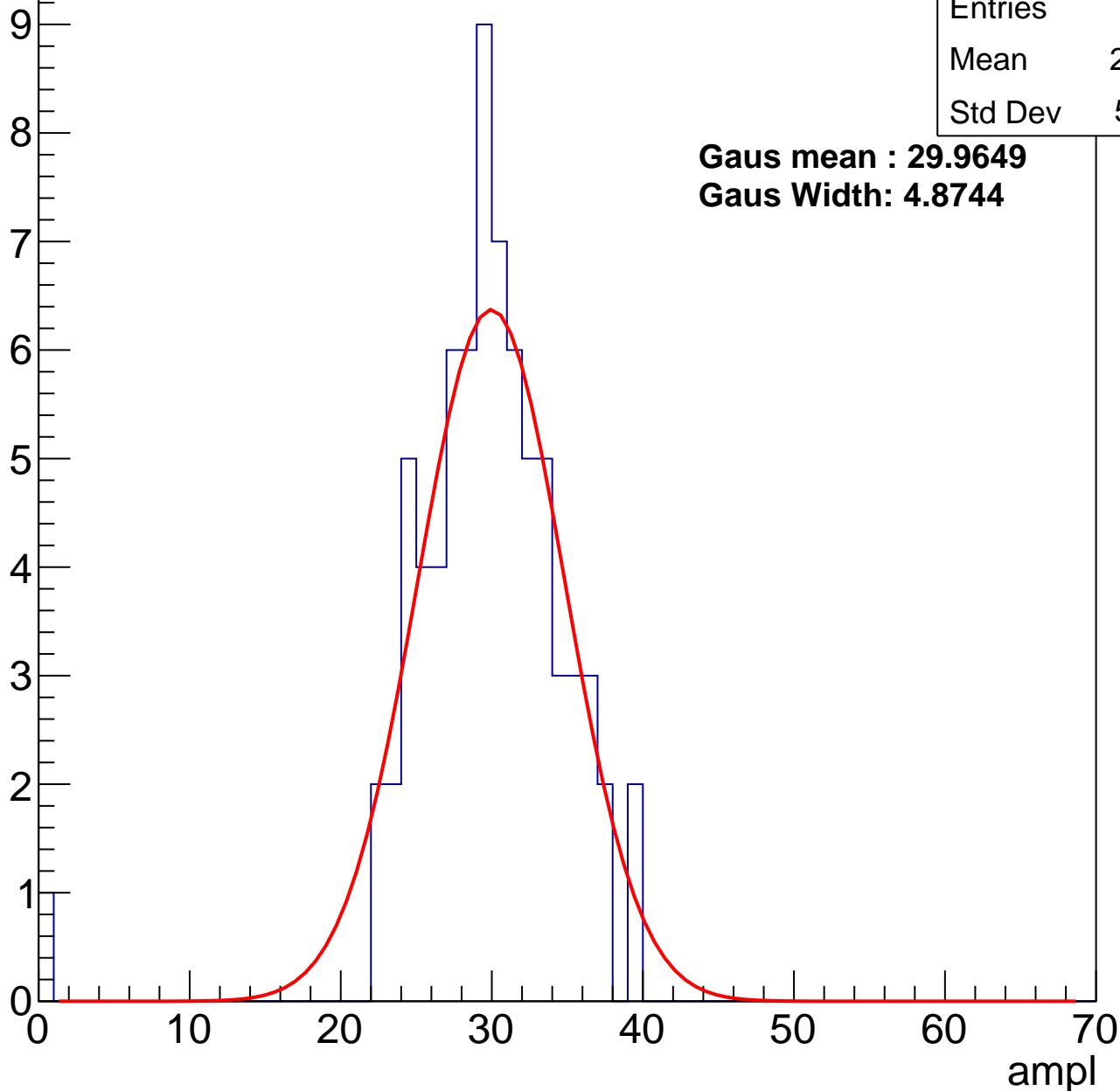
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	29.24
Std Dev	5.261

Gaus mean : 29.9649

Gaus Width: 4.8744



B1L103S, U14-ch8, adc1

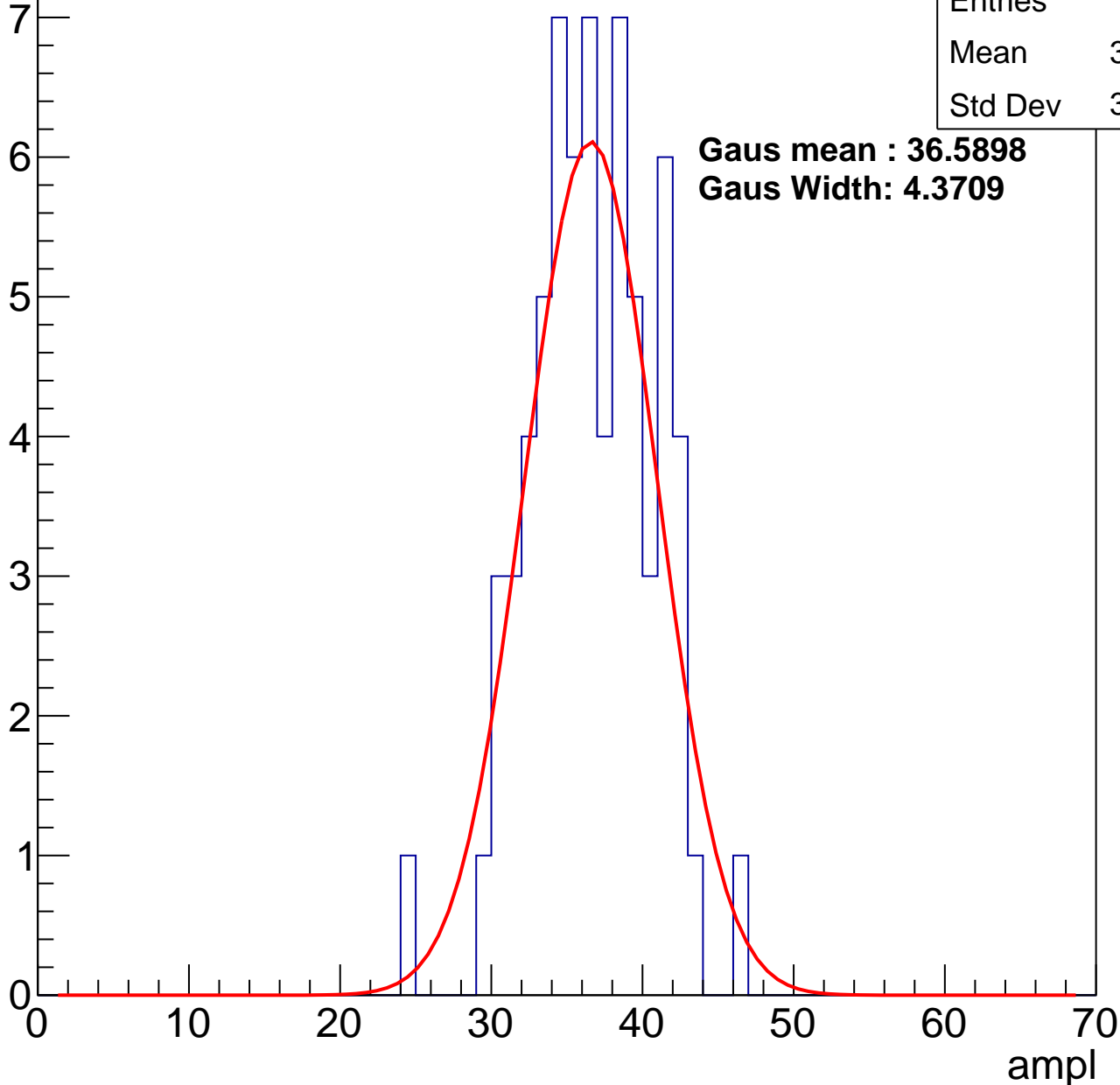
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.19
Std Dev	3.994

Gaus mean : 36.5898

Gaus Width: 4.3709



B1L103S, U14-ch8, adc2

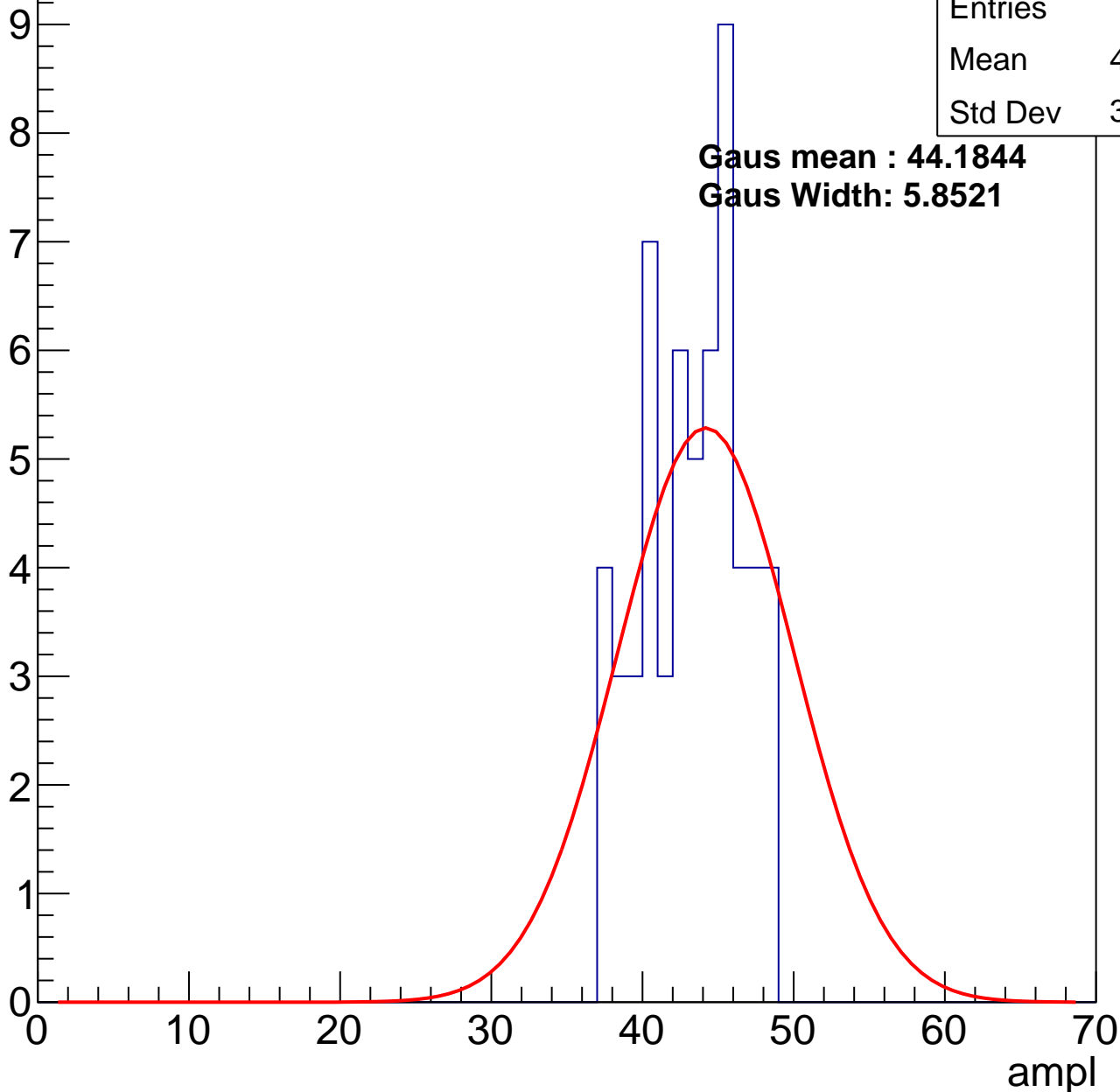
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.79
Std Dev	3.183

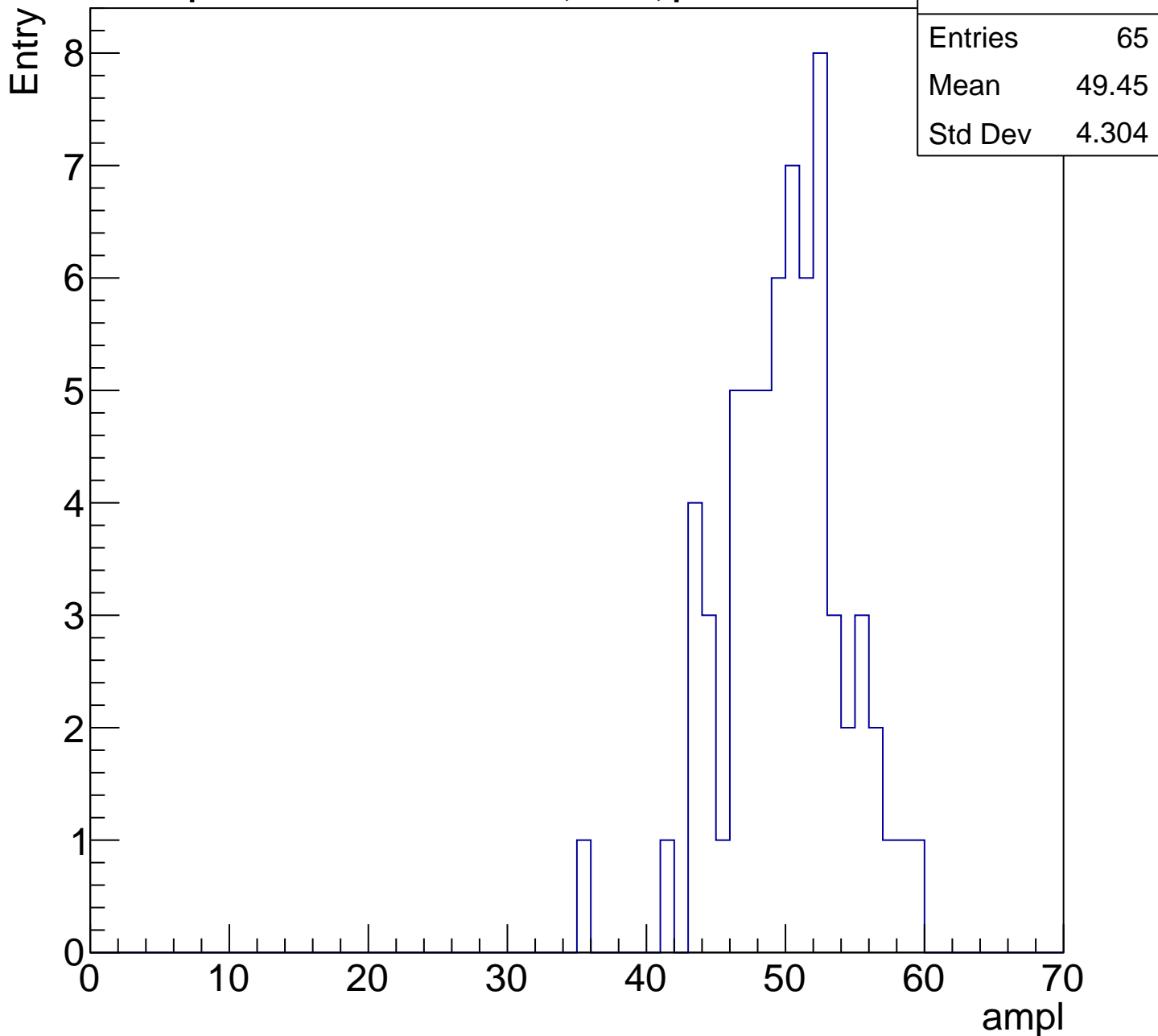
Gaus mean : 44.1844

Gaus Width: 5.8521



B1L103S, U14-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2

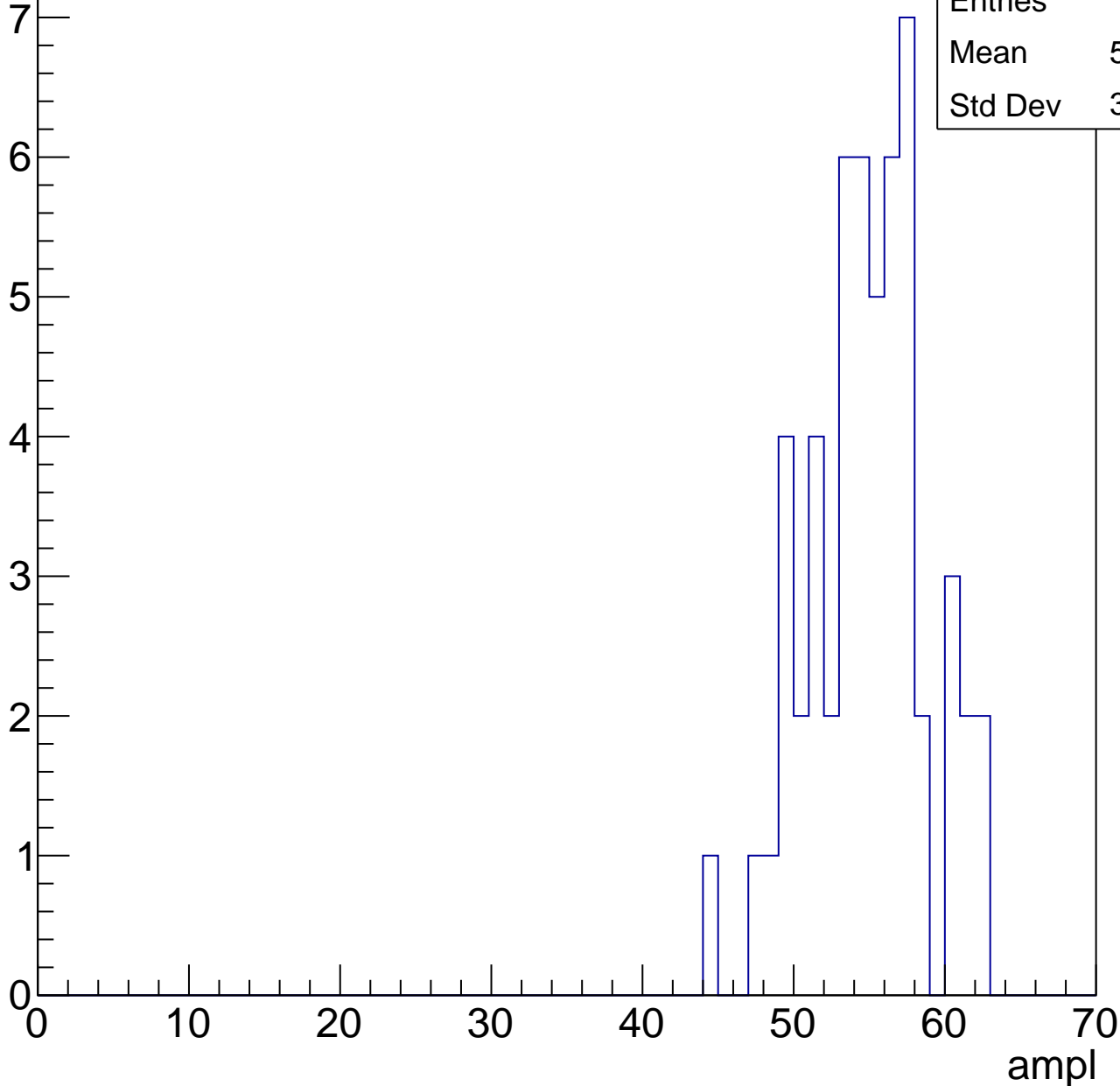


B1L103S, U14-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2

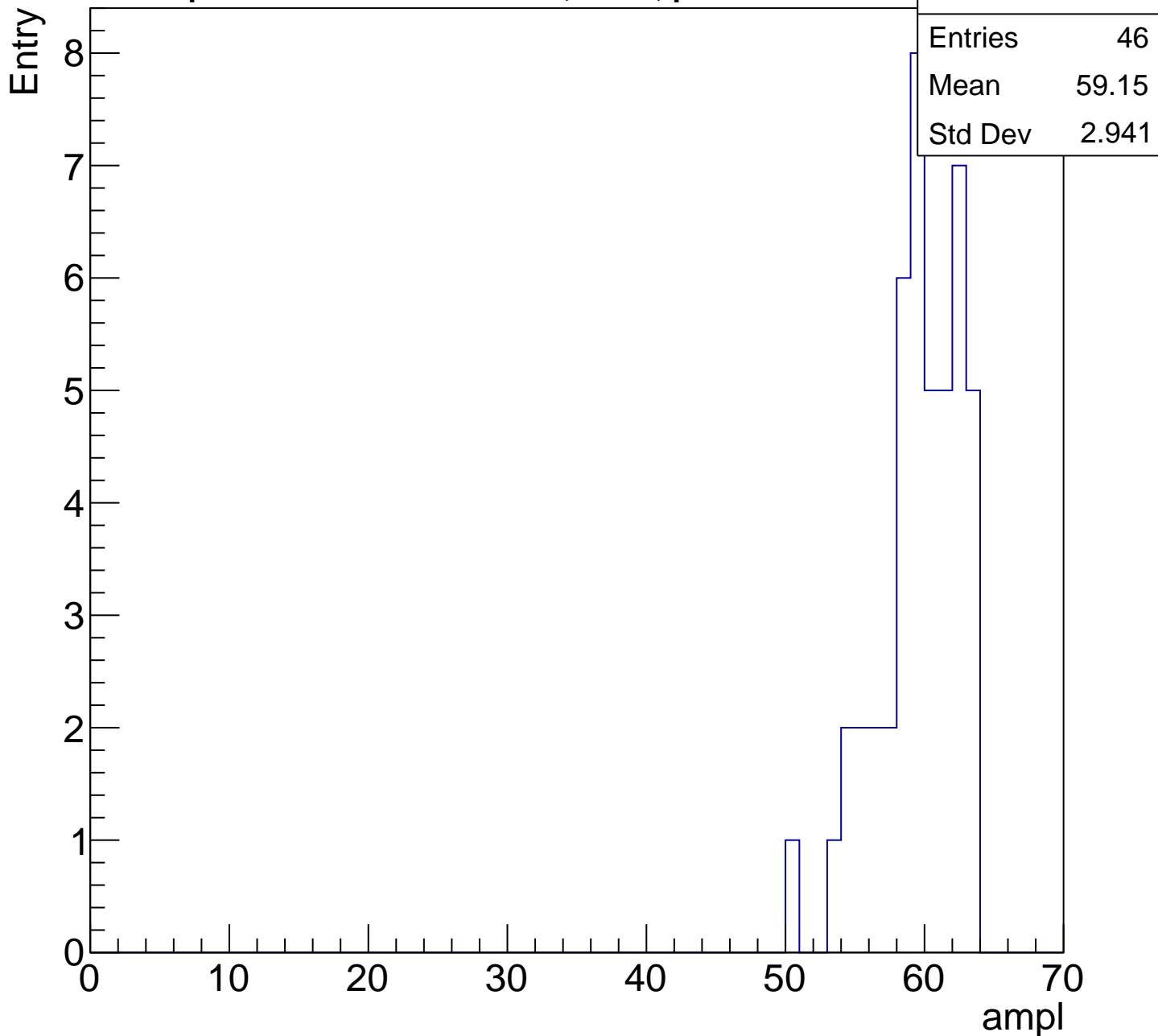
Entry

Entries	54
Mean	54.39
Std Dev	3.889



B1L103S, U14-ch8, adc5

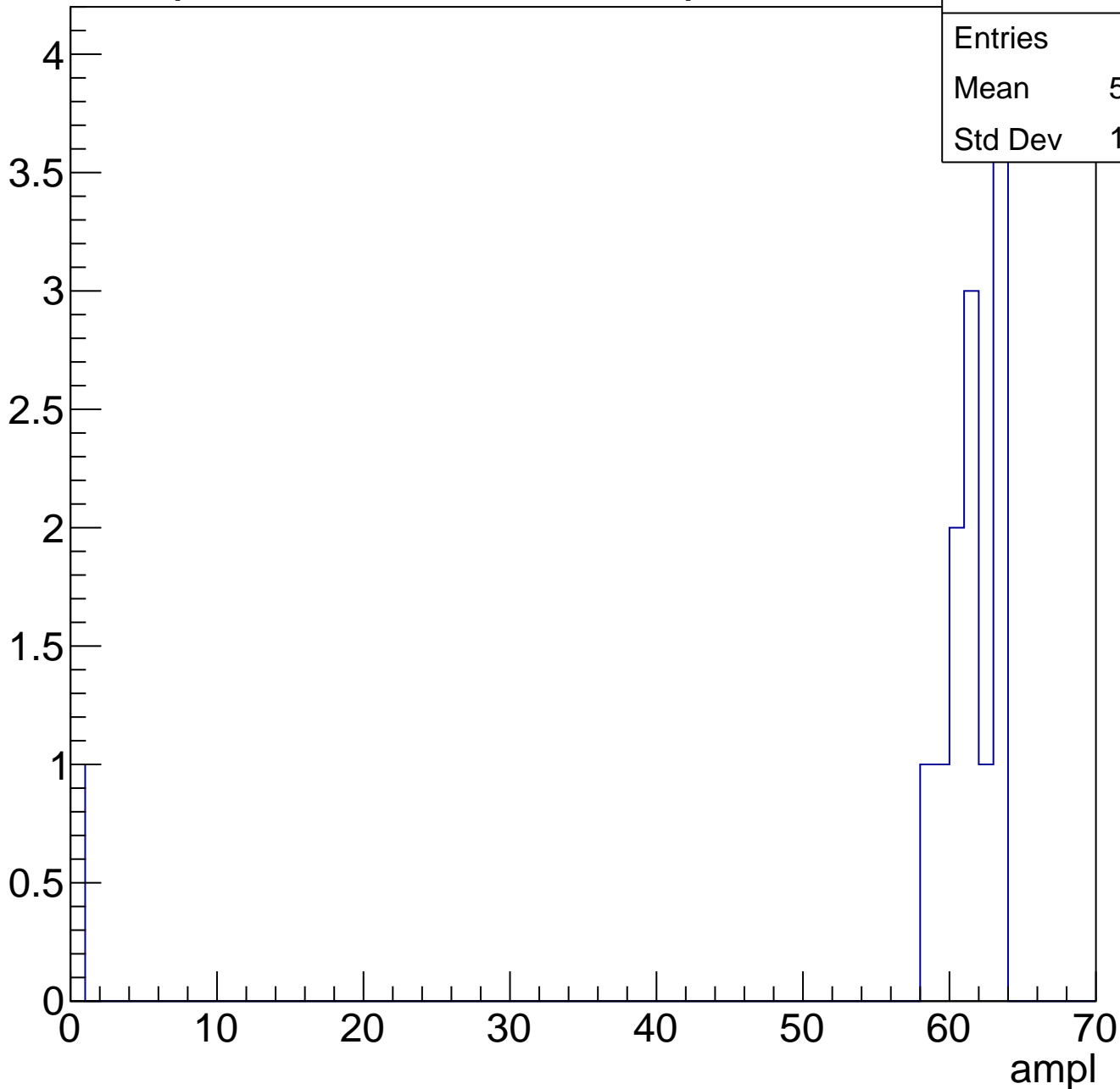
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U14-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch9, adc0

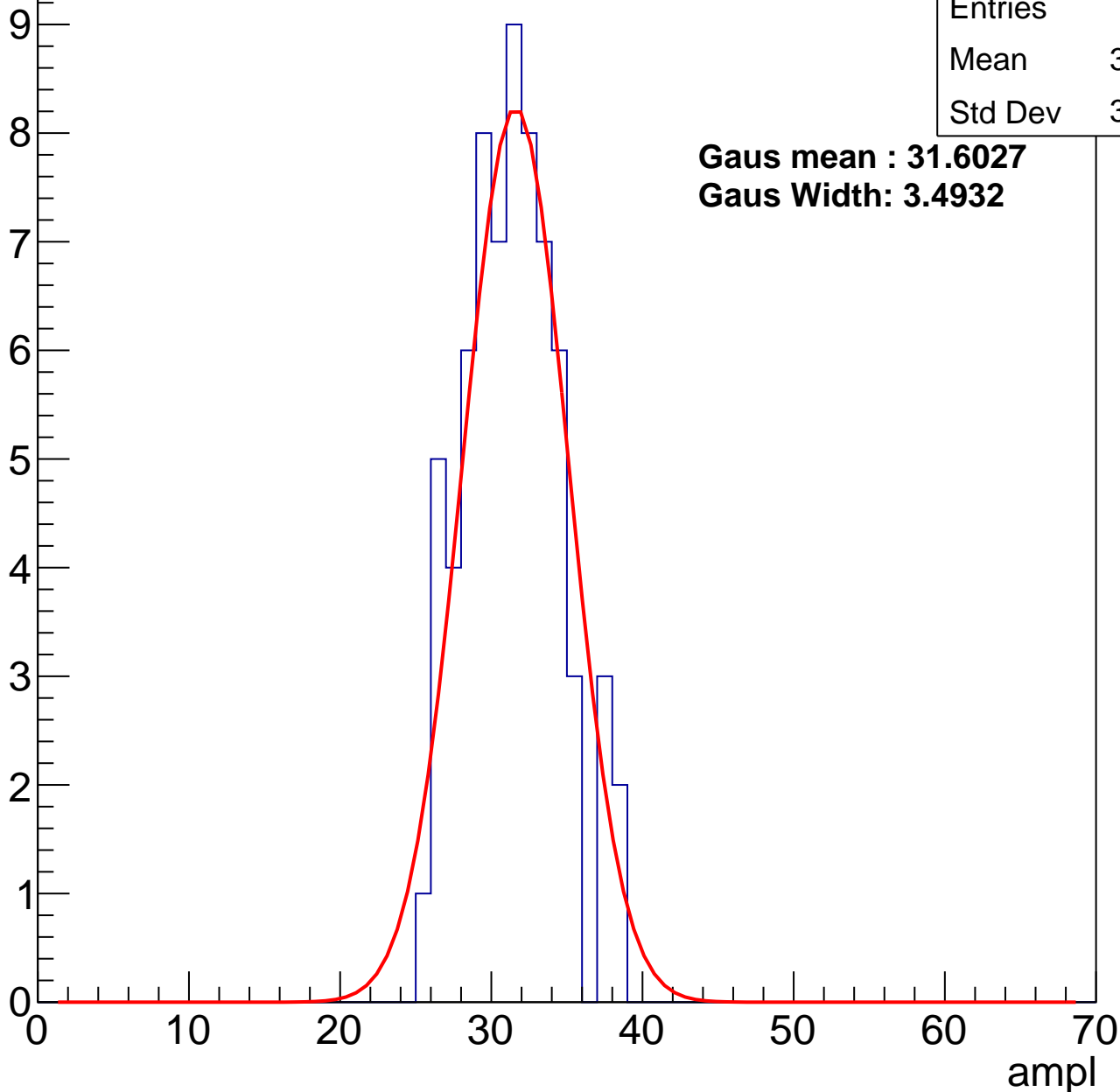
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	30.94
Std Dev	3.097

Gaus mean : 31.6027

Gaus Width: 3.4932



B1L103S, U14-ch9, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	58
Mean	37.17
Std Dev	3.41

Gaus mean : 38.0663

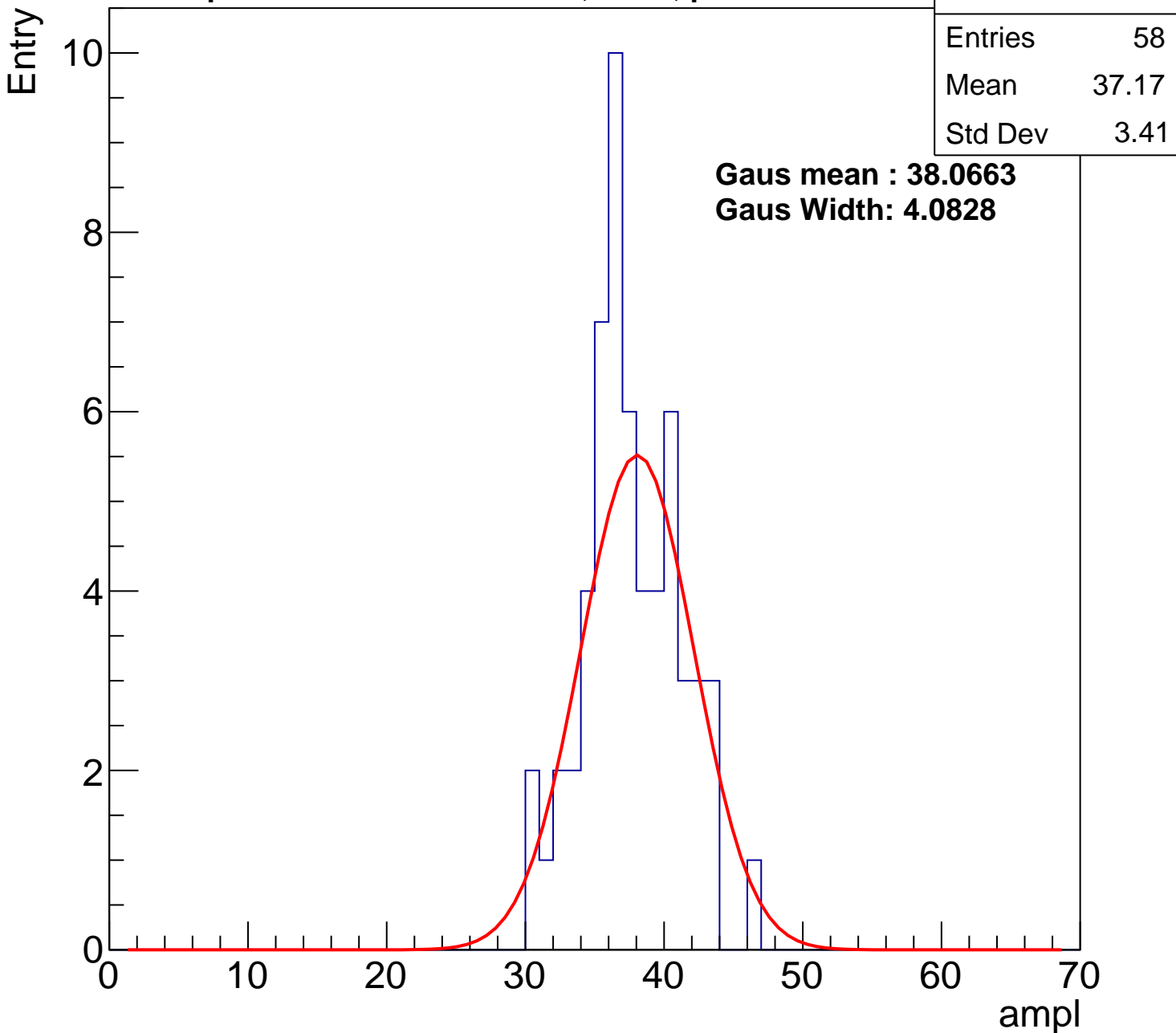
Gaus Width: 4.0828

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U14-ch9, adc2

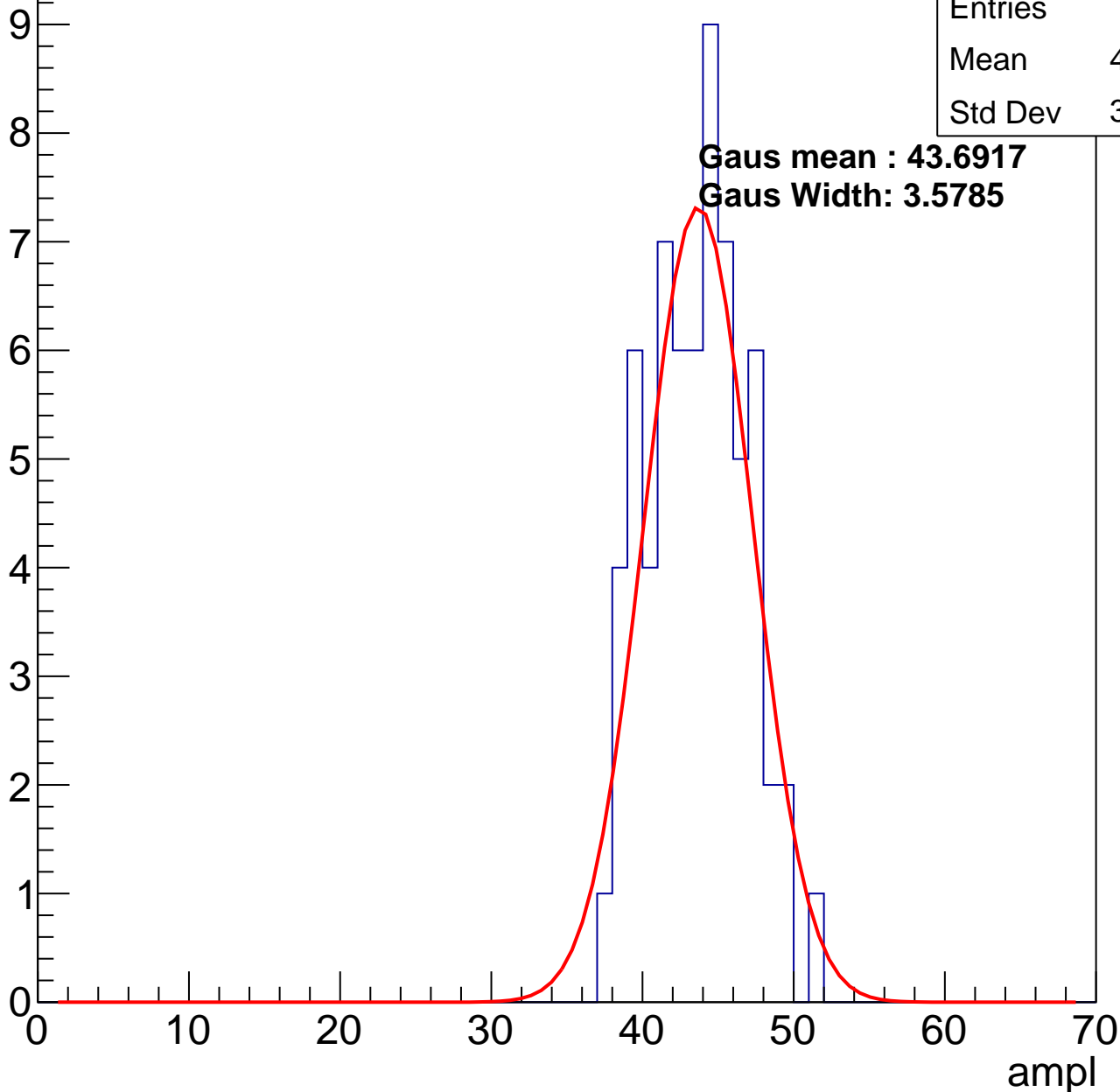
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	43.15
Std Dev	3.173

Gaus mean : 43.6917

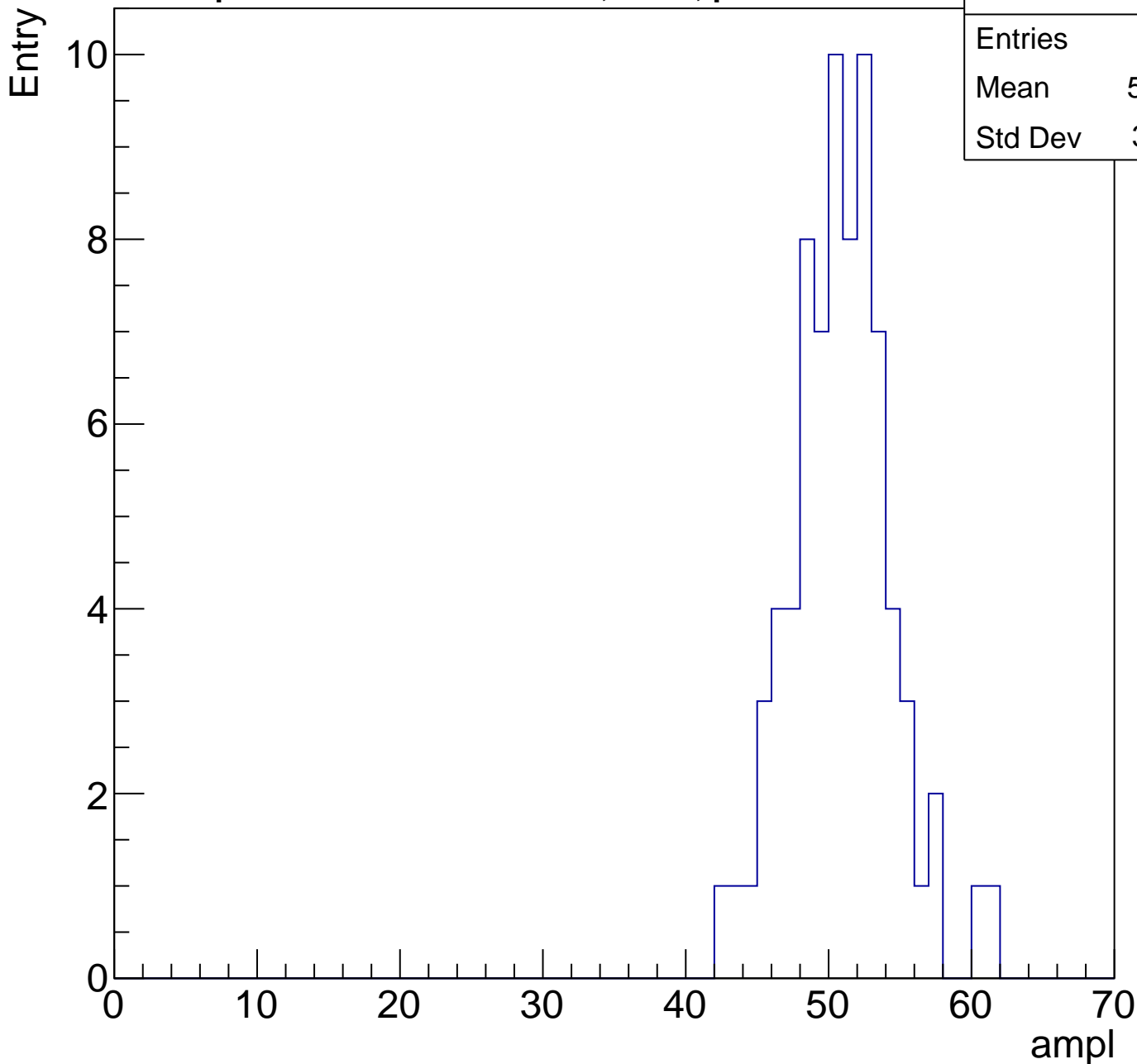
Gaus Width: 3.5785



B1L103S, U14-ch9, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	50.45
Std Dev	3.541

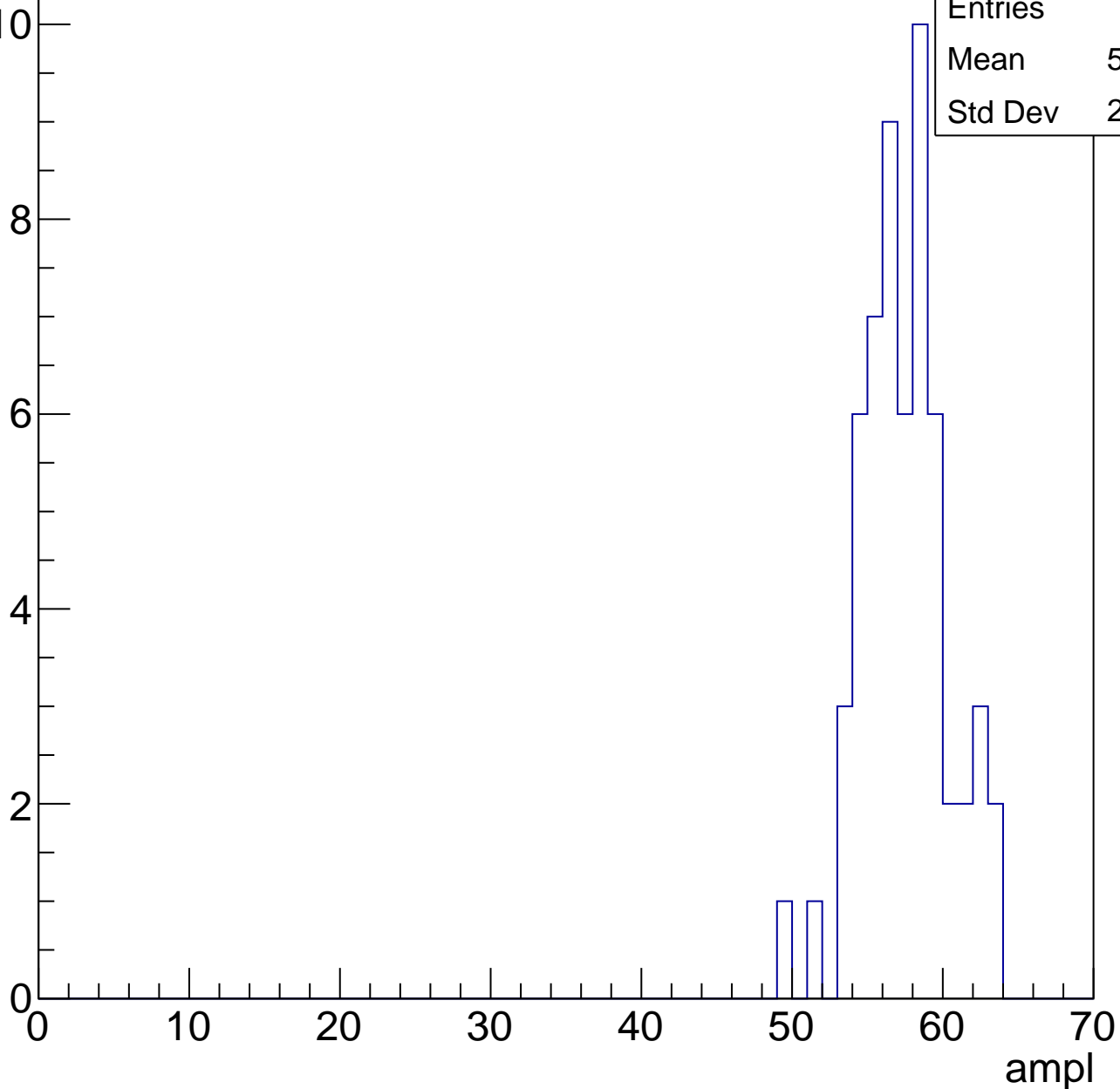


B1L103S, U14-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

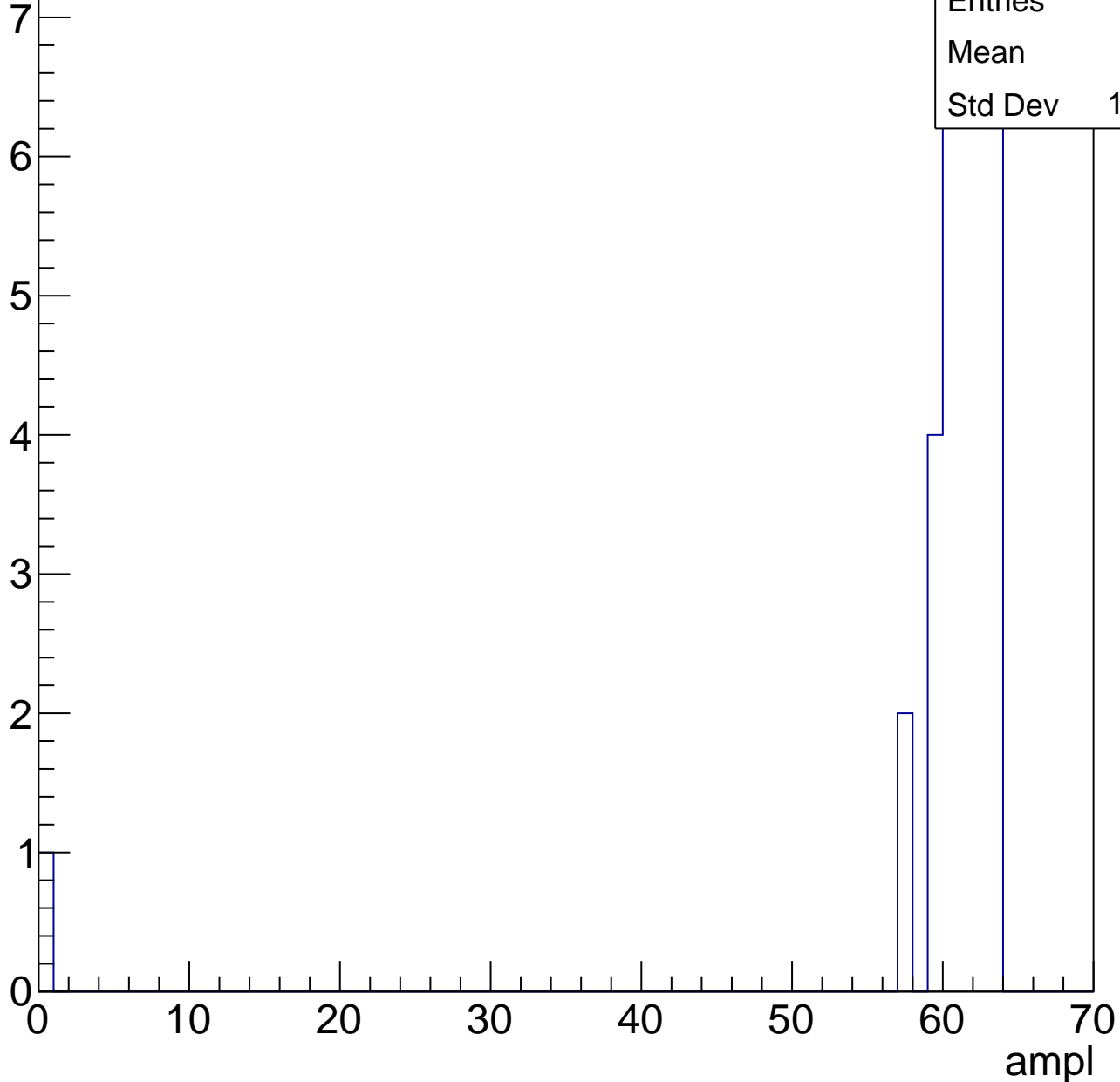
Entries	58
Mean	56.93
Std Dev	2.846



B1L103S, U14-ch9, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	25
Std Dev	0

B1L103S, U14-ch10, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	28.9
Std Dev	3.212

Gaus mean : 29.7249

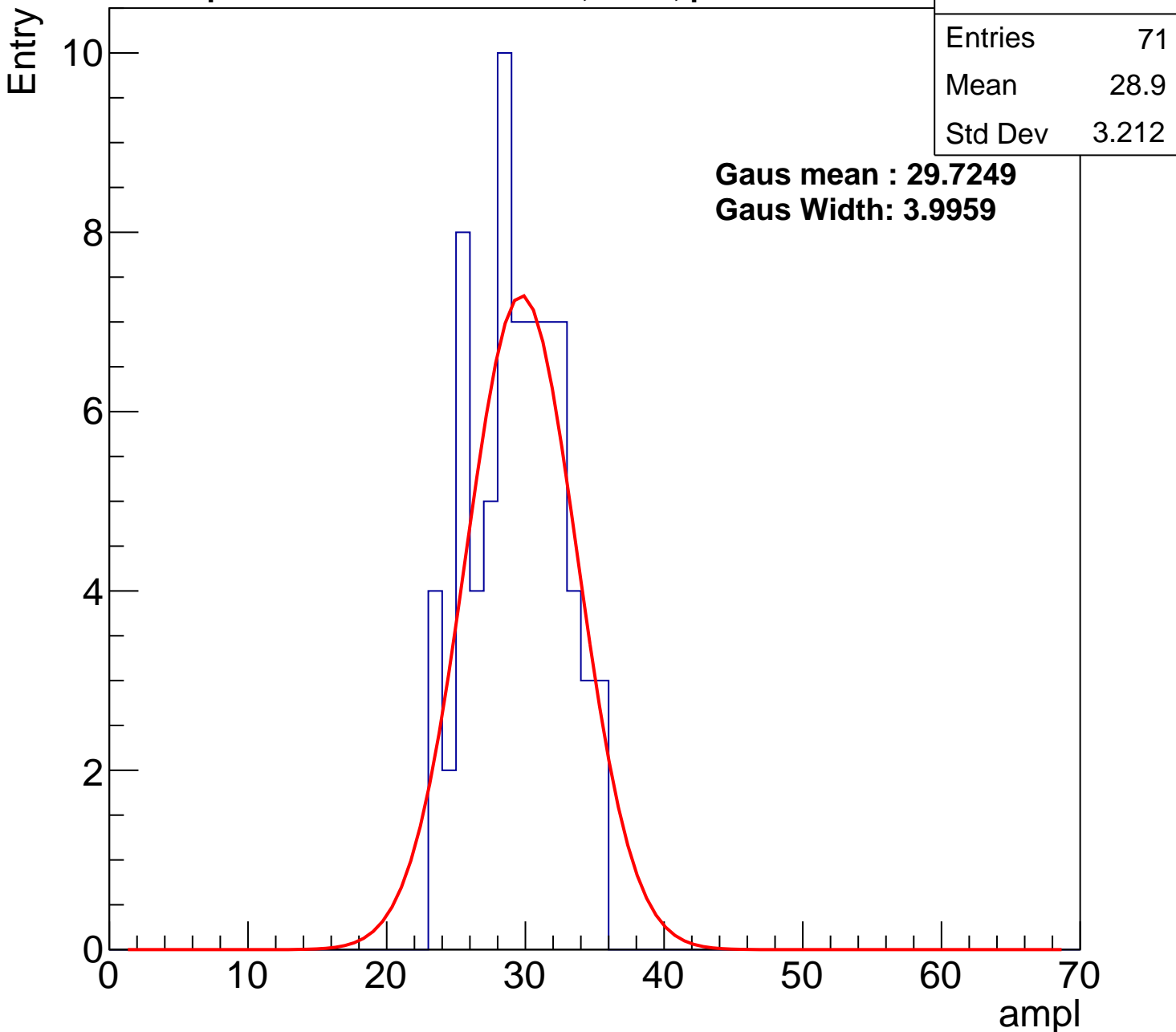
Gaus Width: 3.9959

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U14-ch10, adc1

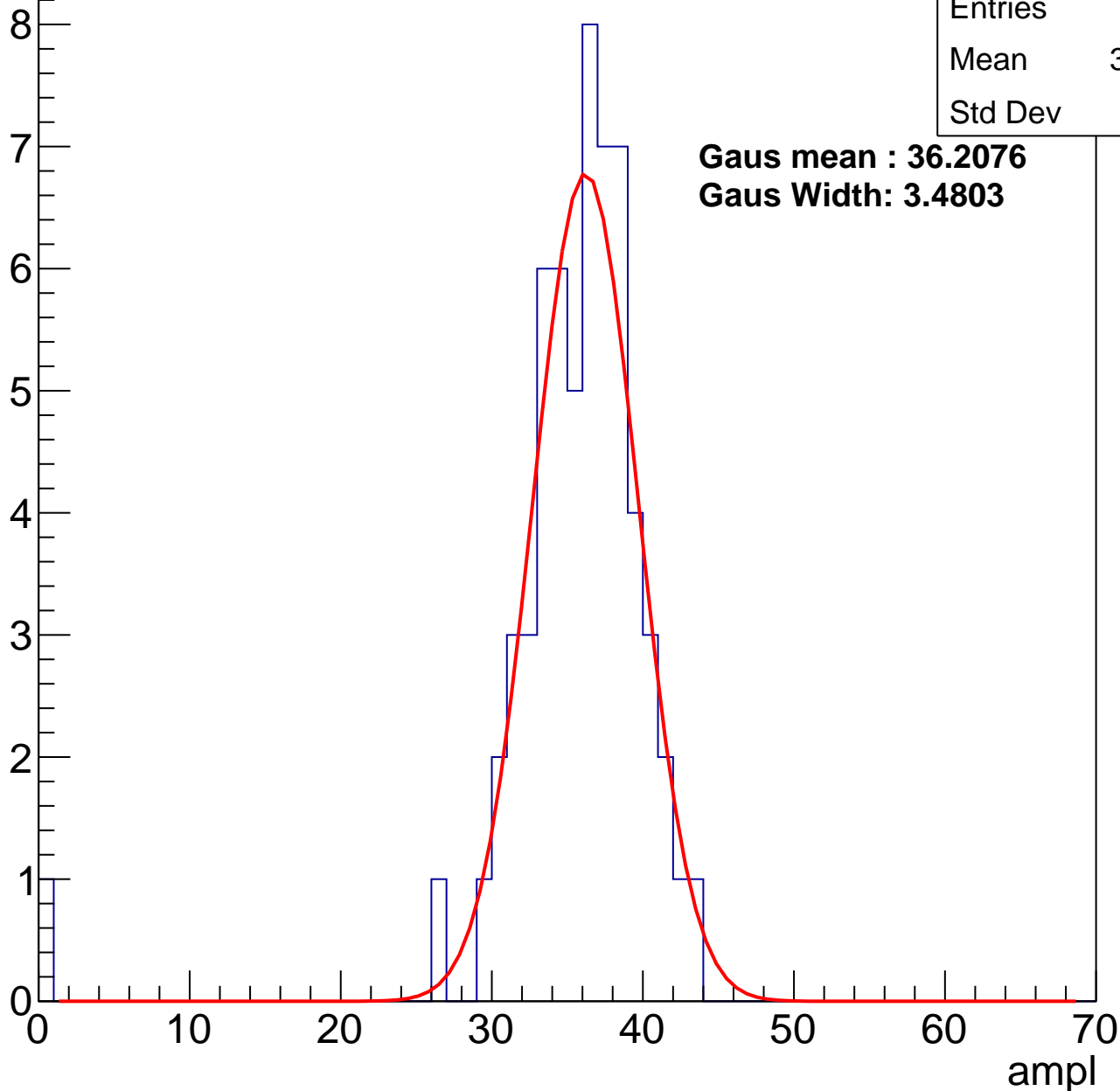
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.03
Std Dev	5.61

Gaus mean : 36.2076

Gaus Width: 3.4803



B1L103S, U14-ch10, adc2

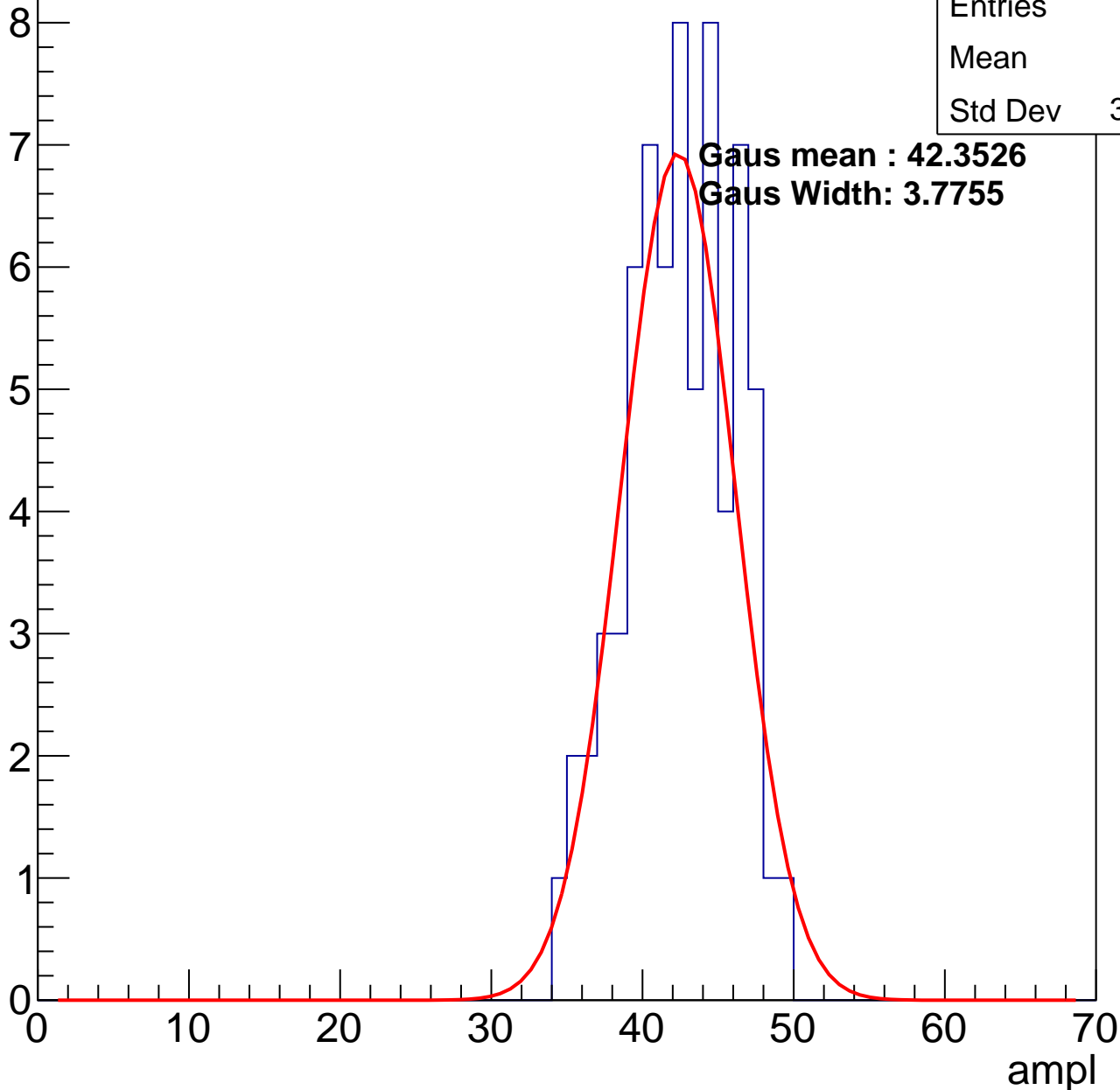
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	42
Std Dev	3.489

Gaus mean : 42.3526

Gaus Width: 3.7755

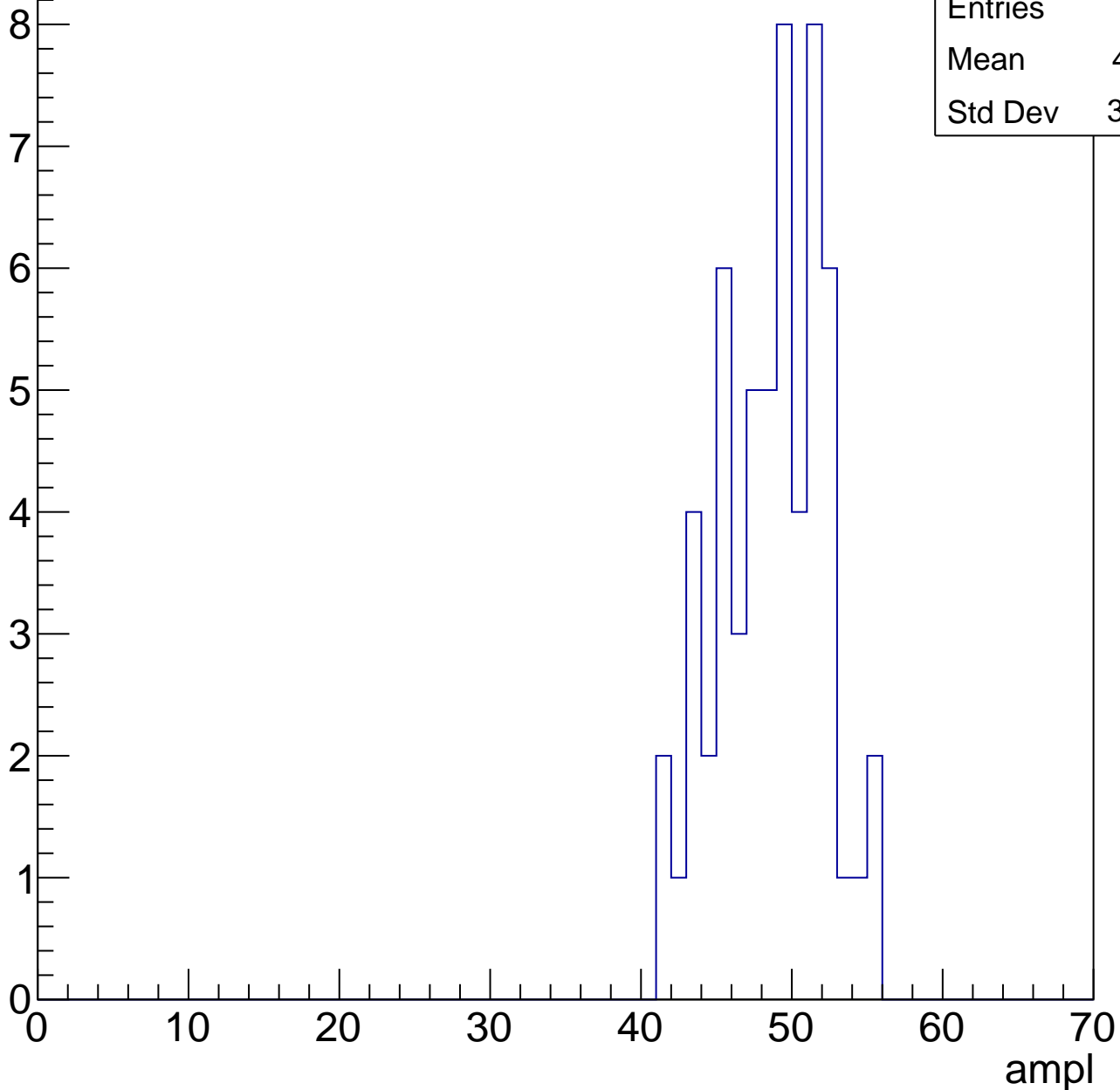


B1L103S, U14-ch10, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	48.21
Std Dev	3.438

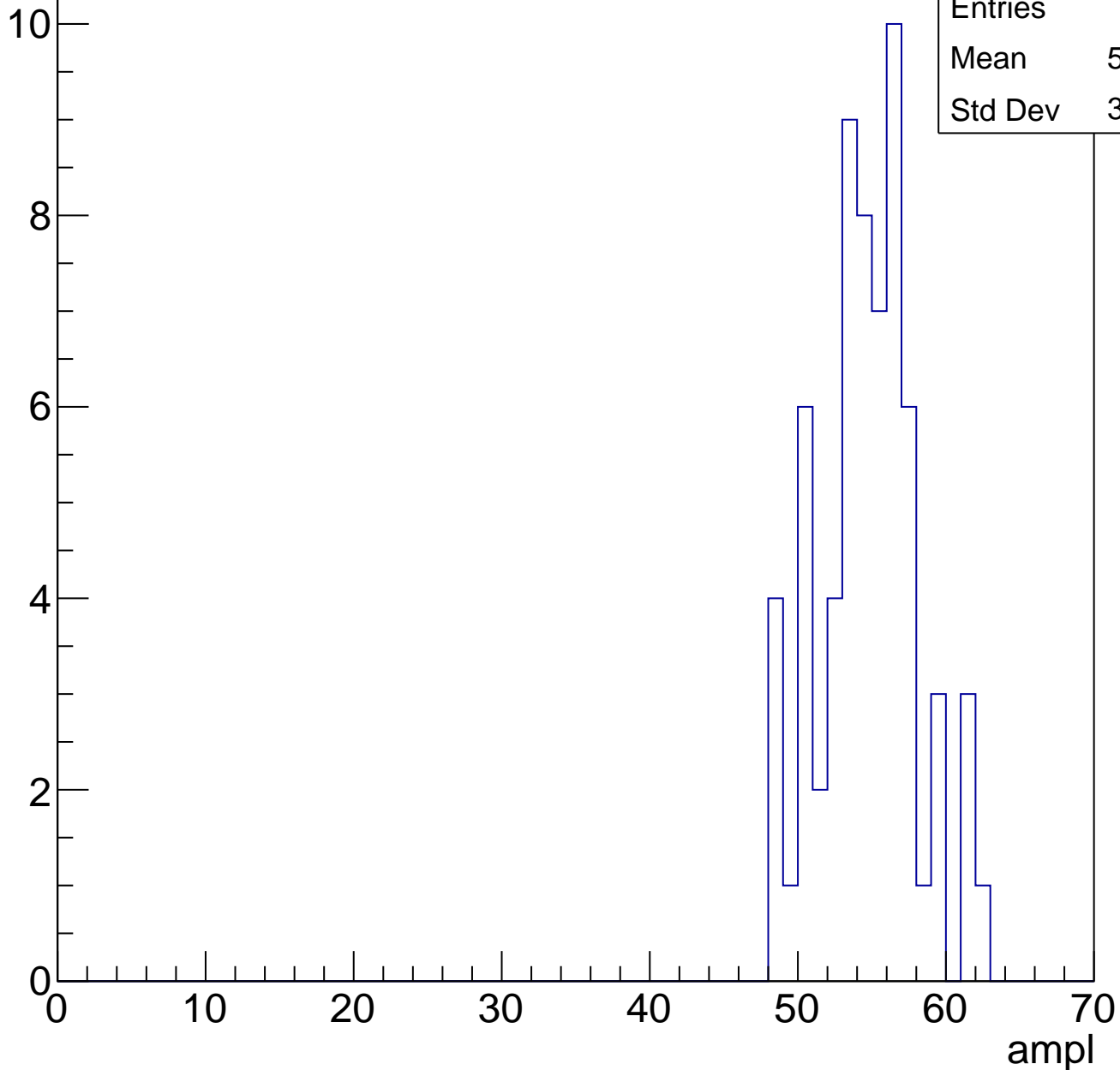


B1L103S, U14-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	65
Mean	54.26
Std Dev	3.297

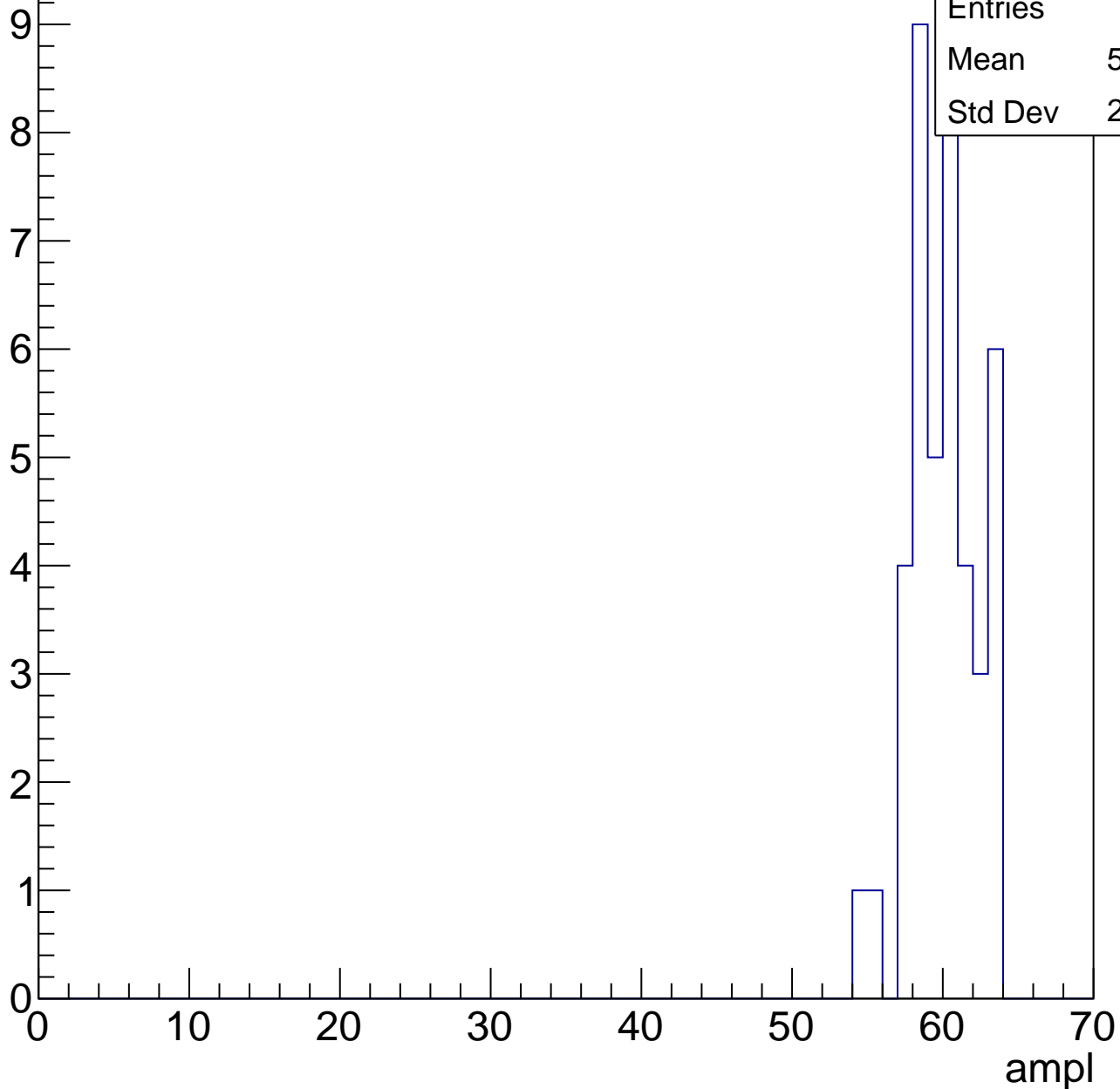
Entry



B1L103S, U14-ch10, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

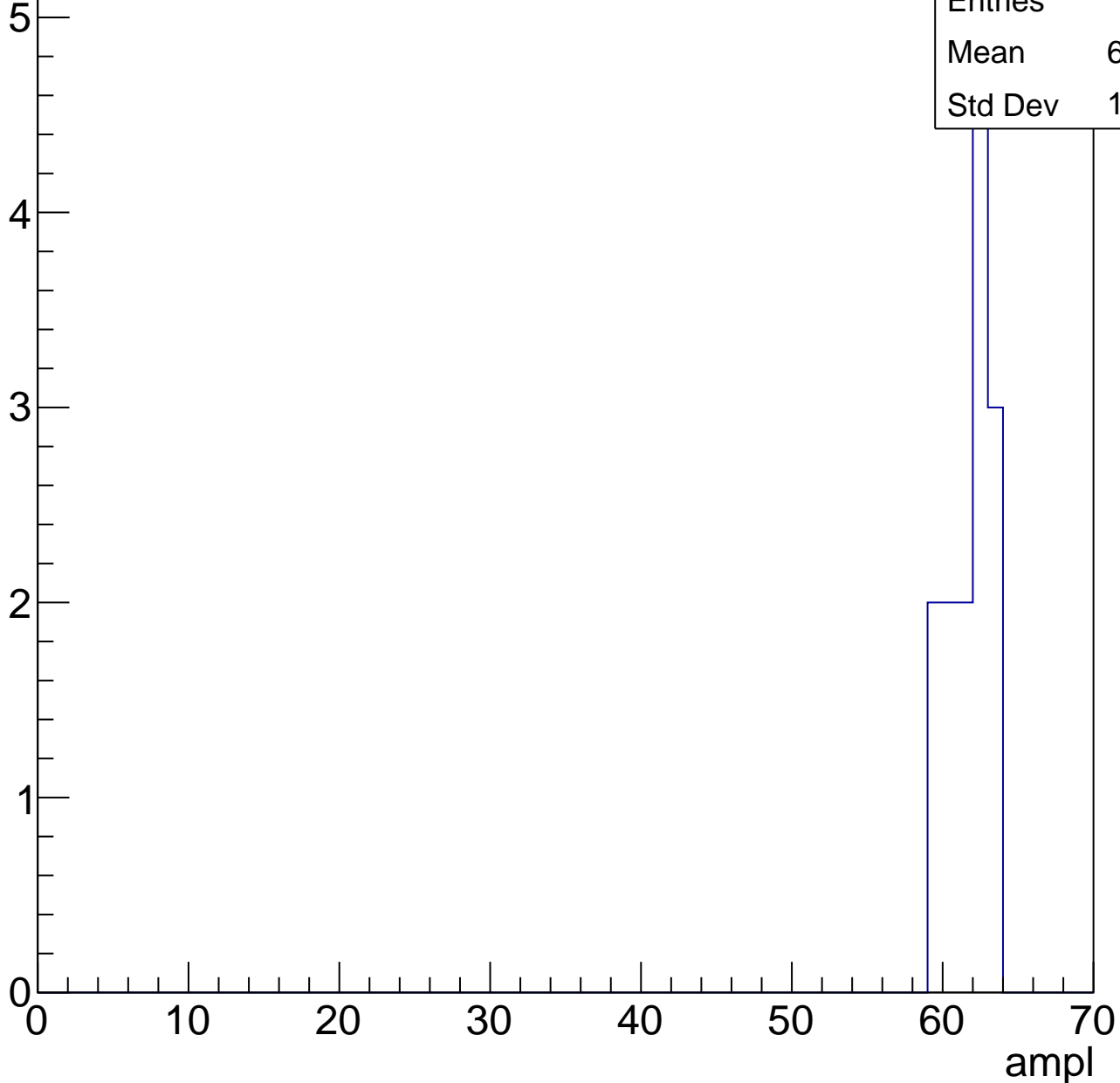


B1L103S, U14-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

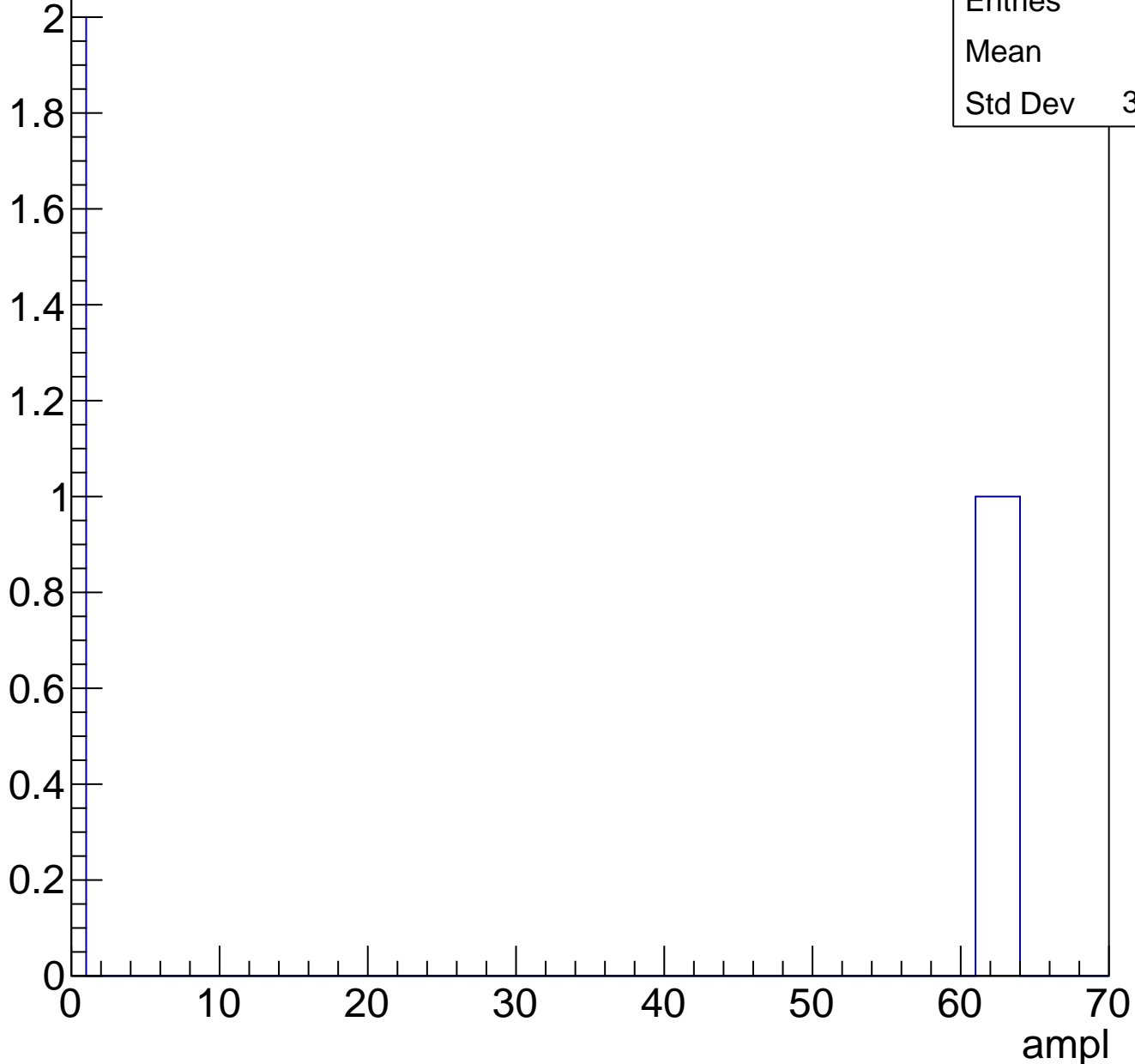
Entries	14
Mean	61.36
Std Dev	1.342



B1L103S, U14-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	37.2
Std Dev	30.38

B1L103S, U14-ch11, adc0

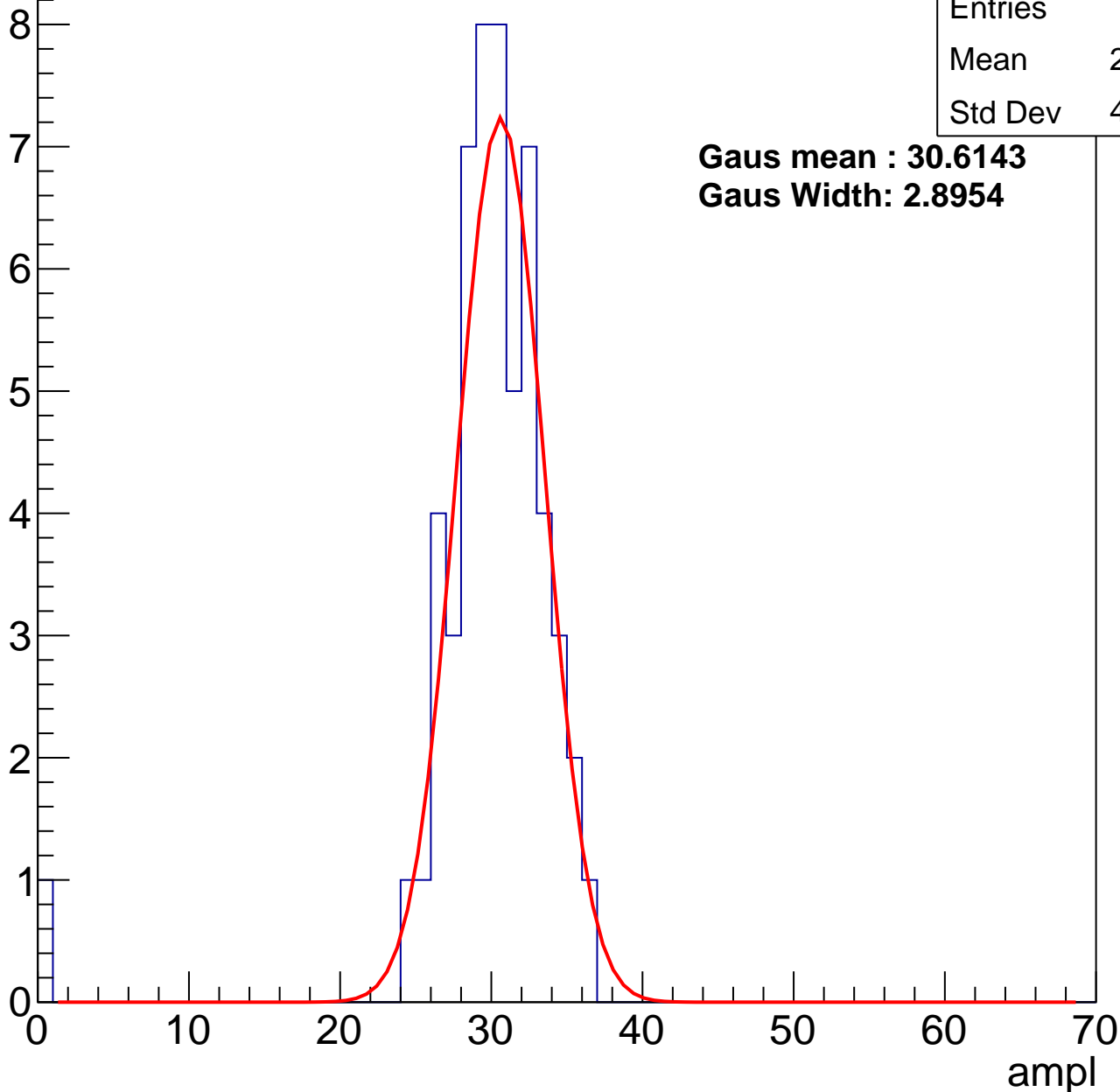
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	29.47
Std Dev	4.816

Gaus mean : 30.6143

Gaus Width: 2.8954



B1L103S, U14-ch11, adc1

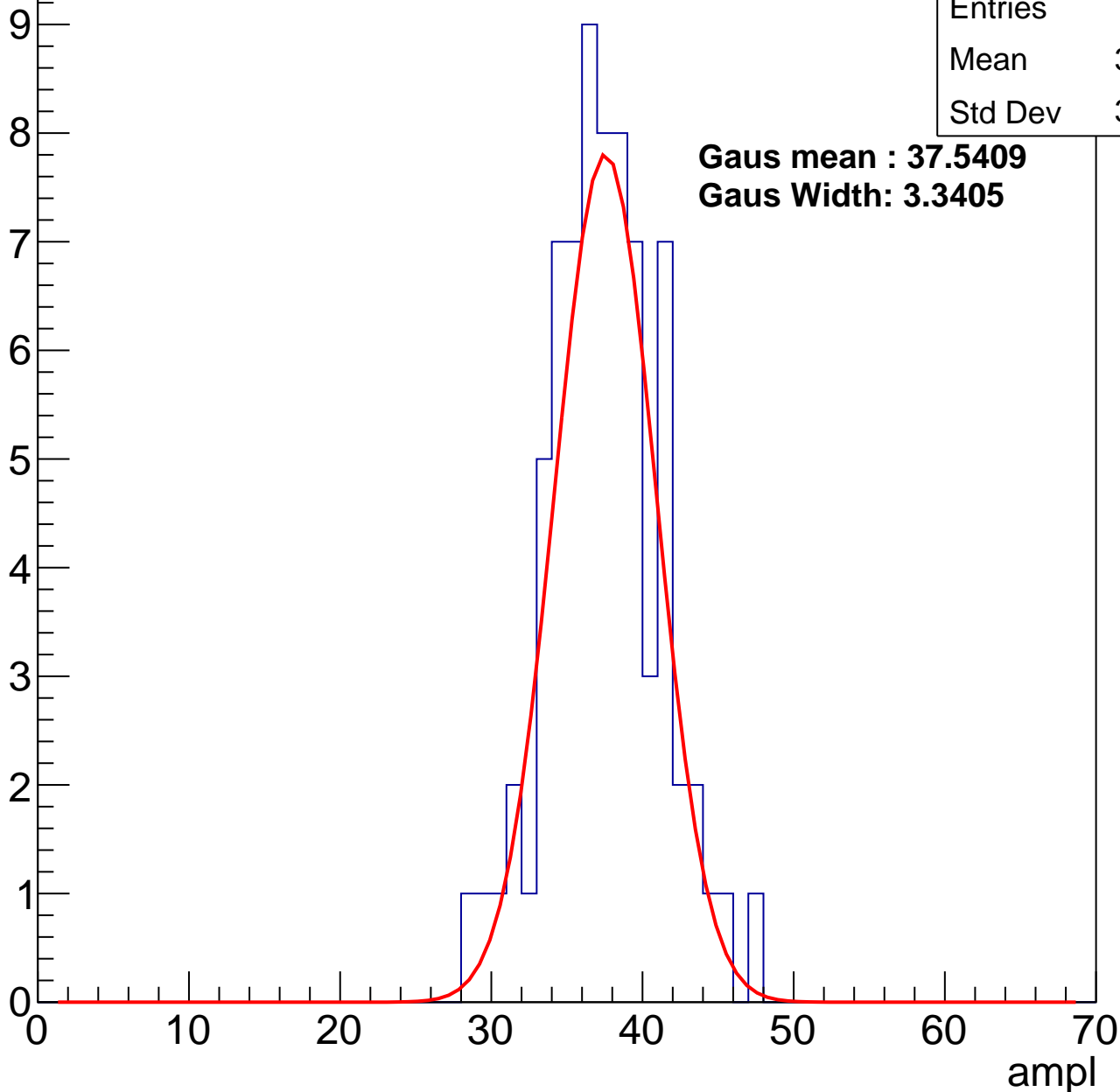
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	37.01
Std Dev	3.641

Gaus mean : 37.5409

Gaus Width: 3.3405

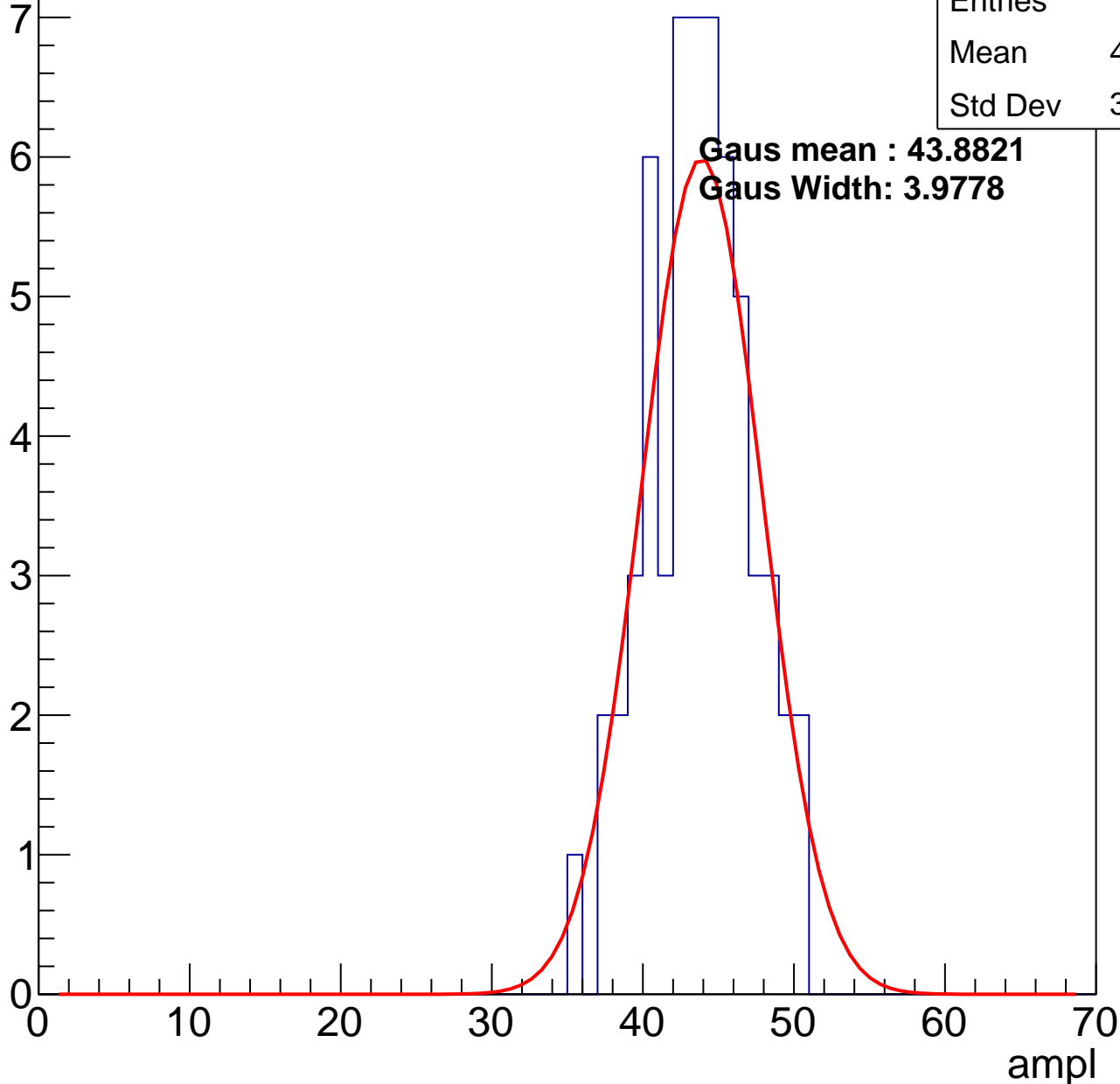


B1L103S, U14-ch11, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.24
Std Dev	3.377

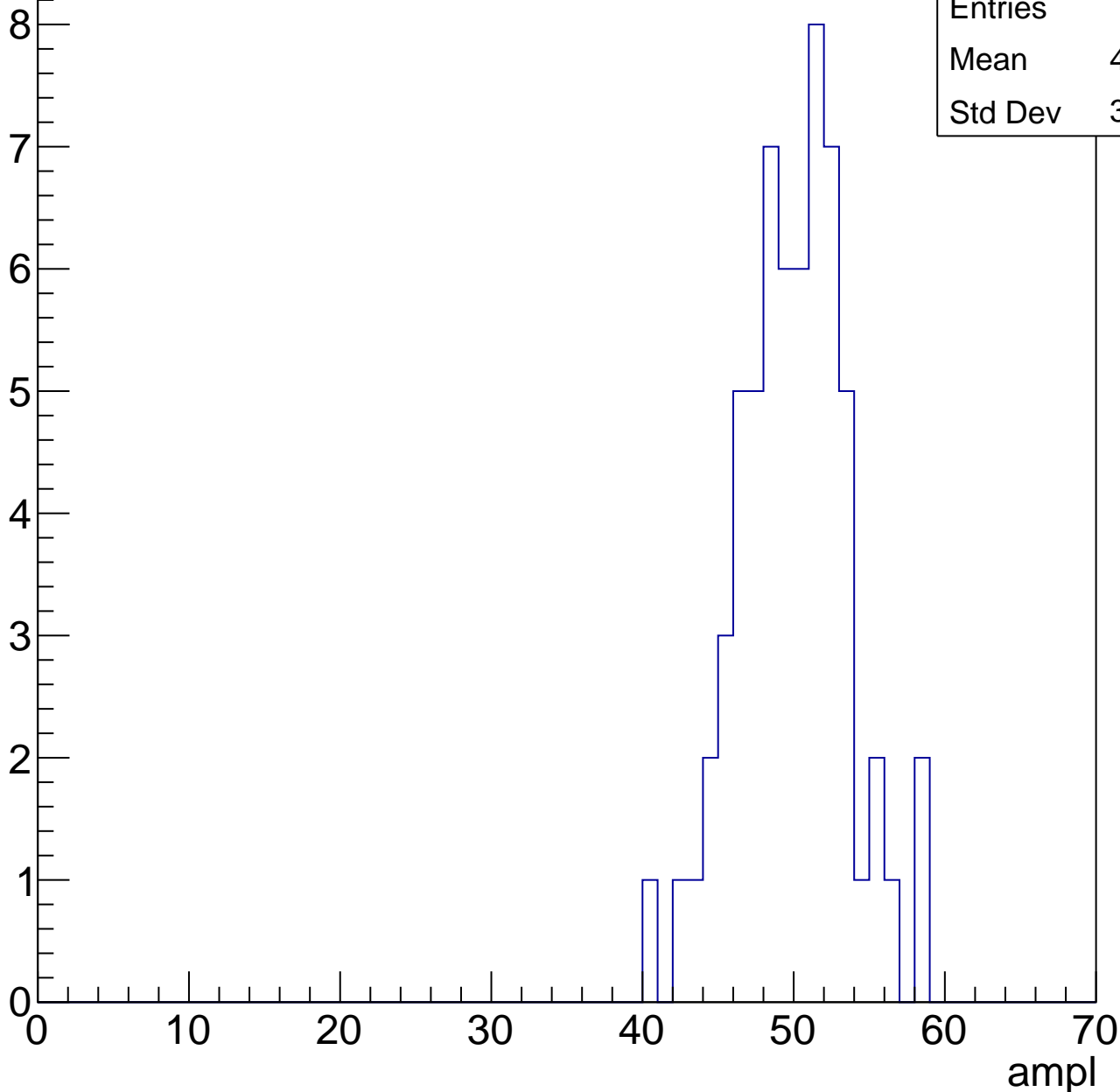


B1L103S, U14-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

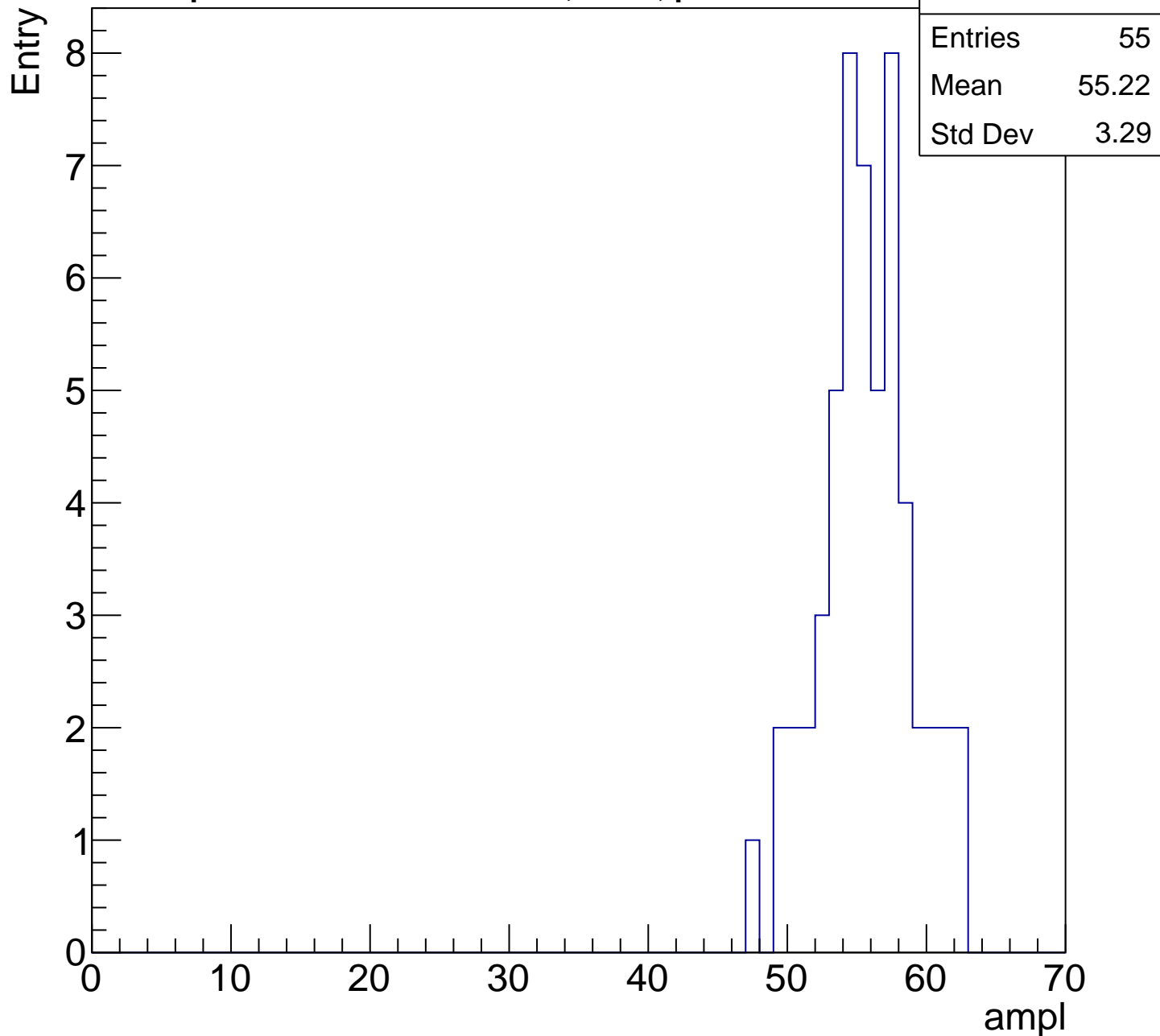
Entry

Entries	63
Mean	49.46
Std Dev	3.589



B1L103S, U14-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U14-ch11, adc5

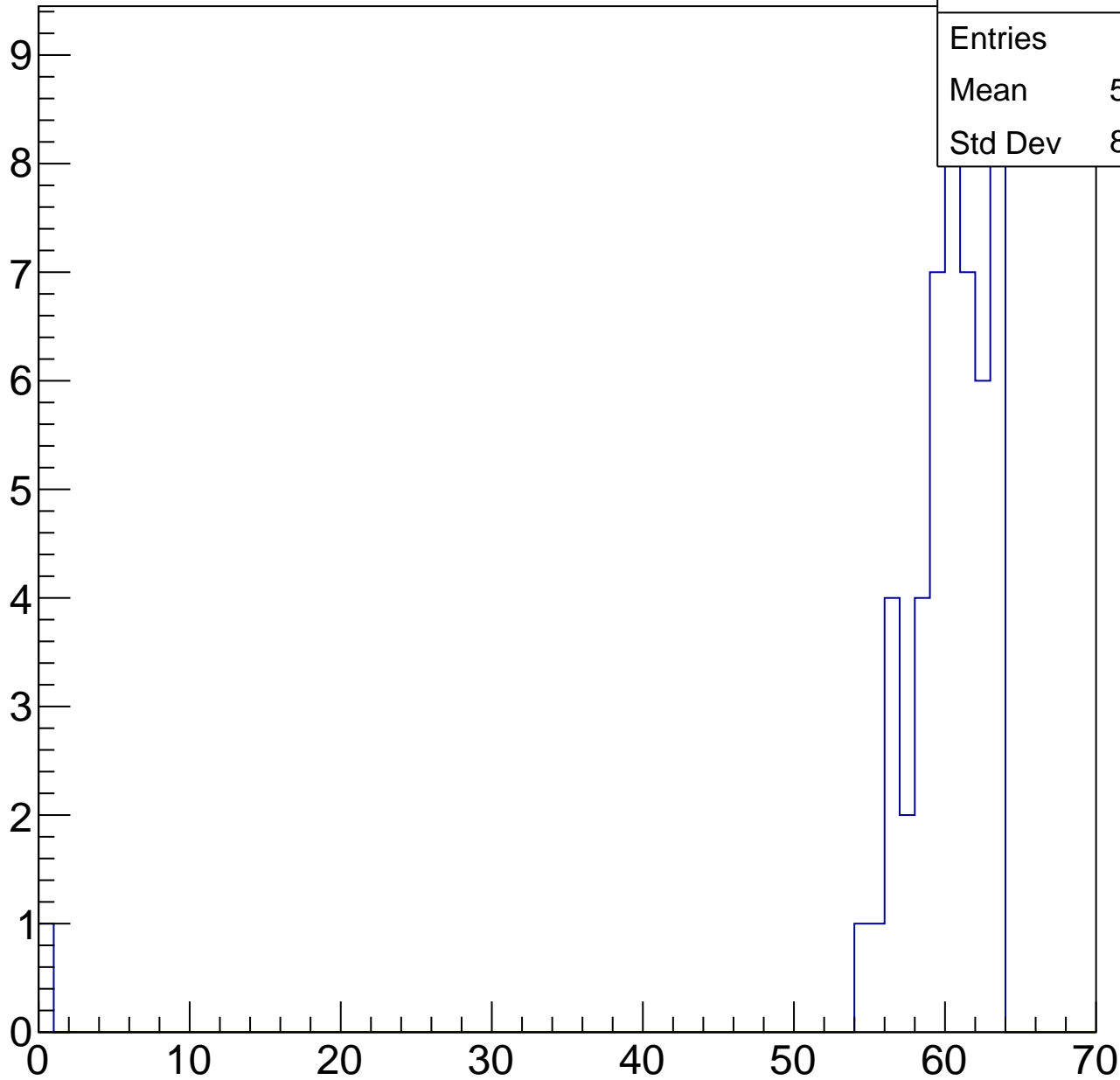
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.76
Std Dev	8.719

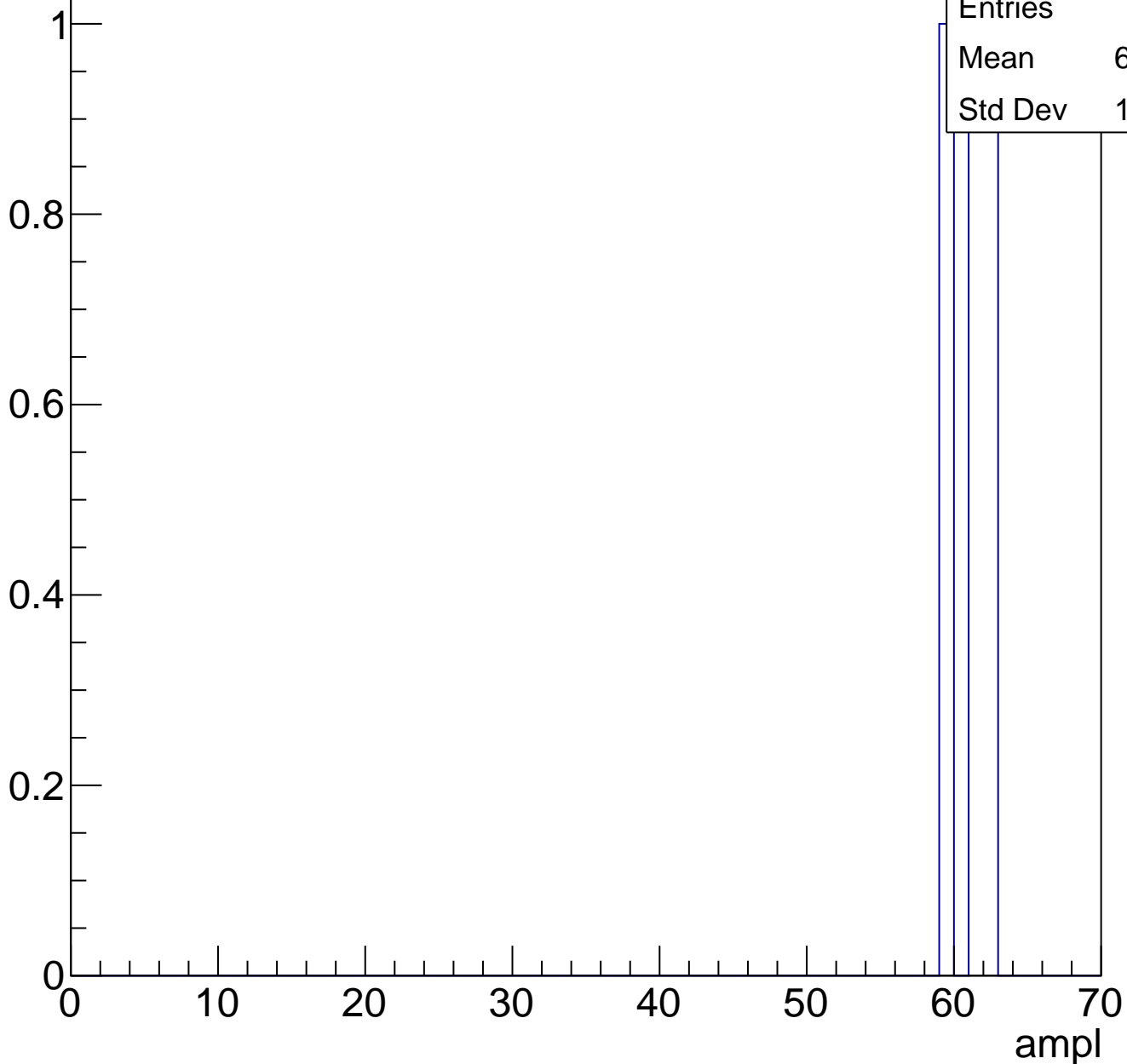
ampl



B1L103S, U14-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

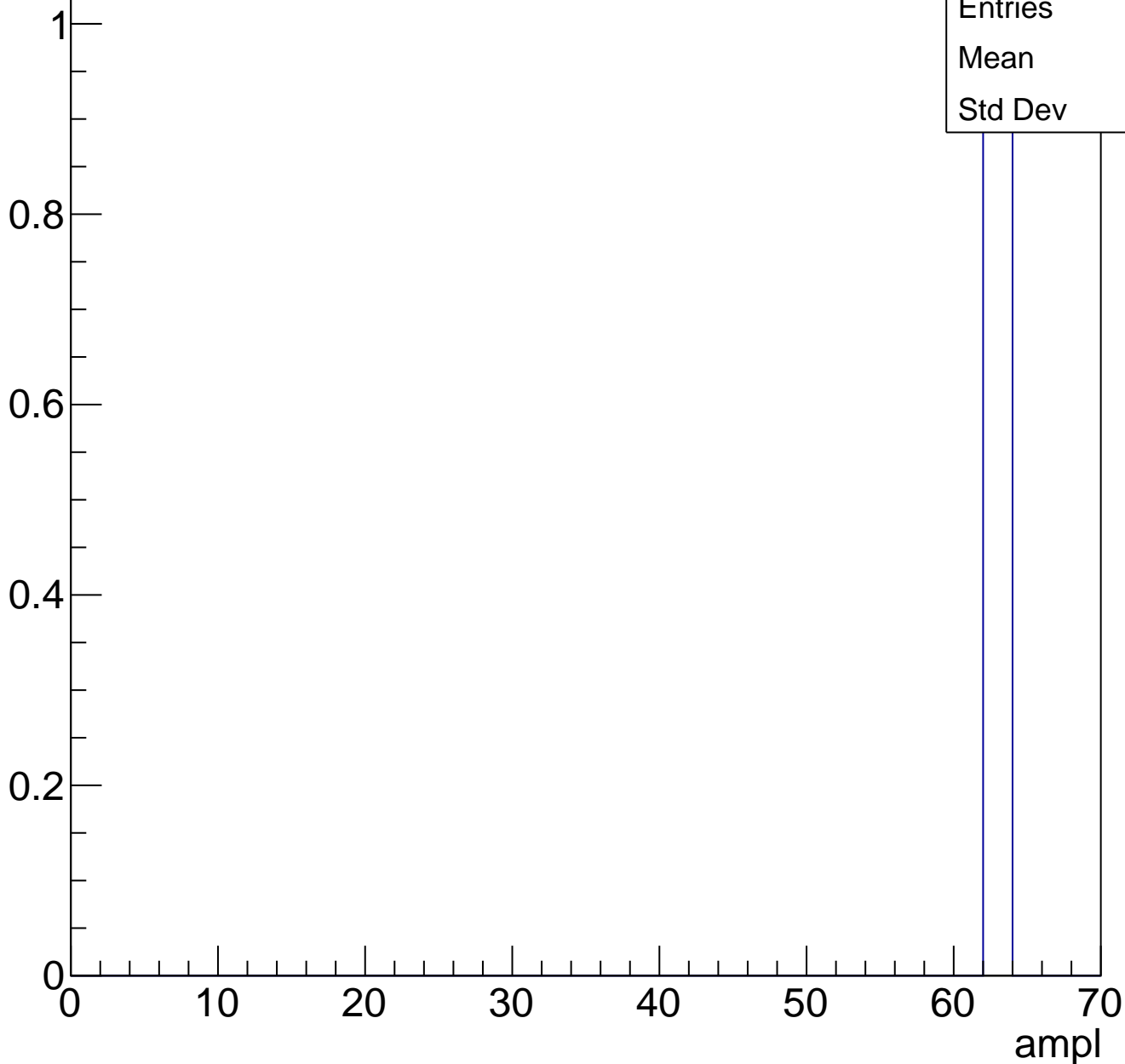
Entry



B1L103S, U14-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch12, adc0

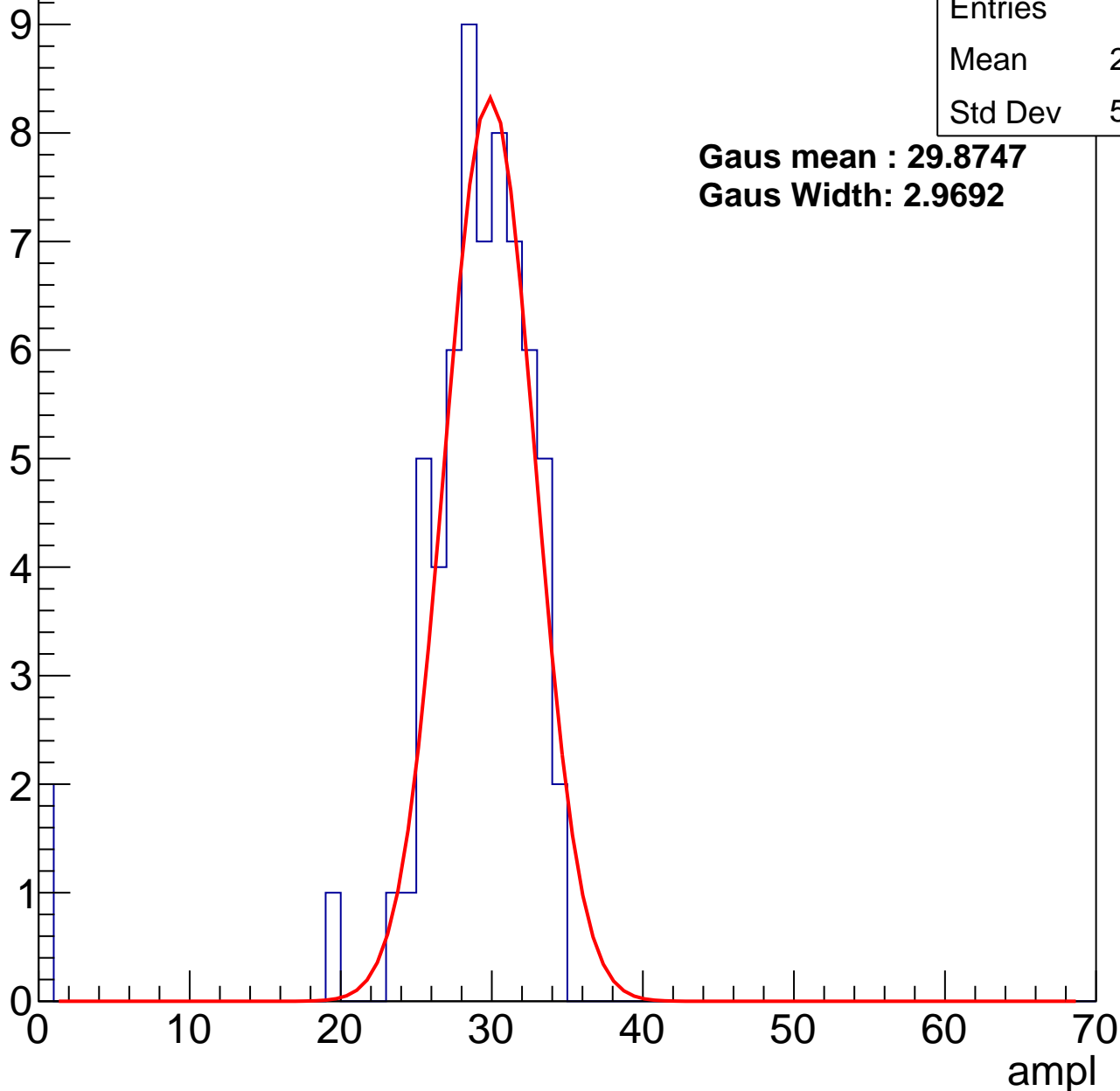
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.03
Std Dev	5.799

Gaus mean : 29.8747

Gaus Width: 2.9692



B1L103S, U14-ch12, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

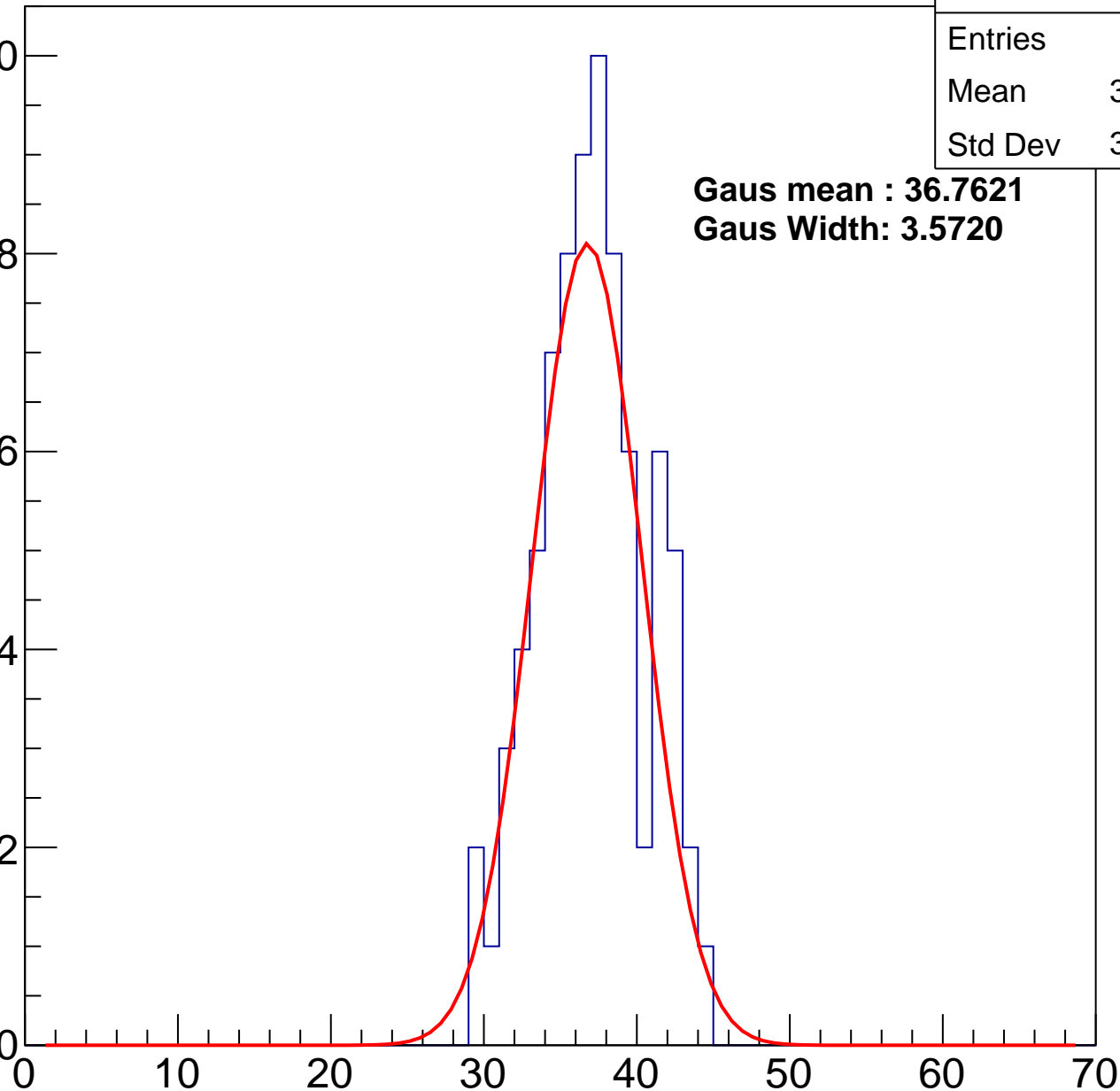
Entries	79
Mean	36.58
Std Dev	3.463

Gaus mean : 36.7621

Gaus Width: 3.5720

10
8
6
4
2
0

ampl



B1L103S, U14-ch12, adc2

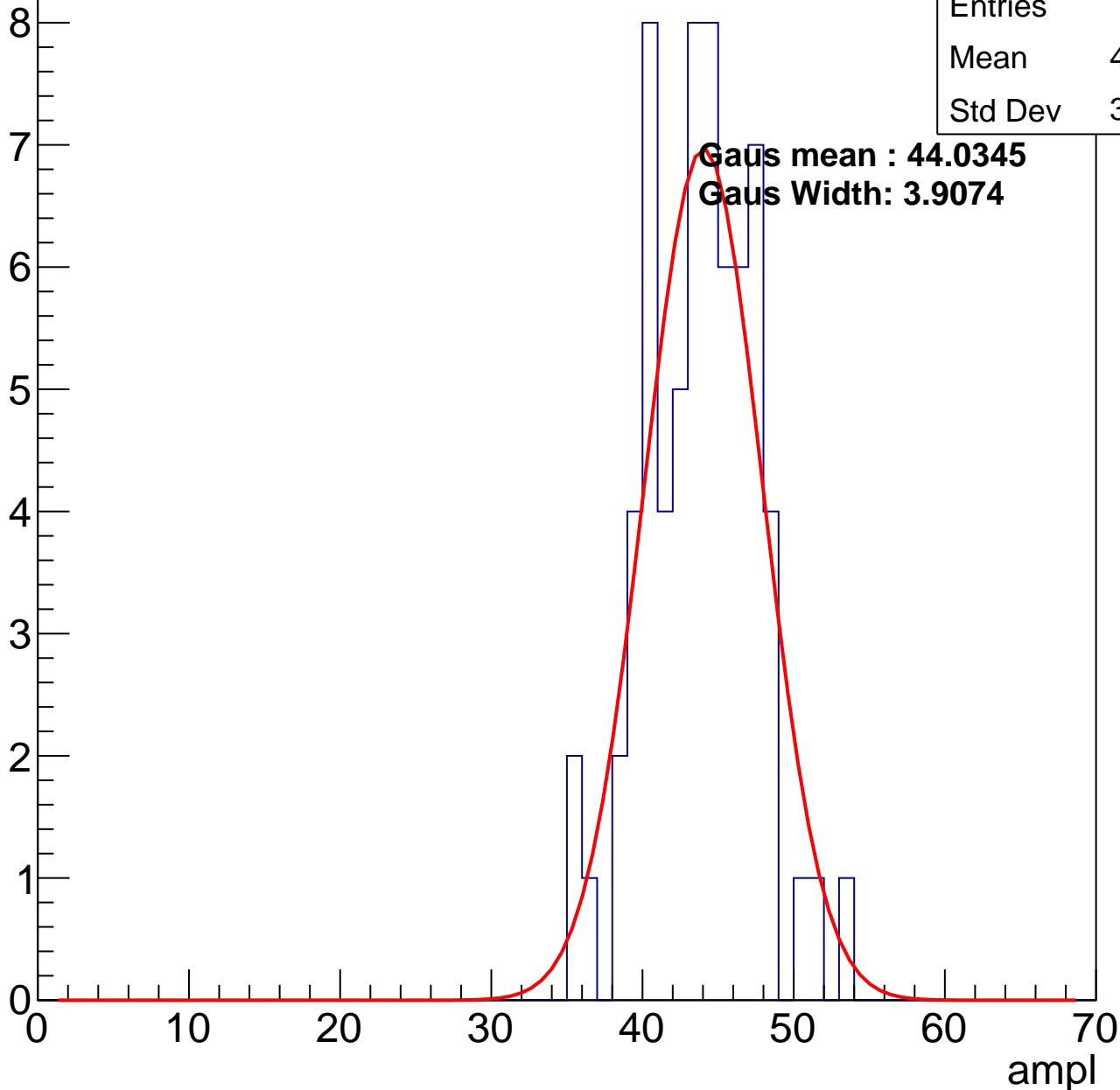
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.37
Std Dev	3.609

Gaus mean : 44.0345

Gaus Width: 3.9074

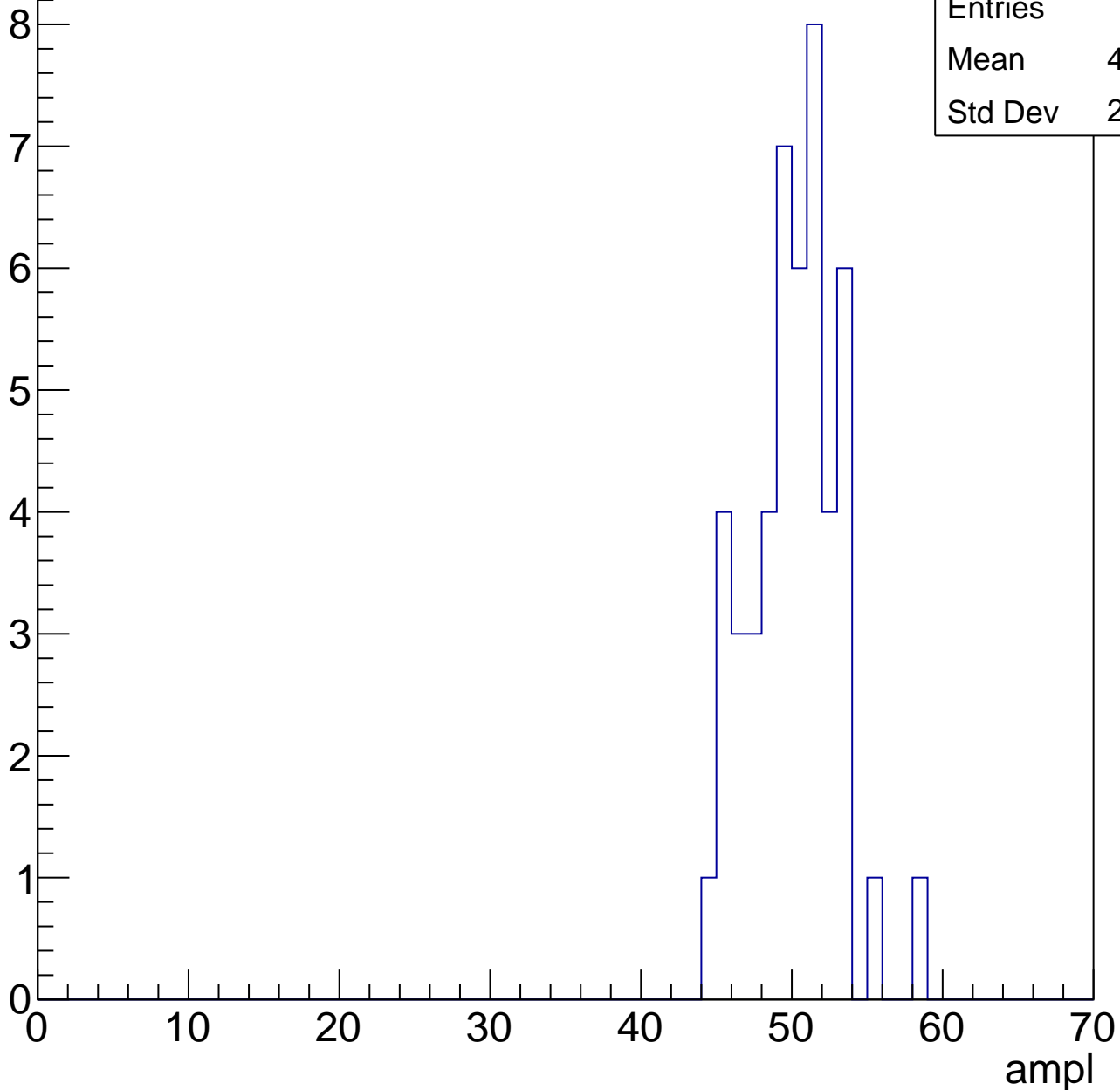


B1L103S, U14-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	49.69
Std Dev	2.873

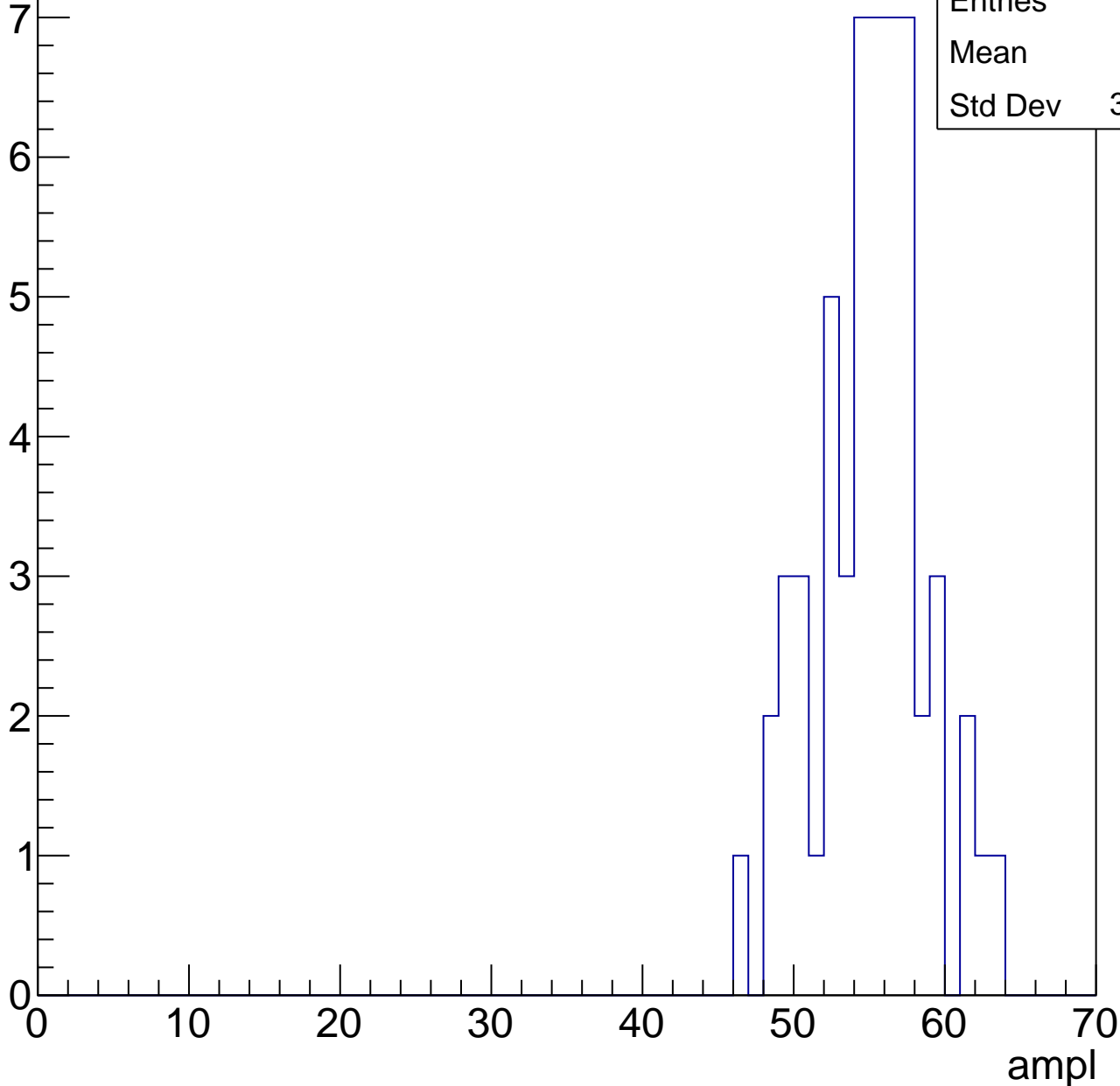


B1L103S, U14-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.6
Std Dev	3.616

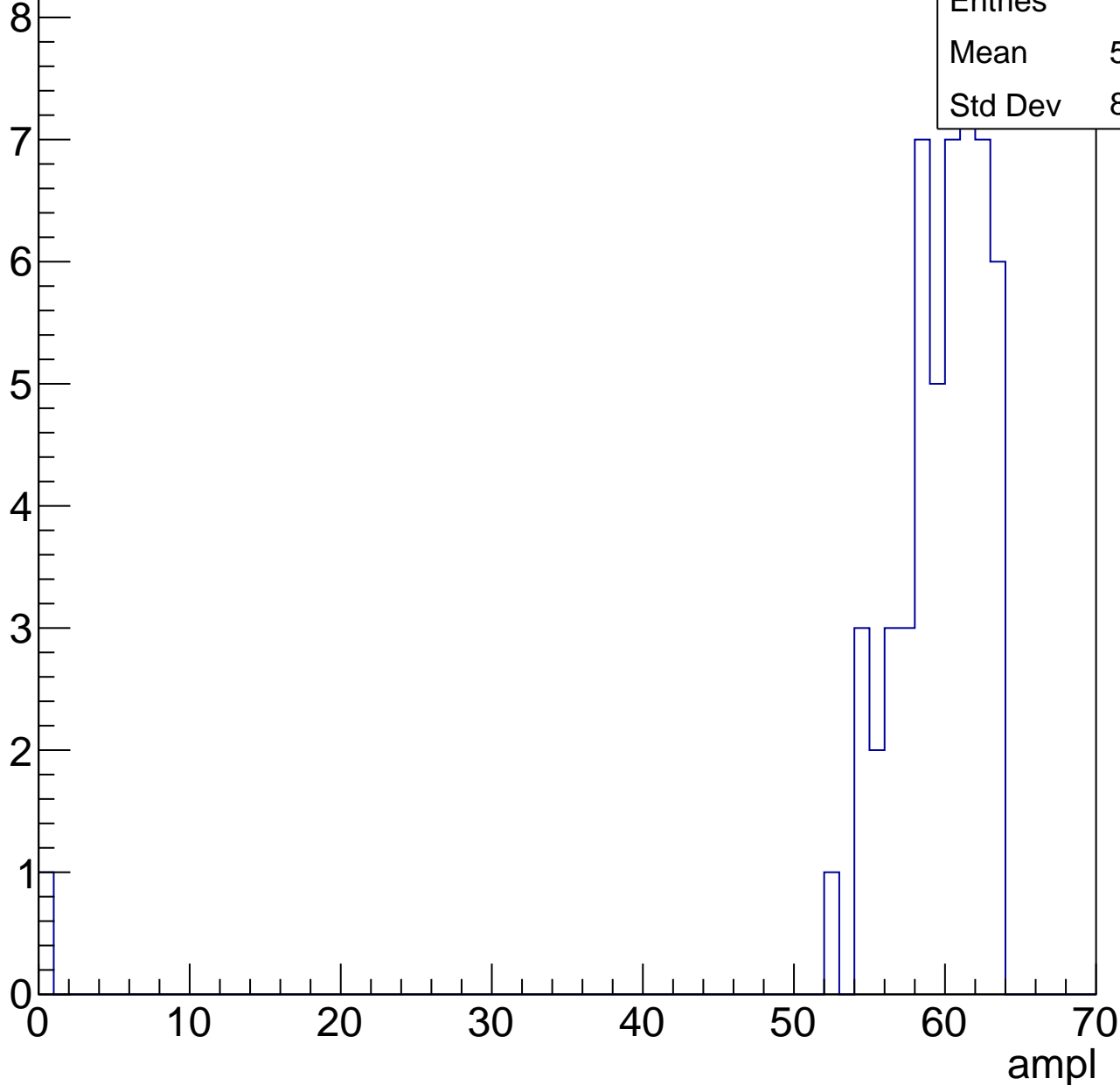


B1L103S, U14-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	58.19
Std Dev	8.519



B1L103S, U14-ch12, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.572

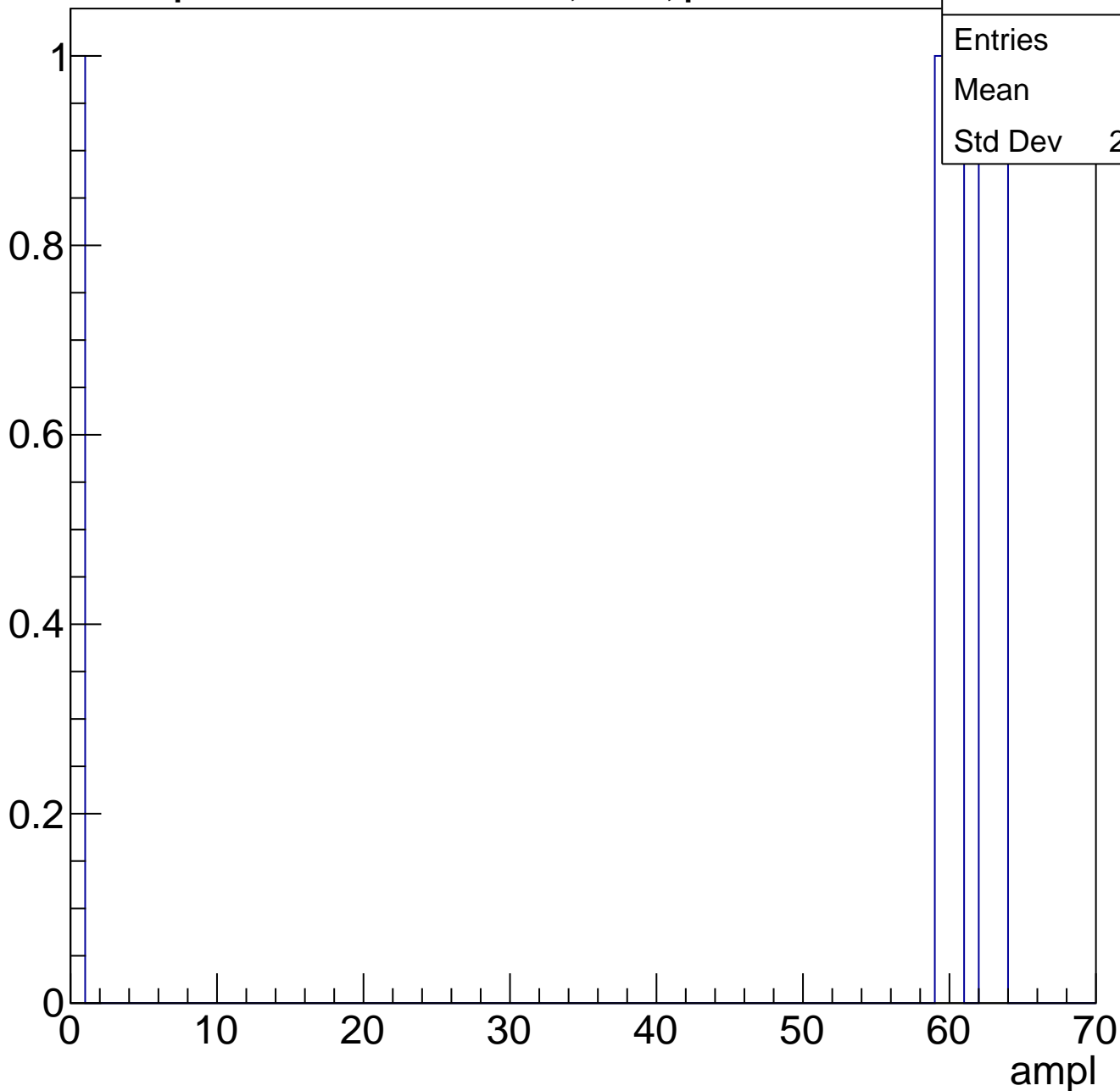
ampl

0 10 20 30 40 50 60 70

B1L103S, U14-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch13, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	29.57
Std Dev	5.911

Gaus mean : 31.1332

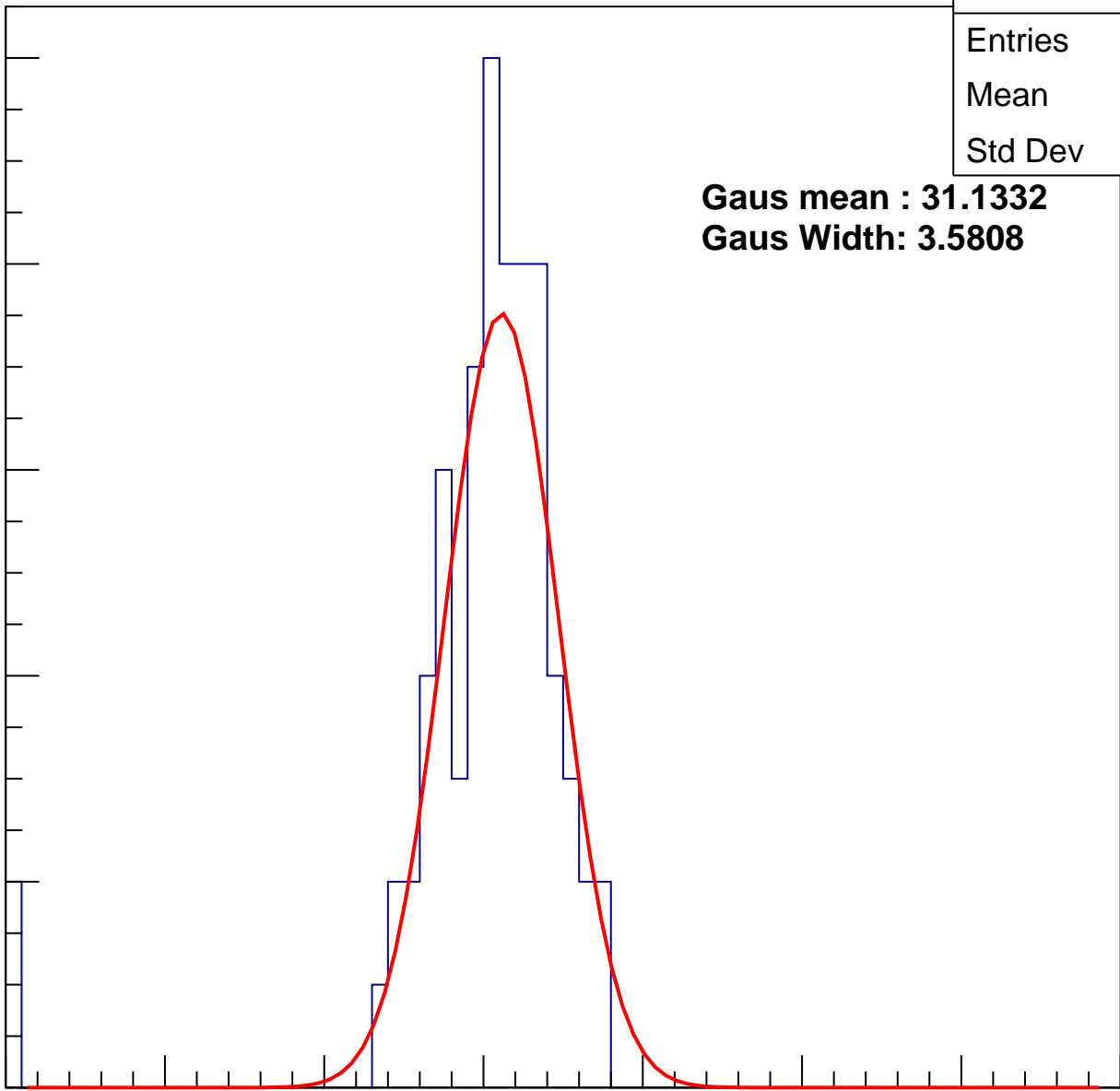
Gaus Width: 3.5808

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



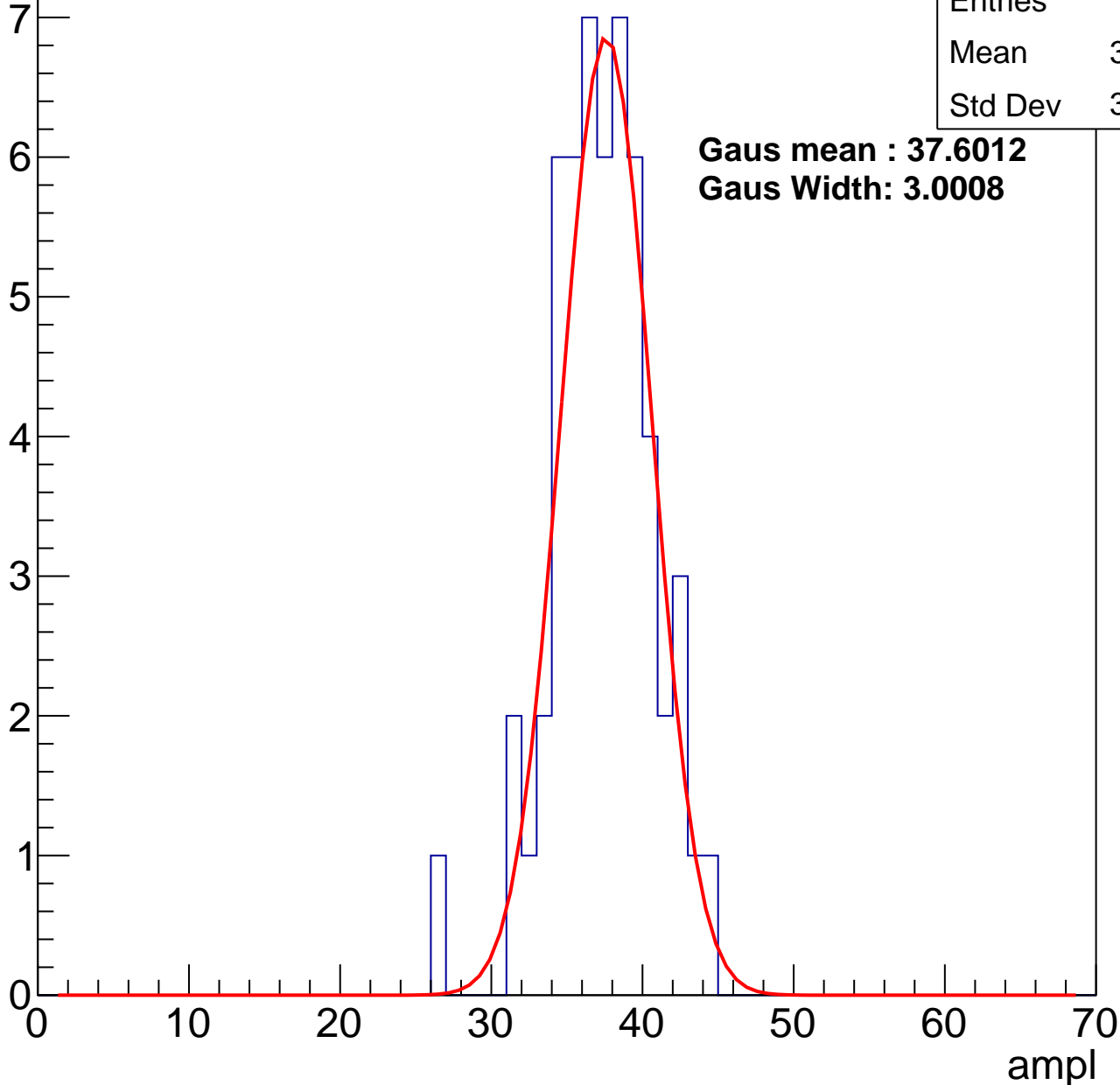
B1L103S, U14-ch13, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	36.89
Std Dev	3.285

Gaus mean : 37.6012
Gaus Width: 3.0008



B1L103S, U14-ch13, adc2

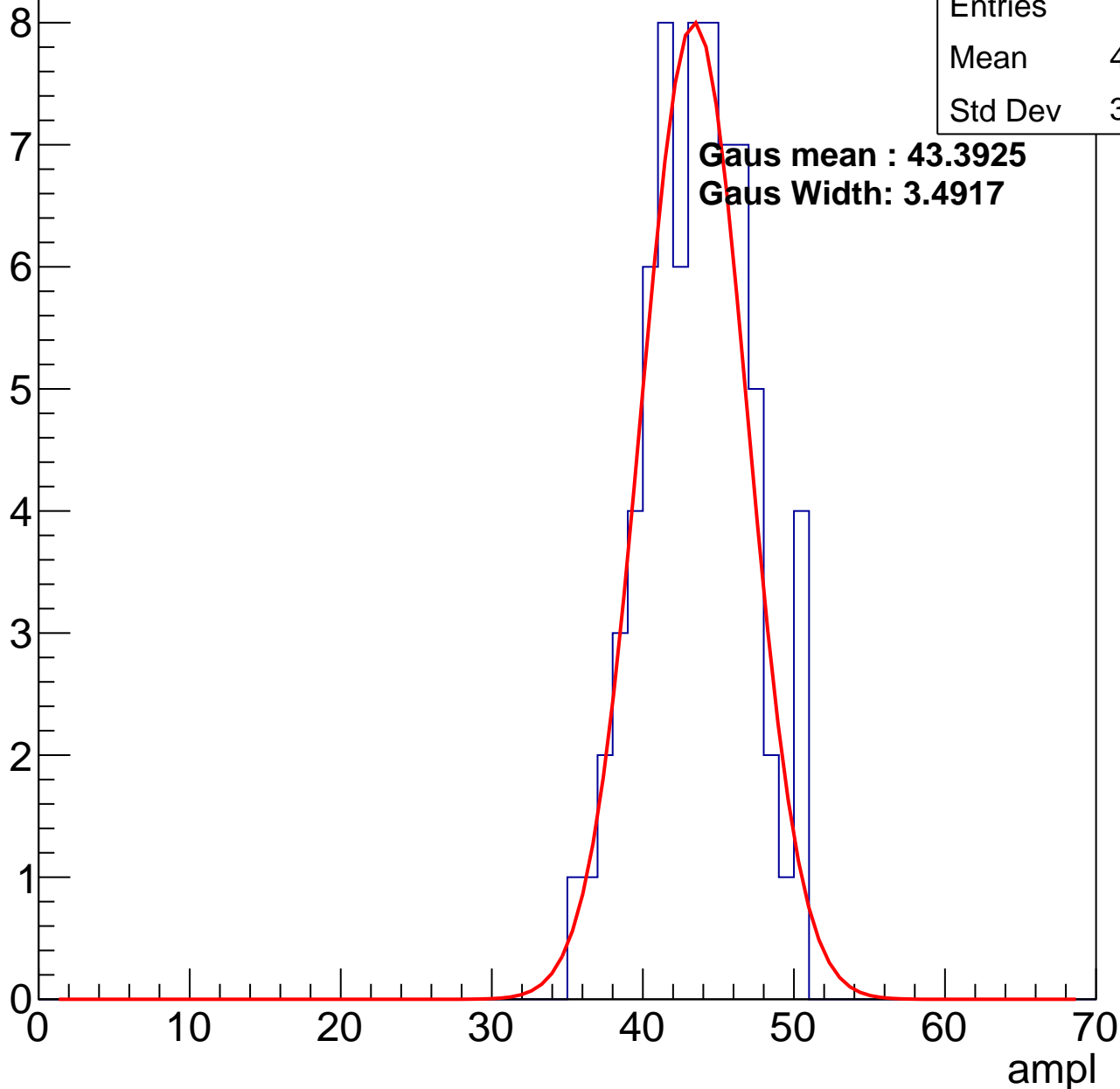
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.12
Std Dev	3.464

Gaus mean : 43.3925

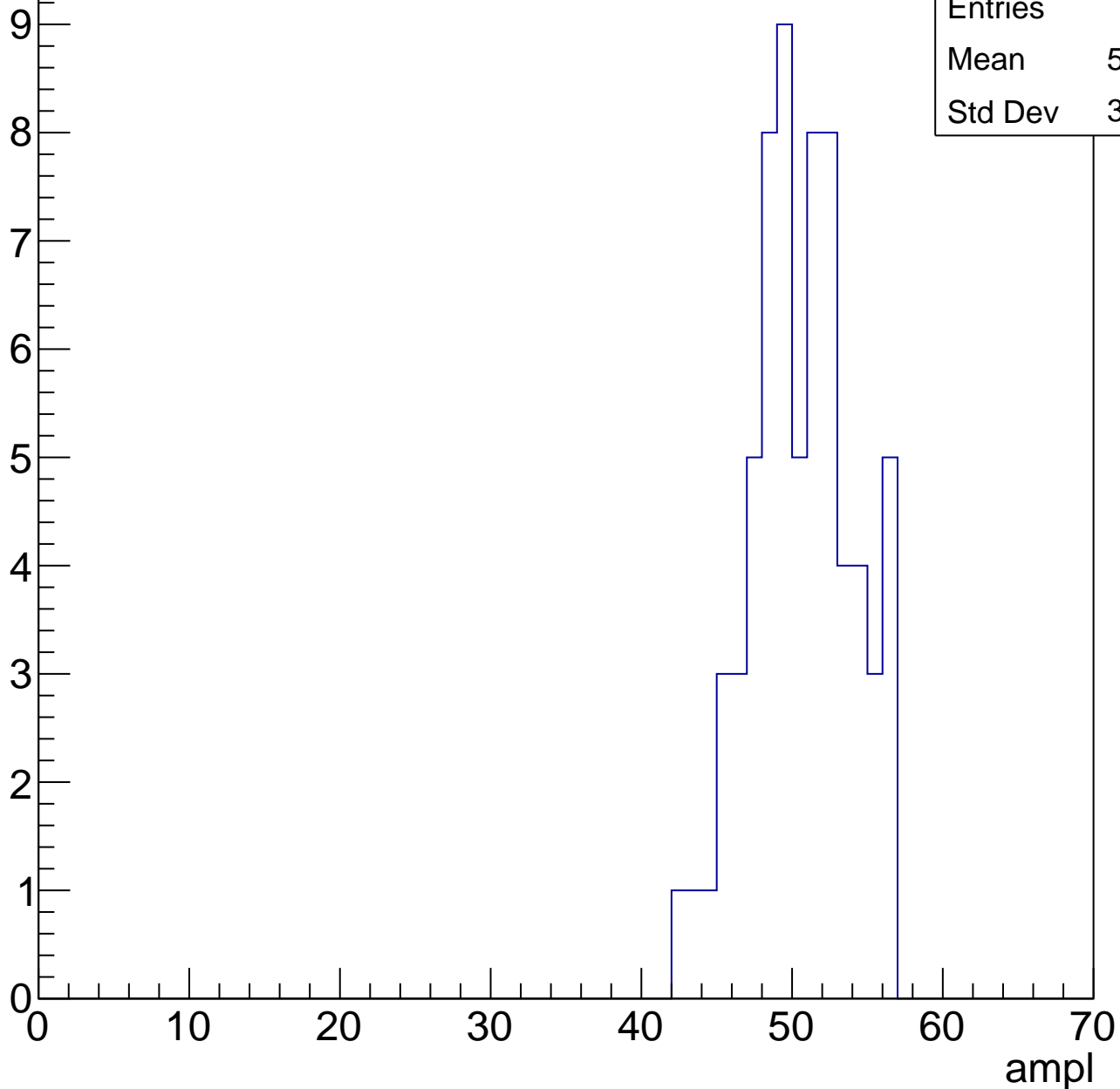
Gaus Width: 3.4917



B1L103S, U14-ch13, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



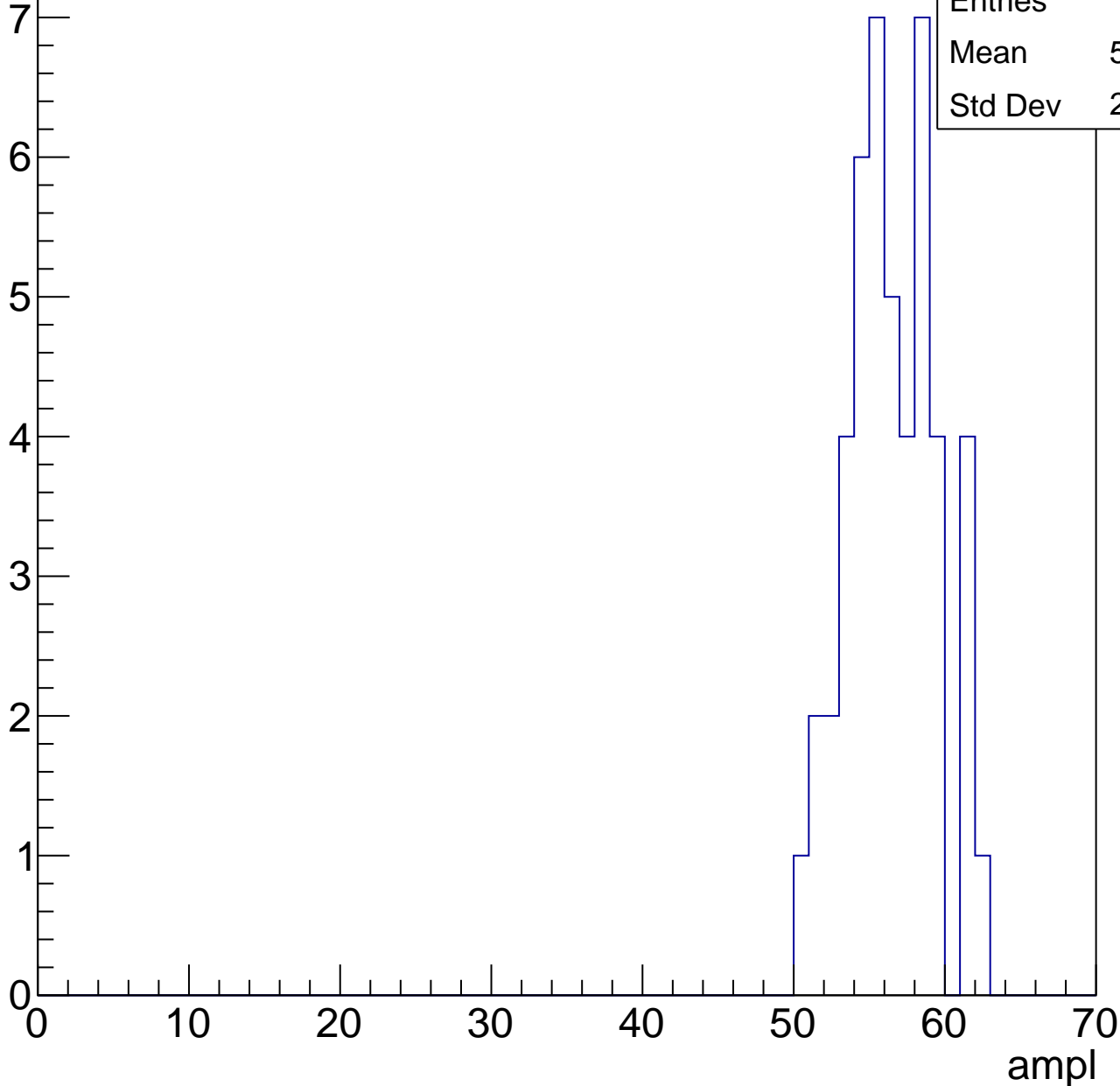
Entries	68
Mean	50.13
Std Dev	3.325

B1L103S, U14-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

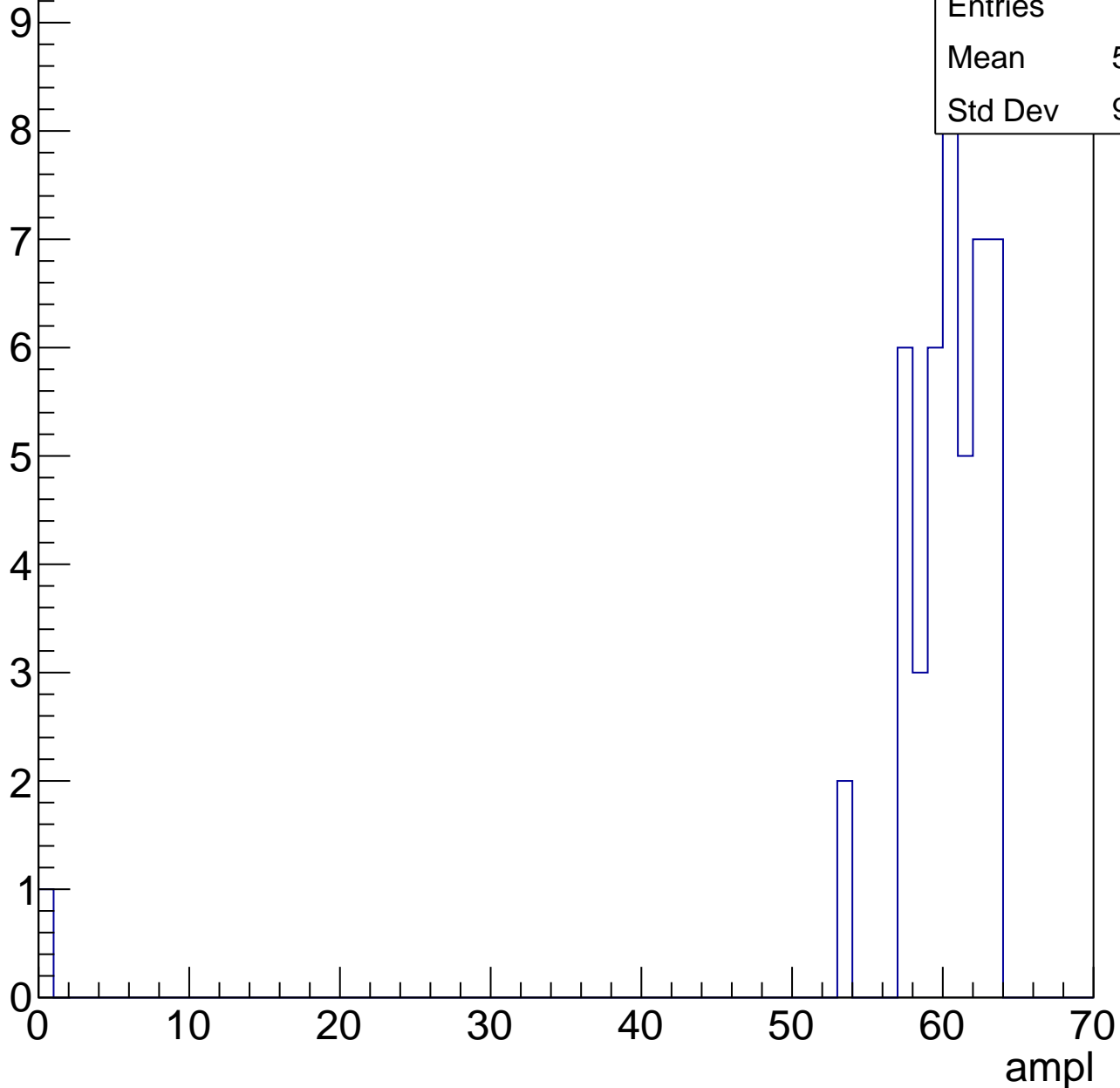
Entries	47
Mean	56.02
Std Dev	2.877



B1L103S, U14-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch13, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

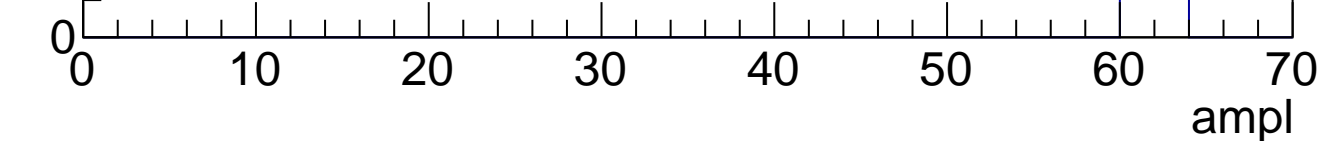
7

Mean

62

Std Dev

1.069



B1L103S, U14-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U14-ch14, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	27.83
Std Dev	4.511

Gaus mean : 27.4645

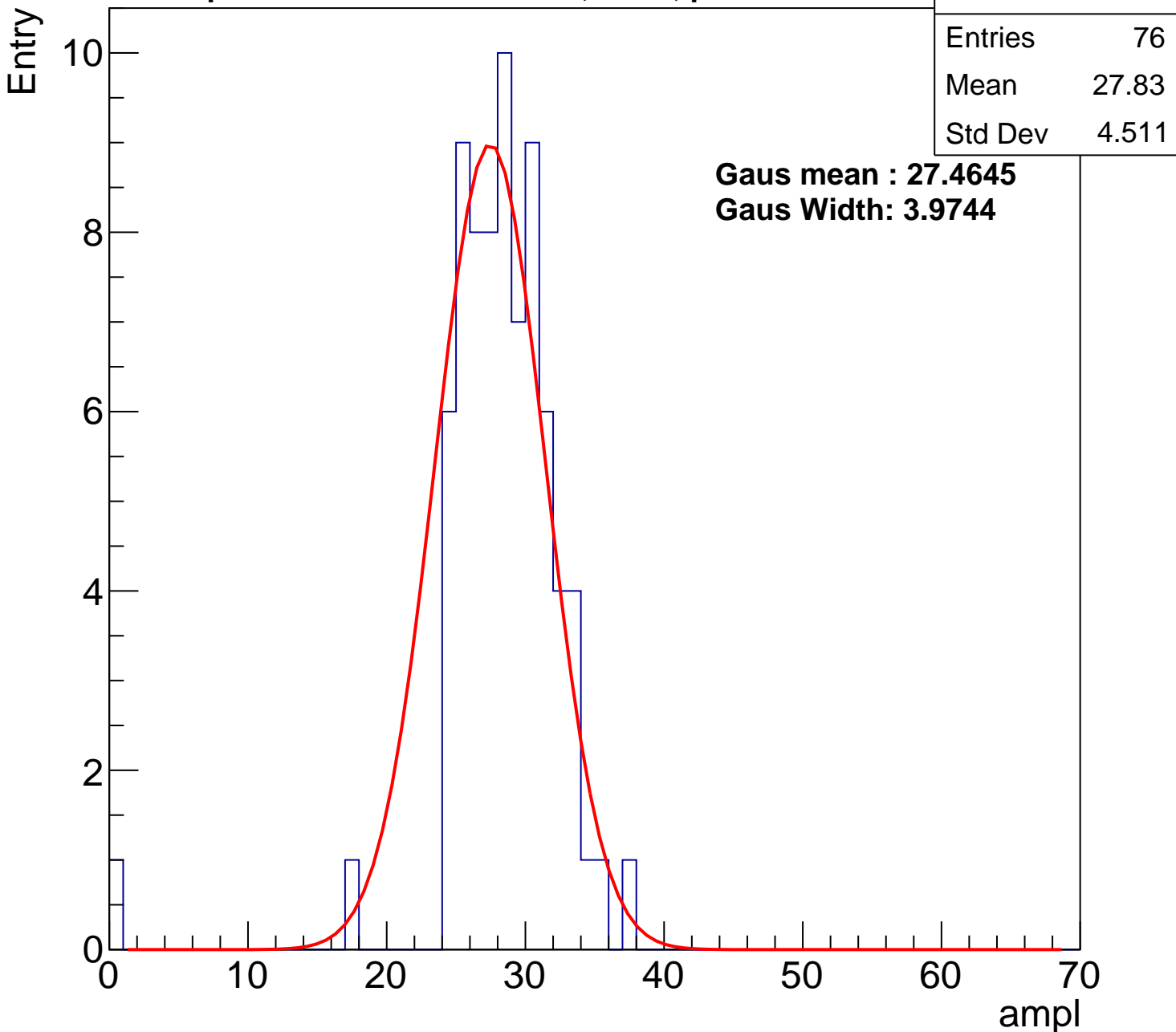
Gaus Width: 3.9744

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch14, adc1

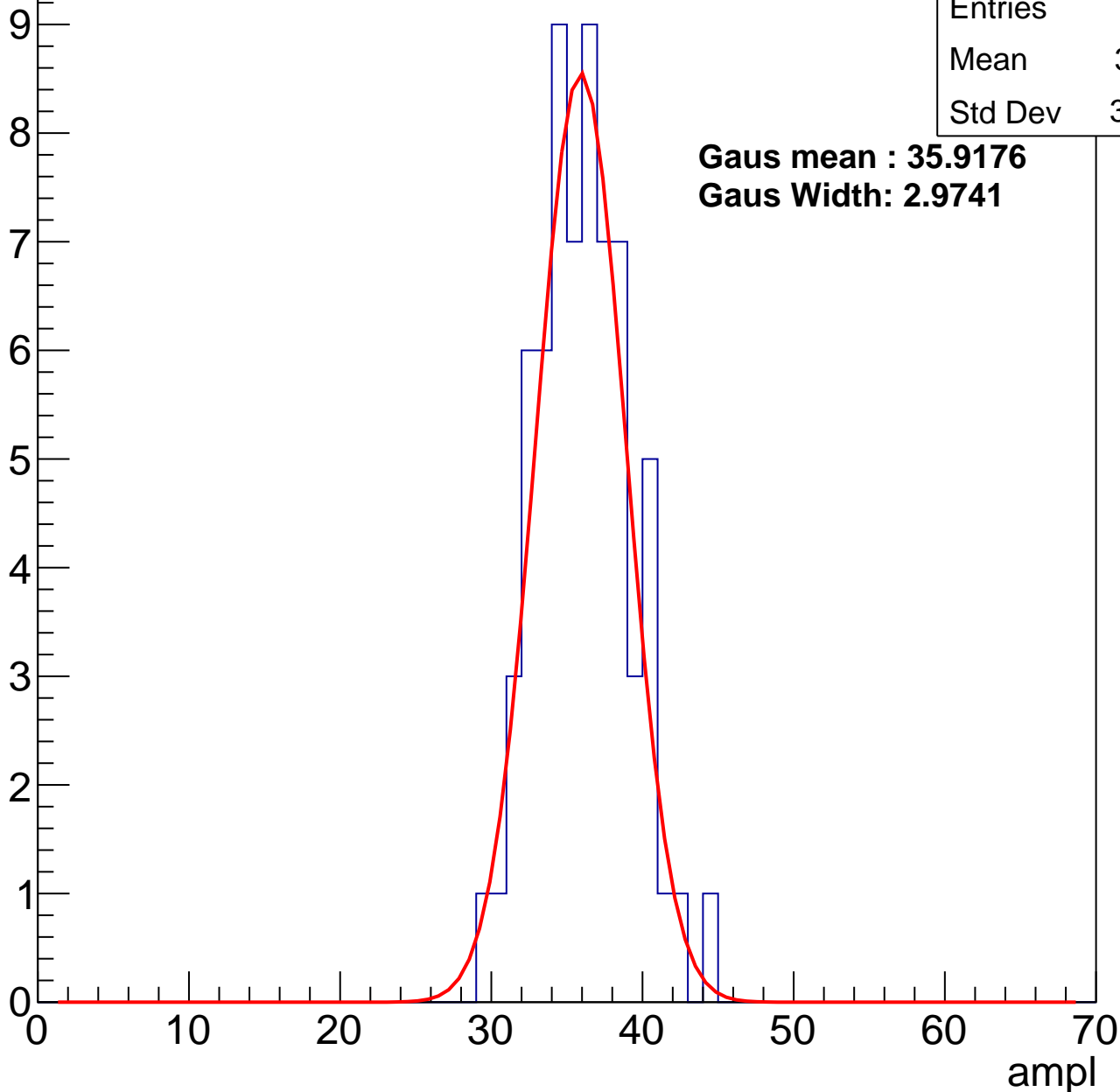
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.61
Std Dev	3.017

Gaus mean : 35.9176

Gaus Width: 2.9741



B1L103S, U14-ch14, adc2

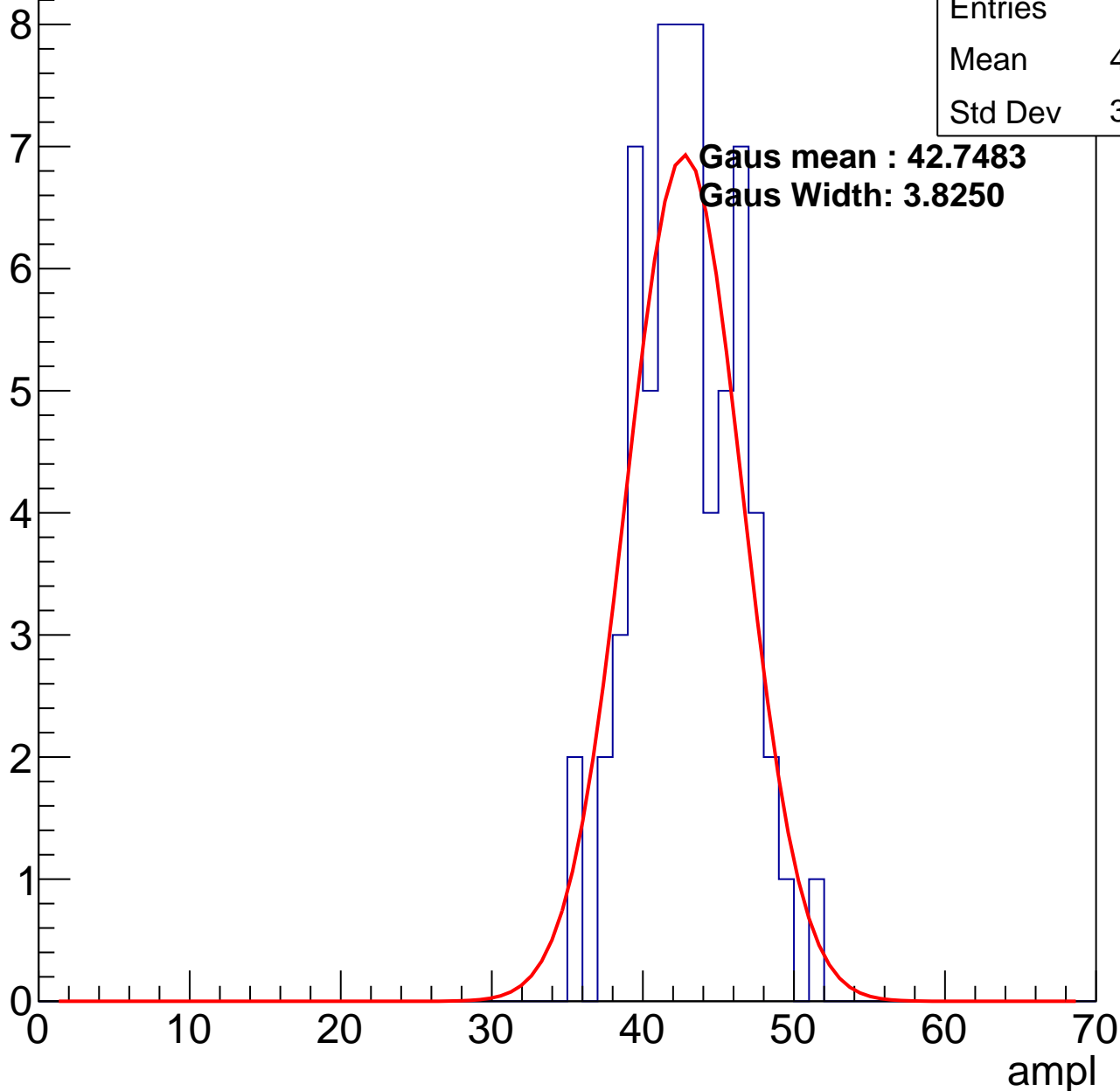
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.48
Std Dev	3.356

Gaus mean : 42.7483

Gaus Width: 3.8250

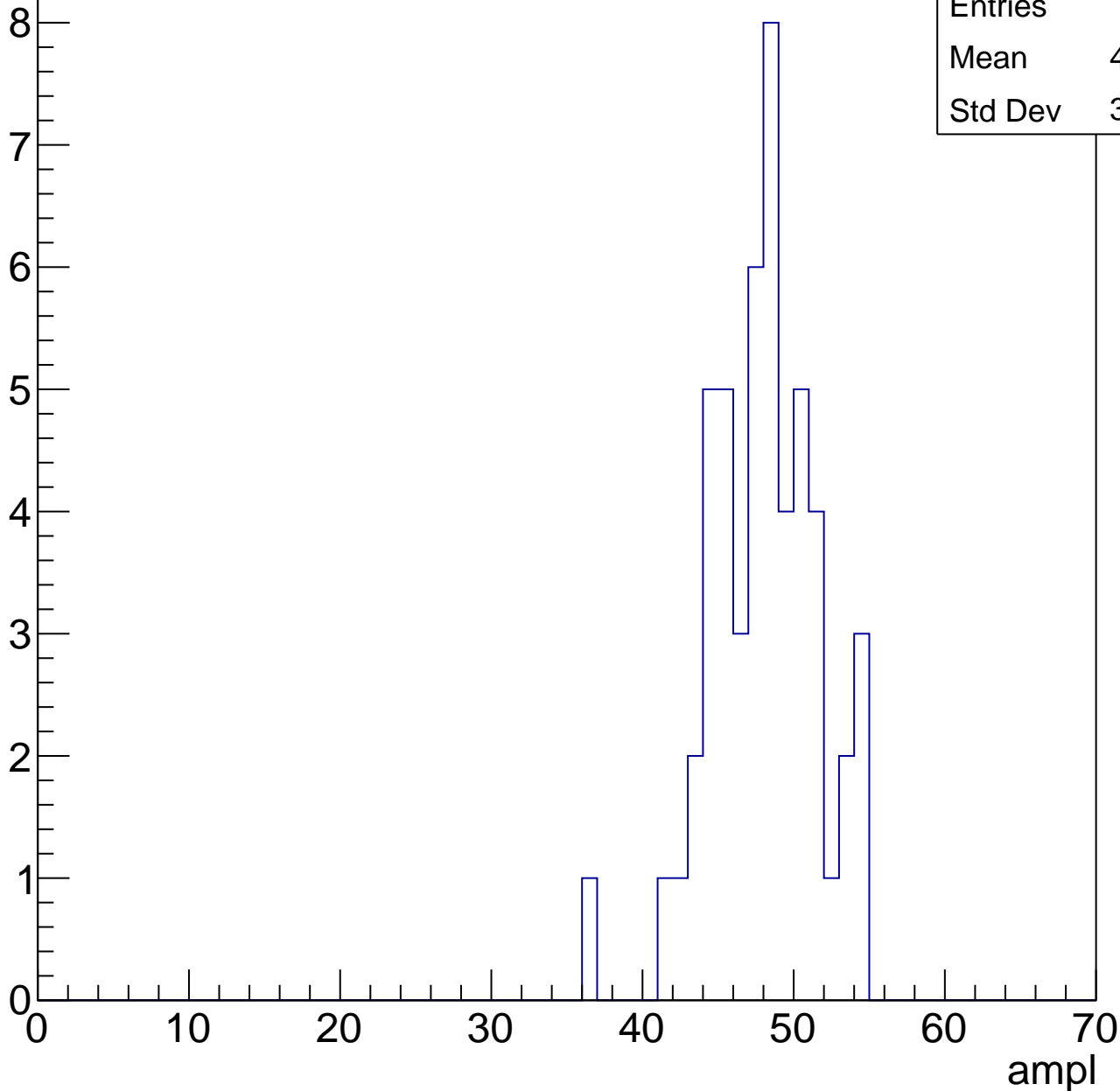


B1L103S, U14-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	47.53
Std Dev	3.555

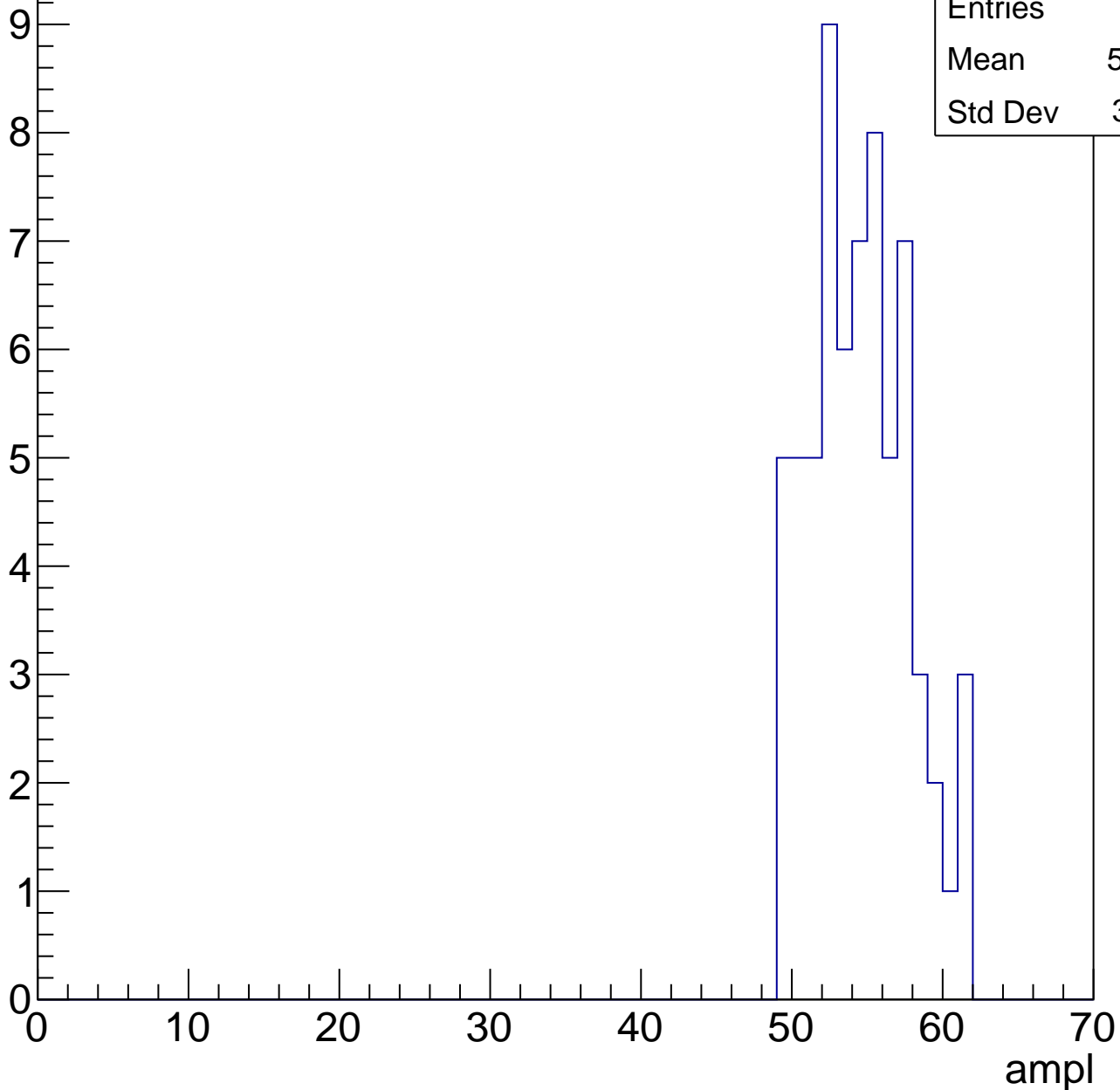


B1L103S, U14-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	54.06
Std Dev	3.171

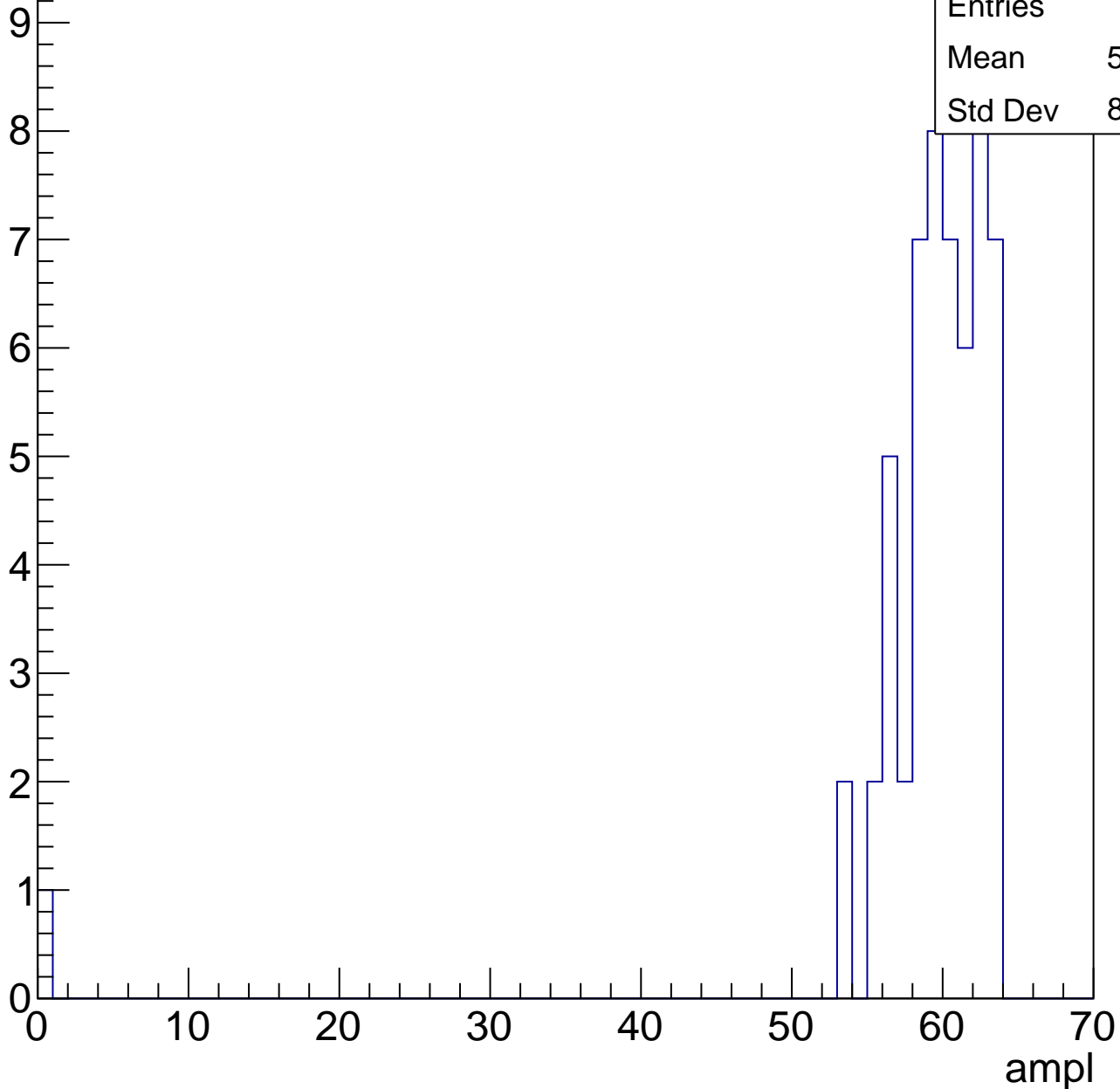


B1L103S, U14-ch14, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

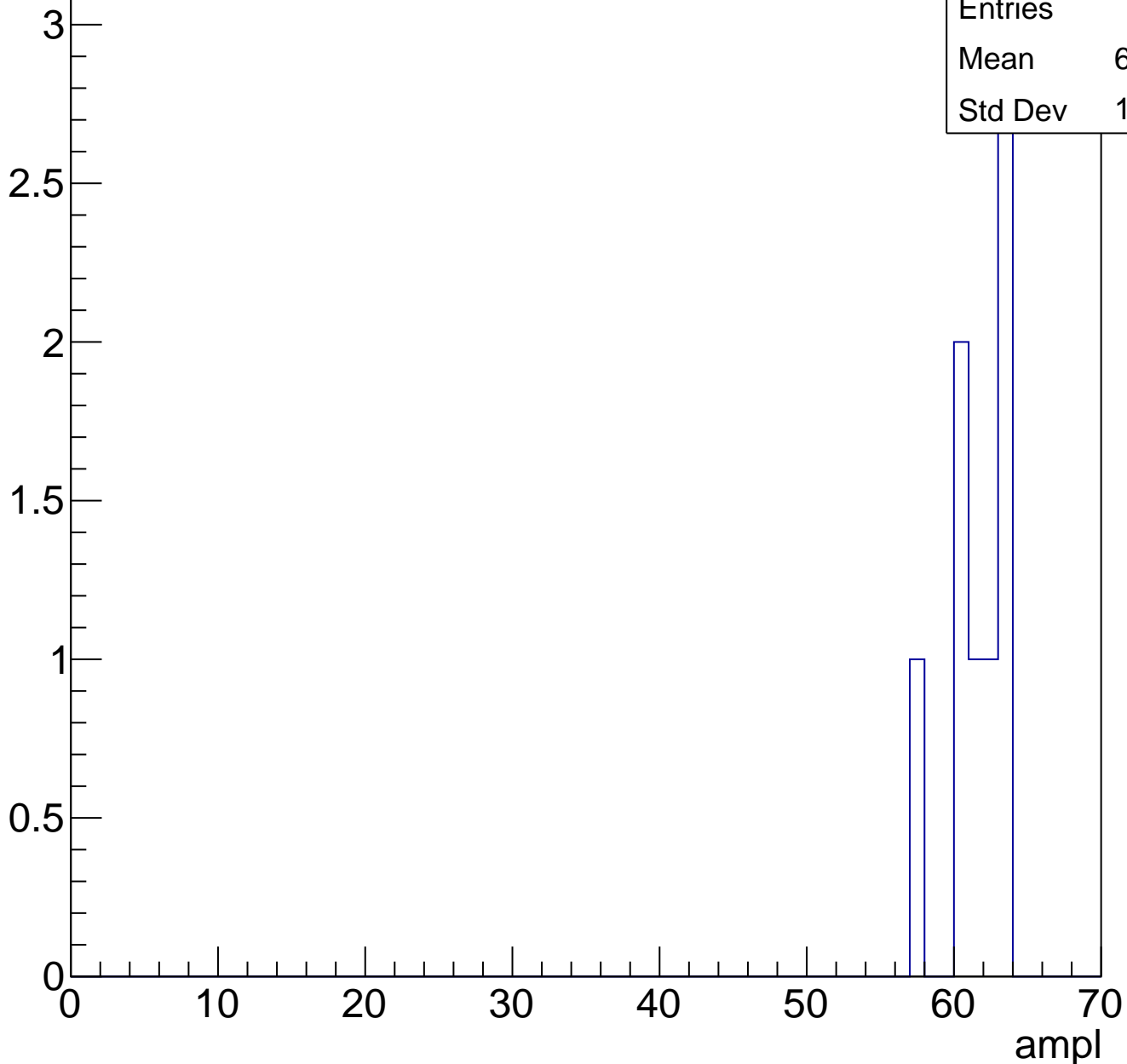
Entries	56
Mean	58.45
Std Dev	8.294



B1L103S, U14-ch14, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch15, adc0

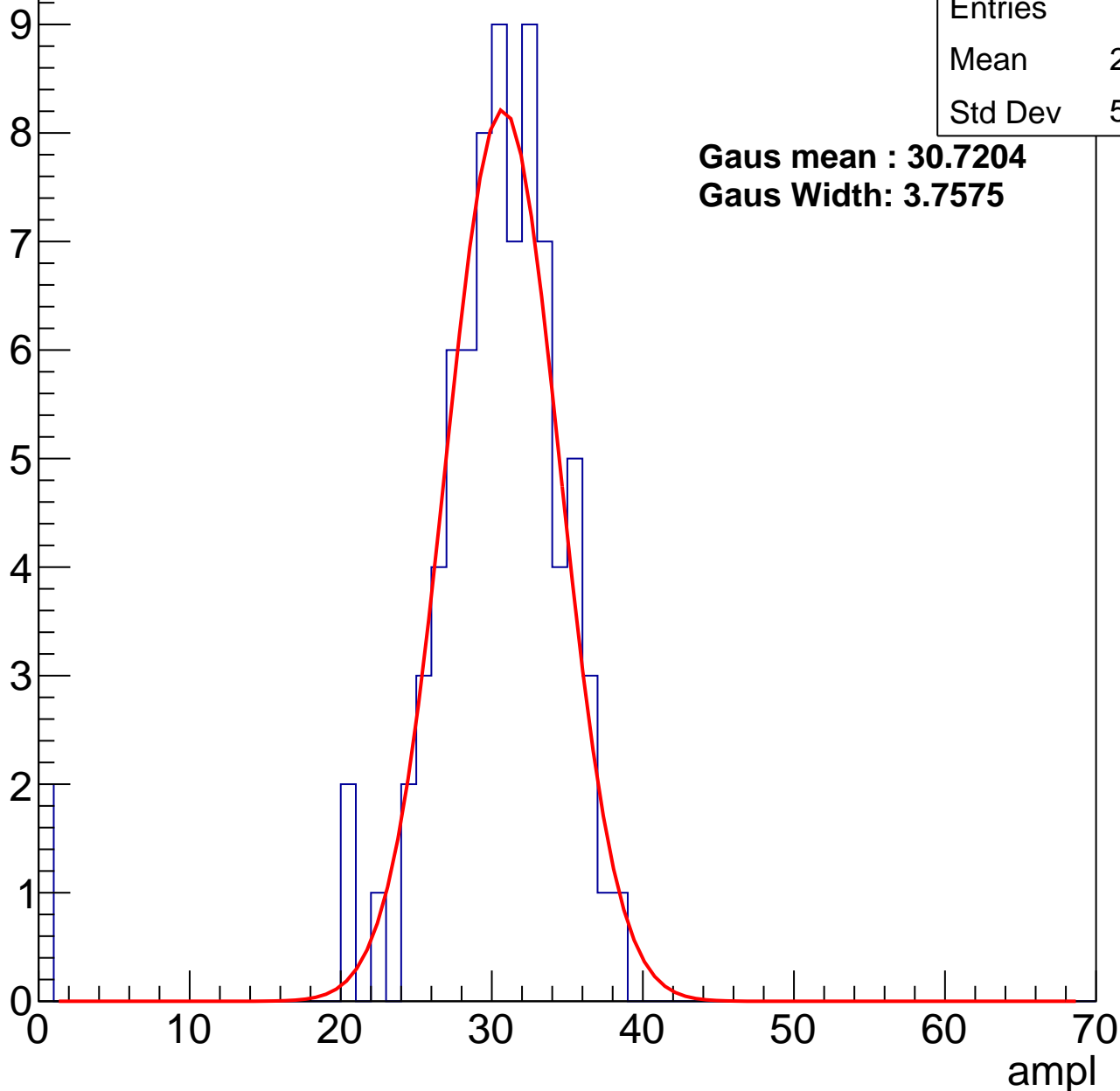
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	29.39
Std Dev	5.966

Gaus mean : 30.7204

Gaus Width: 3.7575



B1L103S, U14-ch15, adc1

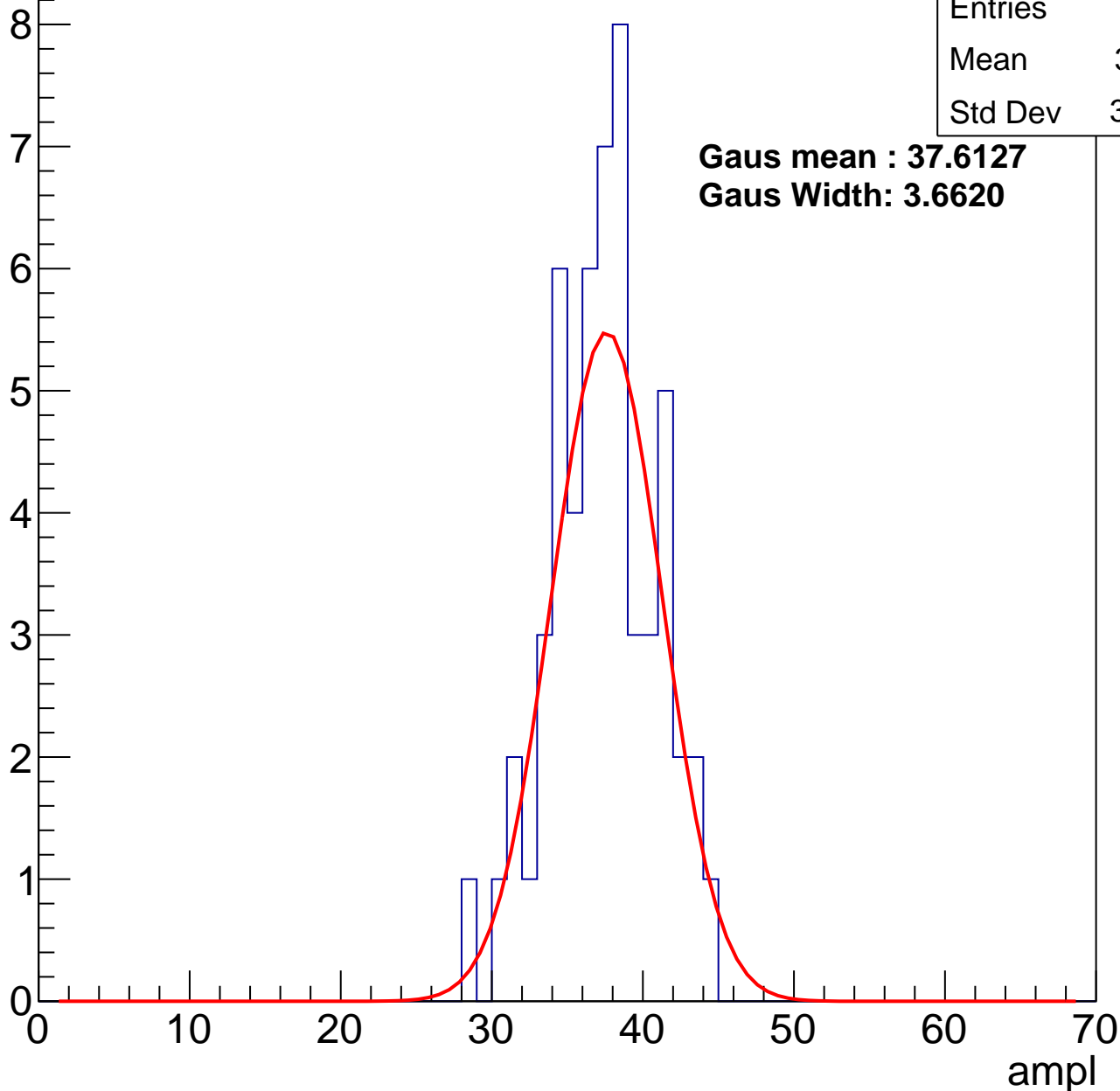
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	36.91
Std Dev	3.434

Gaus mean : 37.6127

Gaus Width: 3.6620



B1L103S, U14-ch15, adc2

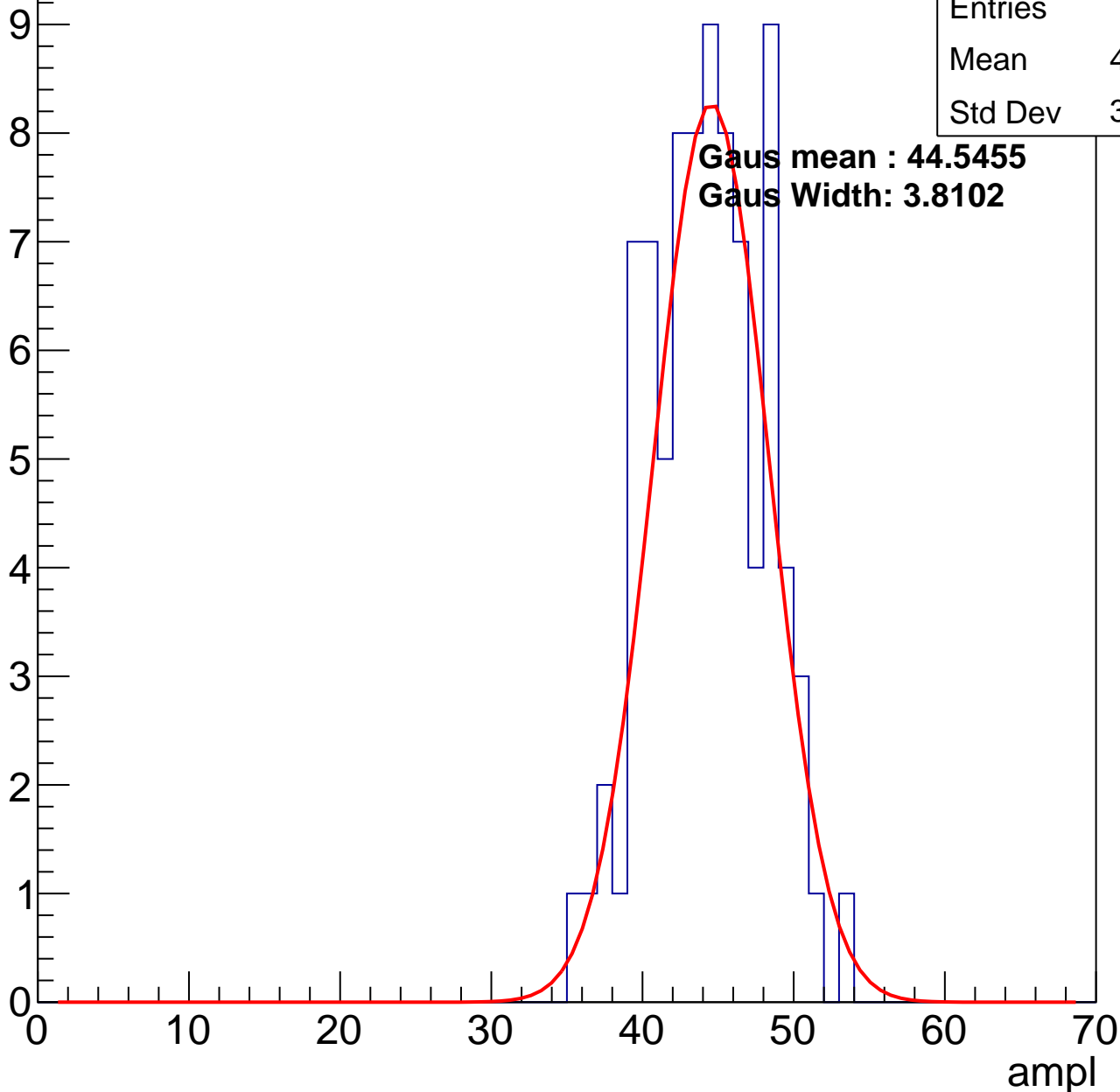
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	43.83
Std Dev	3.739

Gaus mean : 44.5455

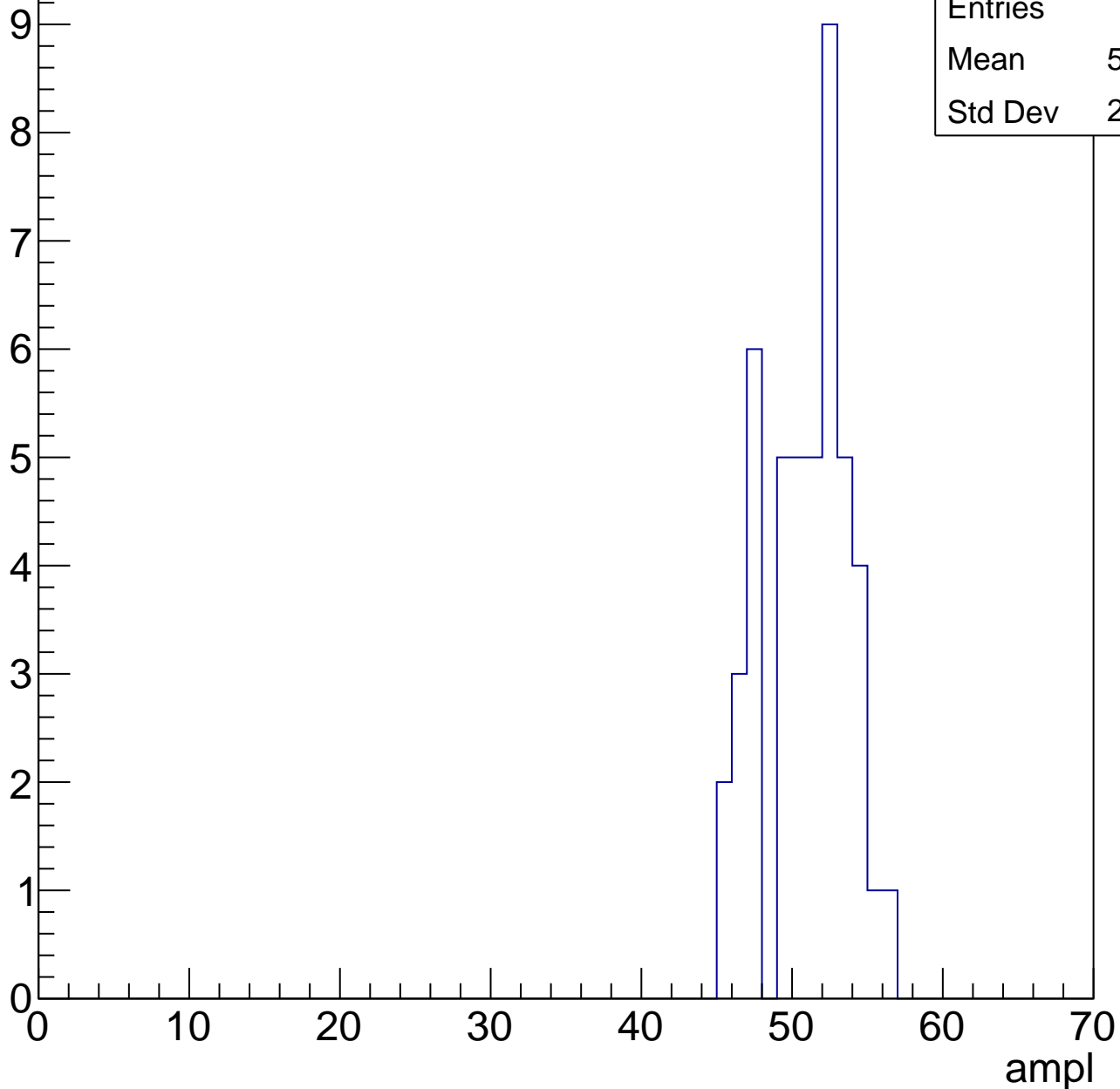
Gaus Width: 3.8102



B1L103S, U14-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

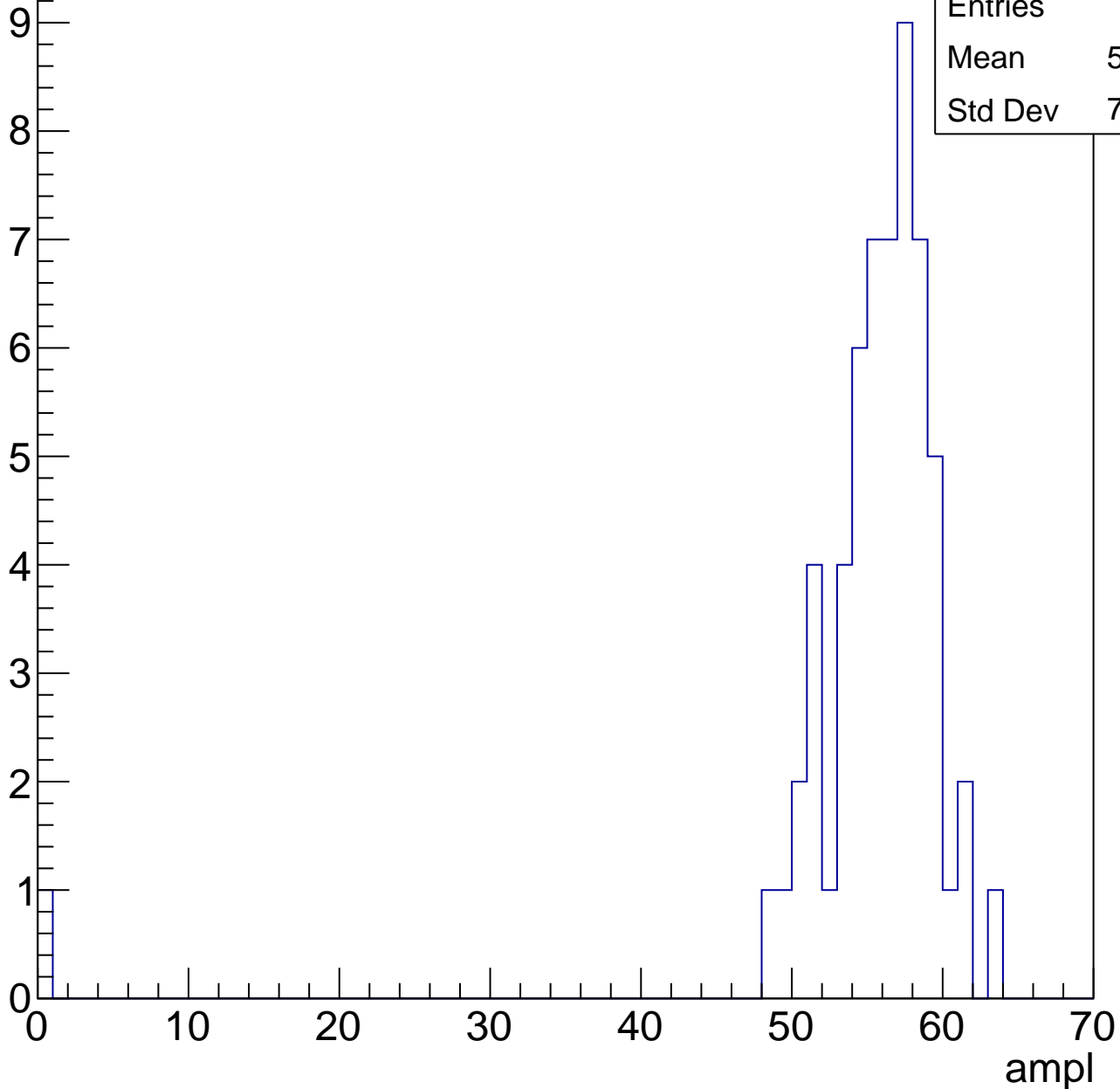


B1L103S, U14-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	54.66
Std Dev	7.806



B1L103S, U14-ch15, adc5

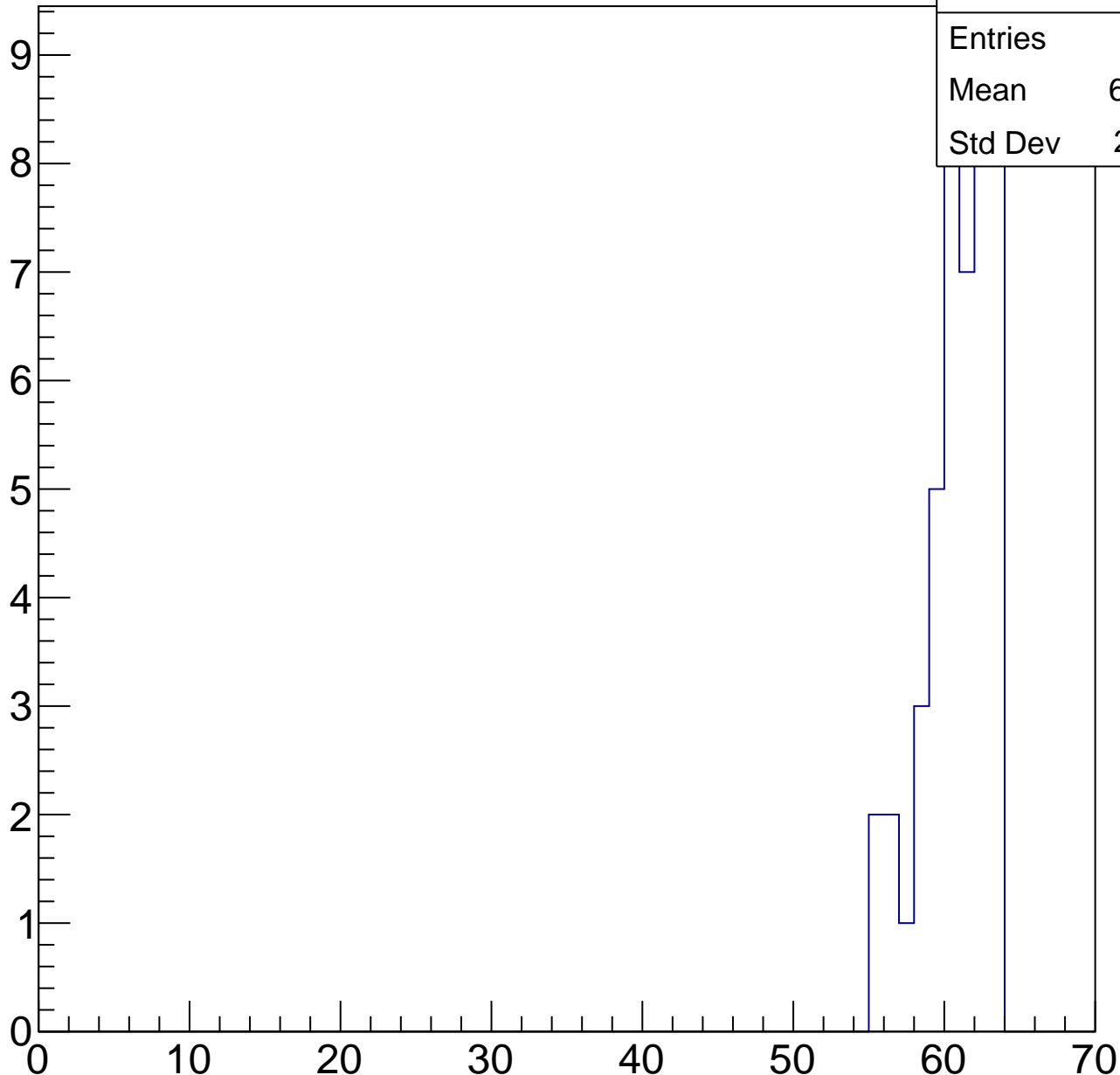
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	45
Mean	60.33
Std Dev	2.181

ampl



B1L103S, U14-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

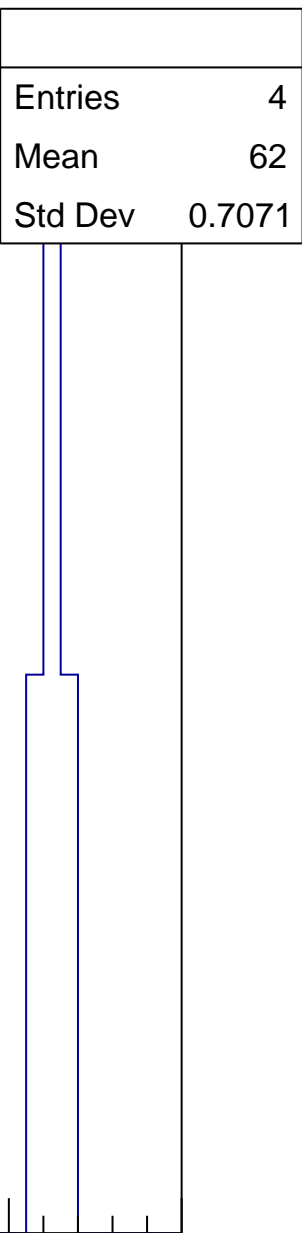
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

0 10 20 30 40 50 60 70

ampl



B1L103S, U14-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch16, adc0

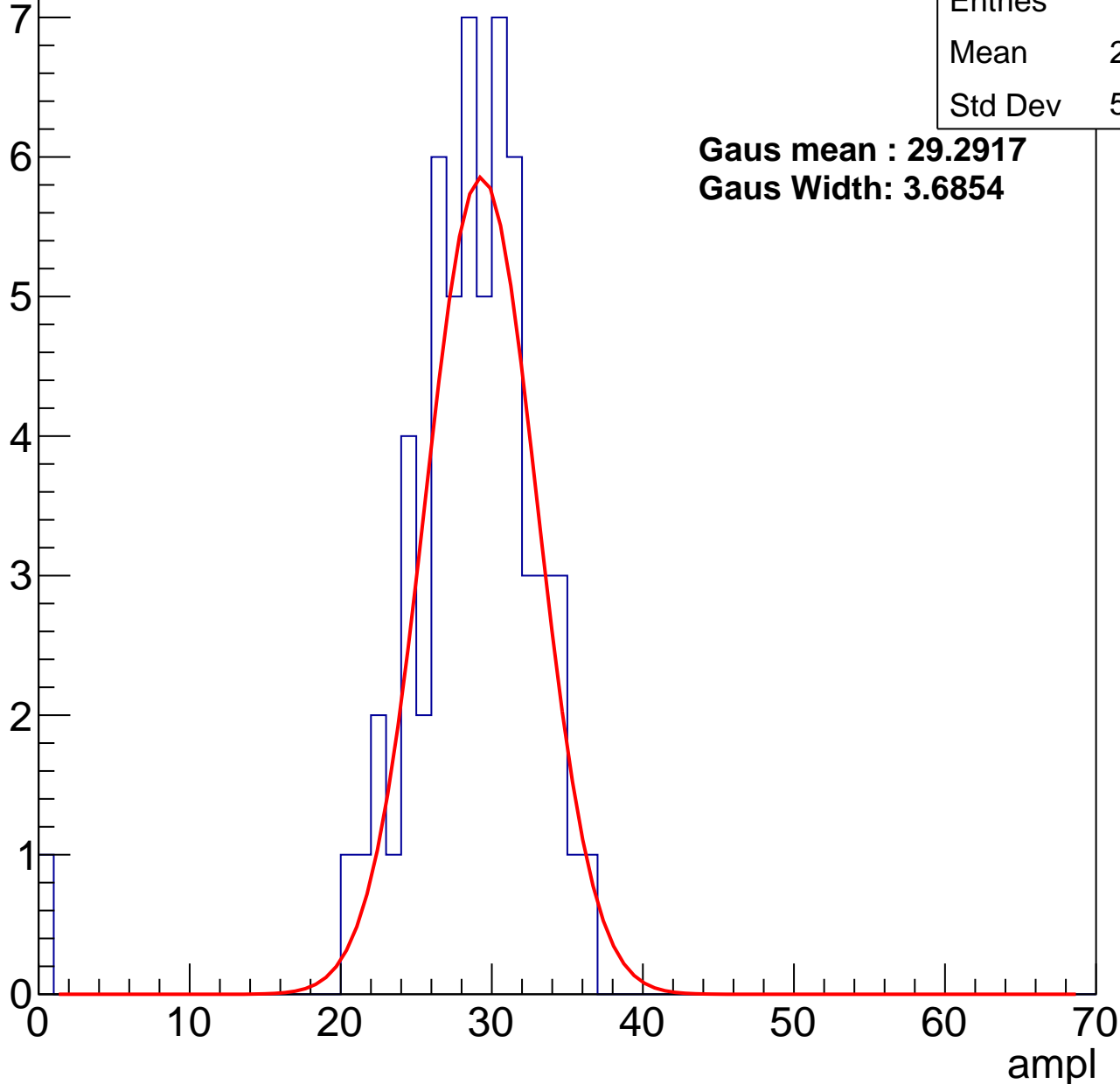
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	27.97
Std Dev	5.102

Gaus mean : 29.2917

Gaus Width: 3.6854



B1L103S, U14-ch16, adc1

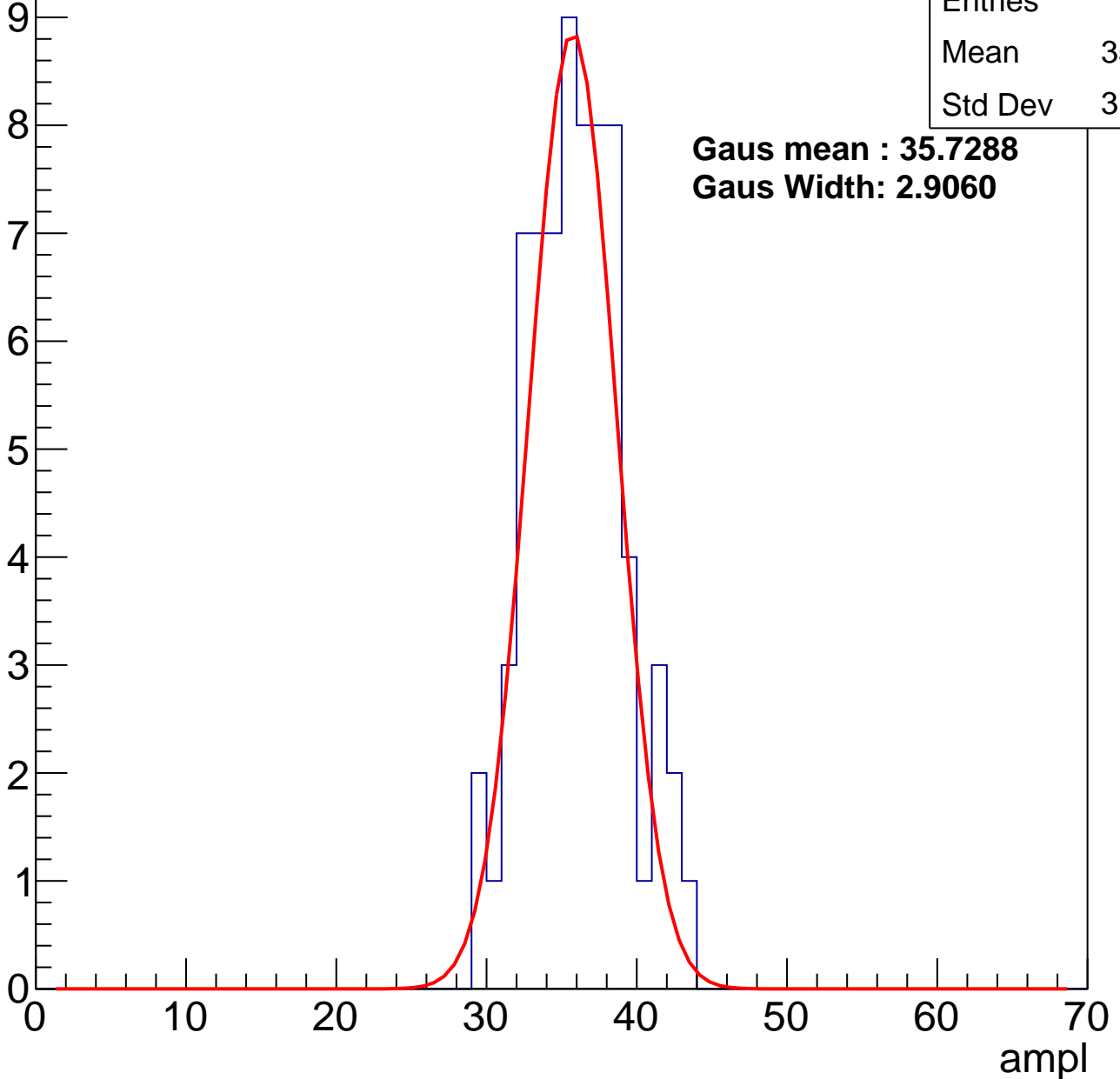
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	35.54
Std Dev	3.126

Gaus mean : 35.7288

Gaus Width: 2.9060



B1L103S, U14-ch16, adc2

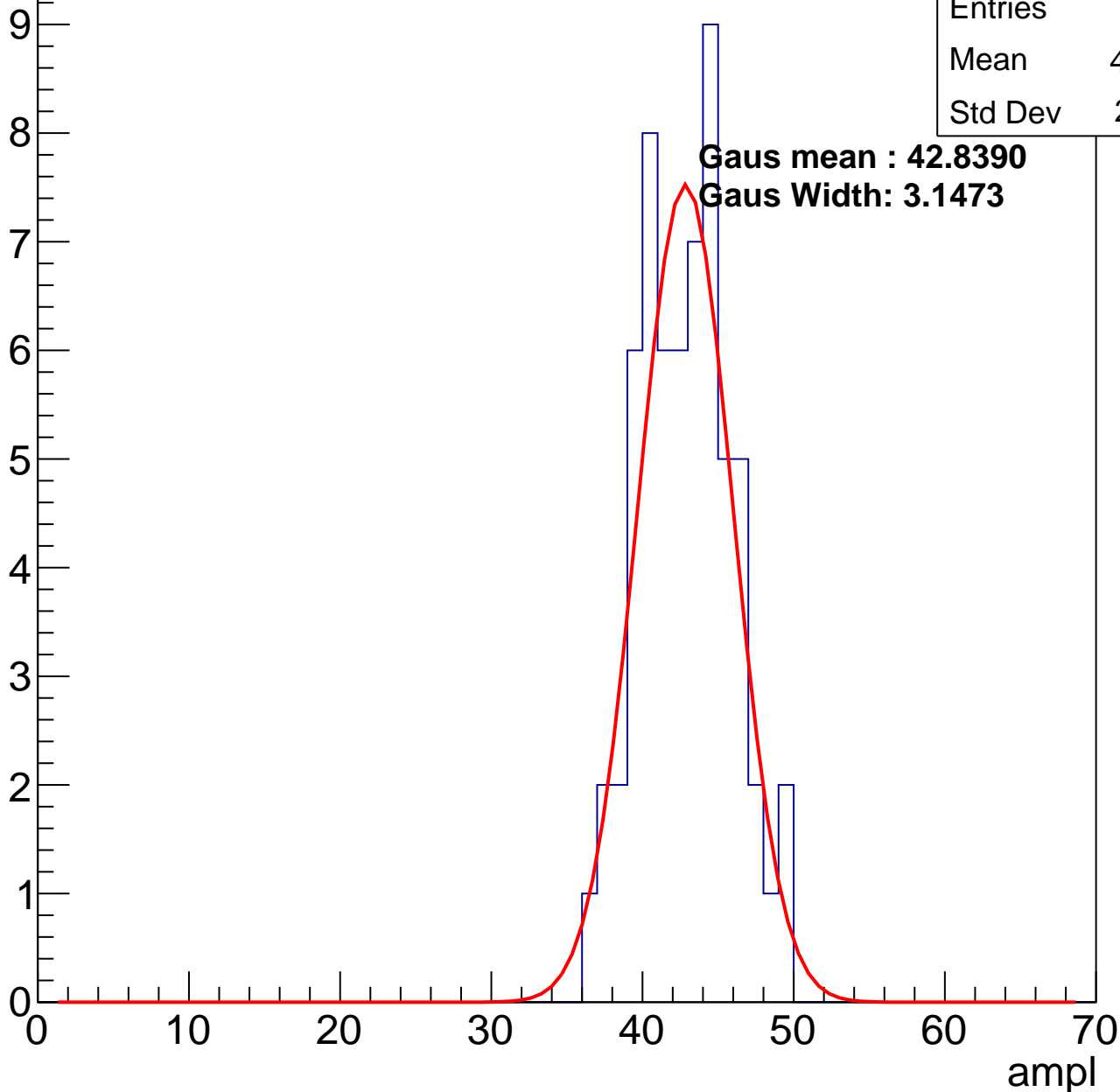
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.42
Std Dev	2.981

Gaus mean : 42.8390

Gaus Width: 3.1473

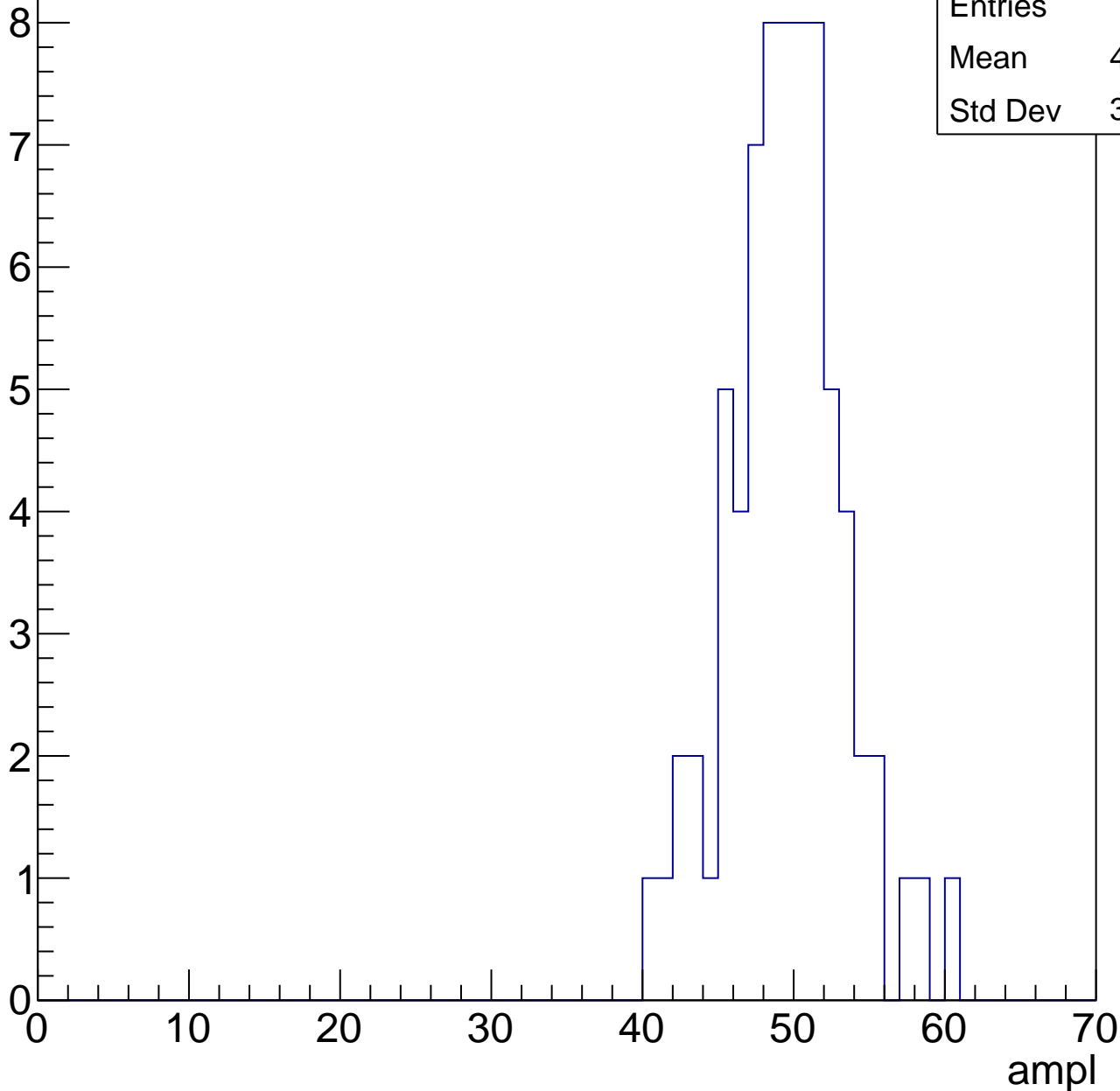


B1L103S, U14-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	49.04
Std Dev	3.807

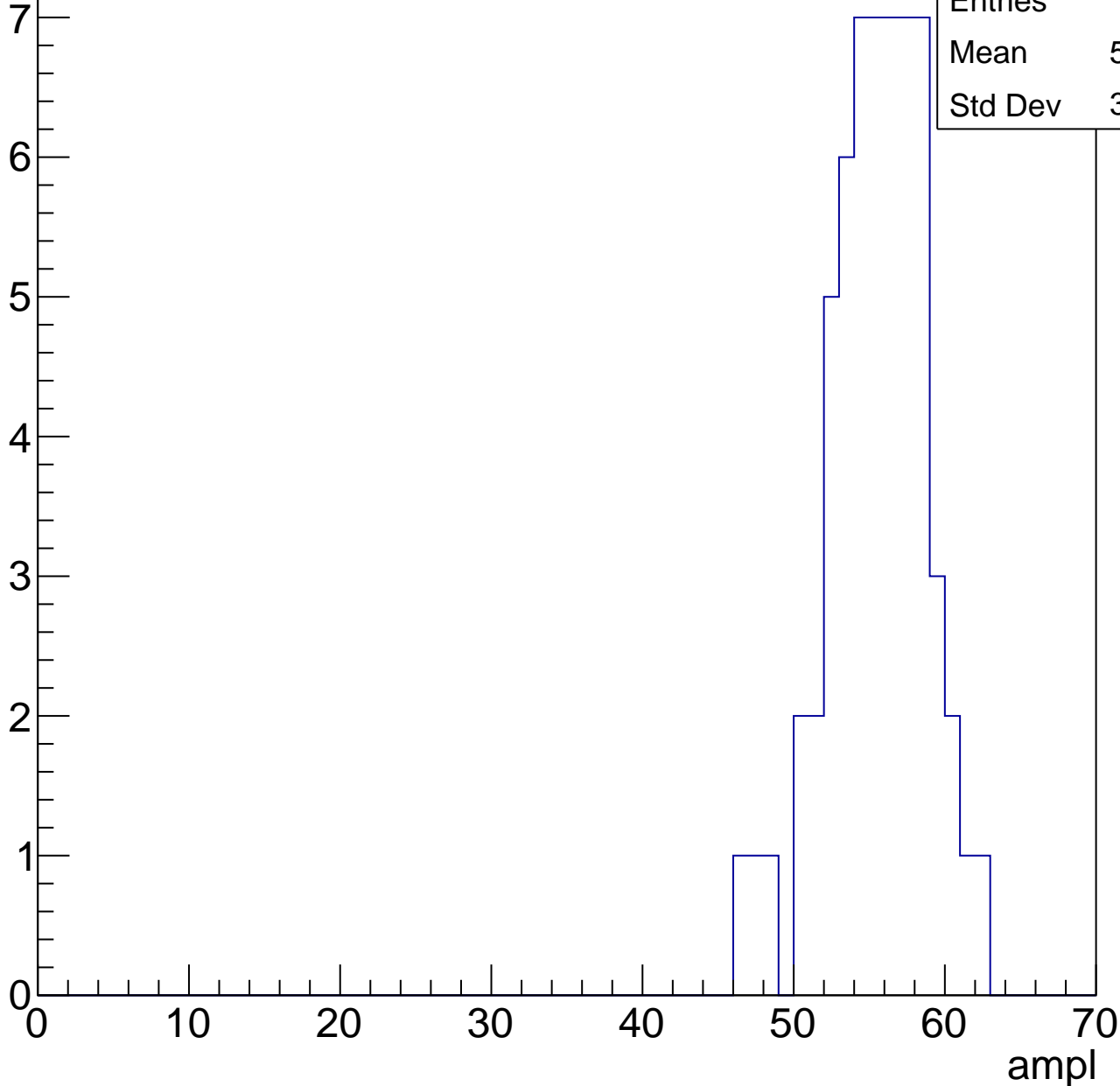


B1L103S, U14-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.02
Std Dev	3.248



B1L103S, U14-ch16, adc5

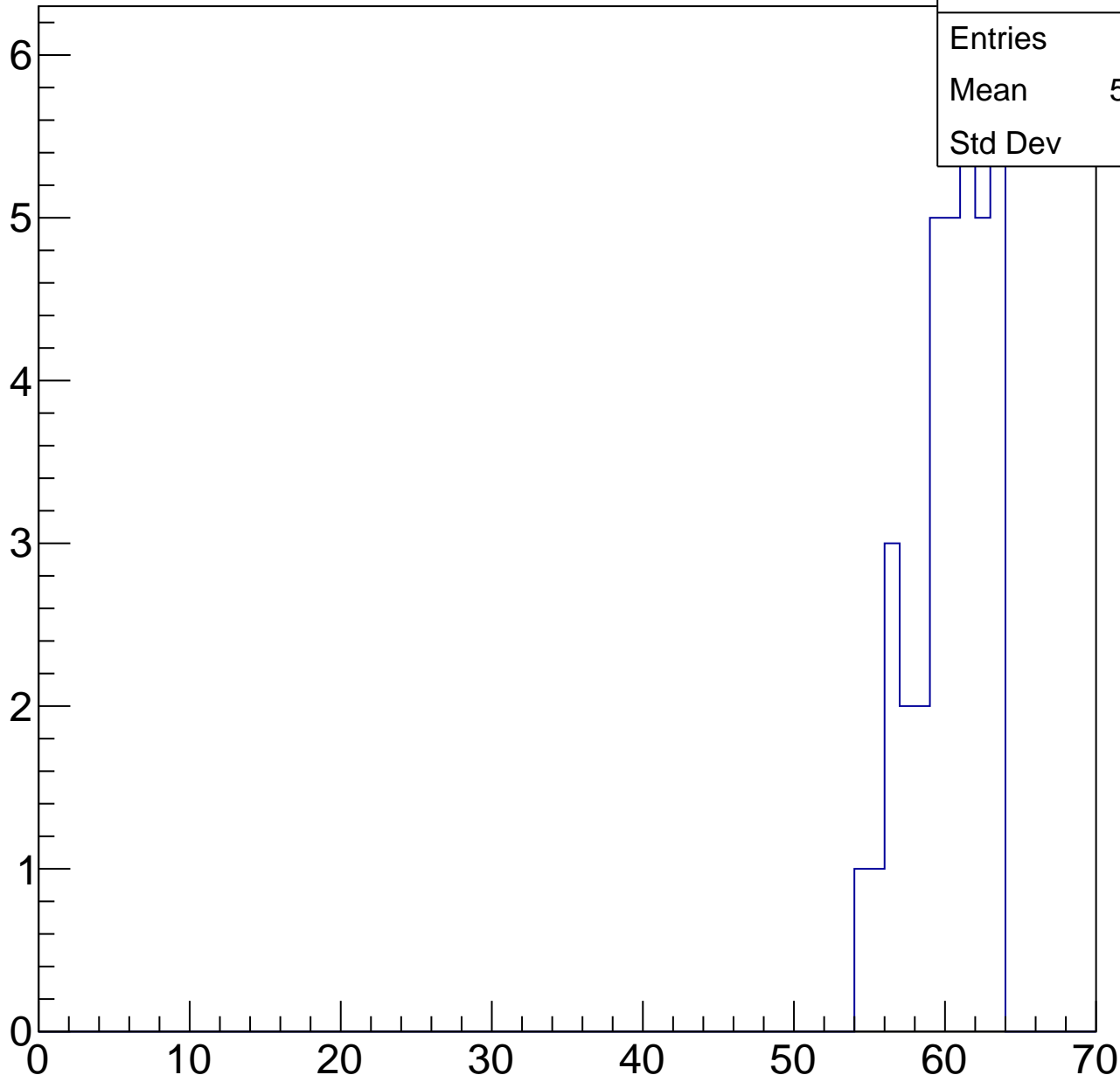
calib_packv5_042523_0143.root, FC#7, port C2

Entry

6
5
4
3
2
1
0

Entries	36
Mean	59.89
Std Dev	2.47

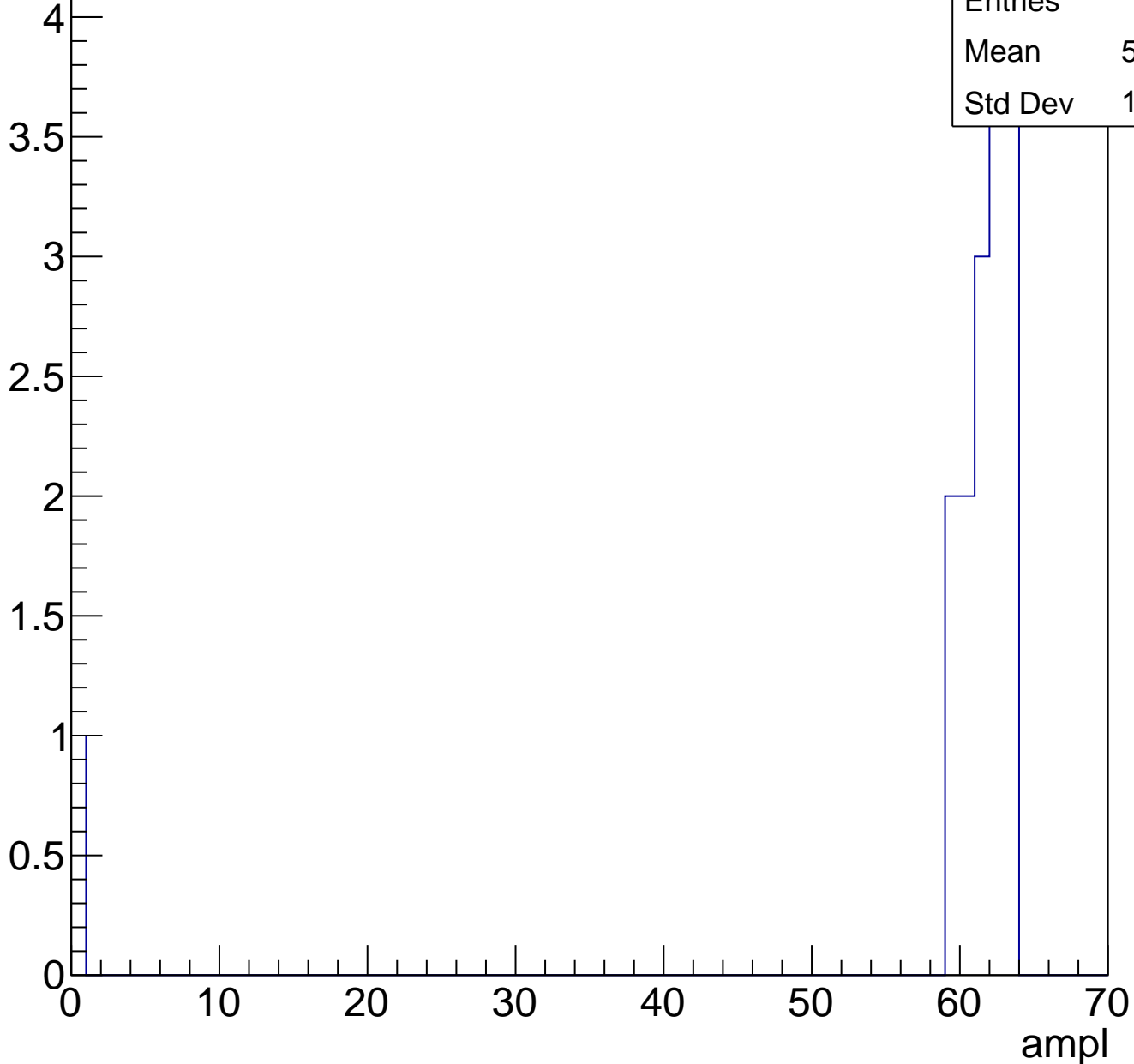
ampl



B1L103S, U14-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch17, adc0

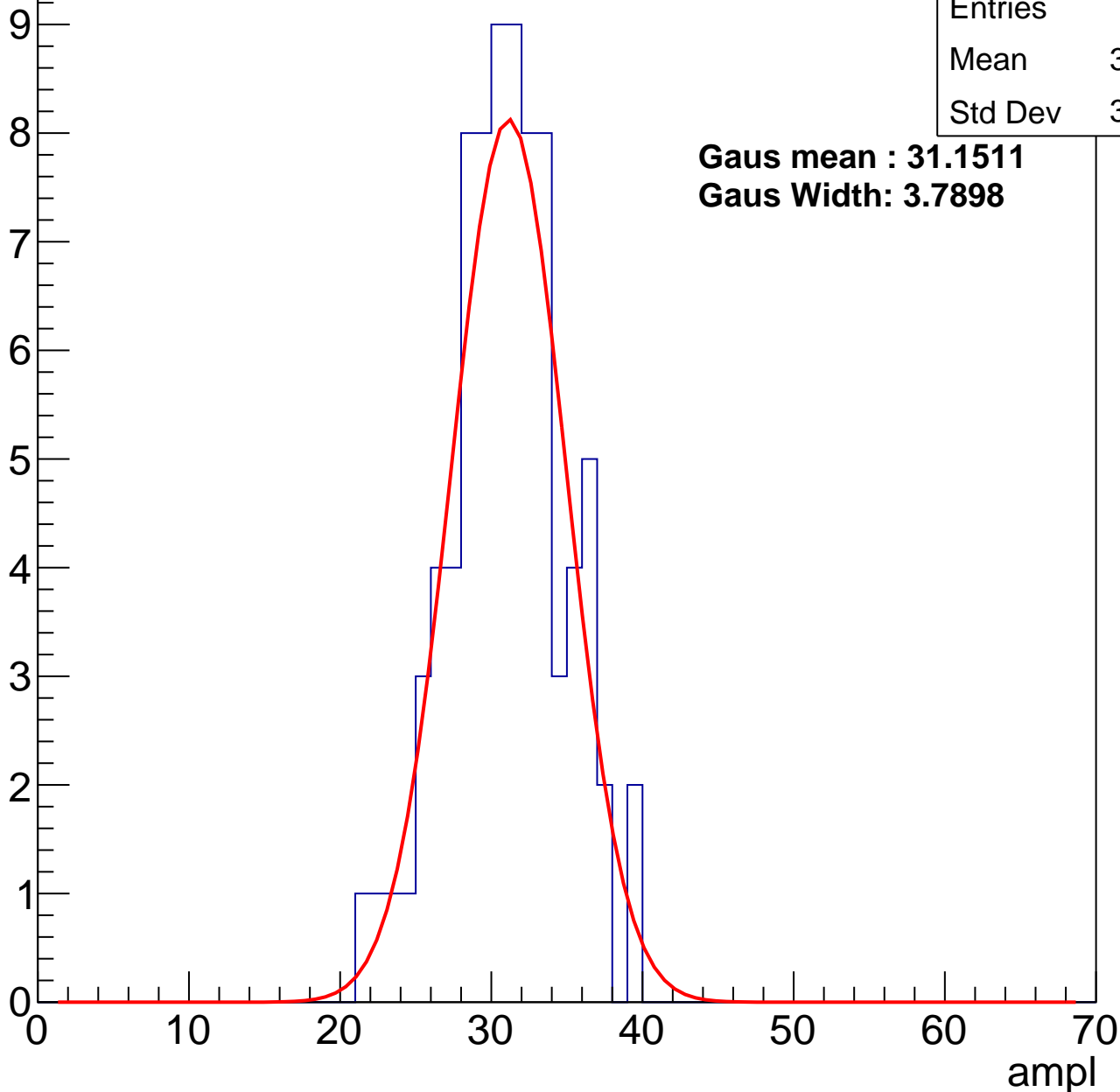
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	30.57
Std Dev	3.718

Gaus mean : 31.1511

Gaus Width: 3.7898



B1L103S, U14-ch17, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	49
Mean	36.67
Std Dev	3.086

Gaus mean : 37.4637

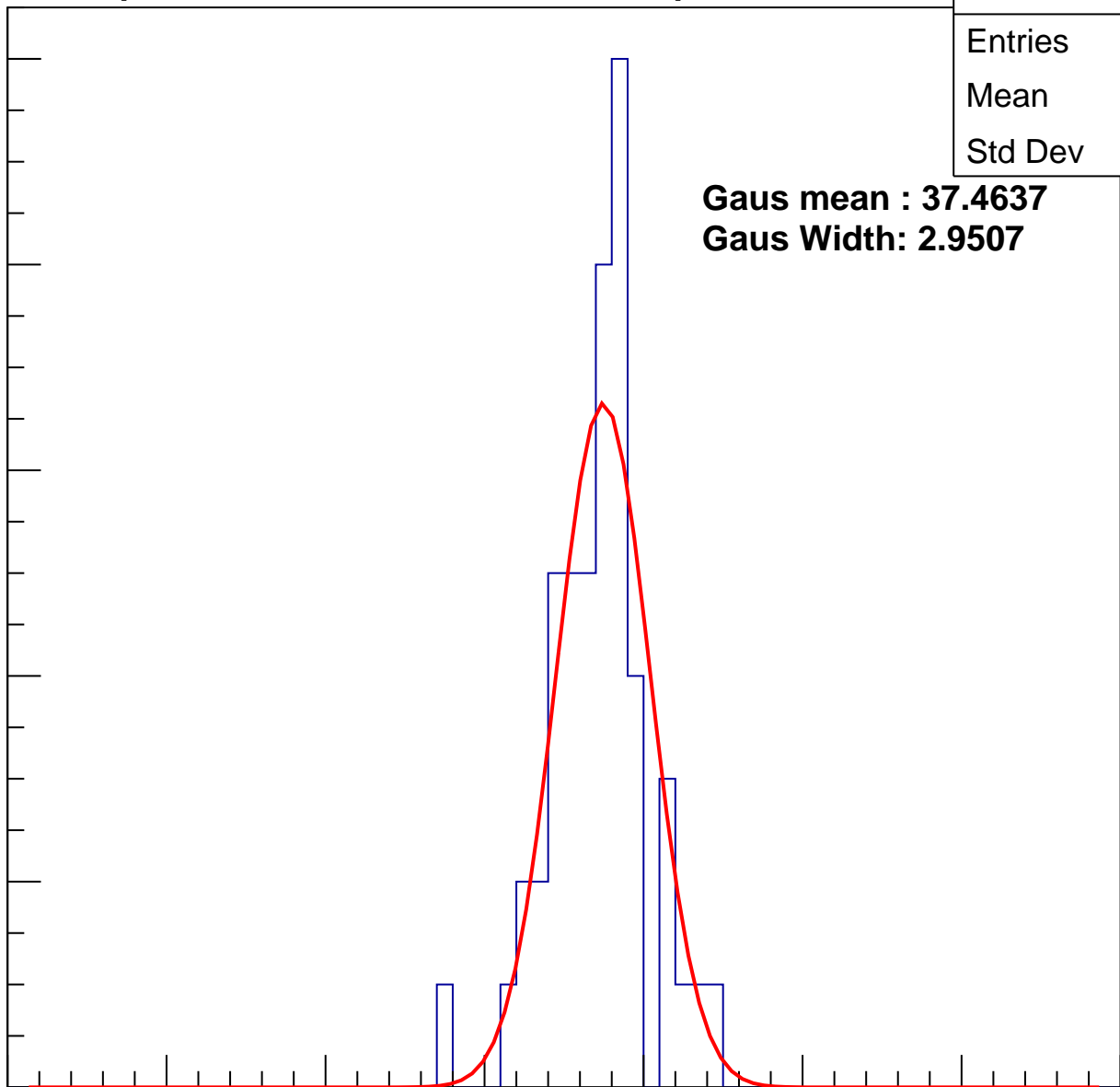
Gaus Width: 2.9507

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch17, adc2

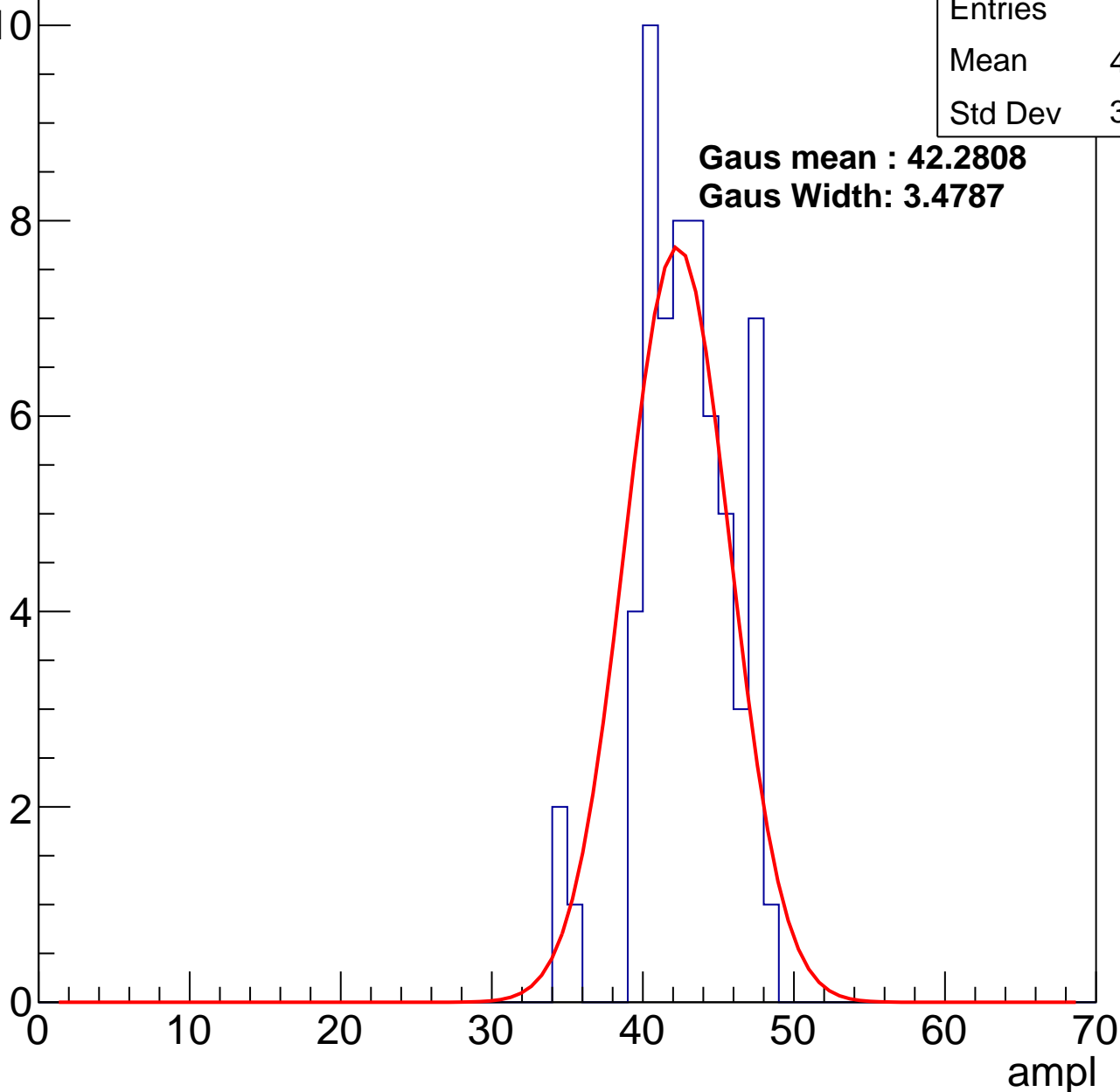
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.42
Std Dev	3.072

Gaus mean : 42.2808

Gaus Width: 3.4787

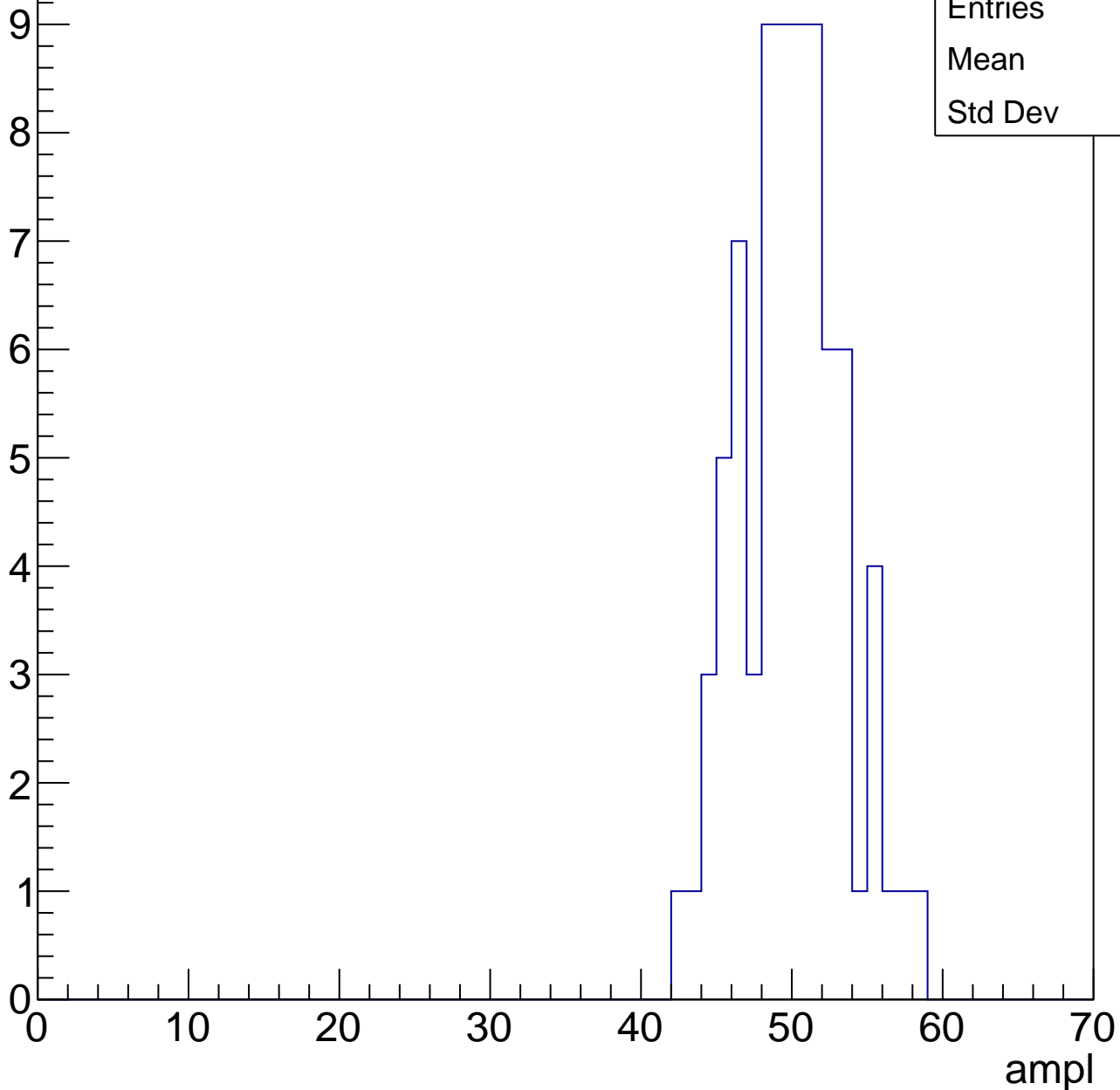


B1L103S, U14-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	49.5
Std Dev	3.37

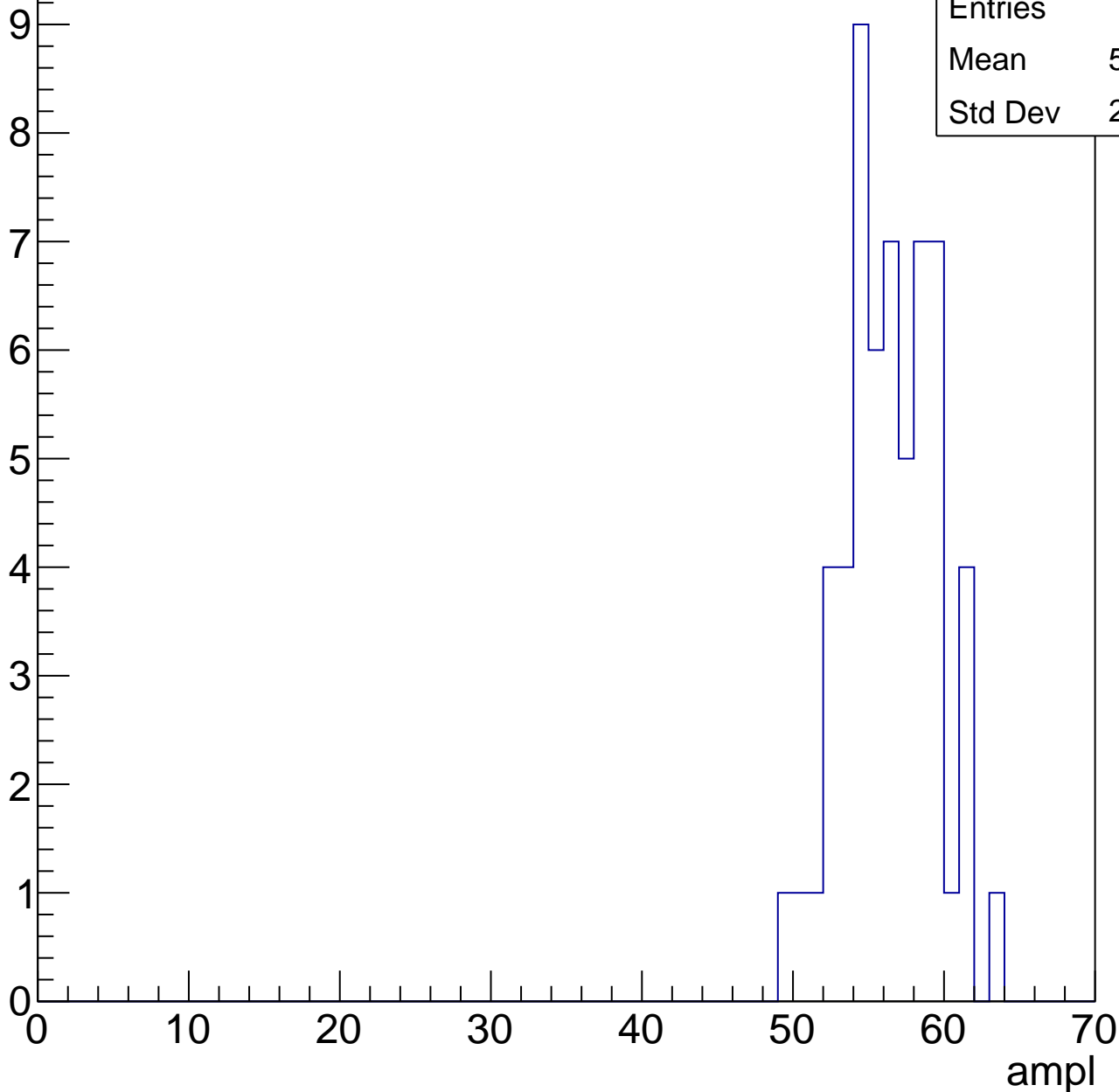


B1L103S, U14-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	56.02
Std Dev	2.974

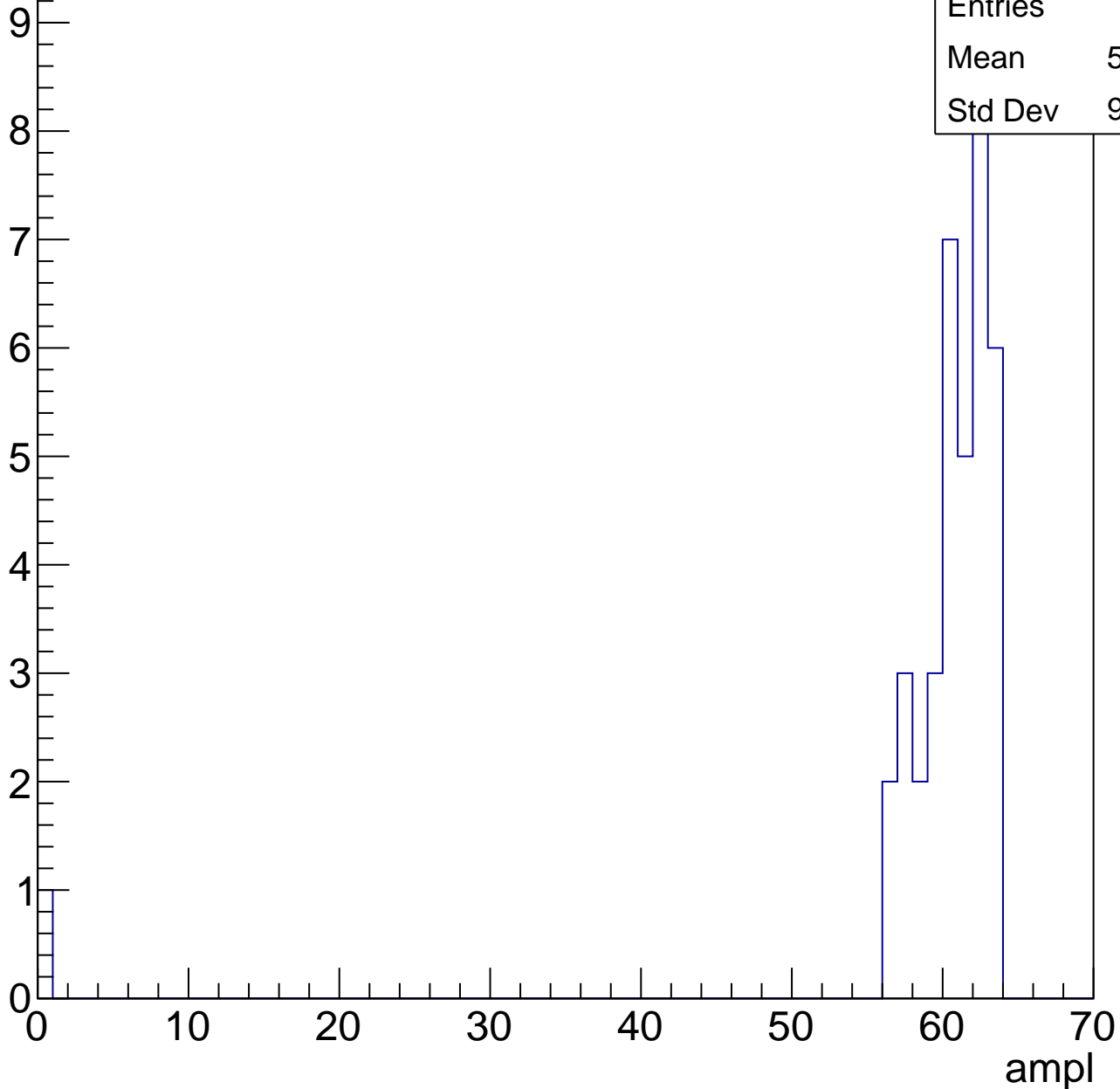


B1L103S, U14-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

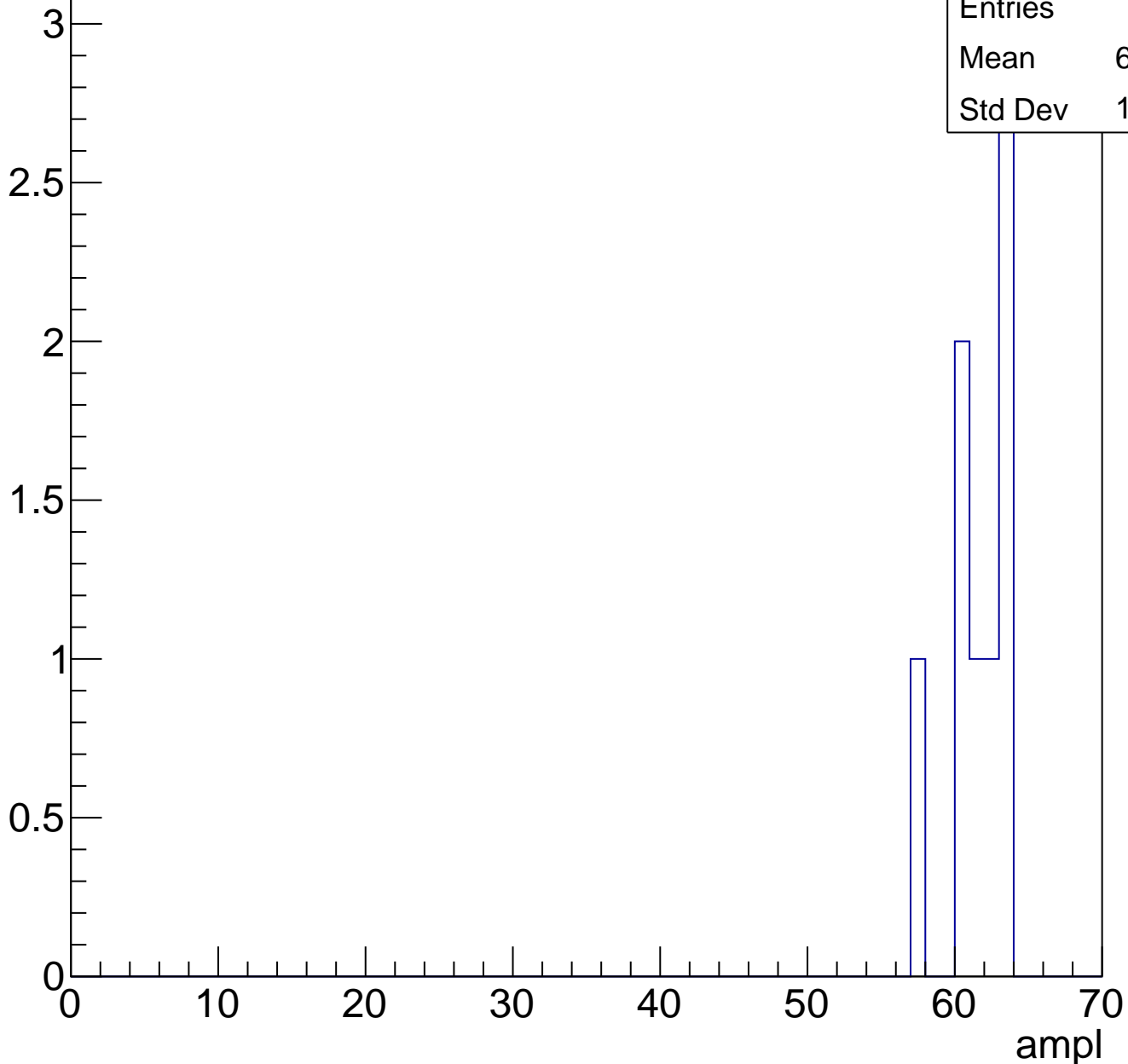
Entries	38
Mean	58.87
Std Dev	9.889



B1L103S, U14-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch18, adc0

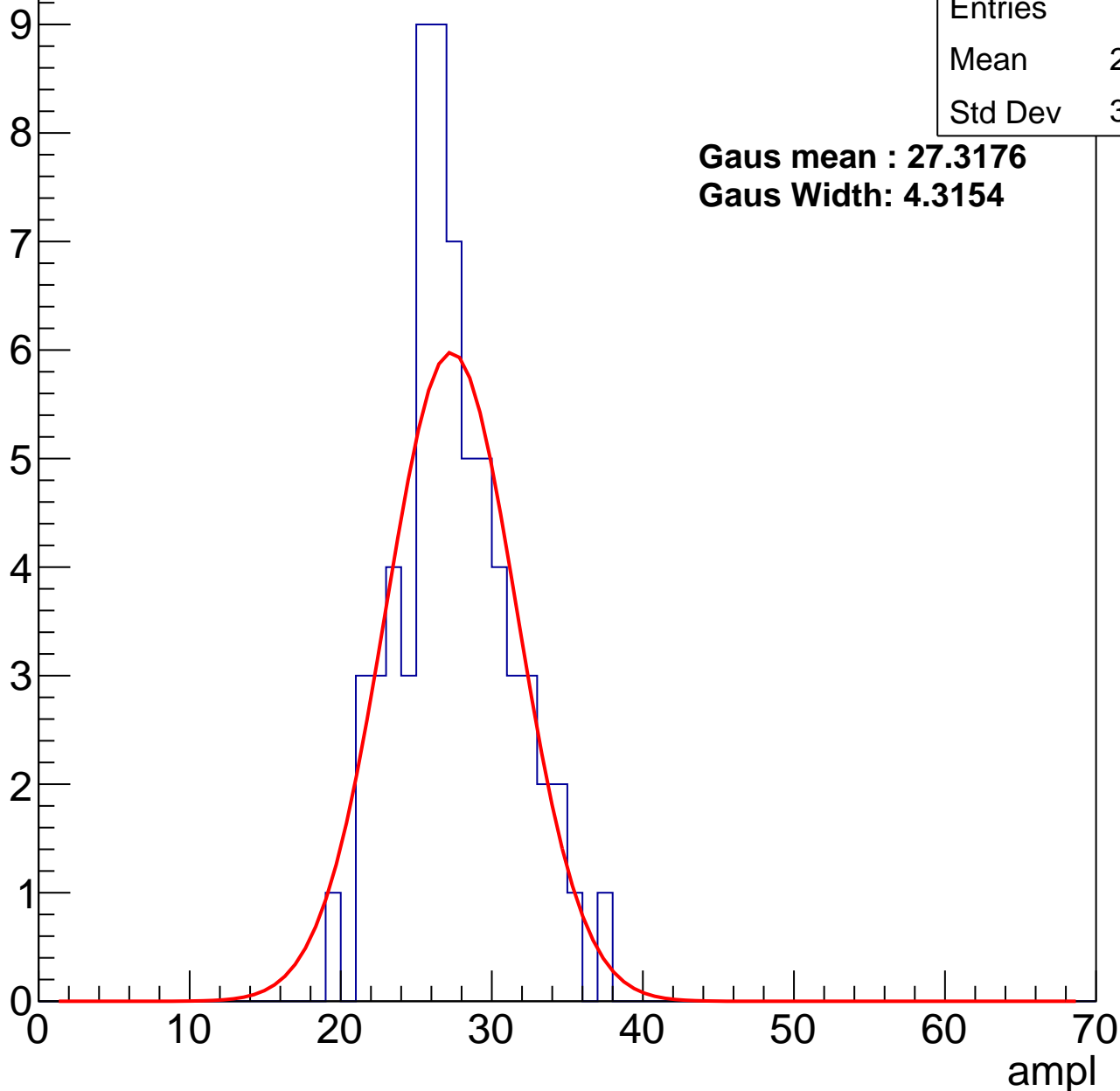
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	27.08
Std Dev	3.722

Gaus mean : 27.3176

Gaus Width: 4.3154



B1L103S, U14-ch18, adc1

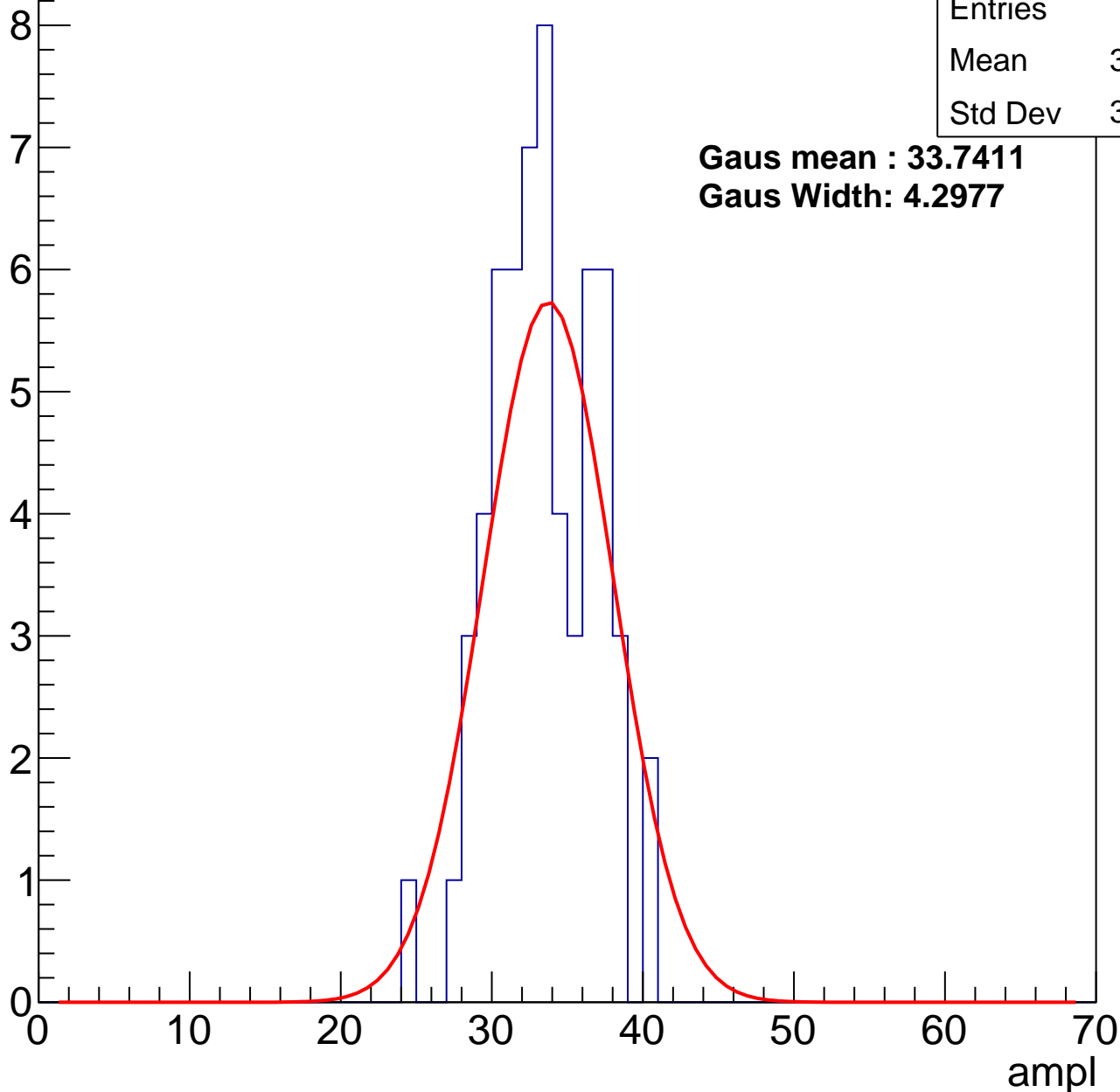
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	32.97
Std Dev	3.366

Gaus mean : 33.7411

Gaus Width: 4.2977



B1L103S, U14-ch18, adc2

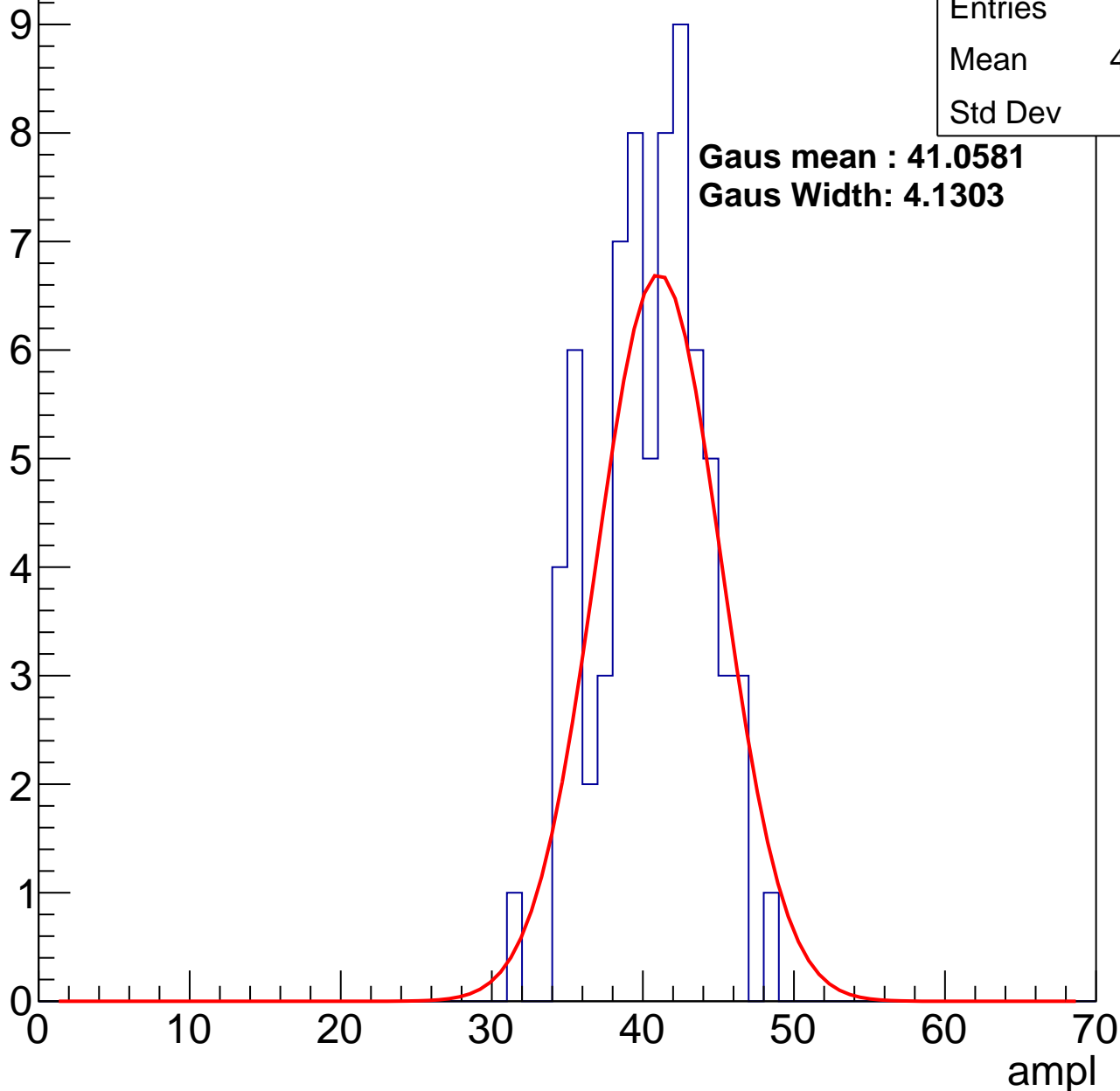
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	40.04
Std Dev	3.55

Gaus mean : 41.0581

Gaus Width: 4.1303

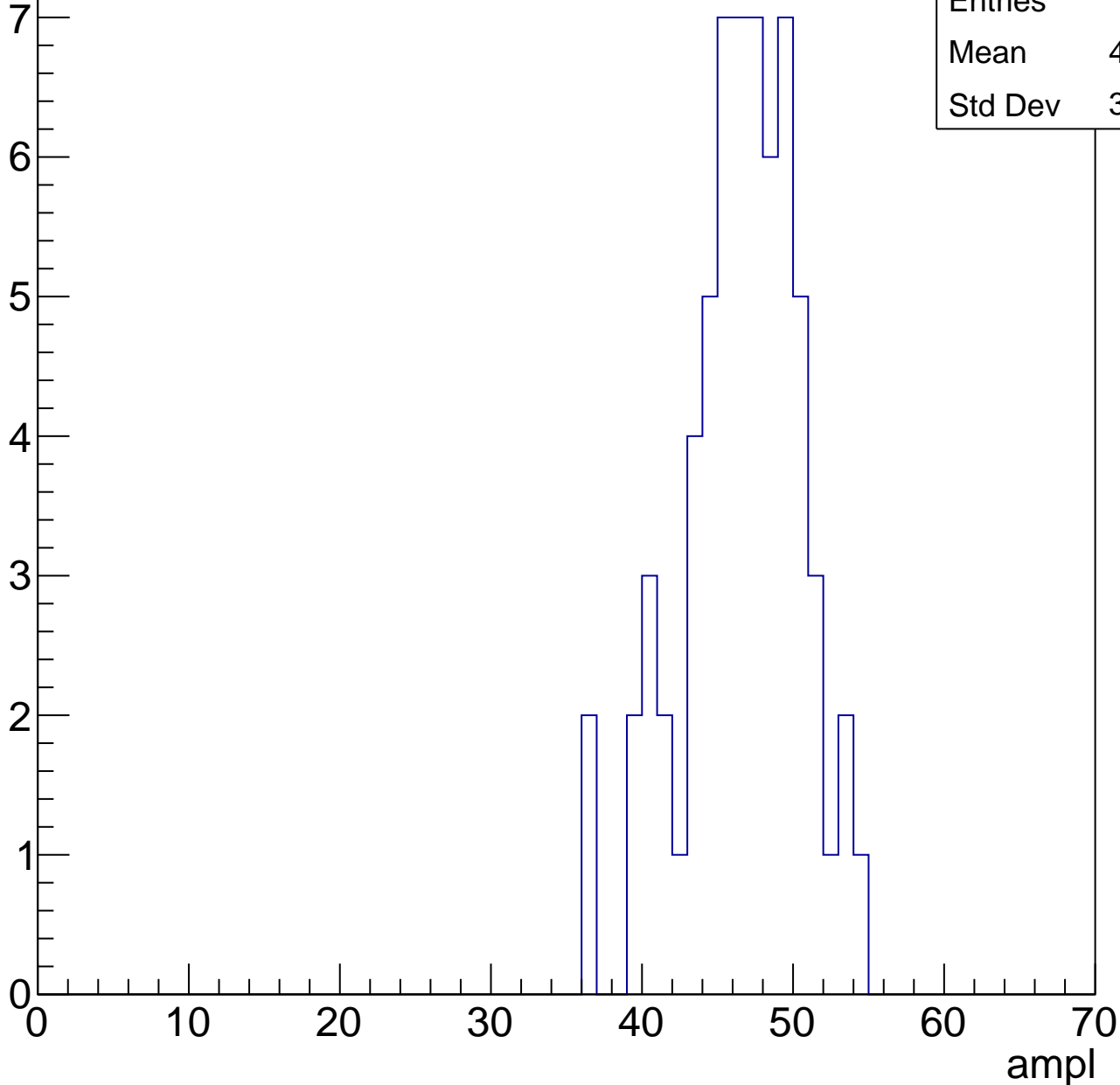


B1L103S, U14-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	46.12
Std Dev	3.885

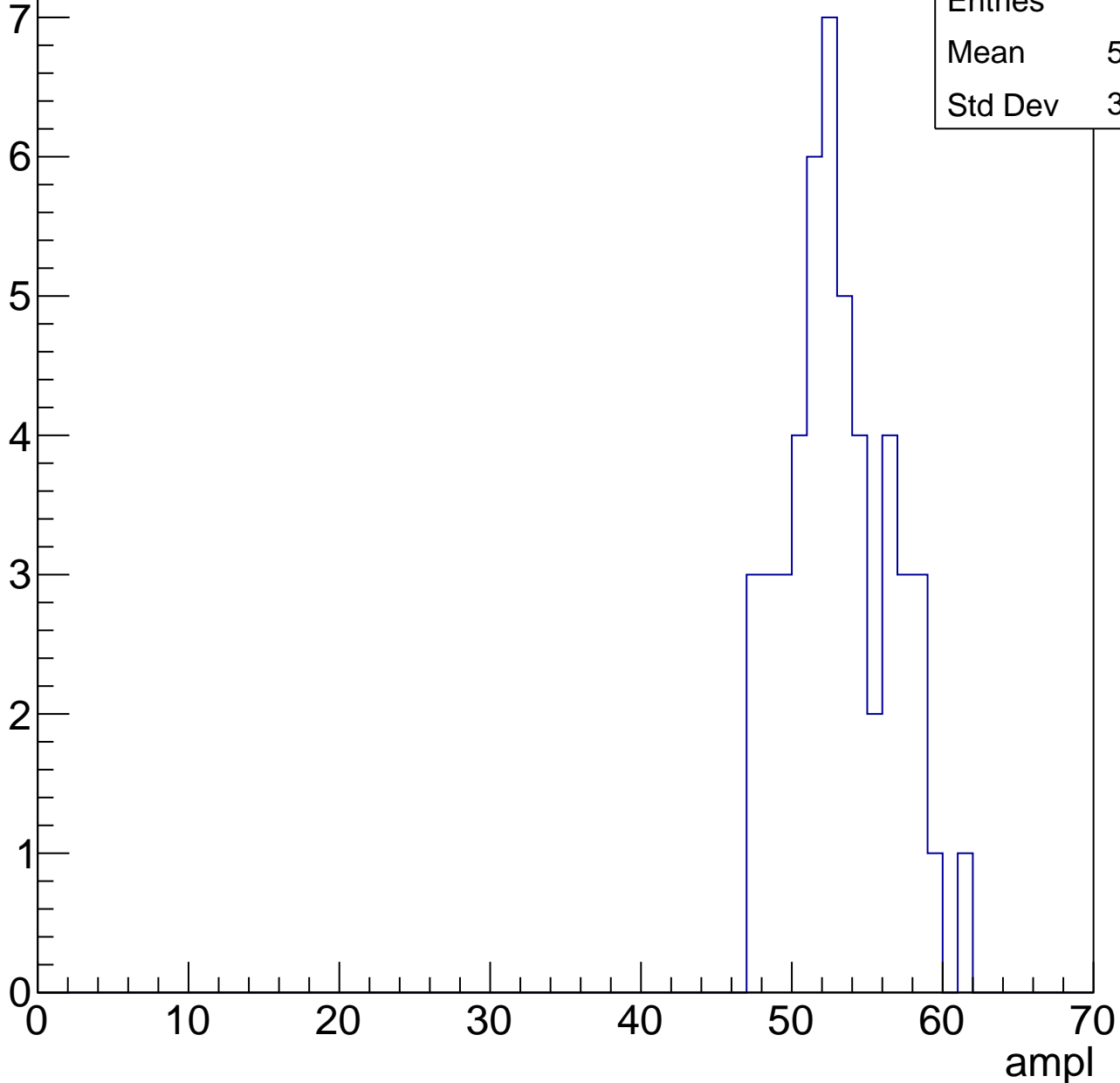


B1L103S, U14-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

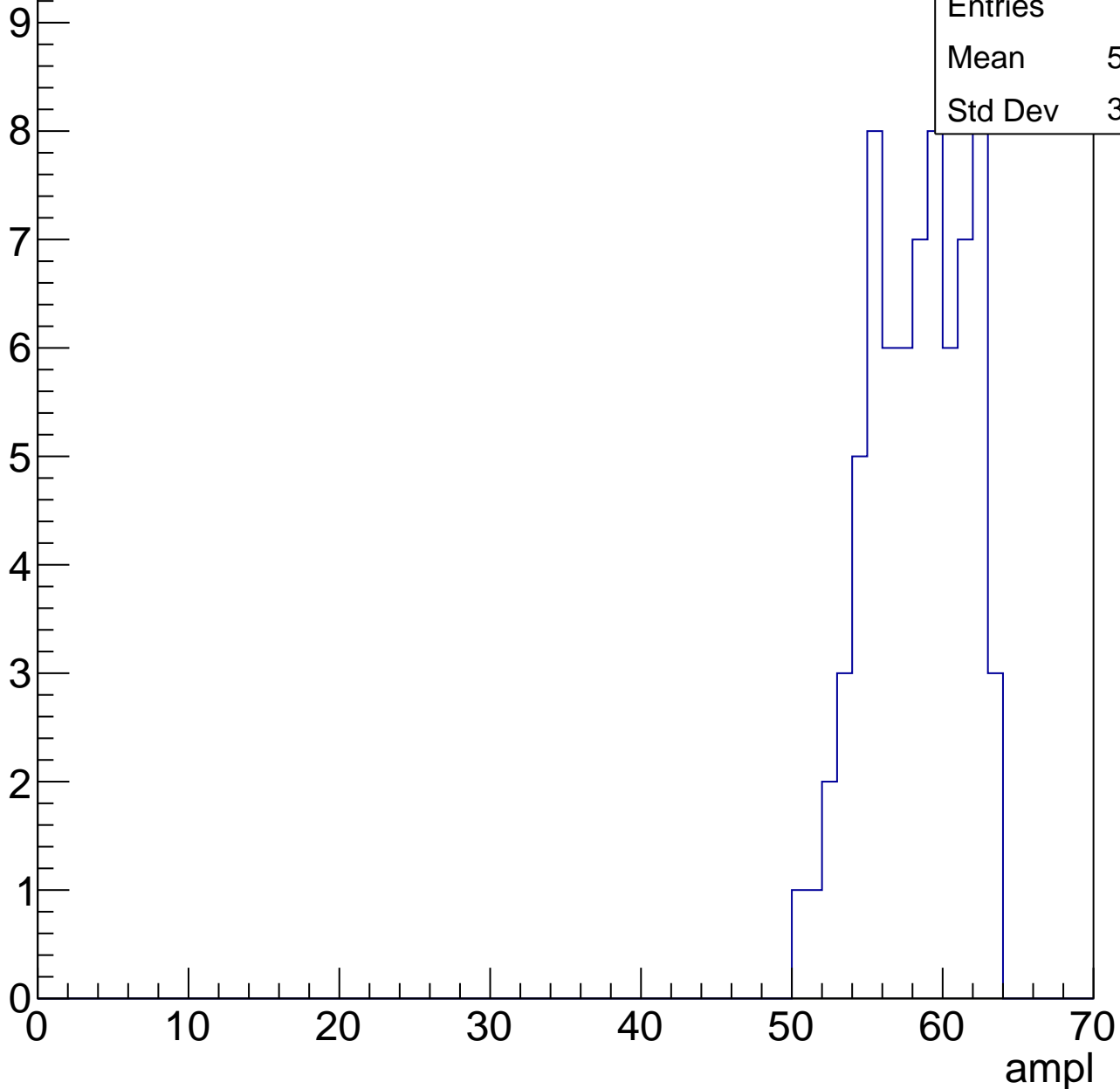
Entries	49
Mean	52.69
Std Dev	3.394



B1L103S, U14-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

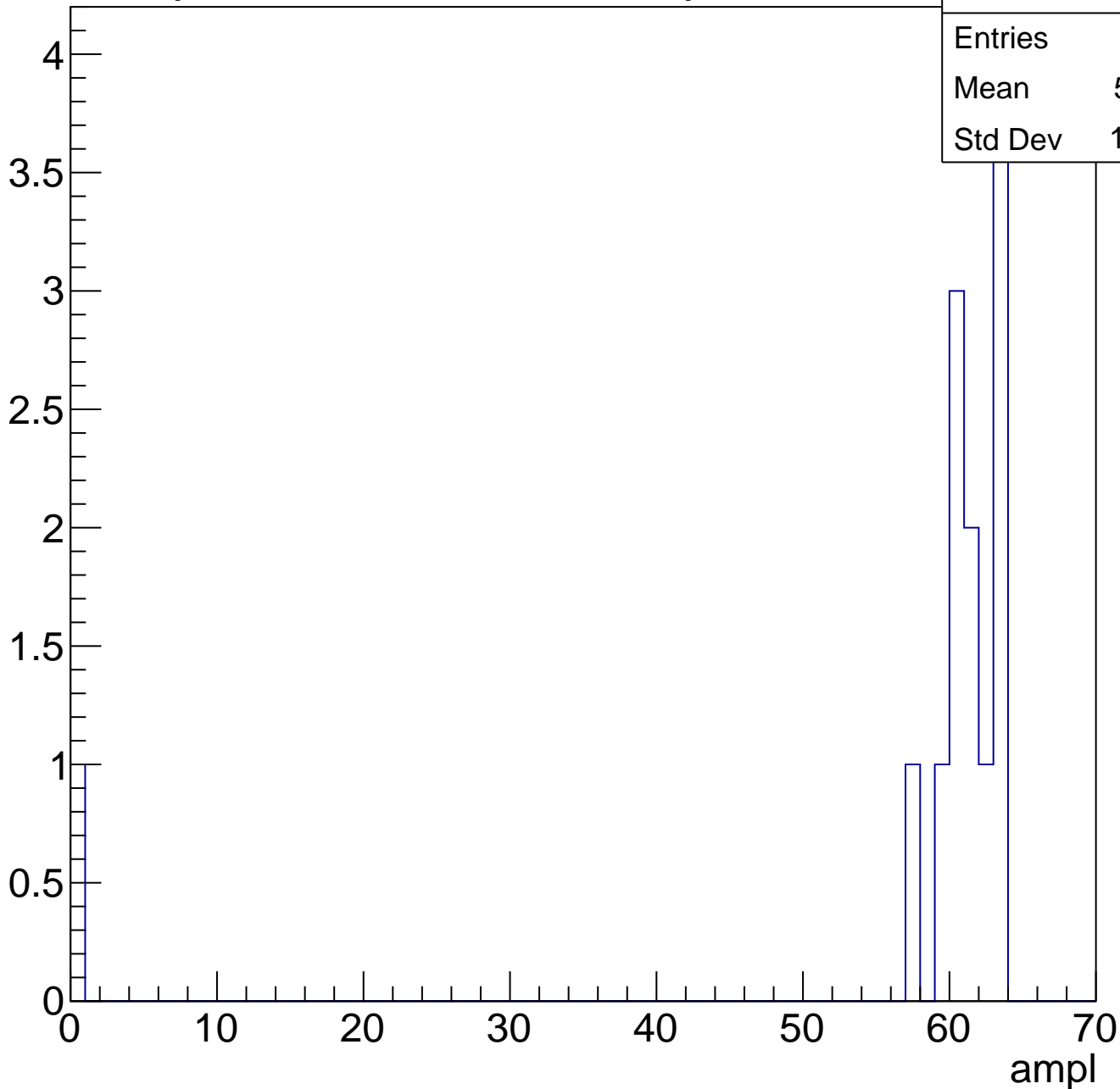


Entries	72
Mean	57.83
Std Dev	3.236

B1L103S, U14-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

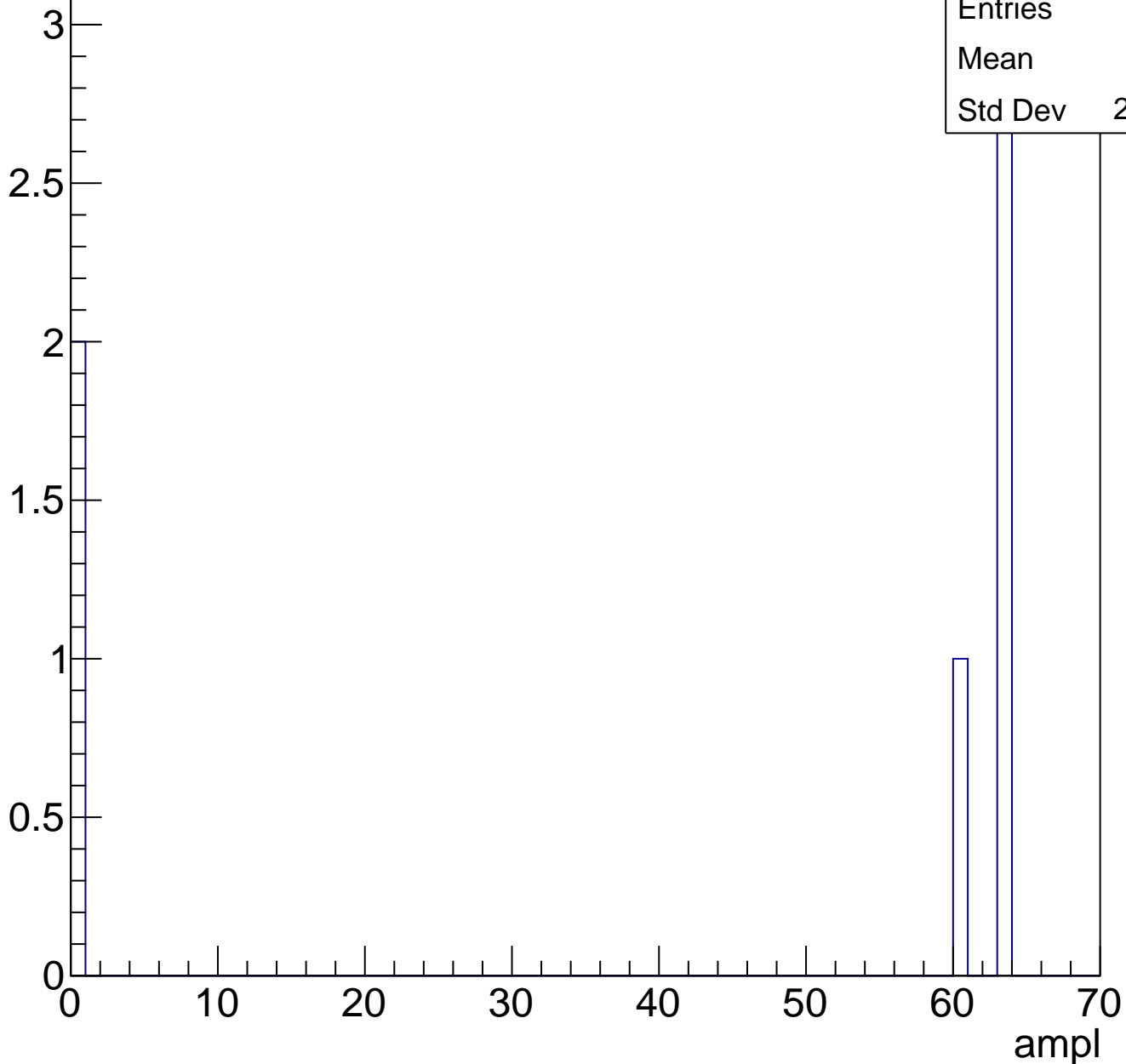
Entry



B1L103S, U14-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch19, adc0

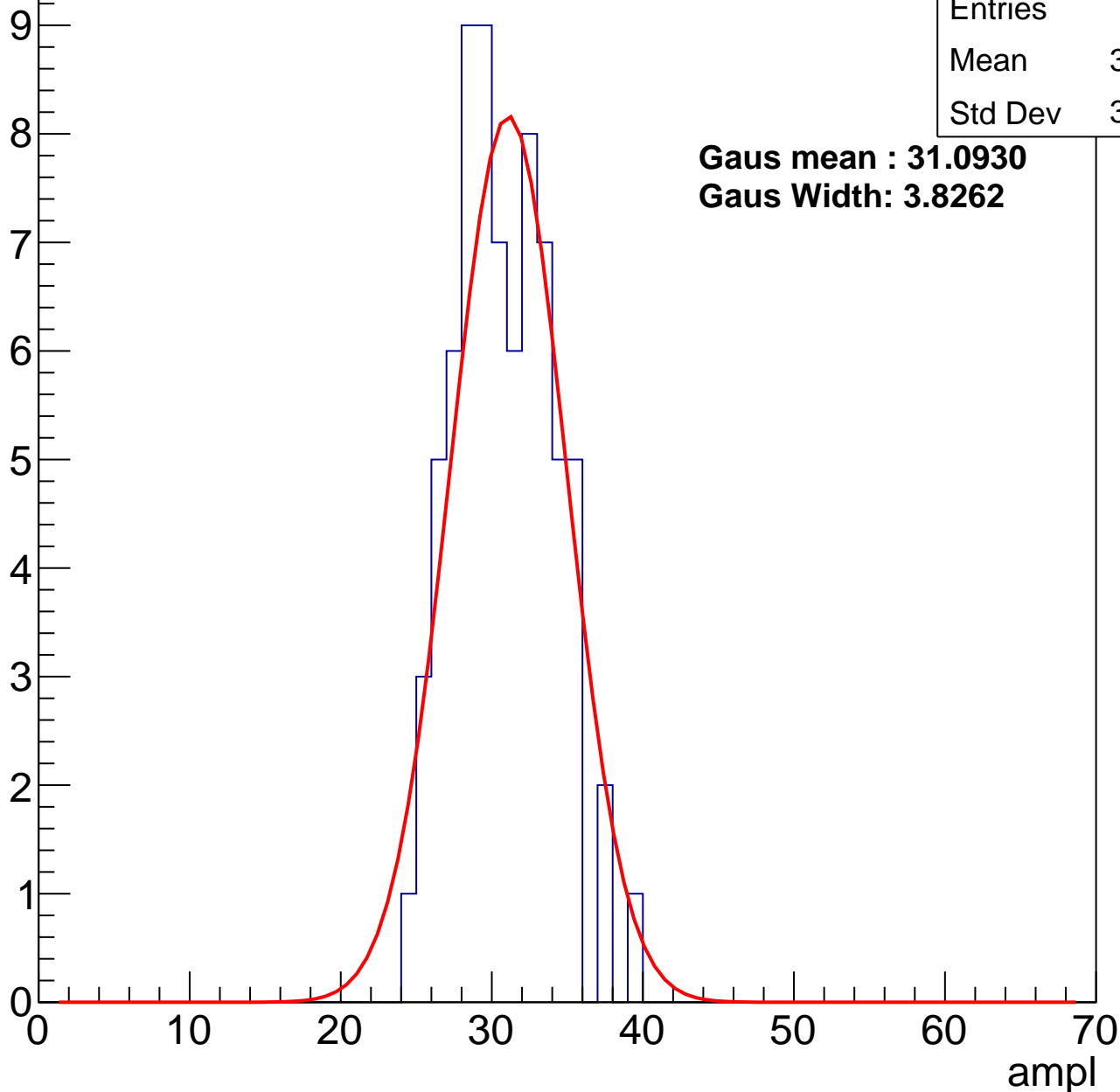
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	30.34
Std Dev	3.219

Gaus mean : 31.0930

Gaus Width: 3.8262



B1L103S, U14-ch19, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	37.3
Std Dev	3.516

Gaus mean : 38.0602

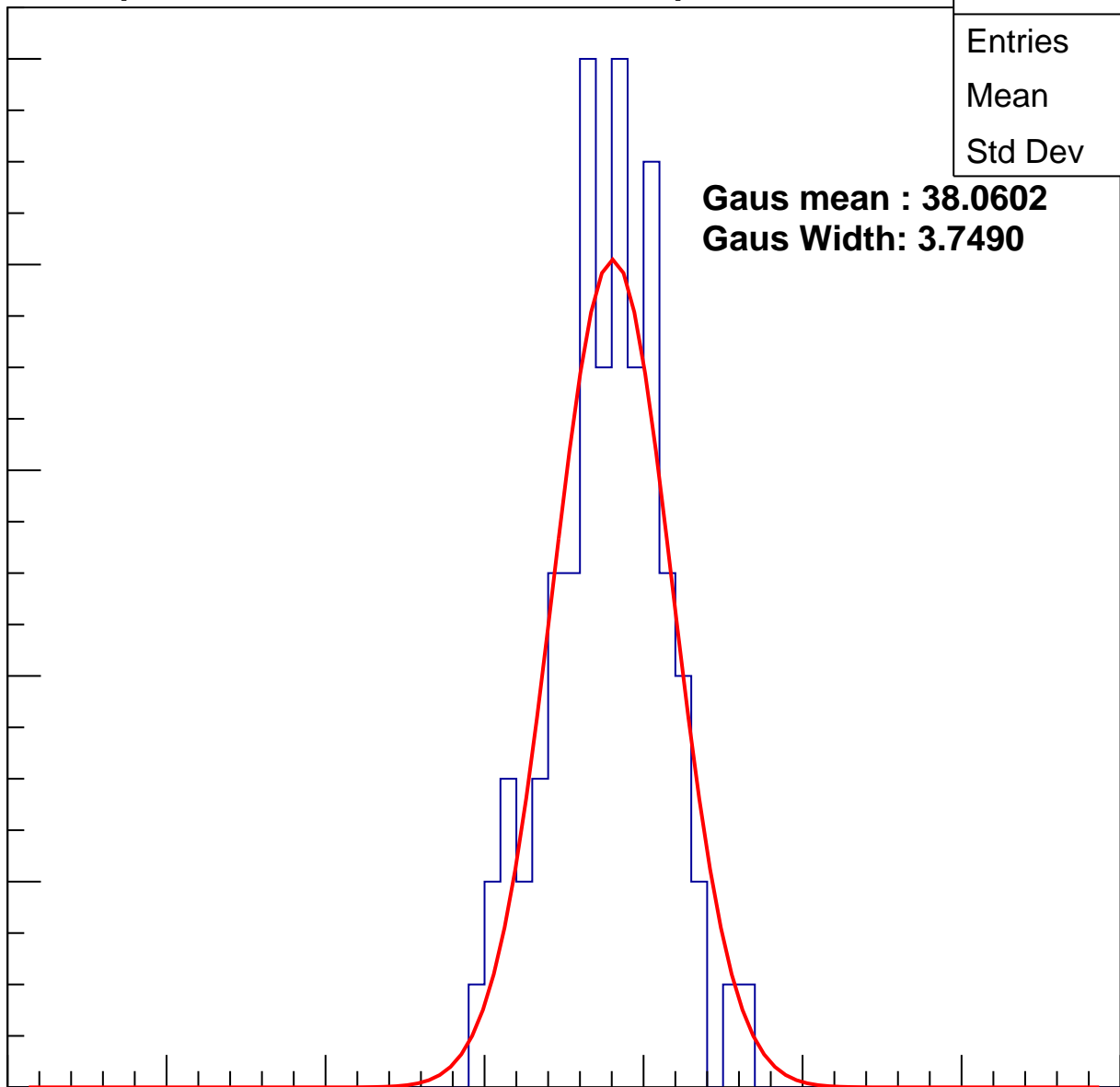
Gaus Width: 3.7490

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

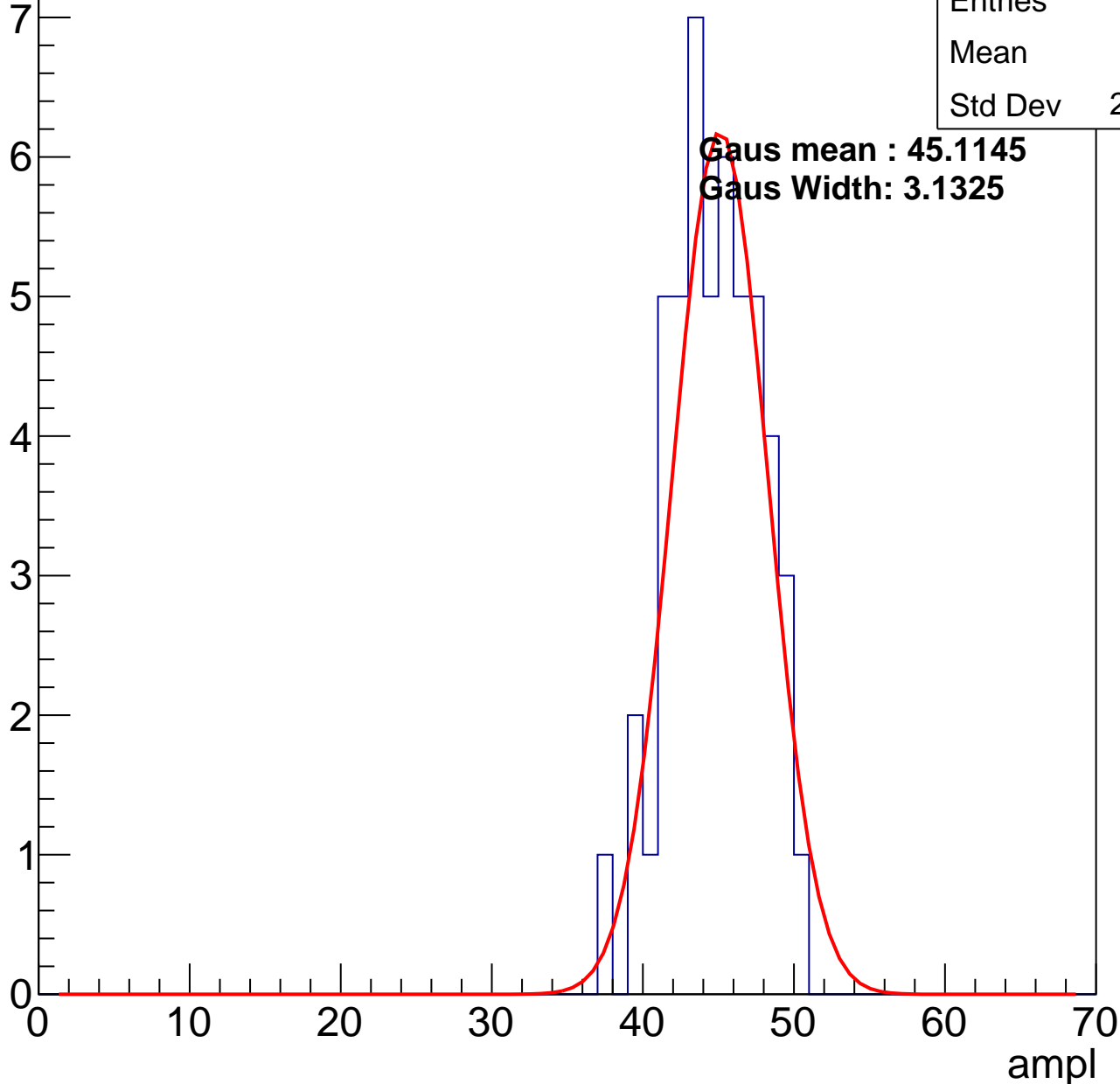


B1L103S, U14-ch19, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	44.3
Std Dev	2.927

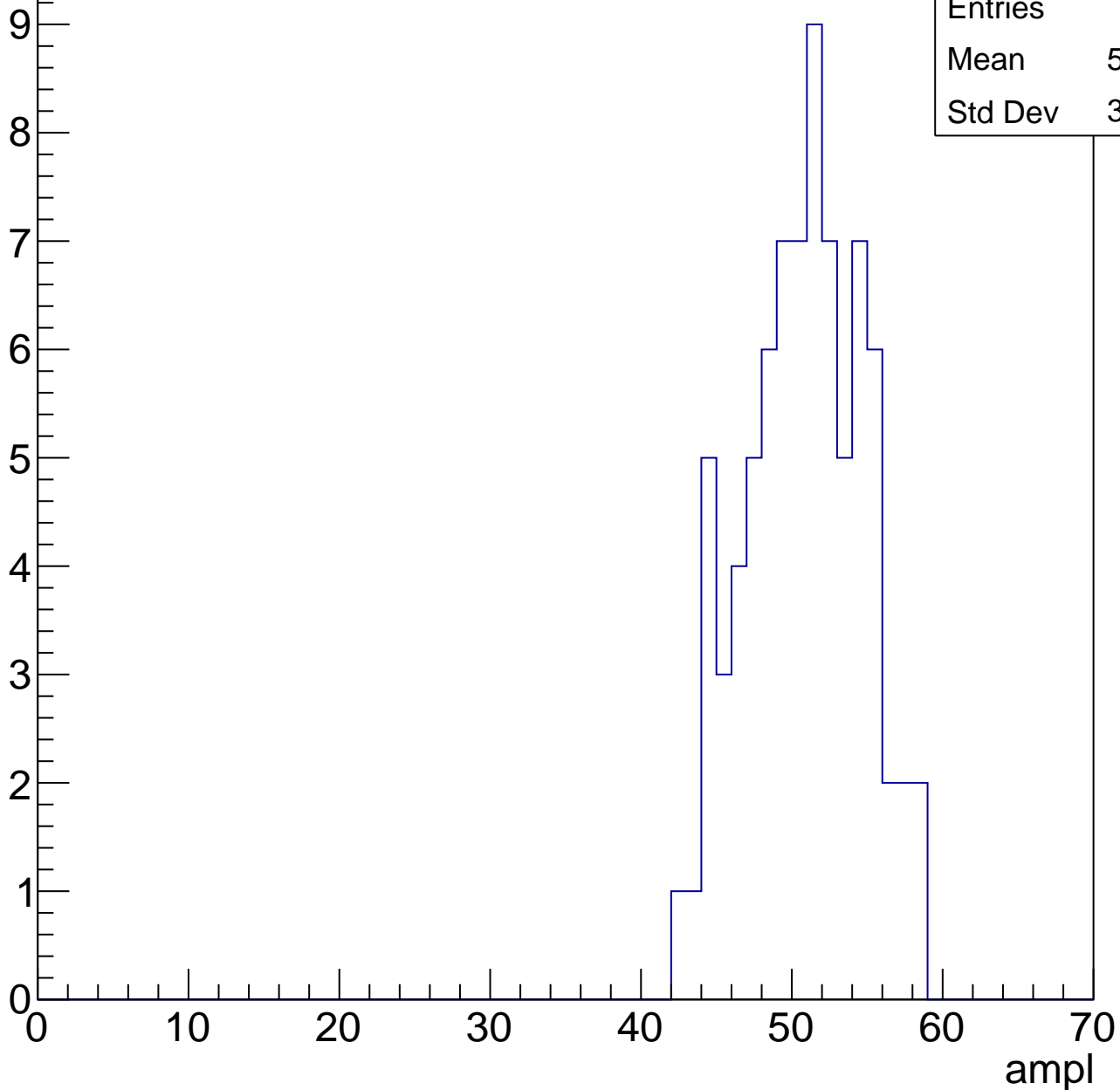


B1L103S, U14-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	50.35
Std Dev	3.799

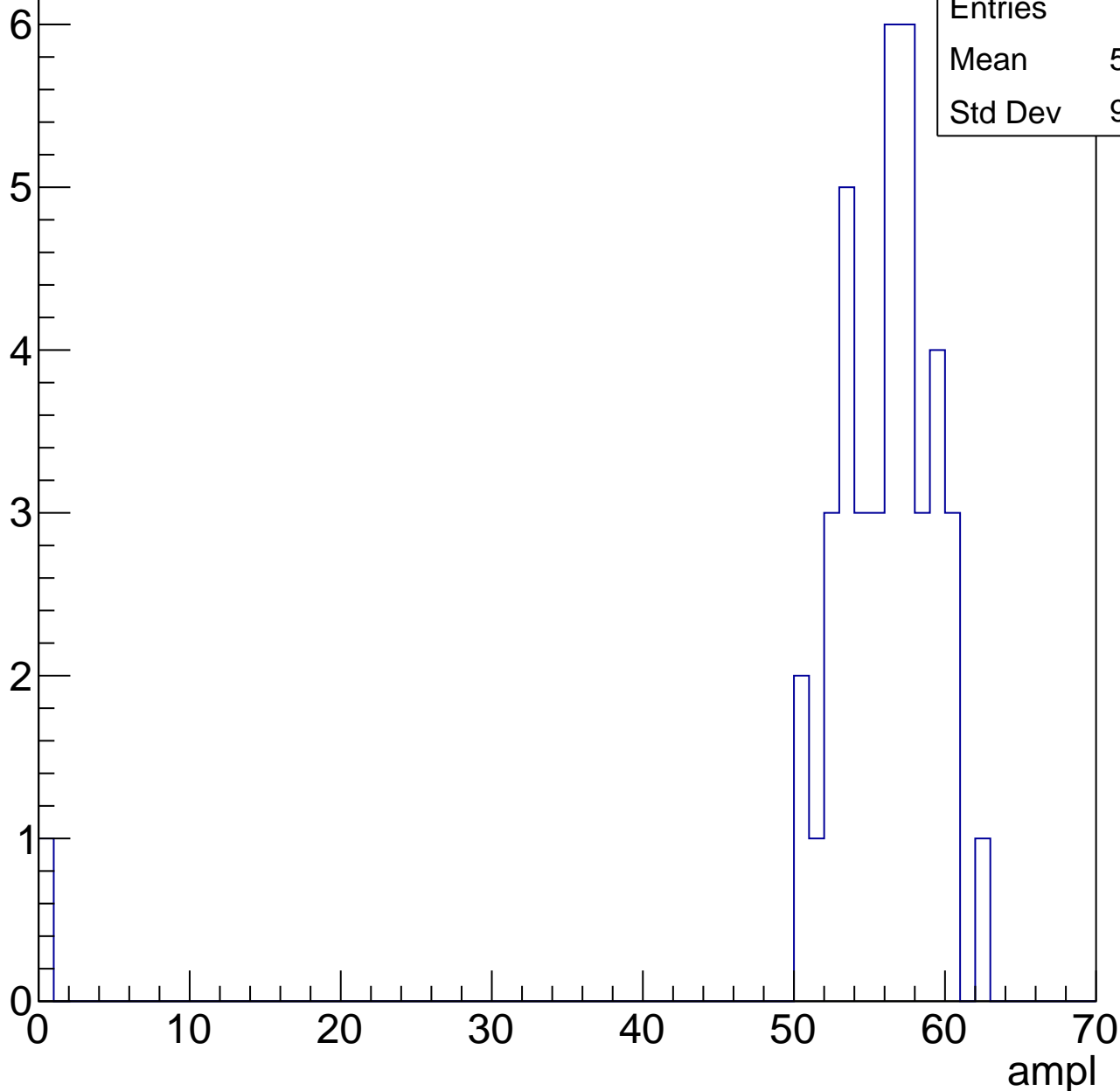


B1L103S, U14-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	54.37
Std Dev	9.065



B1L103S, U14-ch19, adc5

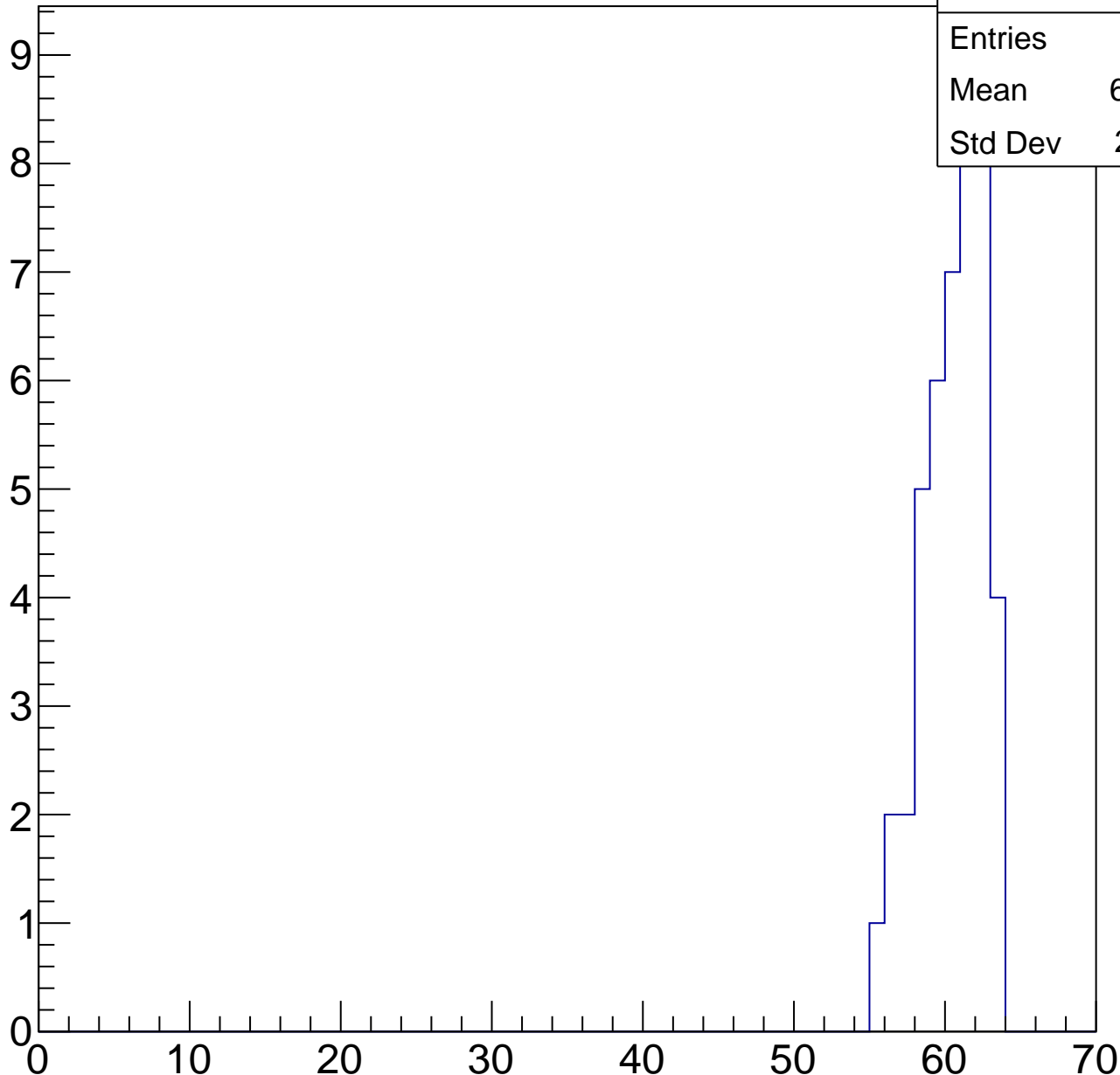
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	44
Mean	60.05
Std Dev	2.011

ampl

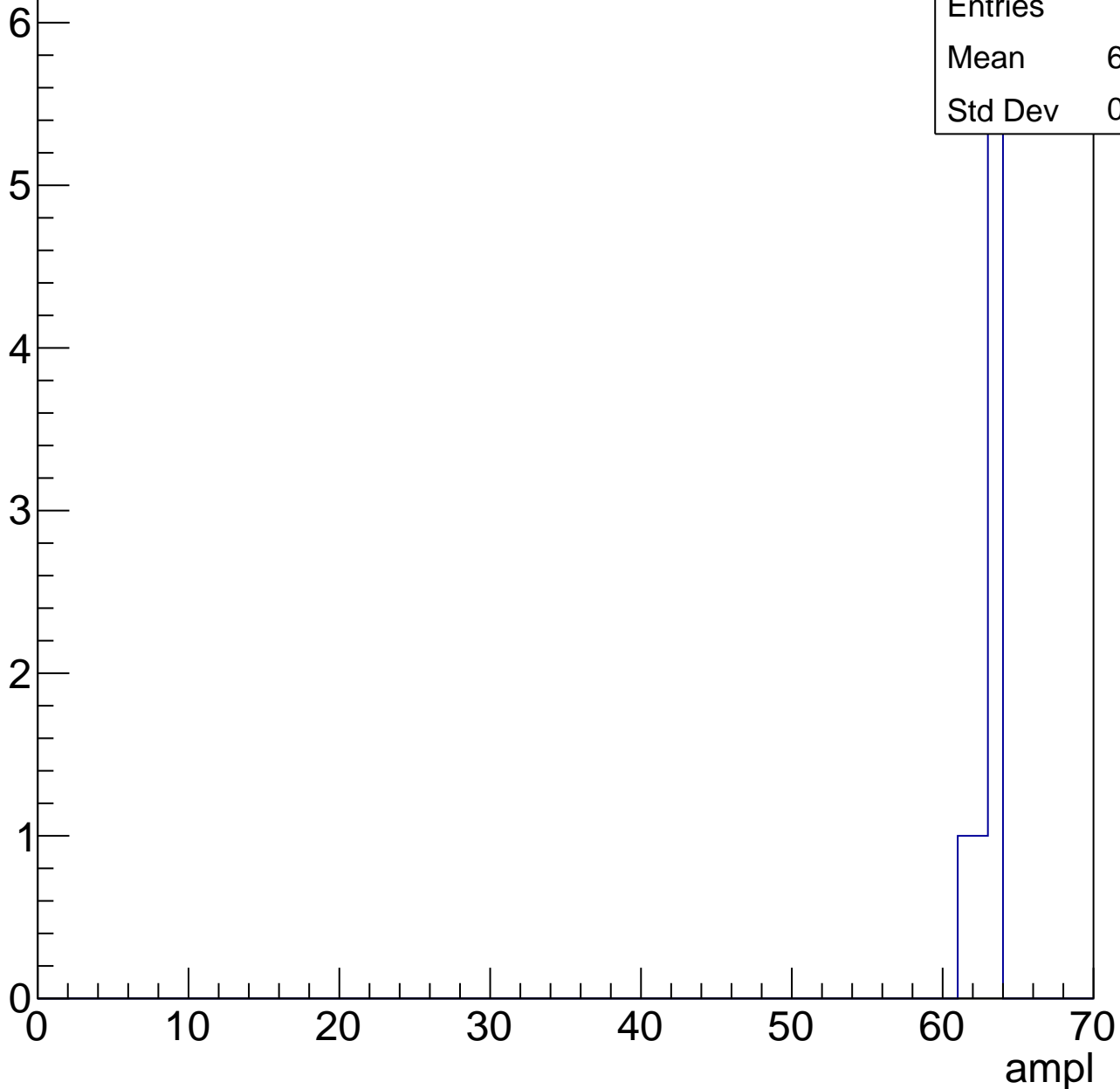


B1L103S, U14-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	62.62
Std Dev	0.696



B1L103S, U14-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch20, adc0

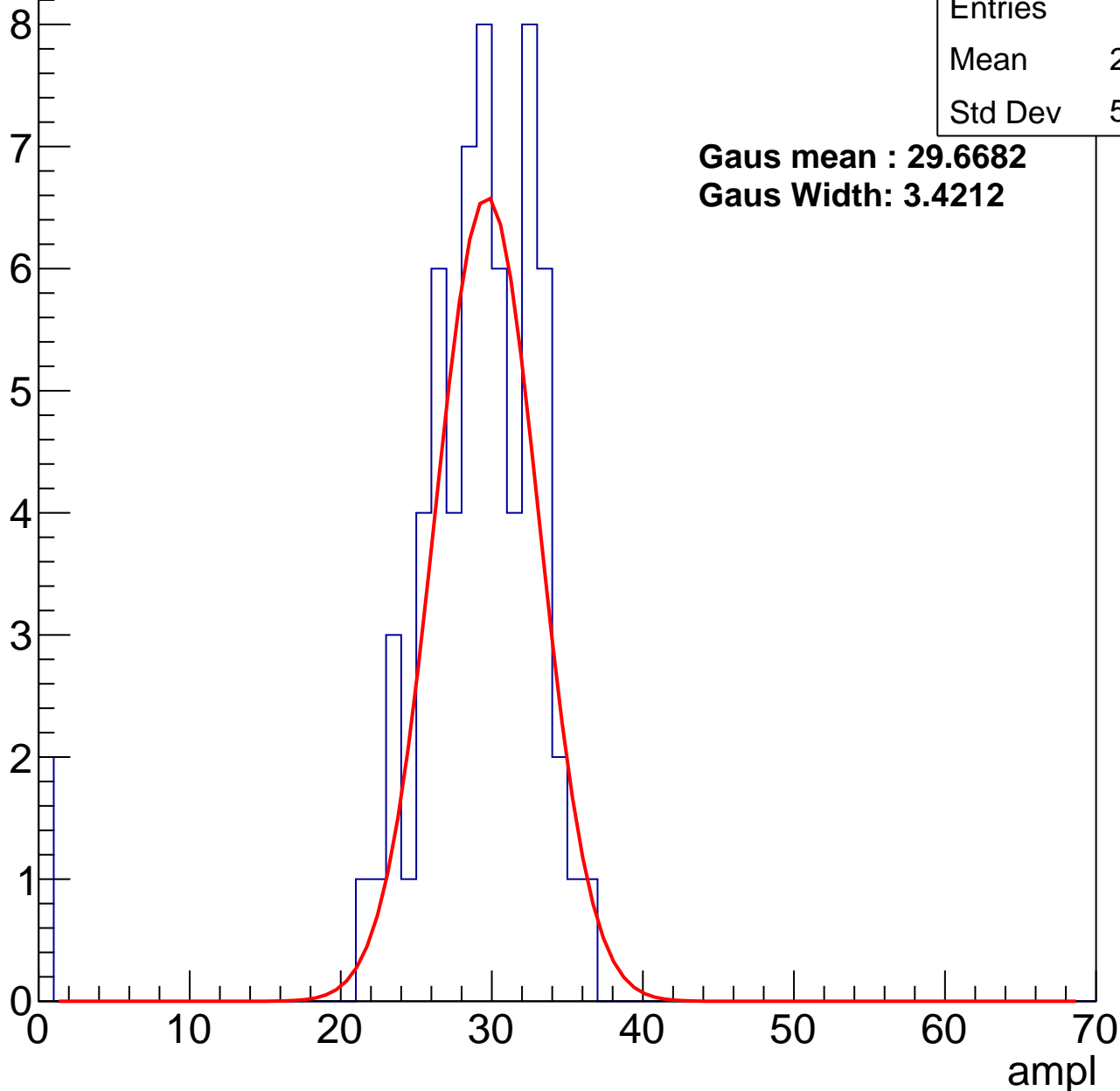
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	28.08
Std Dev	5.998

Gaus mean : 29.6682

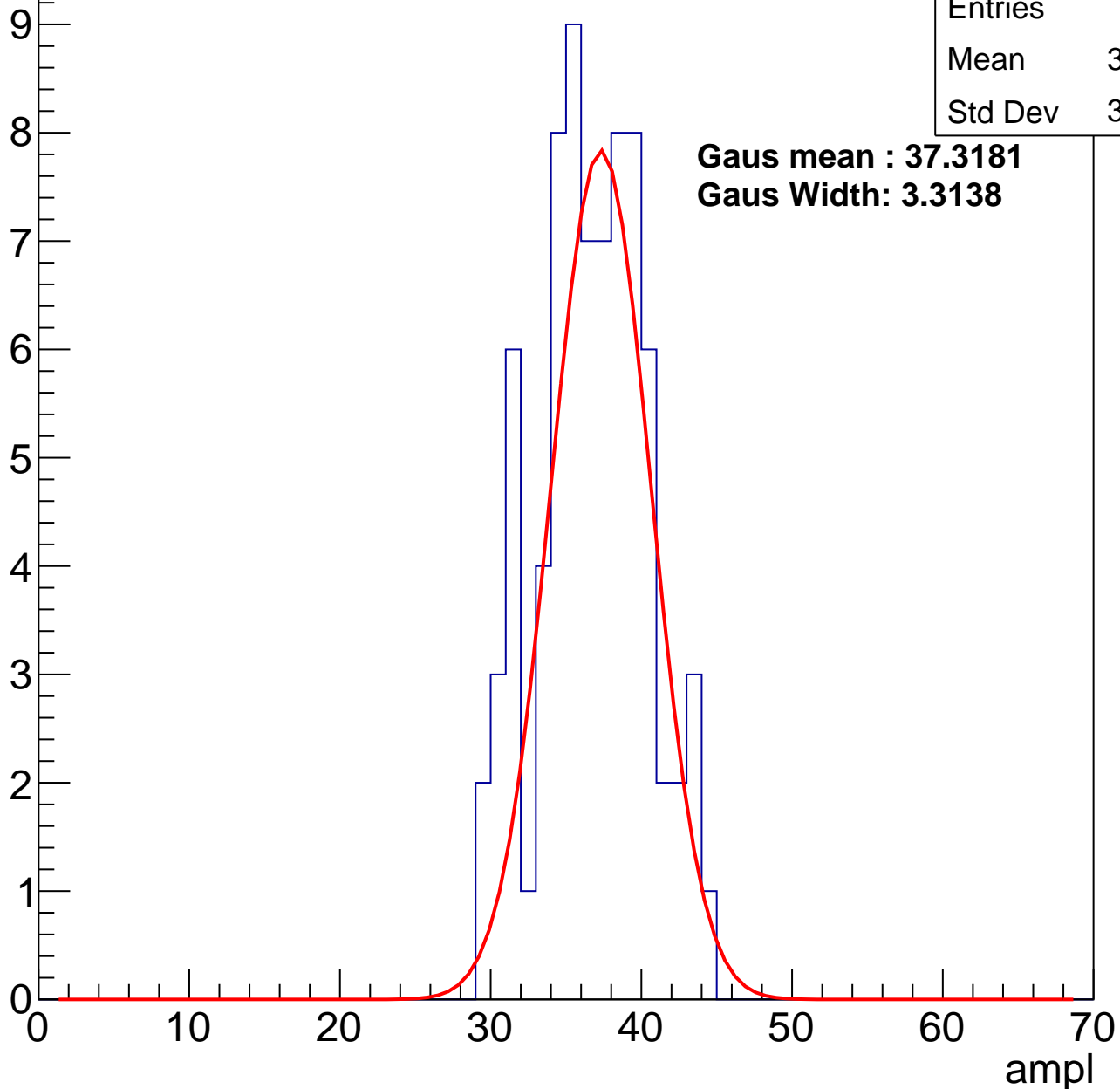
Gaus Width: 3.4212



B1L103S, U14-ch20, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch20, adc2

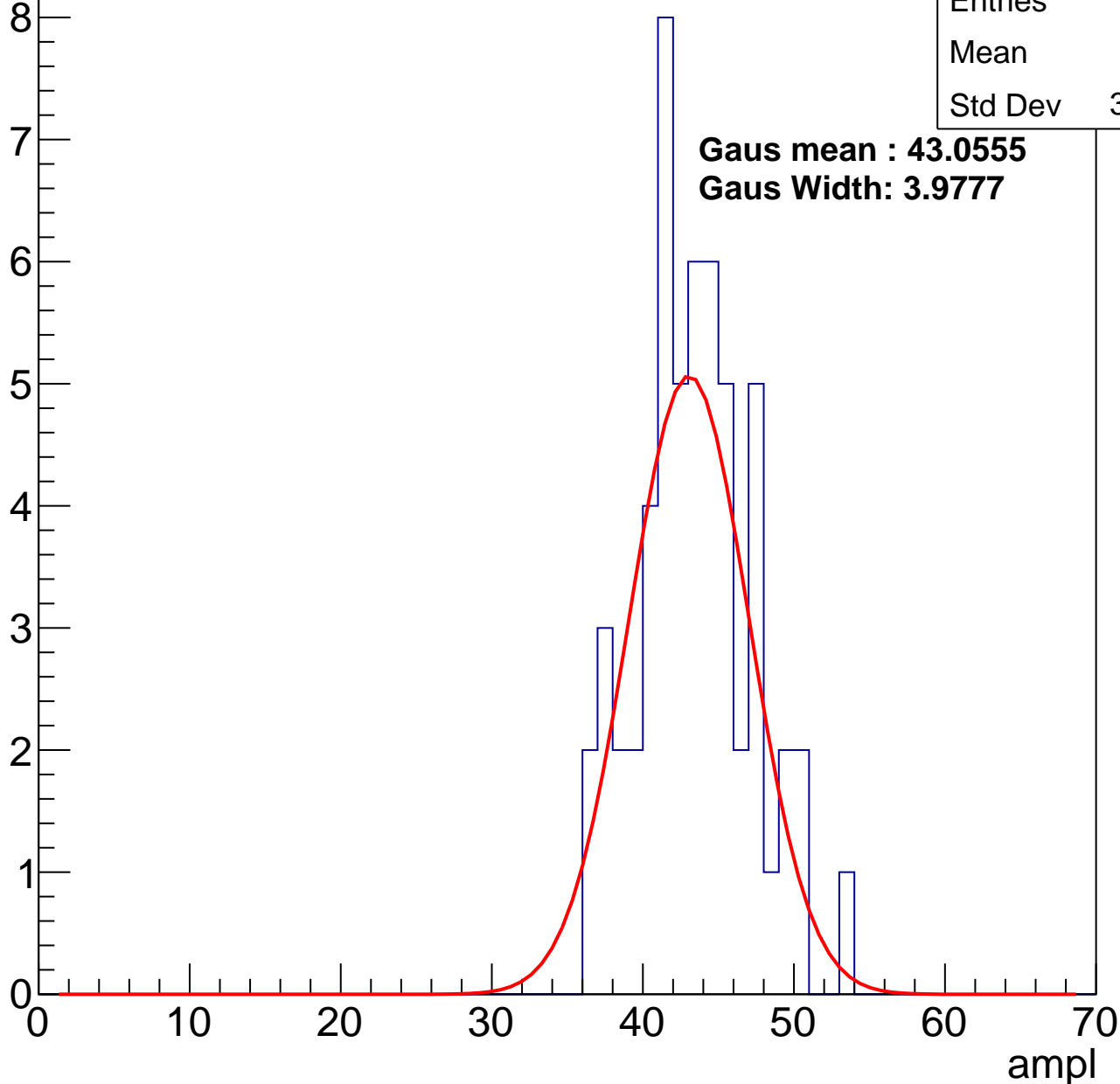
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43
Std Dev	3.732

Gaus mean : 43.0555

Gaus Width: 3.9777

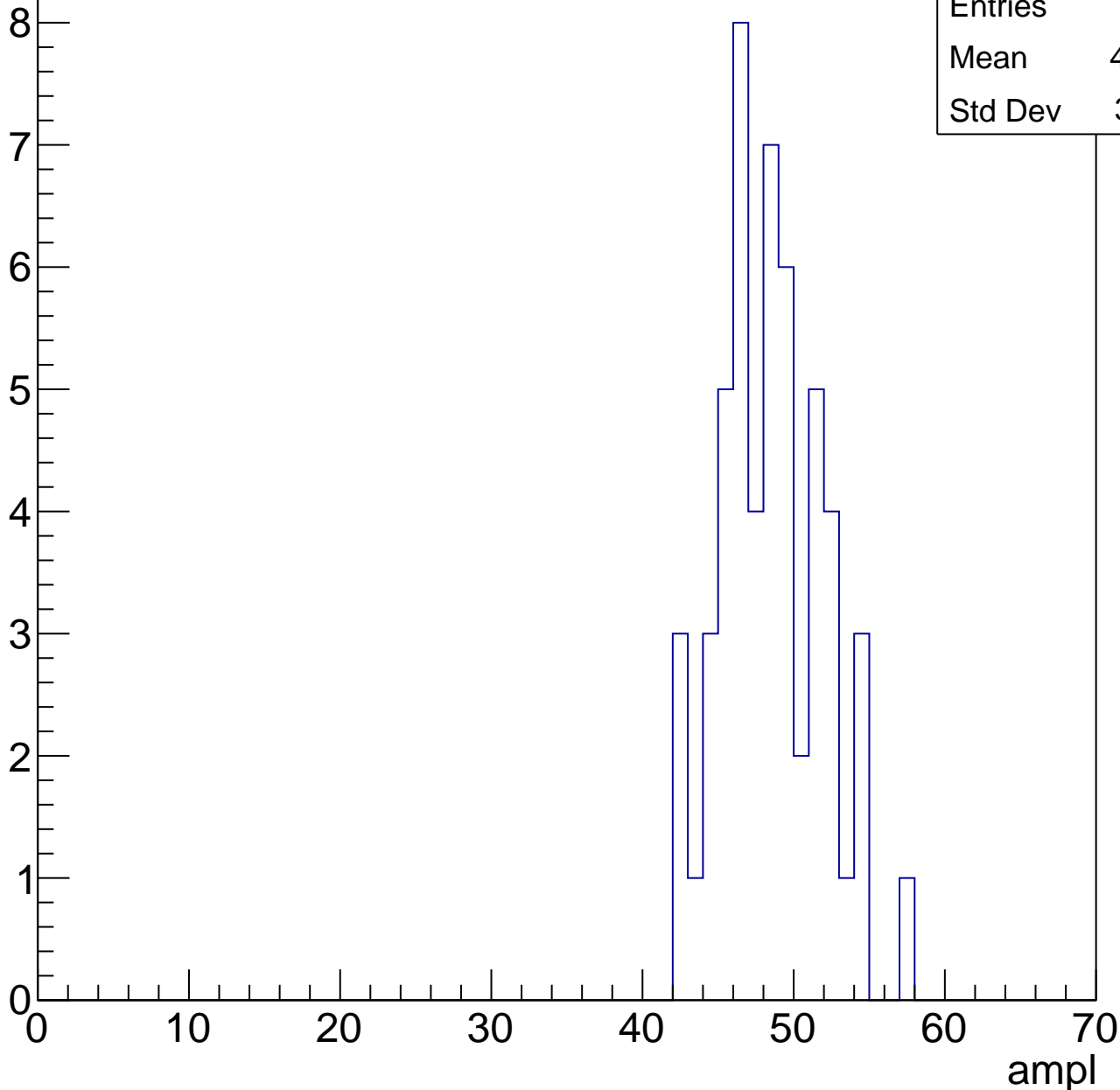


B1L103S, U14-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	48.06
Std Dev	3.361

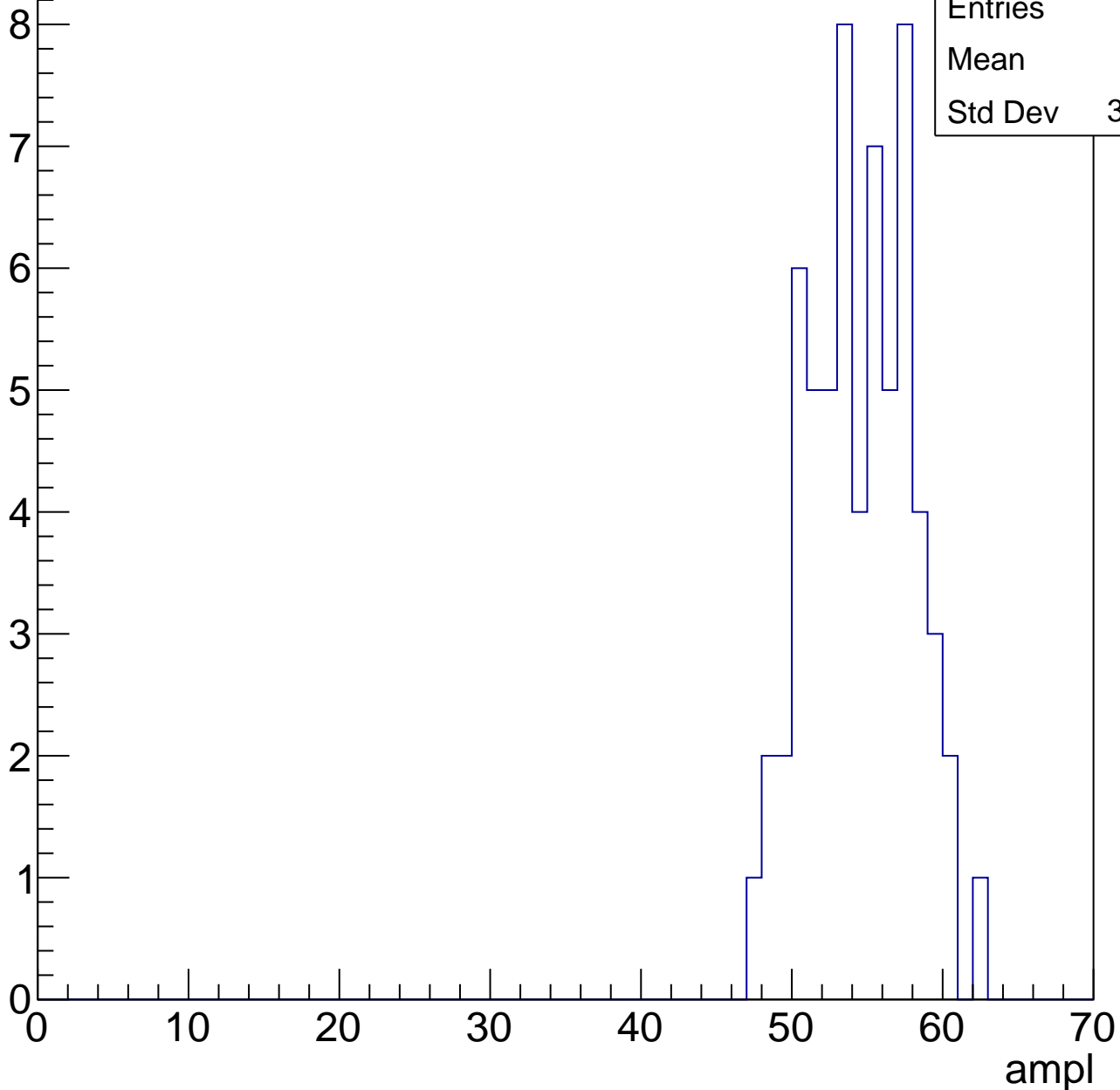


B1L103S, U14-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

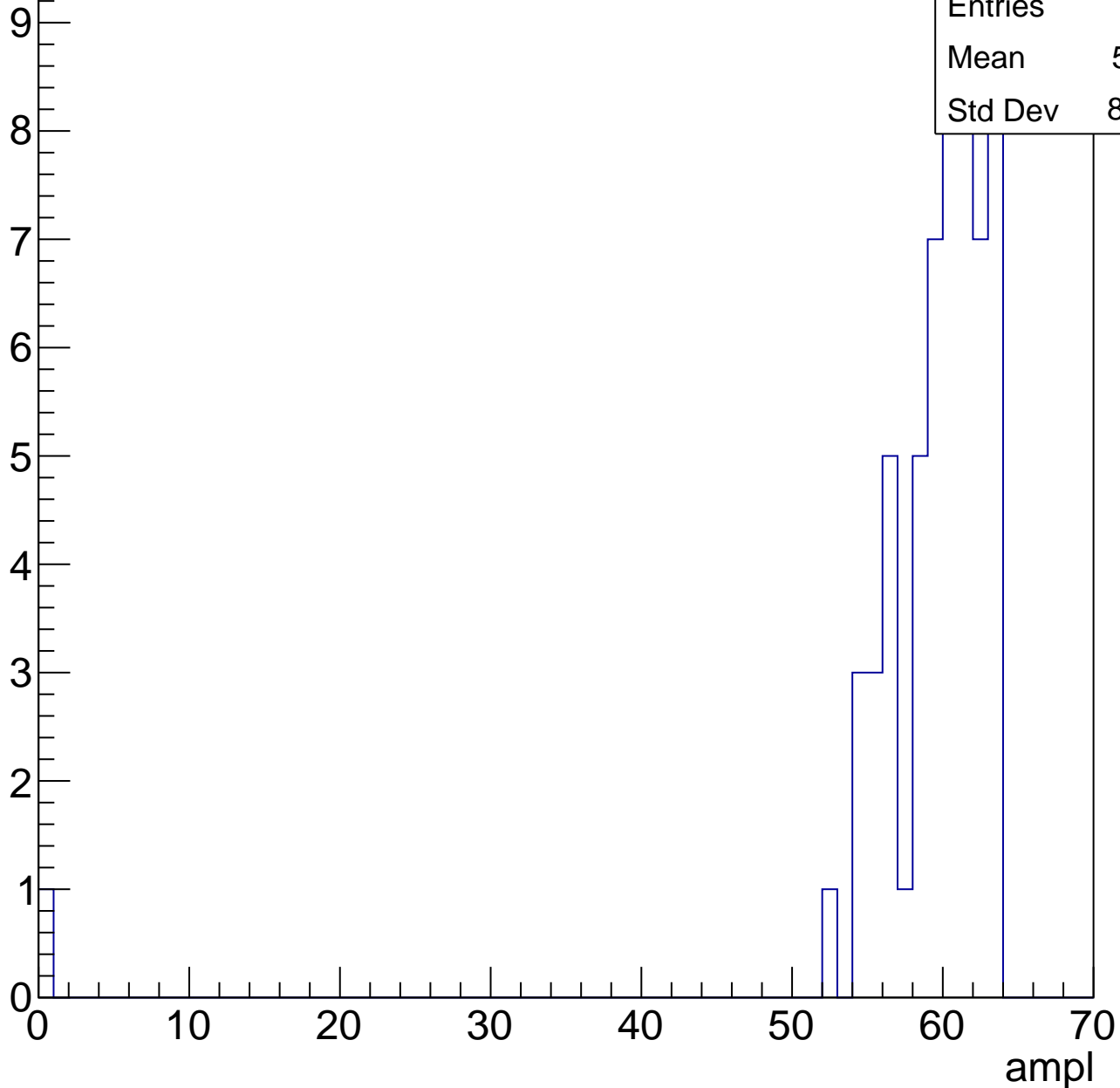
Entries	63
Mean	54.1
Std Dev	3.365



B1L103S, U14-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

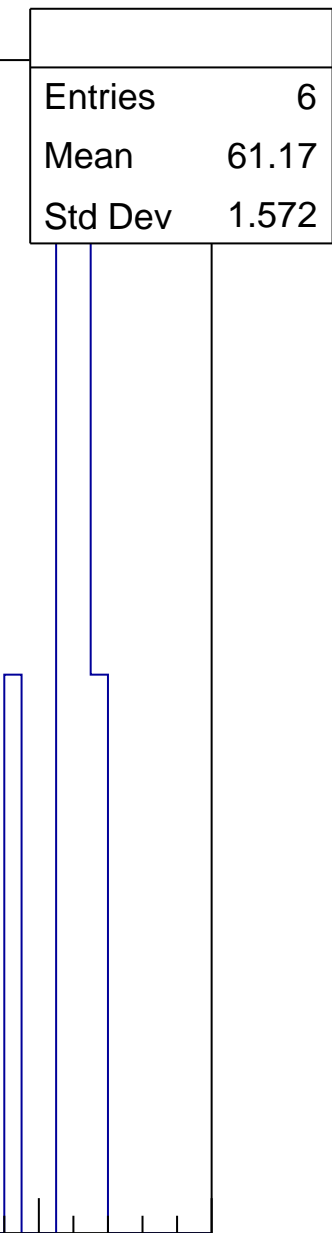
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.572

0 10 20 30 40 50 60 70

ampl



B1L103S, U14-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U14-ch21, adc0

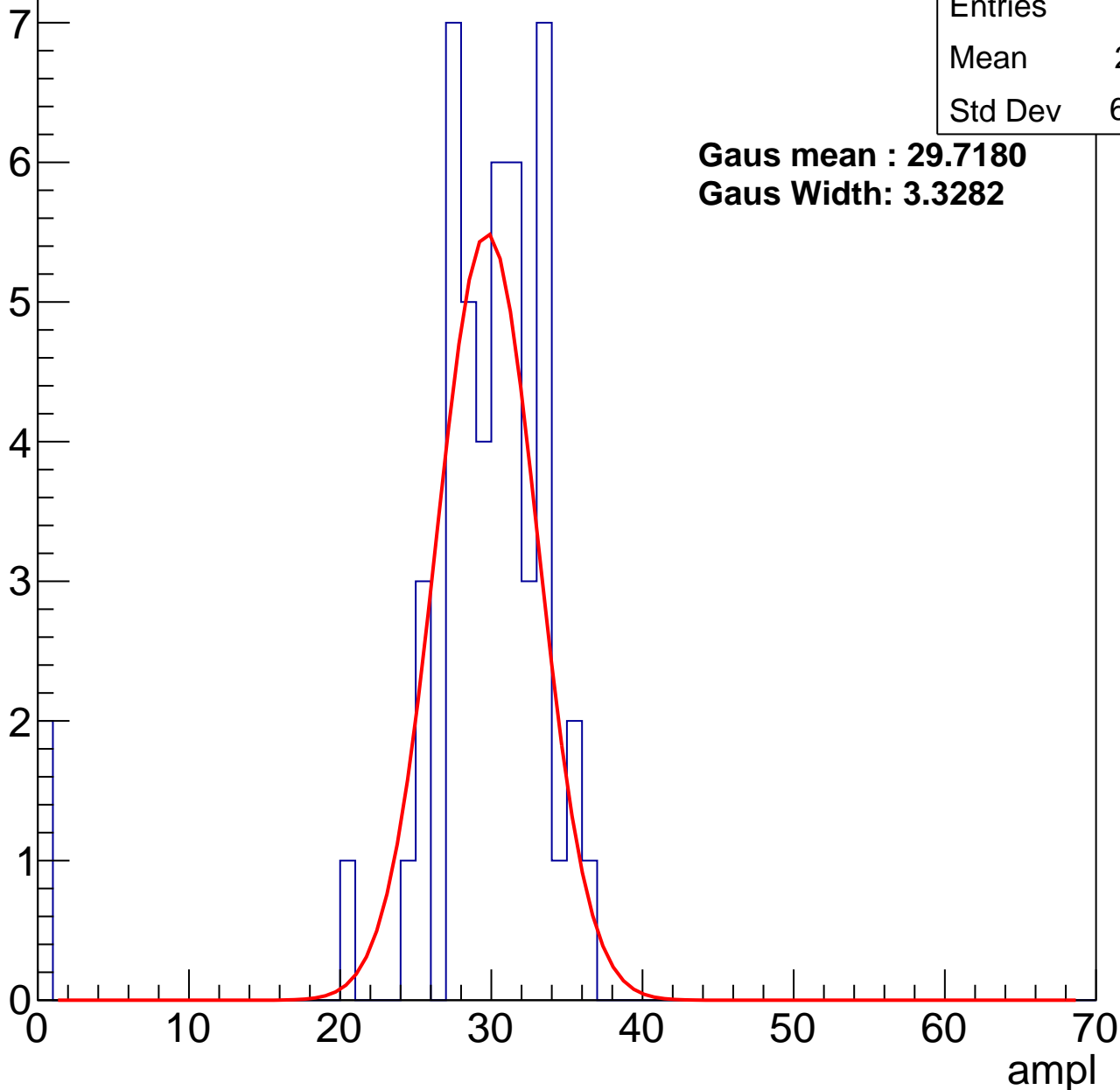
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	28.51
Std Dev	6.658

Gaus mean : 29.7180

Gaus Width: 3.3282



B1L103S, U14-ch21, adc1

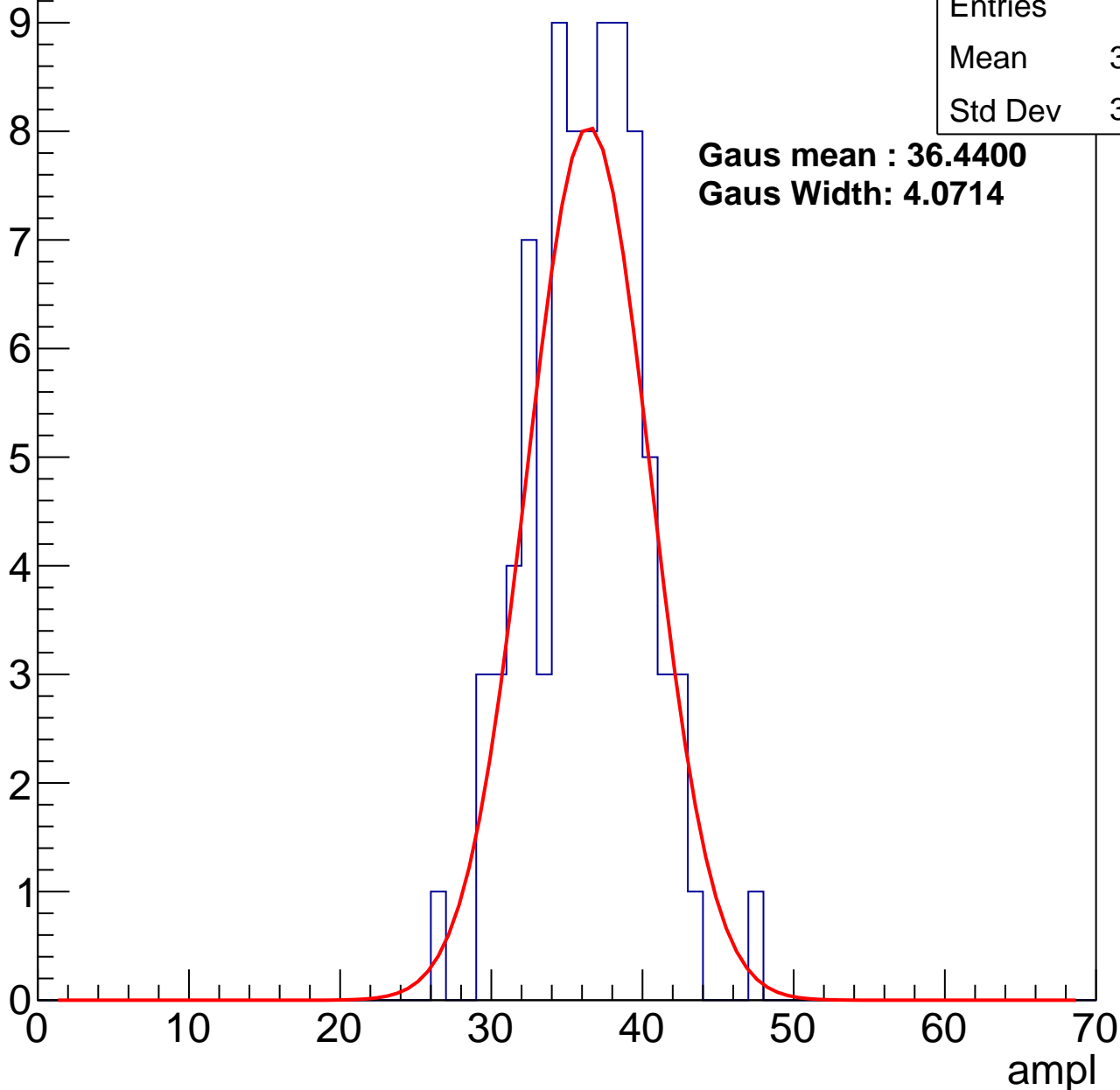
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	35.88
Std Dev	3.737

Gaus mean : 36.4400

Gaus Width: 4.0714



B1L103S, U14-ch21, adc2

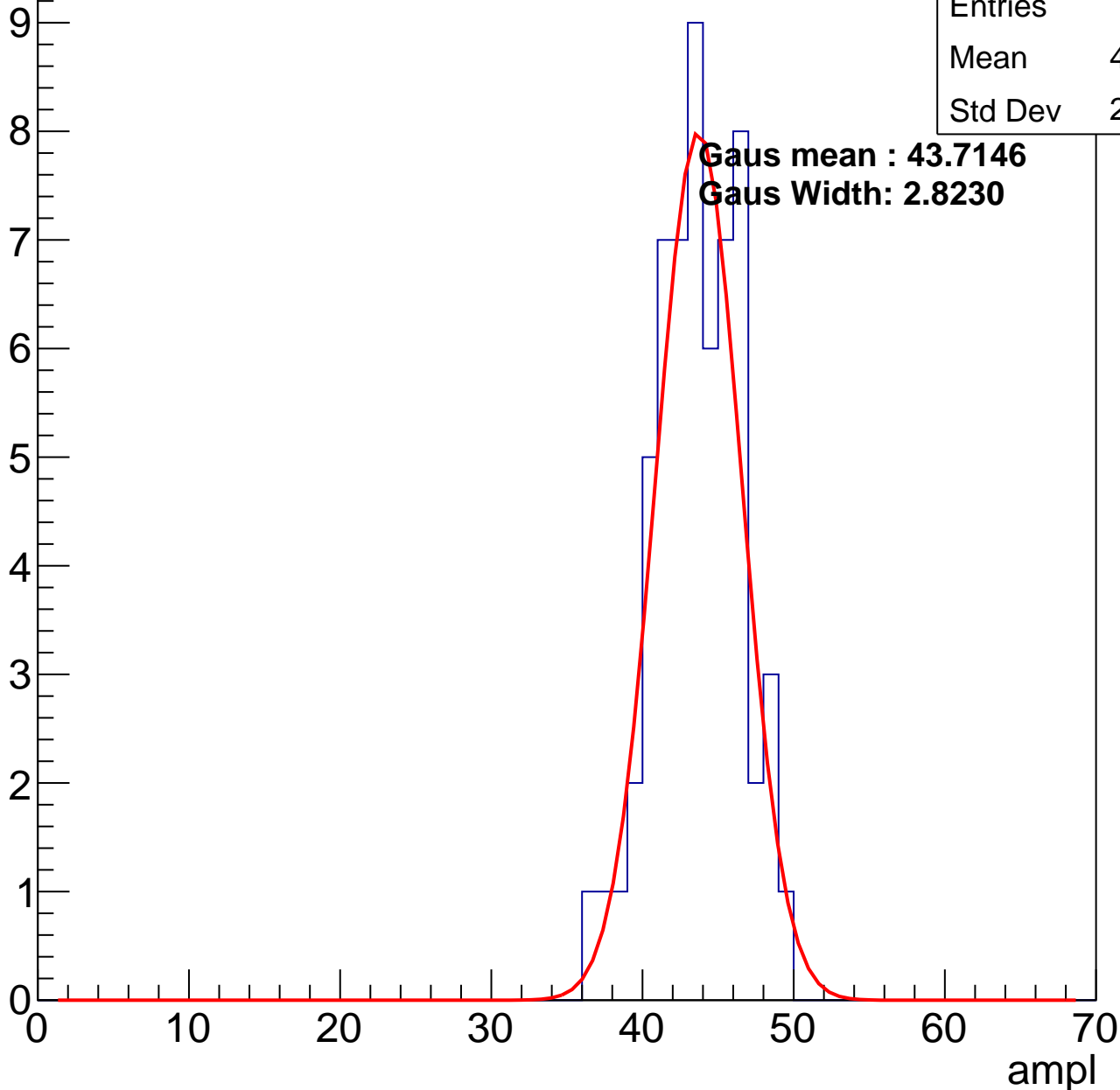
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.18
Std Dev	2.796

Gaus mean : 43.7146

Gaus Width: 2.8230

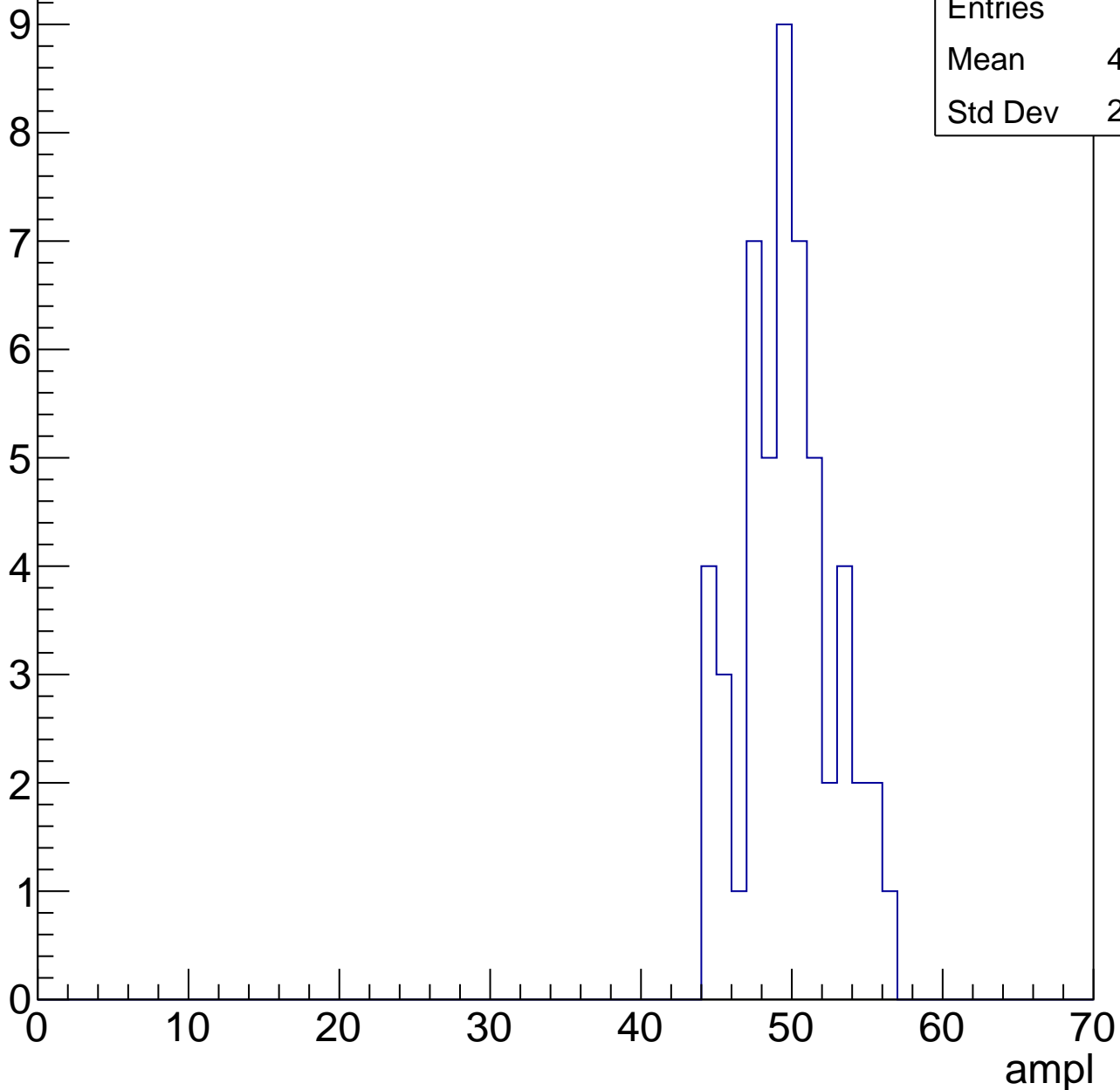


B1L103S, U14-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

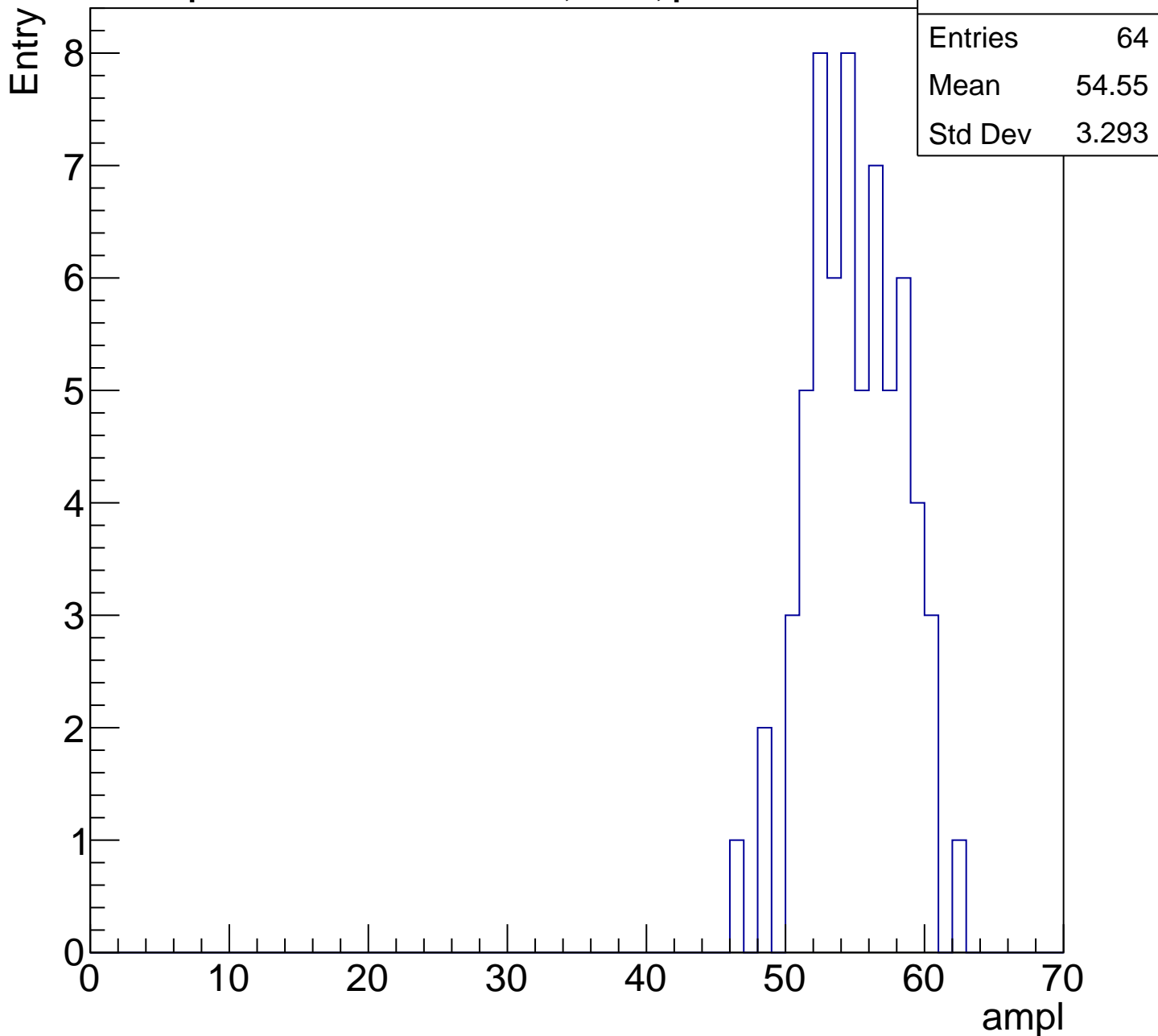
Entry

Entries	52
Mean	49.27
Std Dev	2.994



B1L103S, U14-ch21, adc4

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U14-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	55
Mean	58.98
Std Dev	8.359

Entry

10

8

6

4

2

0

0

10

20

30

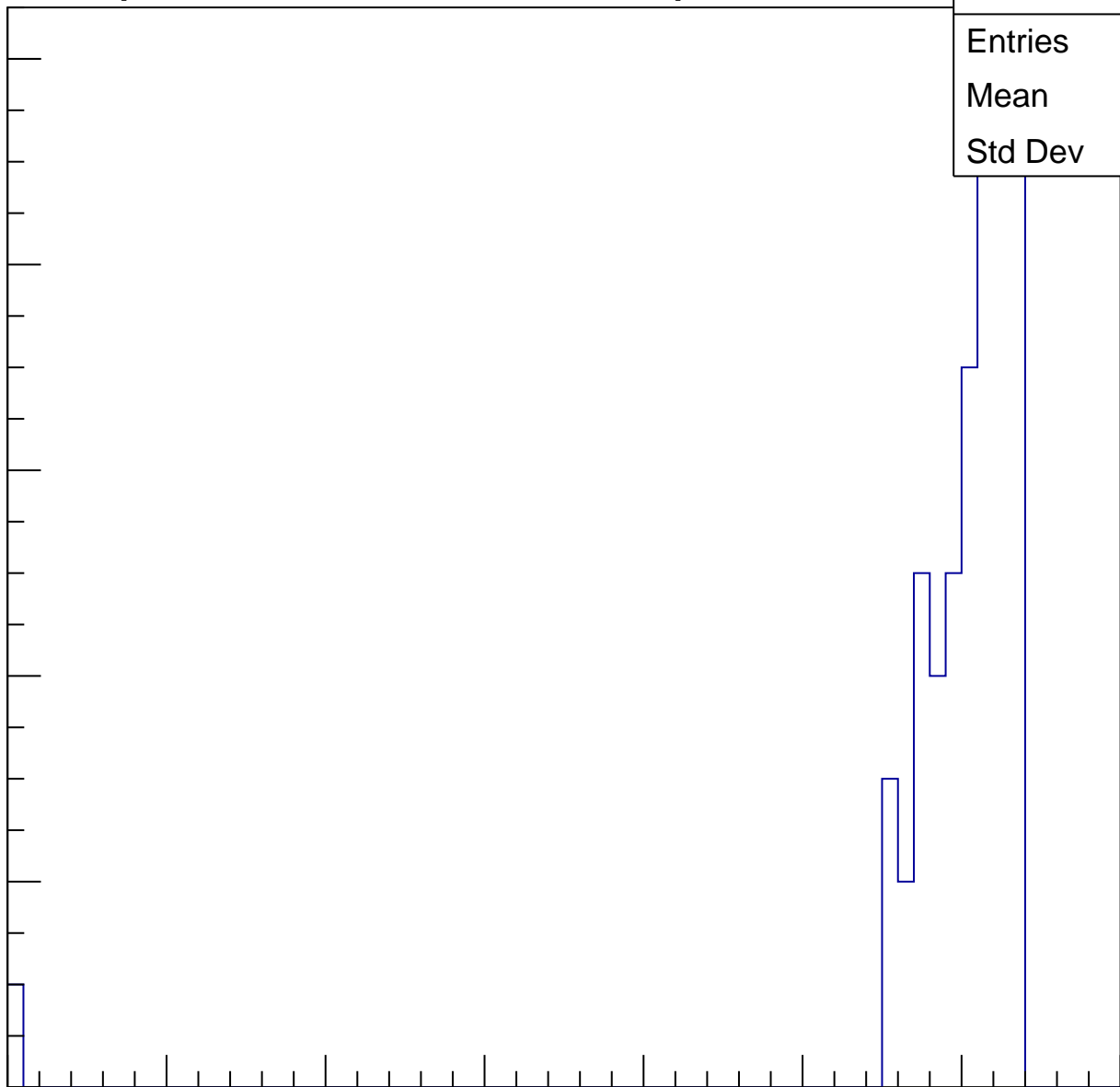
40

50

60

70

ampl



B1L103S, U14-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch22, adc0

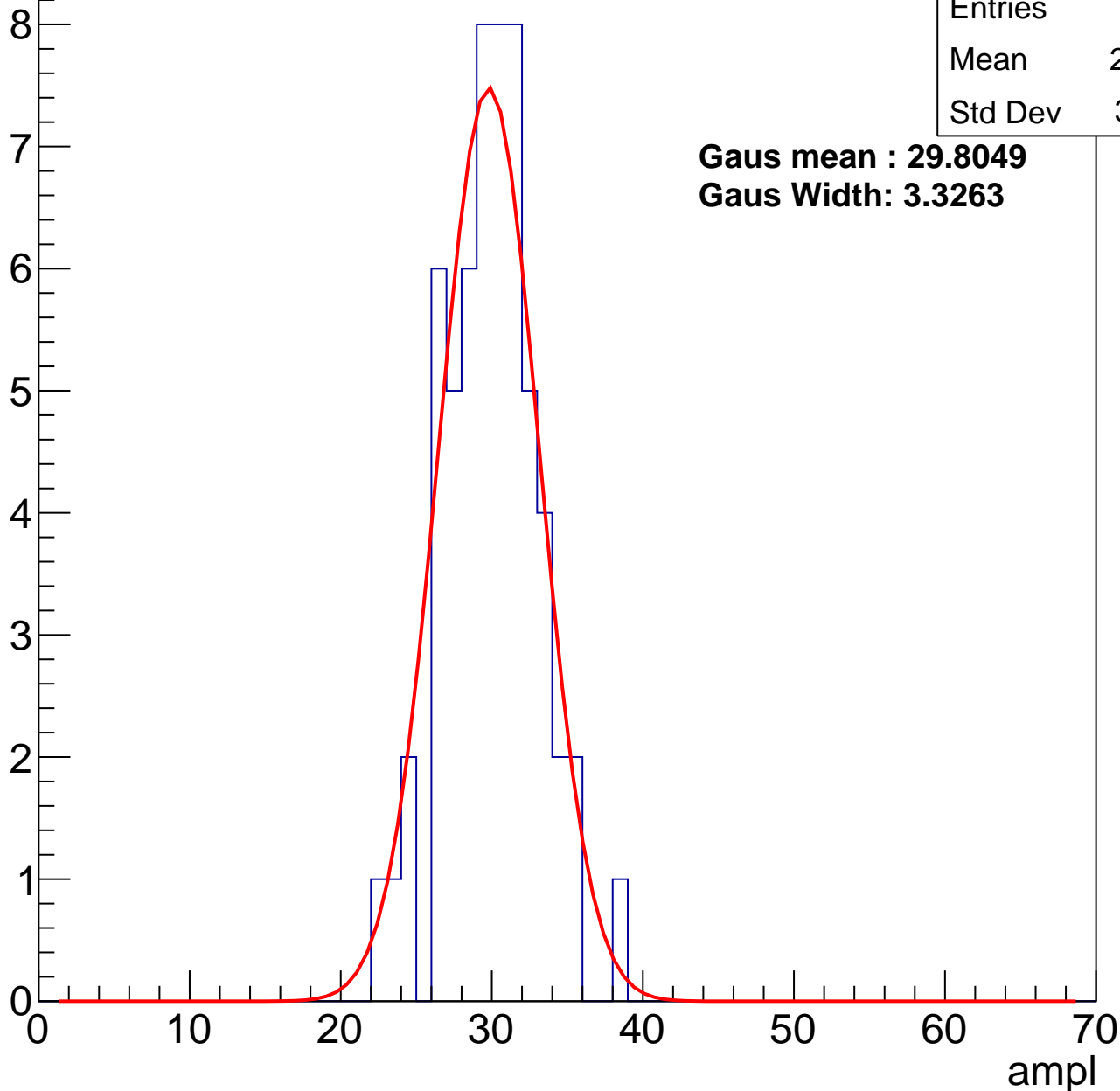
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	29.49
Std Dev	3.061

Gaus mean : 29.8049

Gaus Width: 3.3263



B1L103S, U14-ch22, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	36.78
Std Dev	3.407

Gaus mean : 36.5465

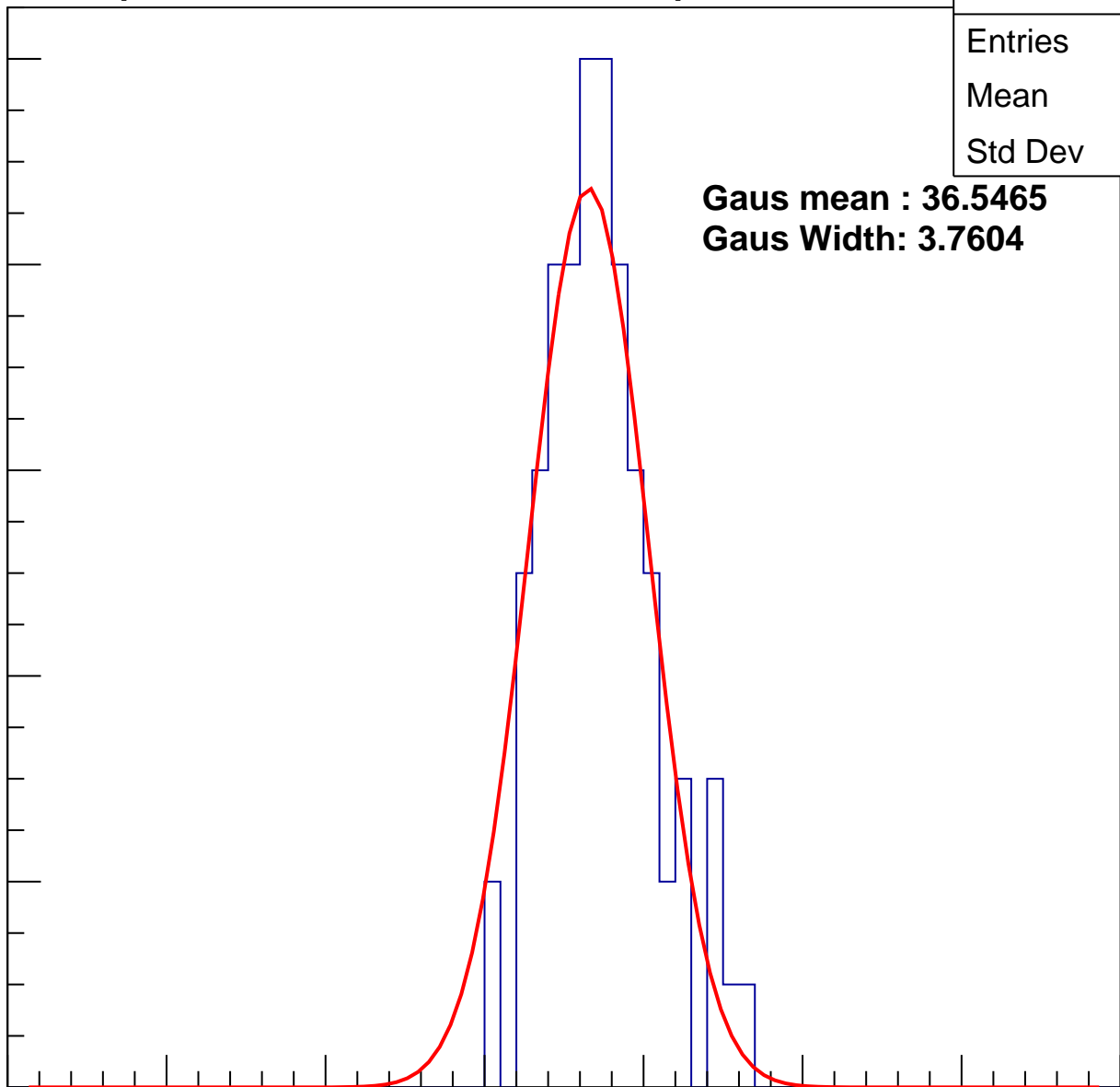
Gaus Width: 3.7604

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch22, adc2

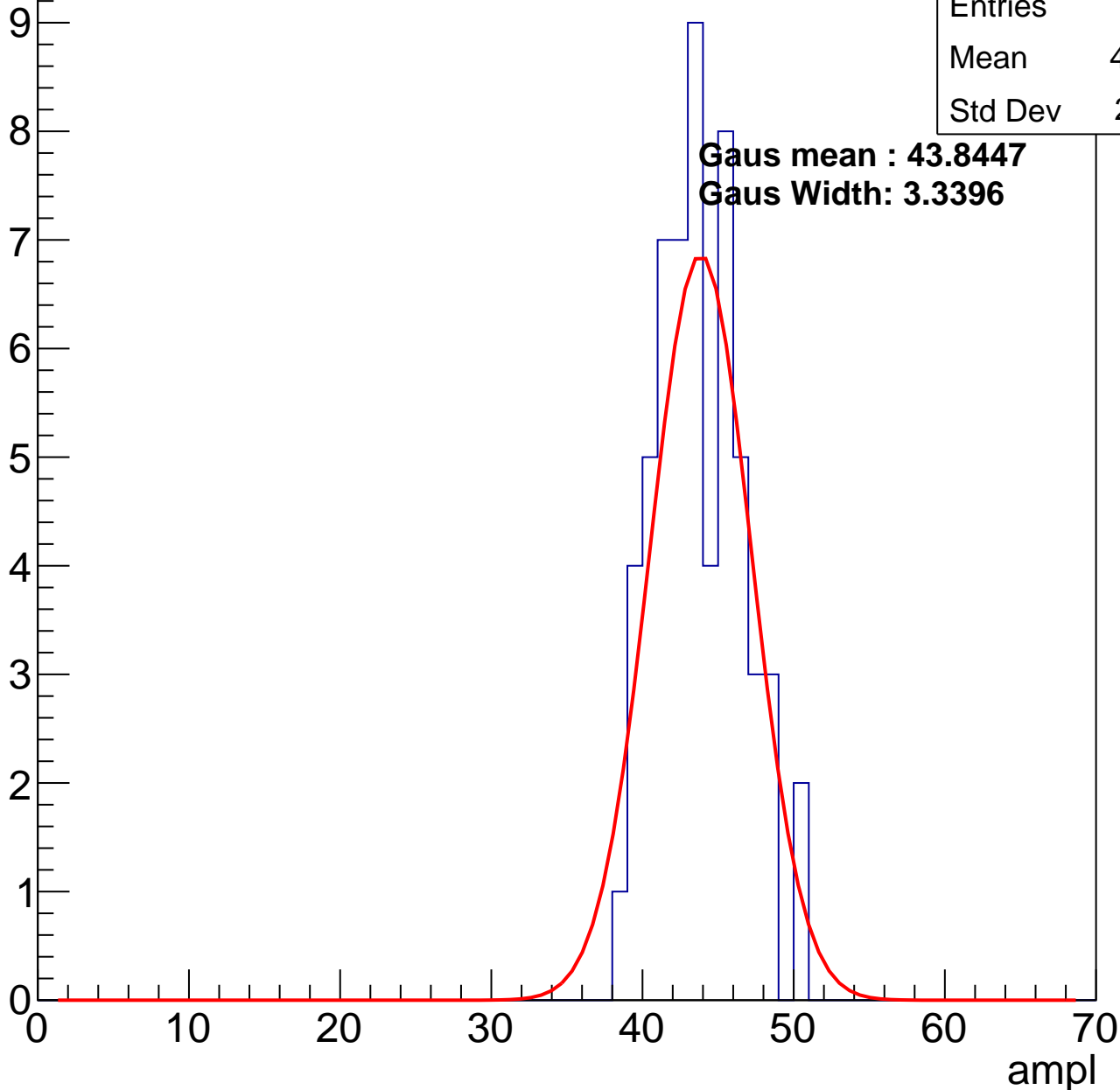
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.33
Std Dev	2.831

Gaus mean : 43.8447

Gaus Width: 3.3396

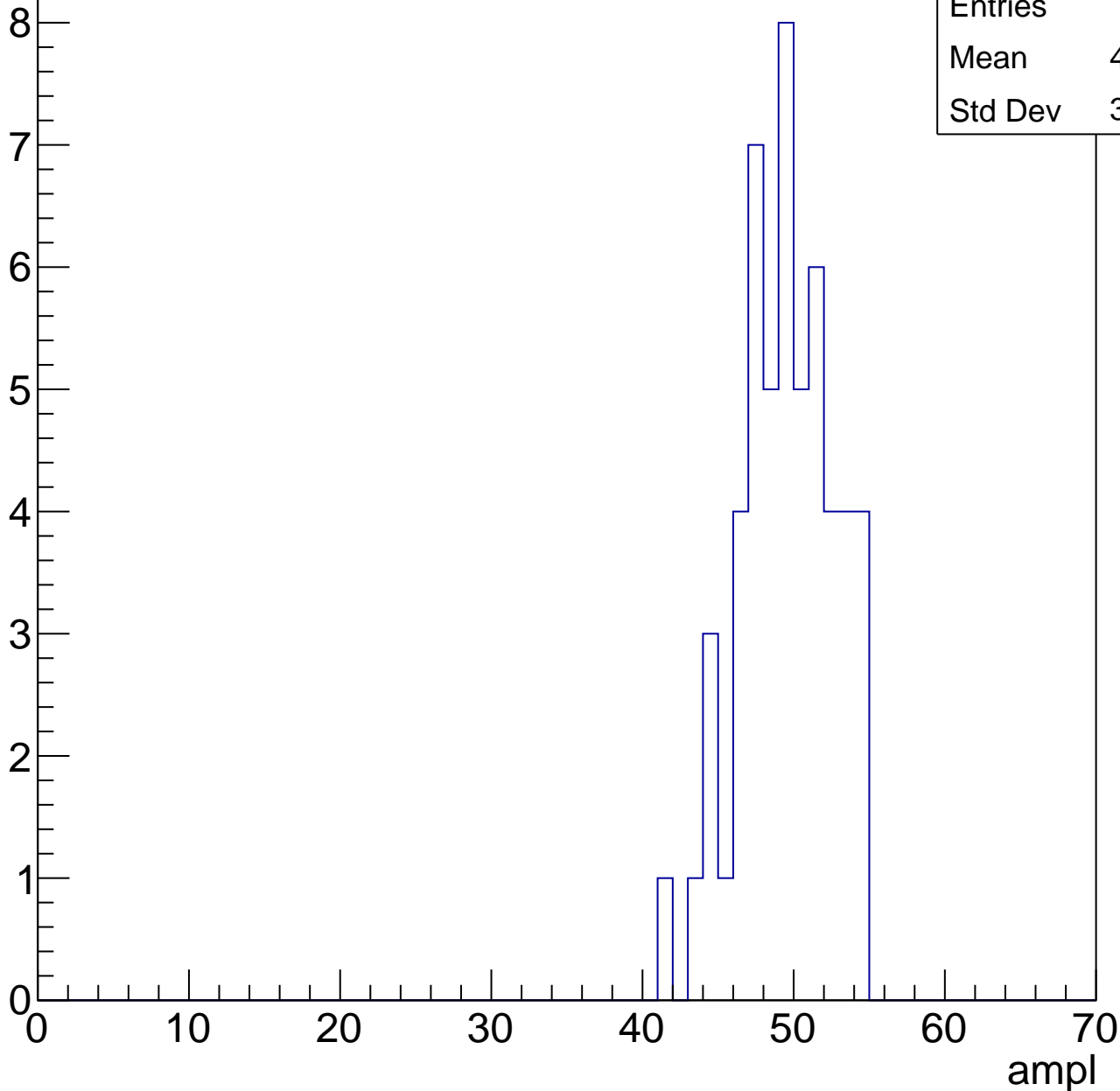


B1L103S, U14-ch22, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

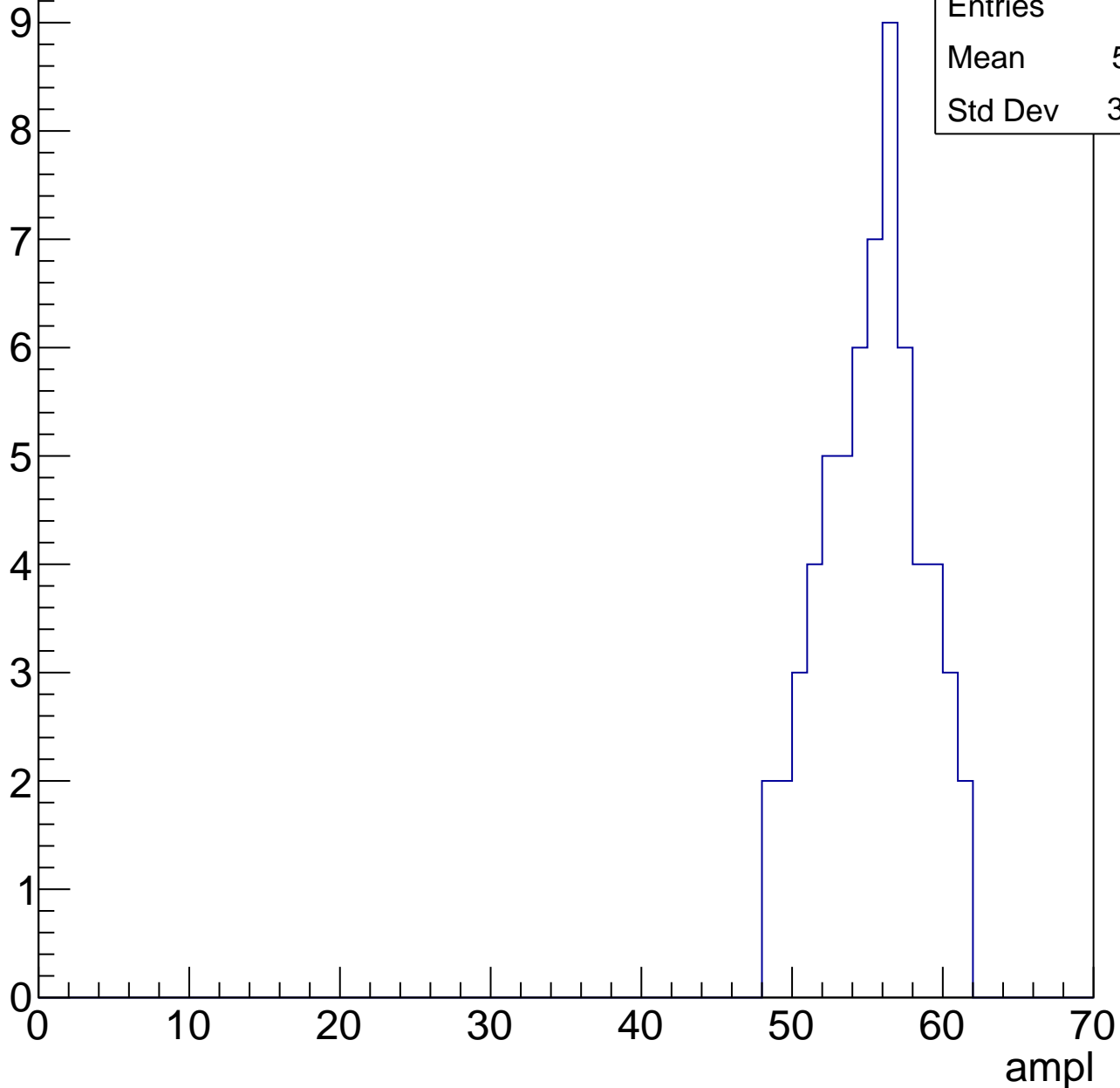
Entries	53
Mean	49.02
Std Dev	3.037



B1L103S, U14-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



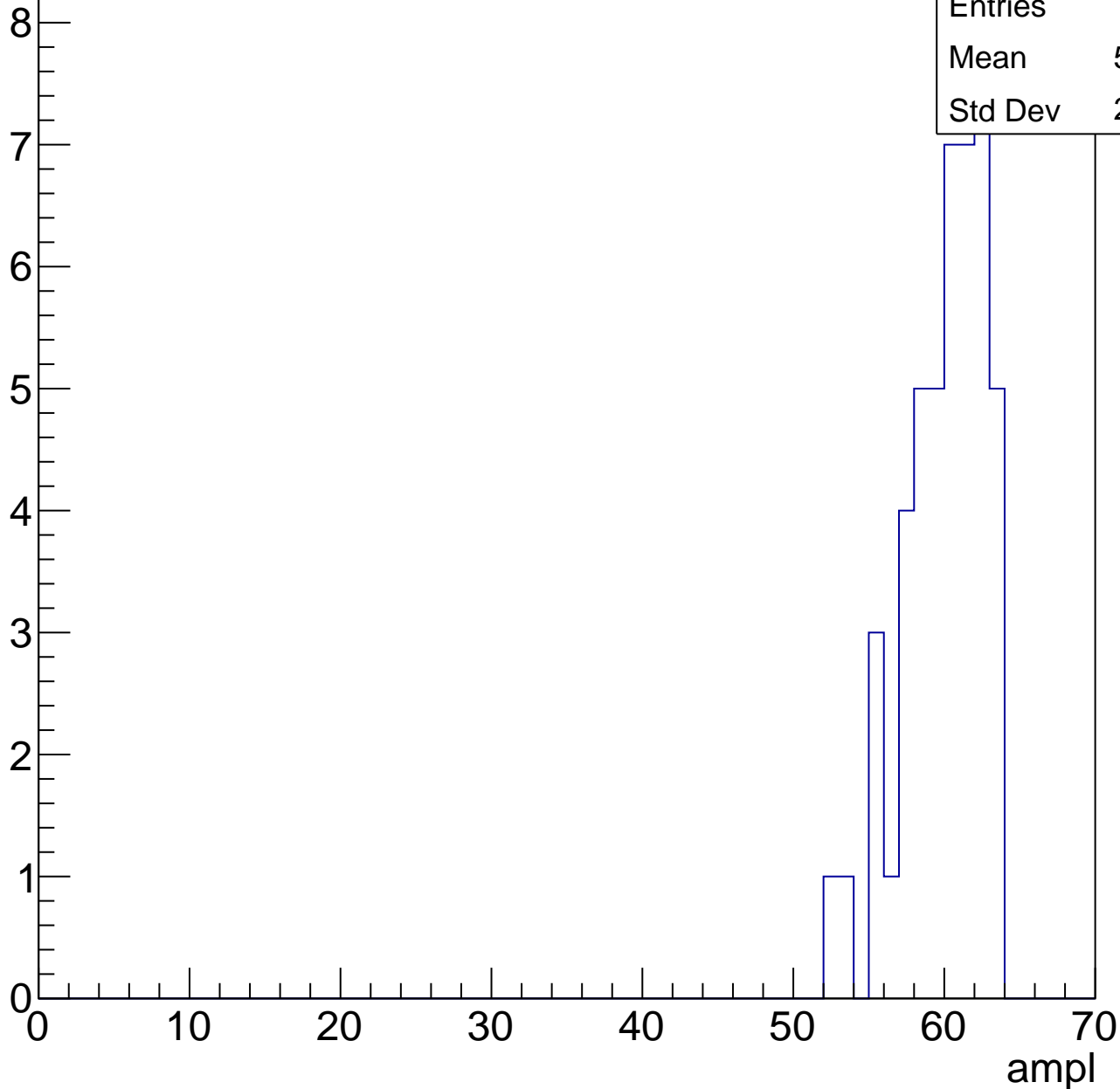
Entries	62
Mean	54.81
Std Dev	3.257

B1L103S, U14-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

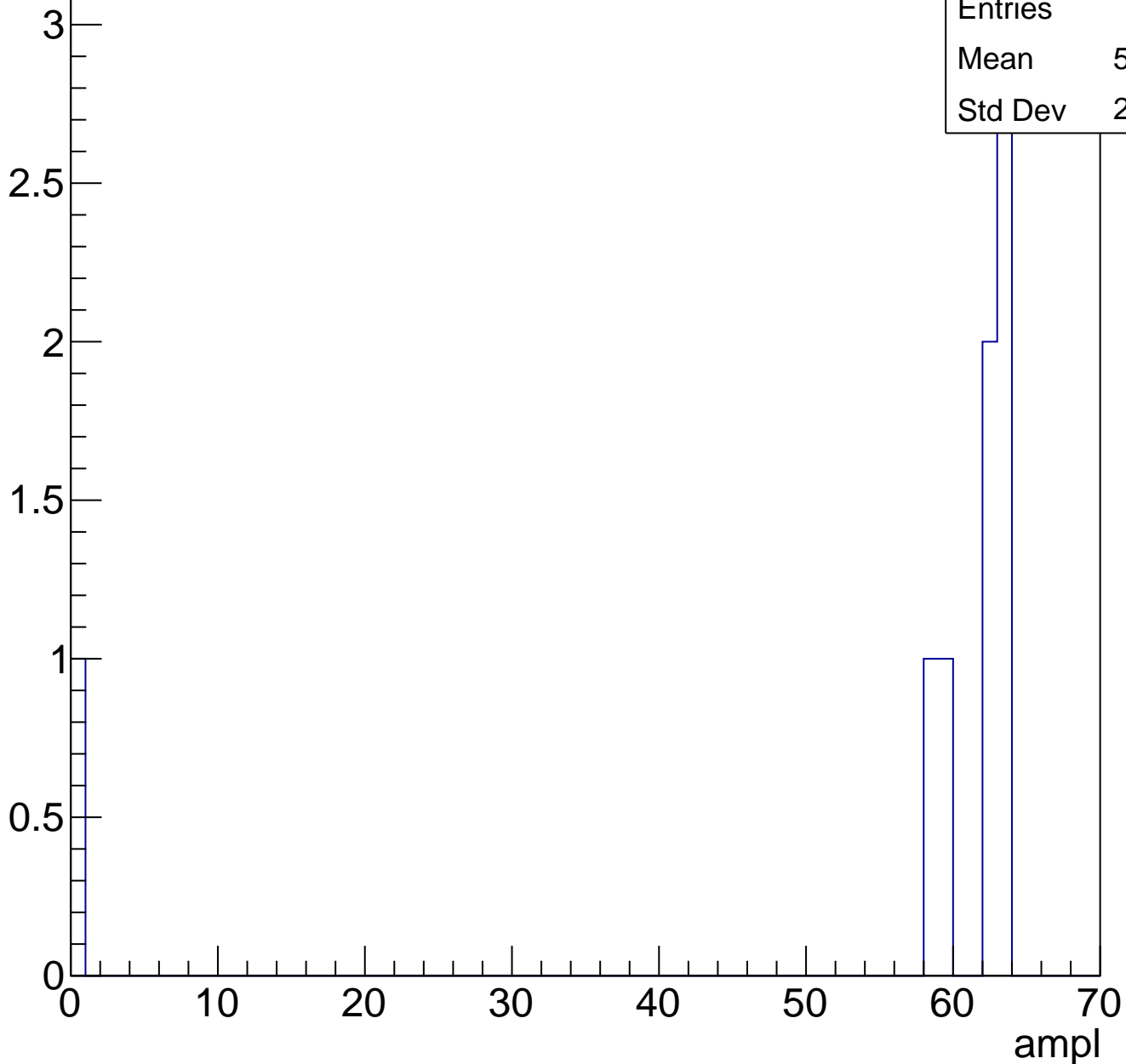
Entries	47
Mean	59.51
Std Dev	2.681



B1L103S, U14-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

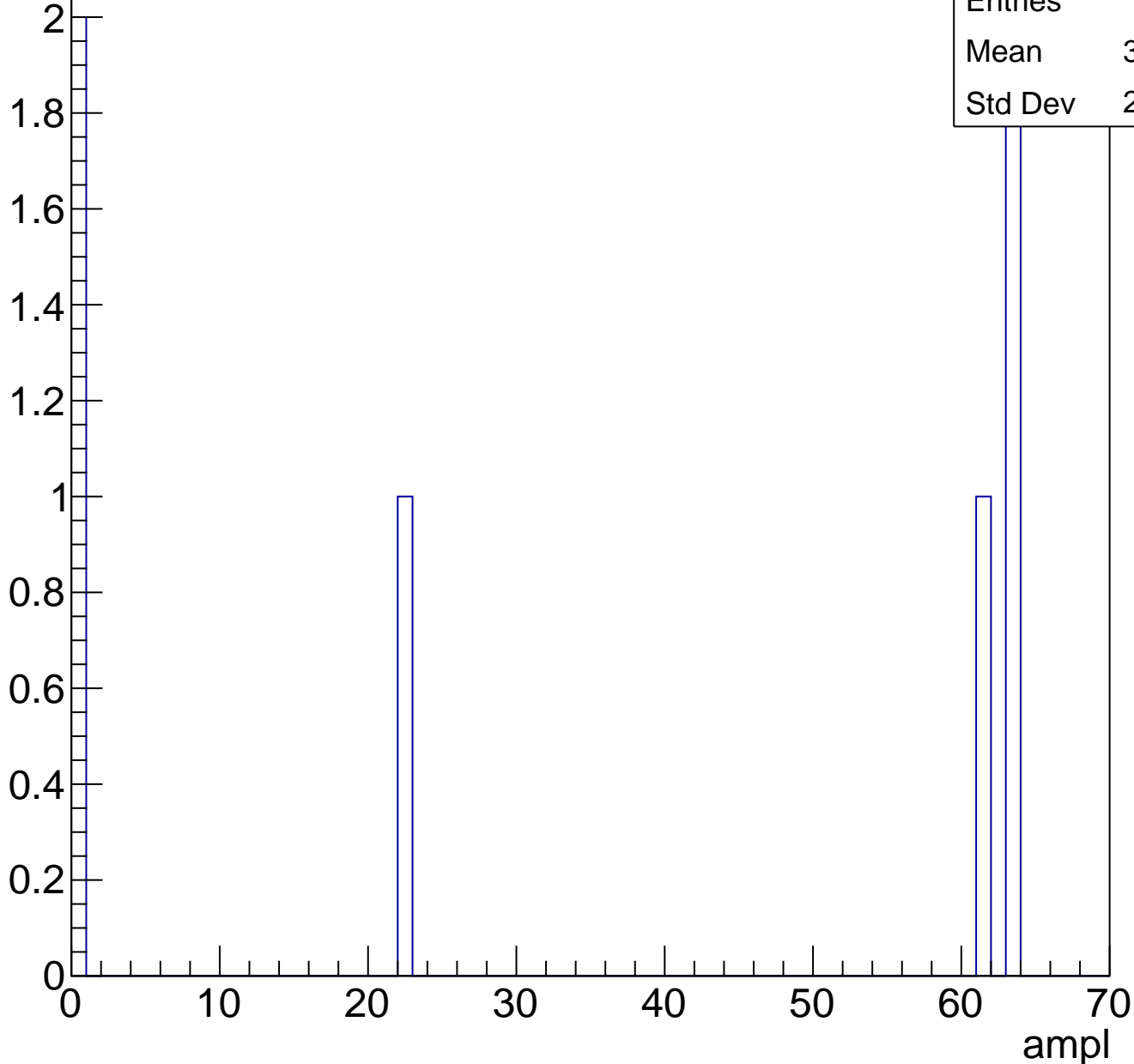
Entry



B1L103S, U14-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	34.83
Std Dev	28.47

B1L103S, U14-ch23, adc0

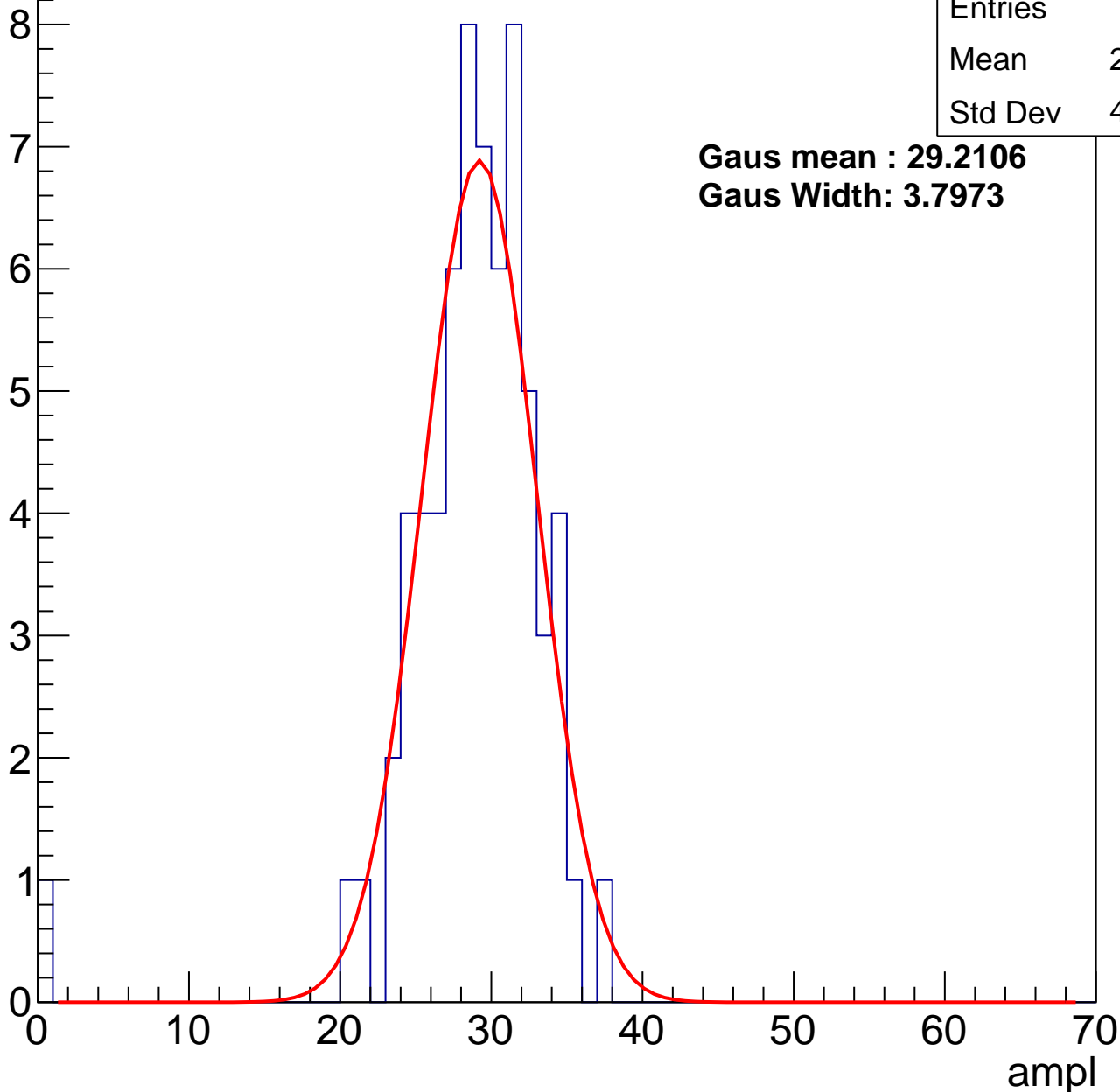
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.35
Std Dev	4.913

Gaus mean : 29.2106

Gaus Width: 3.7973



B1L103S, U14-ch23, adc1

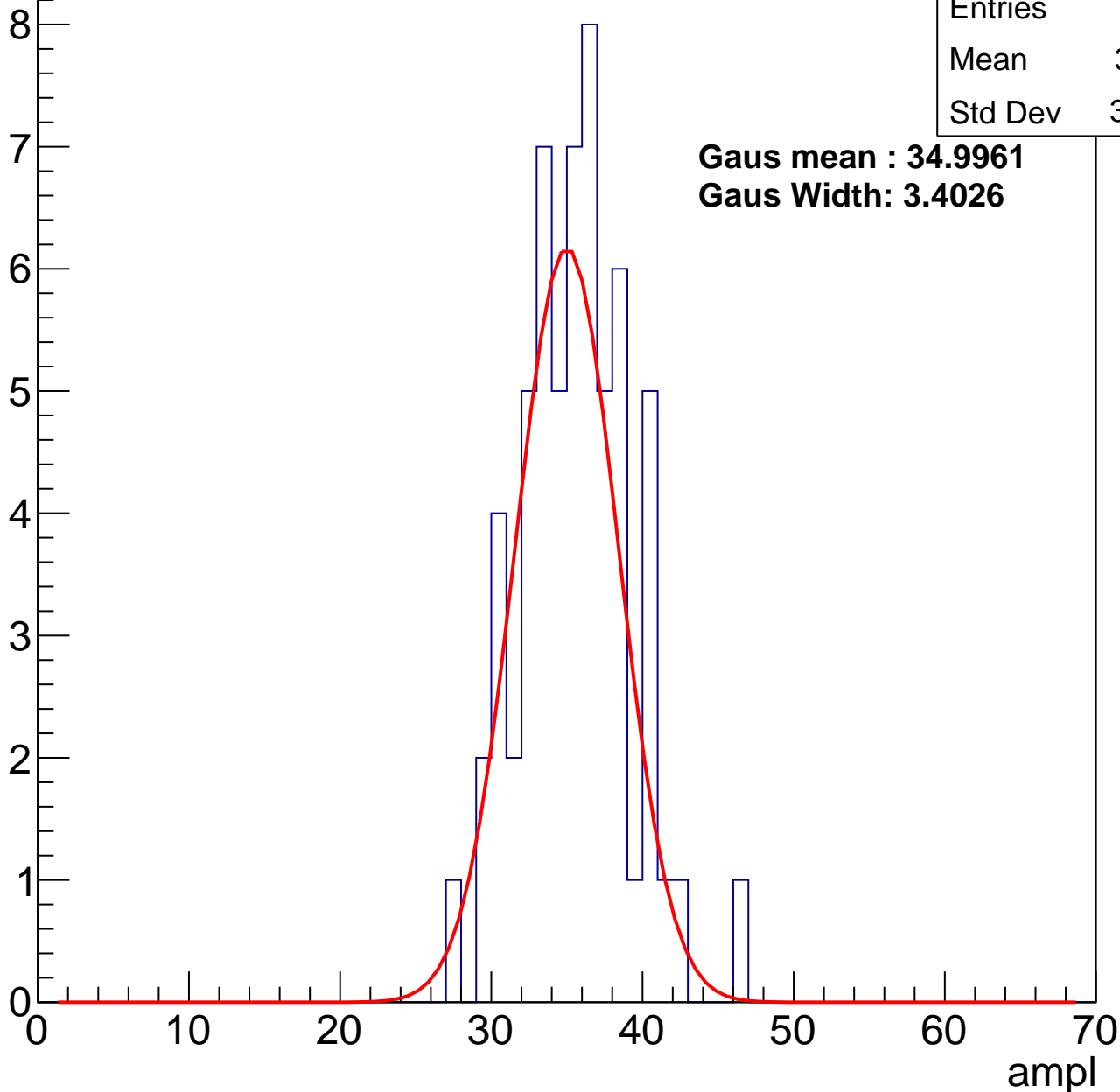
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.11
Std Dev	3.563

Gaus mean : 34.9961

Gaus Width: 3.4026



B1L103S, U14-ch23, adc2

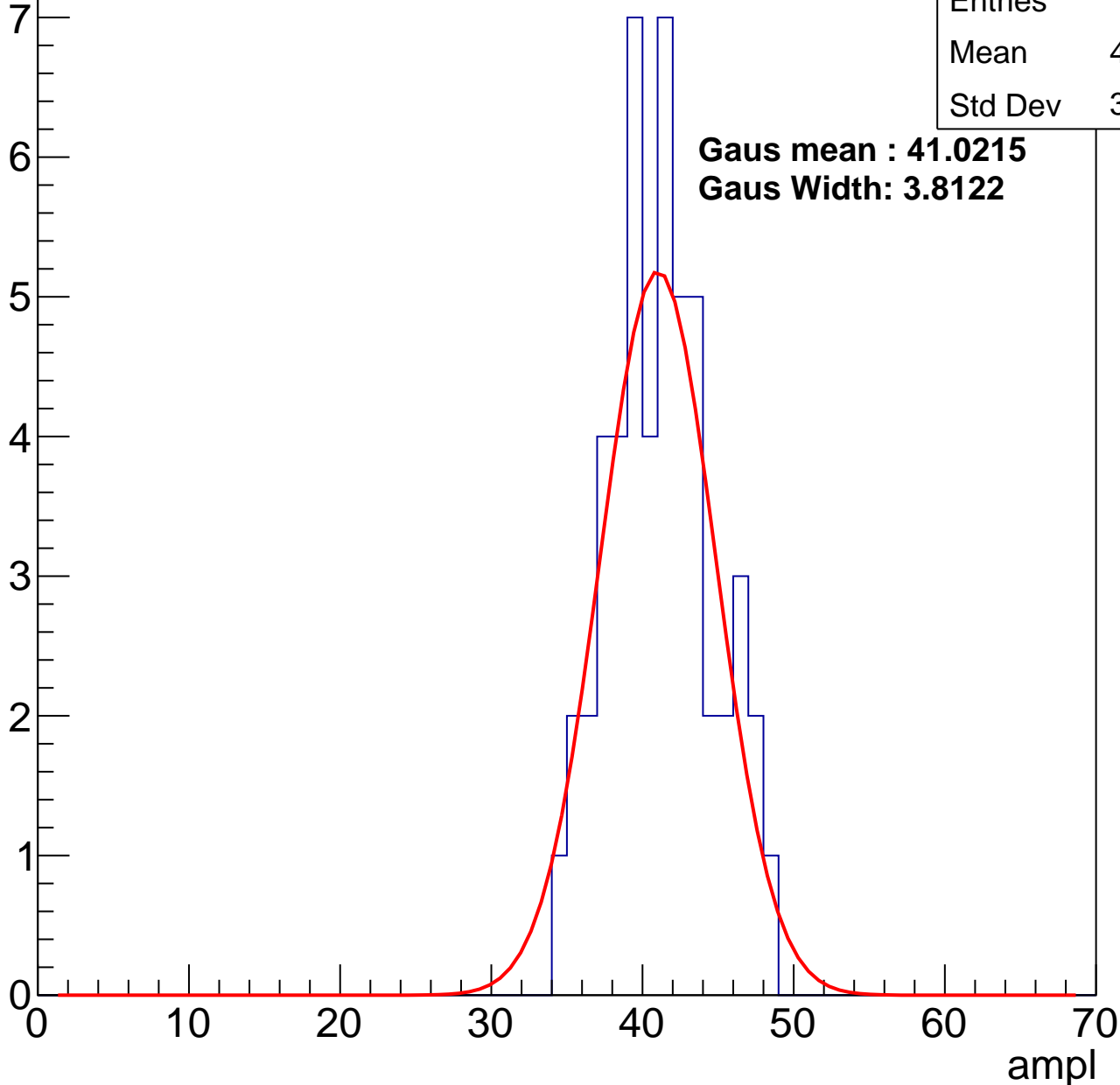
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	40.76
Std Dev	3.347

Gaus mean : 41.0215

Gaus Width: 3.8122

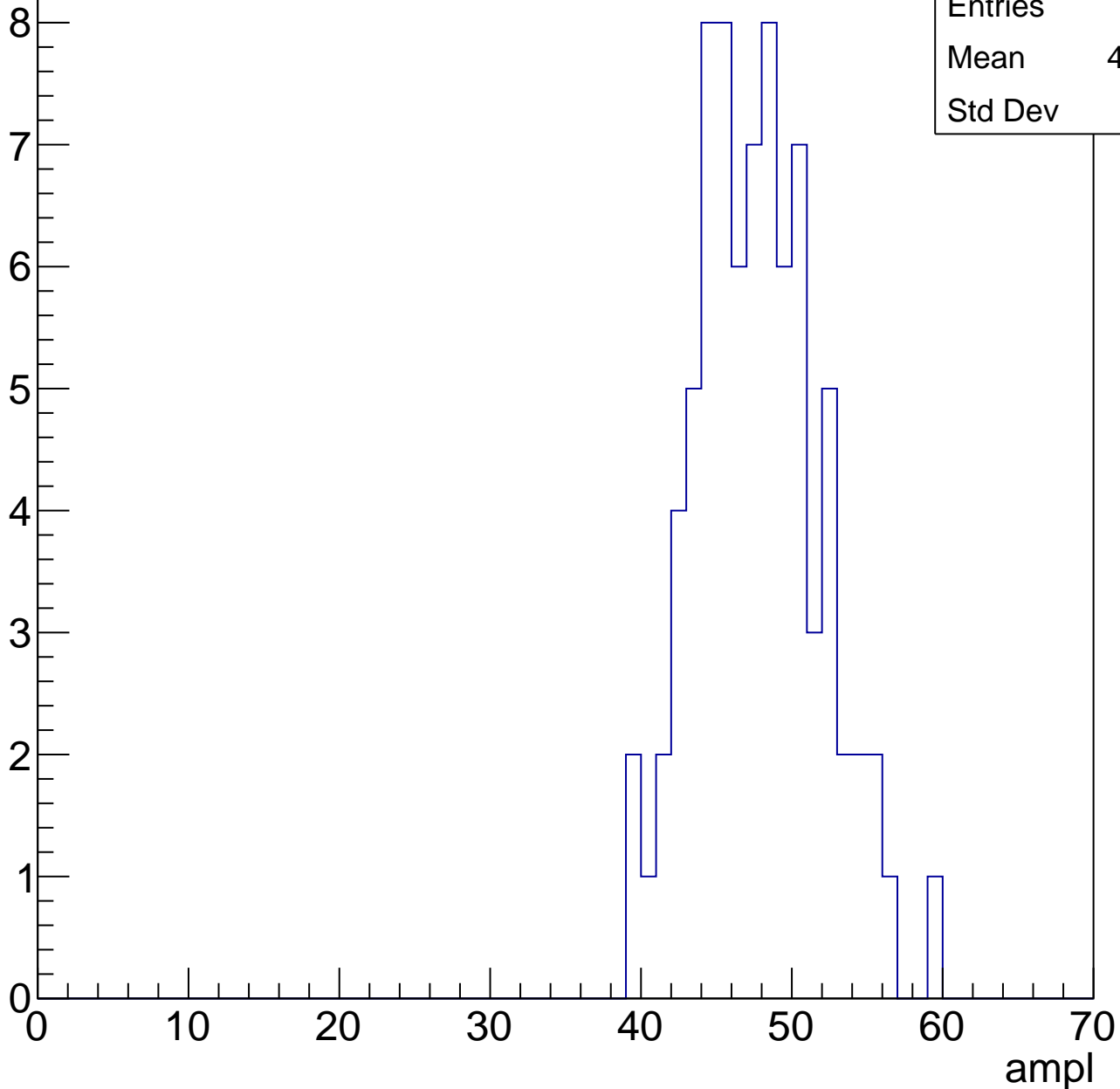


B1L103S, U14-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	47.25
Std Dev	4.07

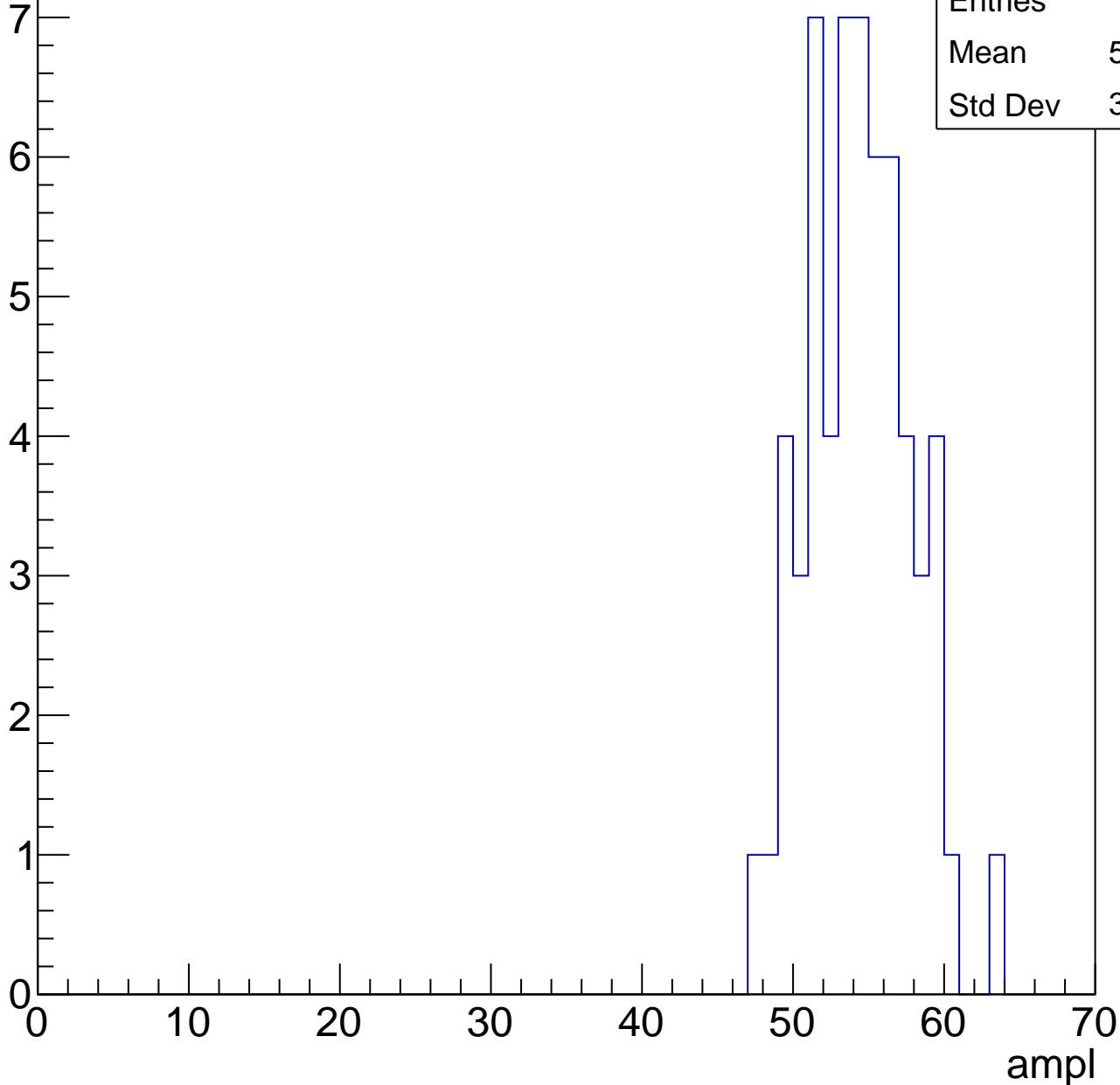


B1L103S, U14-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	53.93
Std Dev	3.318

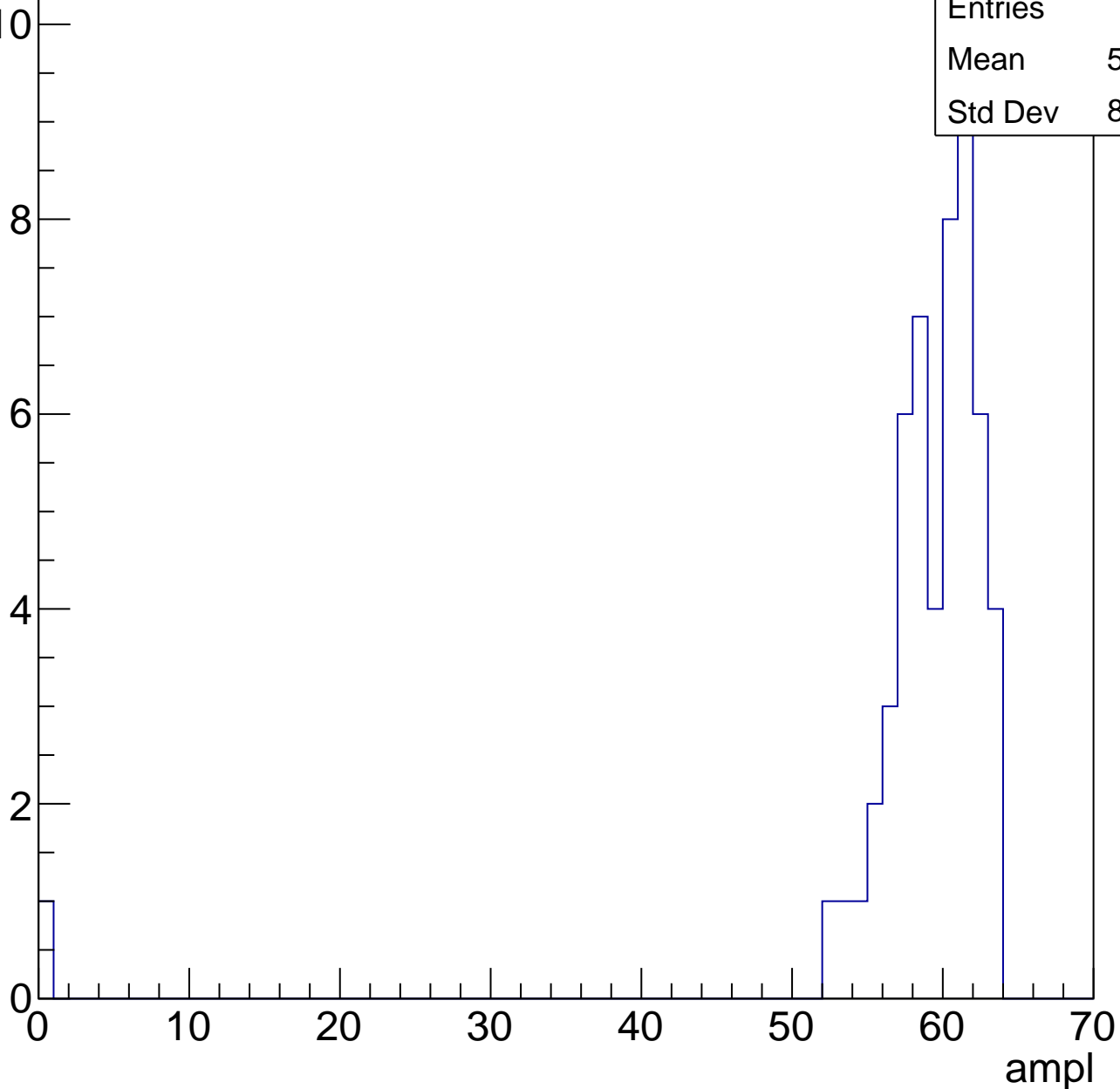


B1L103S, U14-ch23, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	58.06
Std Dev	8.387

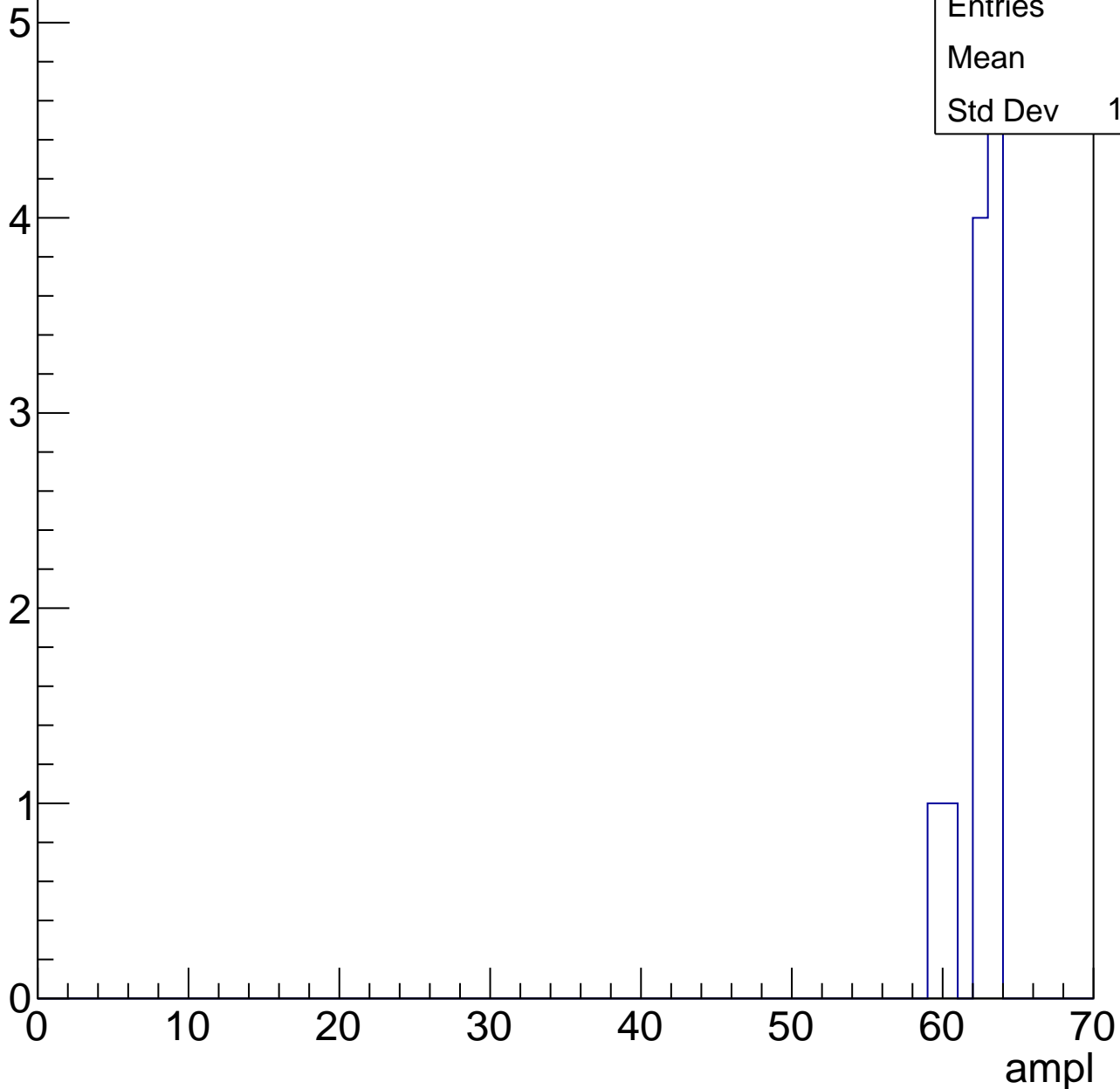


B1L103S, U14-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	62
Std Dev	1.279



B1L103S, U14-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch24, adc0

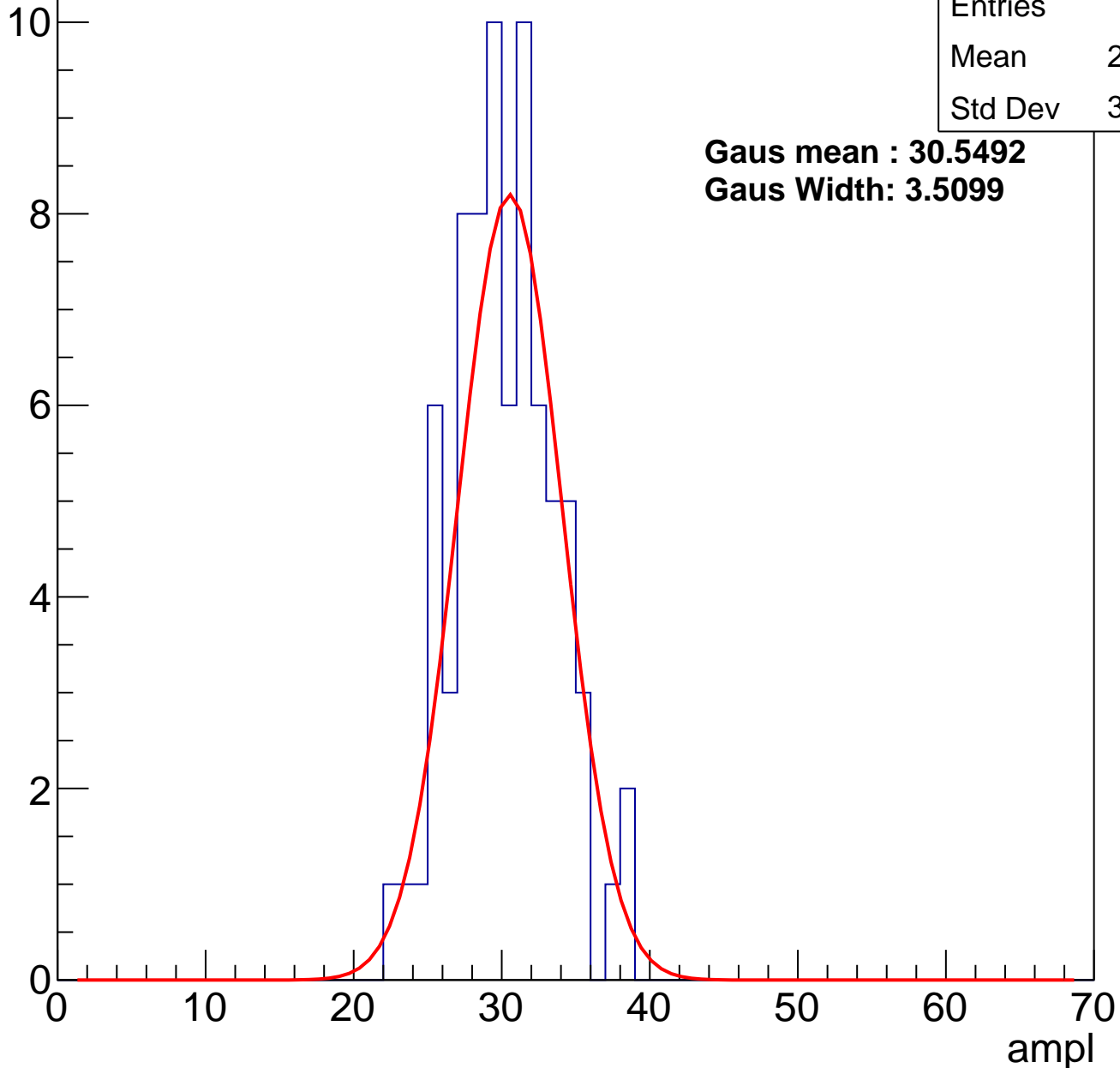
calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	29.76
Std Dev	3.379

Gaus mean : 30.5492

Gaus Width: 3.5099

Entry



B1L103S, U14-ch24, adc1

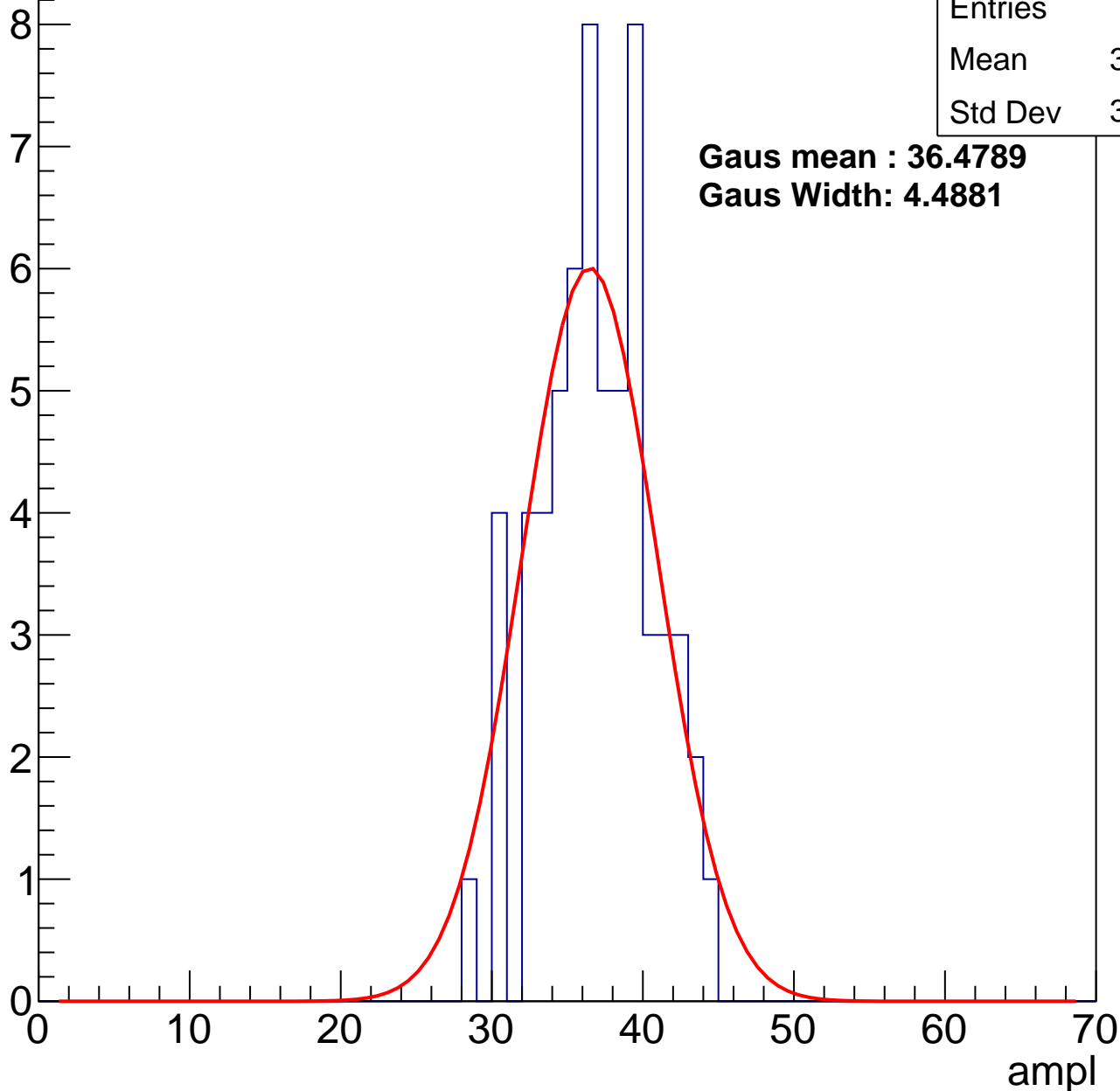
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	36.48
Std Dev	3.613

Gaus mean : 36.4789

Gaus Width: 4.4881



B1L103S, U14-ch24, adc2

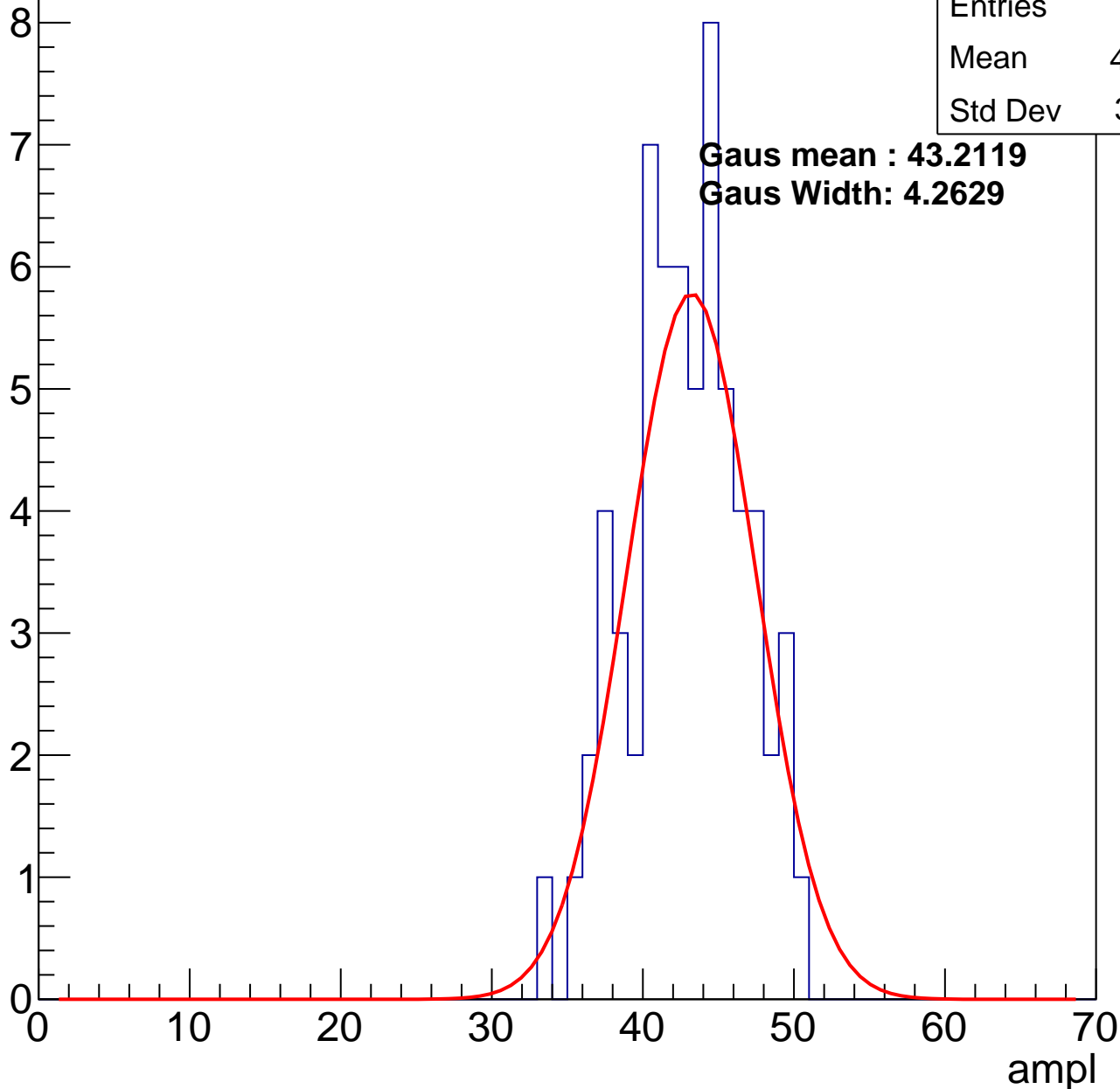
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.42
Std Dev	3.791

Gaus mean : 43.2119

Gaus Width: 4.2629

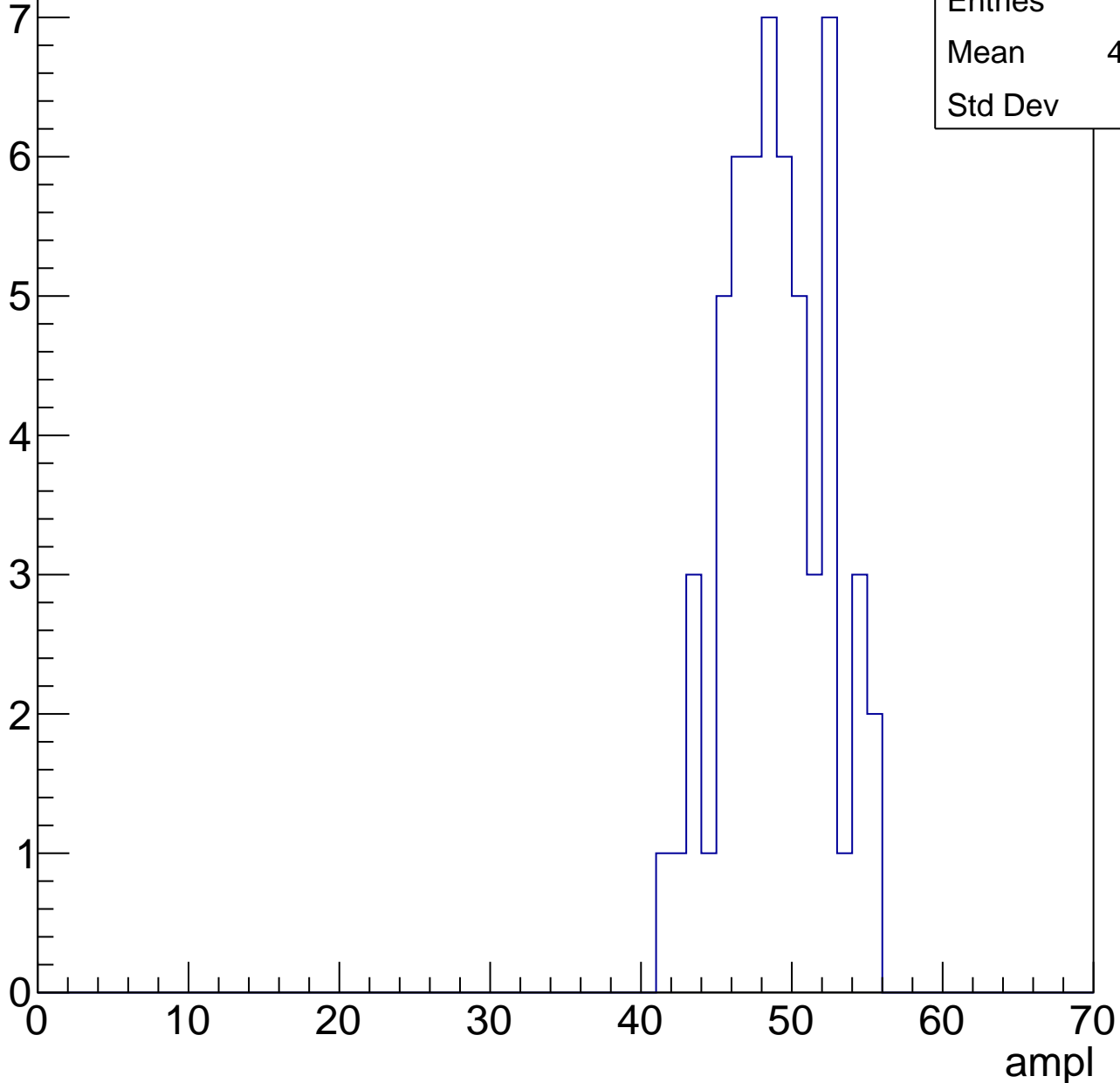


B1L103S, U14-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	48.44
Std Dev	3.34

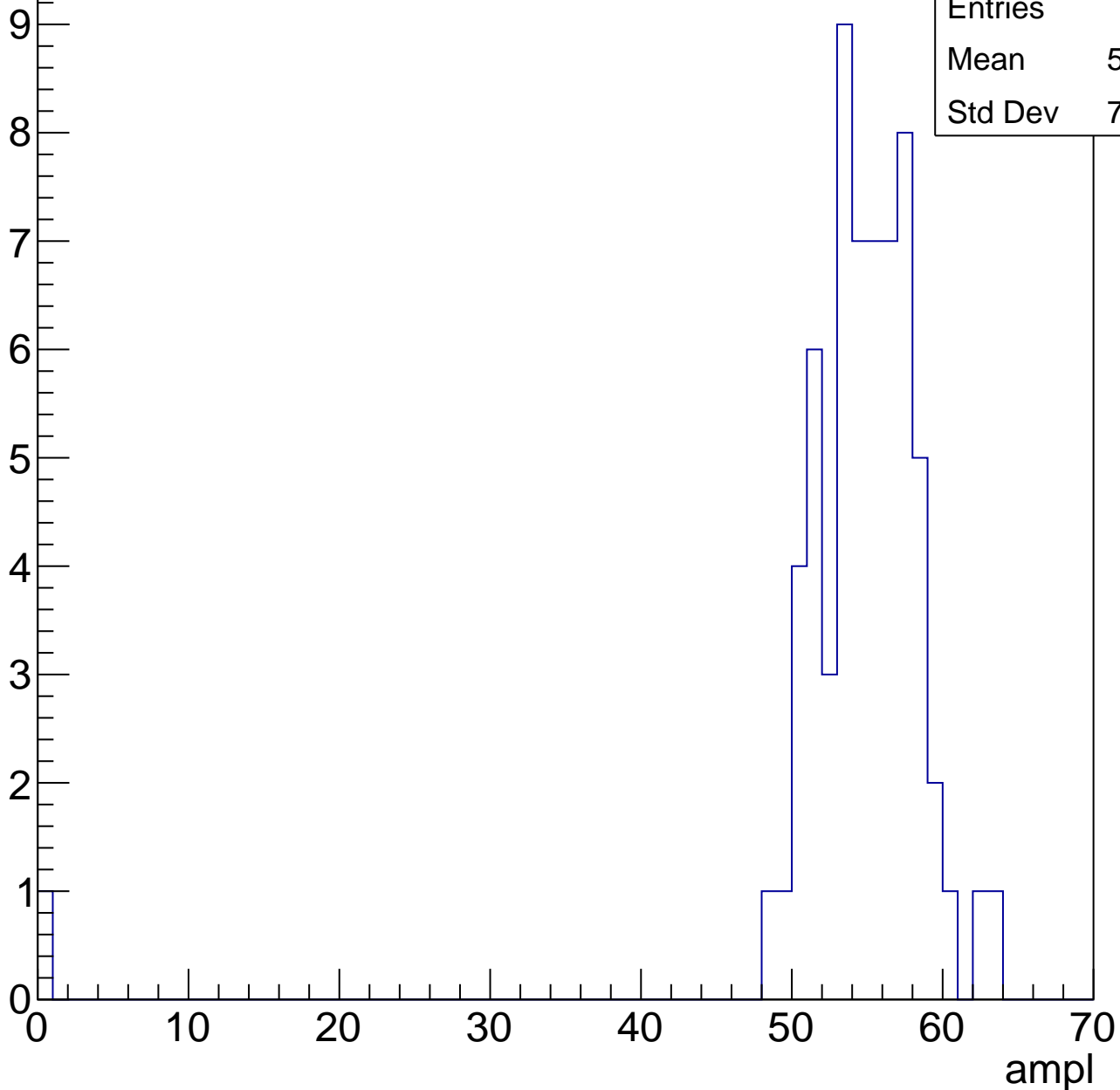


B1L103S, U14-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	53.75
Std Dev	7.427

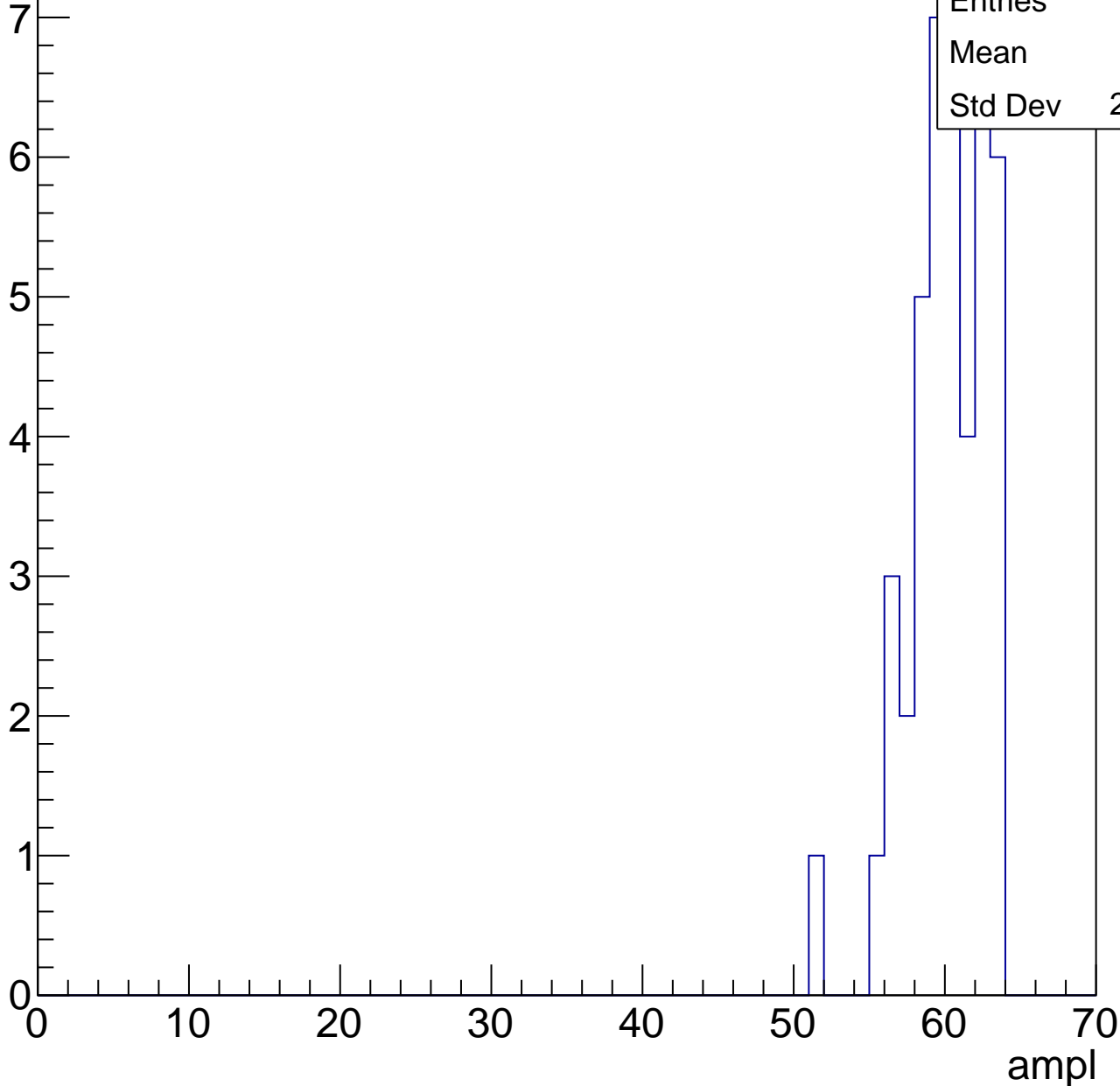


B1L103S, U14-ch24, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	59.7
Std Dev	2.557

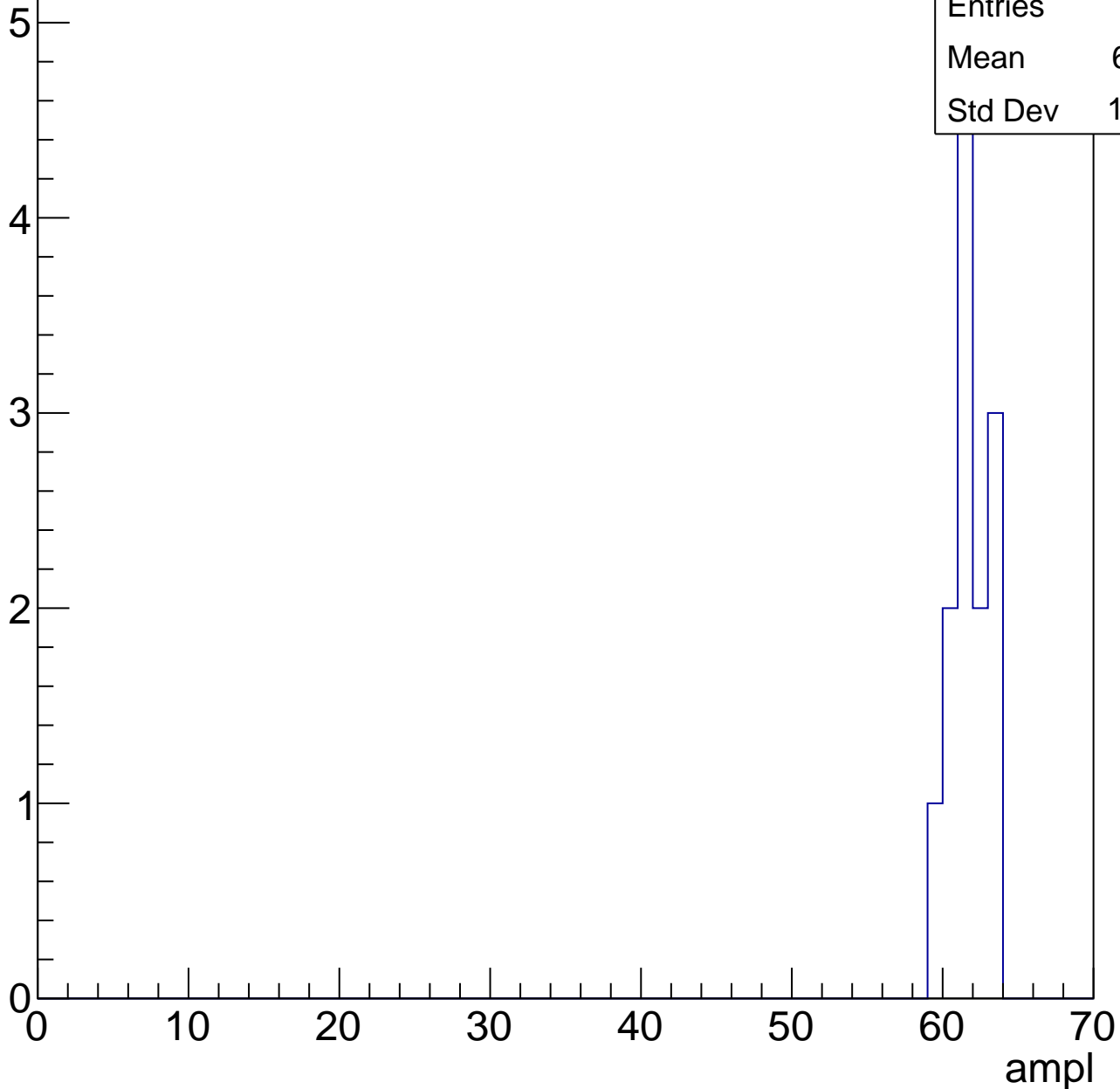


B1L103S, U14-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

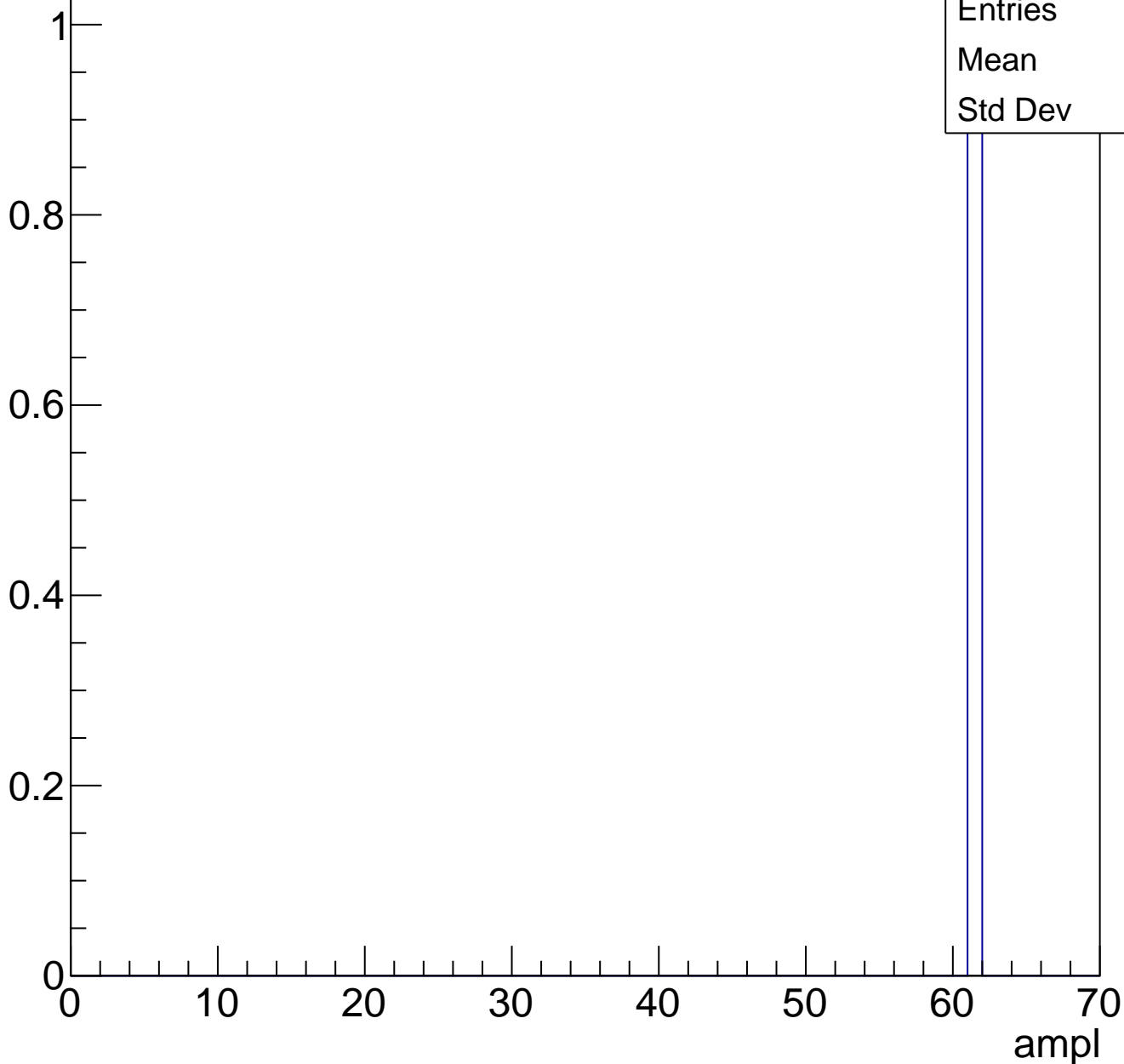
Entries	13
Mean	61.31
Std Dev	1.202



B1L103S, U14-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch25, adc0

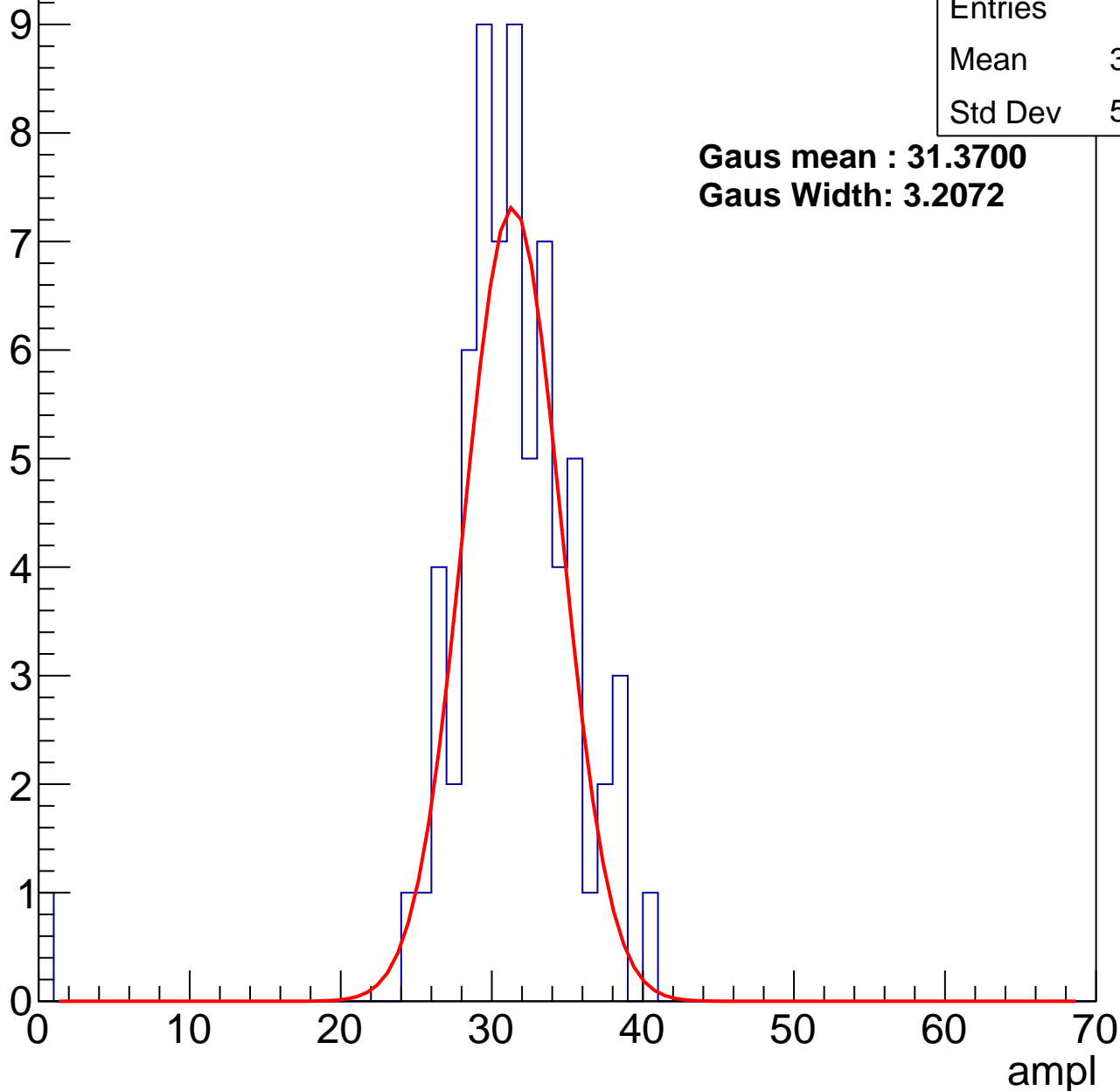
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	30.75
Std Dev	5.065

Gaus mean : 31.3700

Gaus Width: 3.2072



B1L103S, U14-ch25, adc1

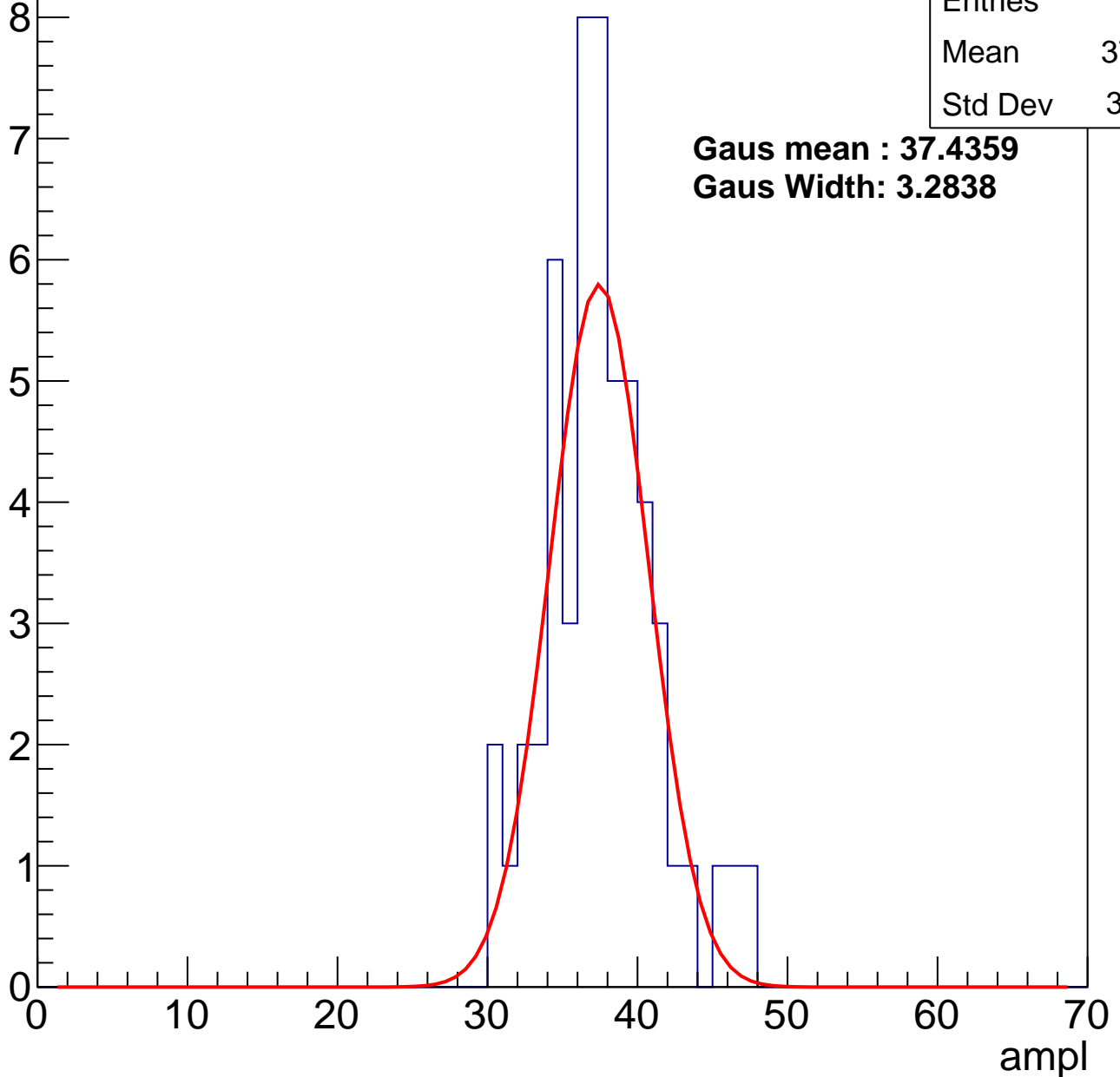
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	37.13
Std Dev	3.611

Gaus mean : 37.4359

Gaus Width: 3.2838



B1L103S, U14-ch25, adc2

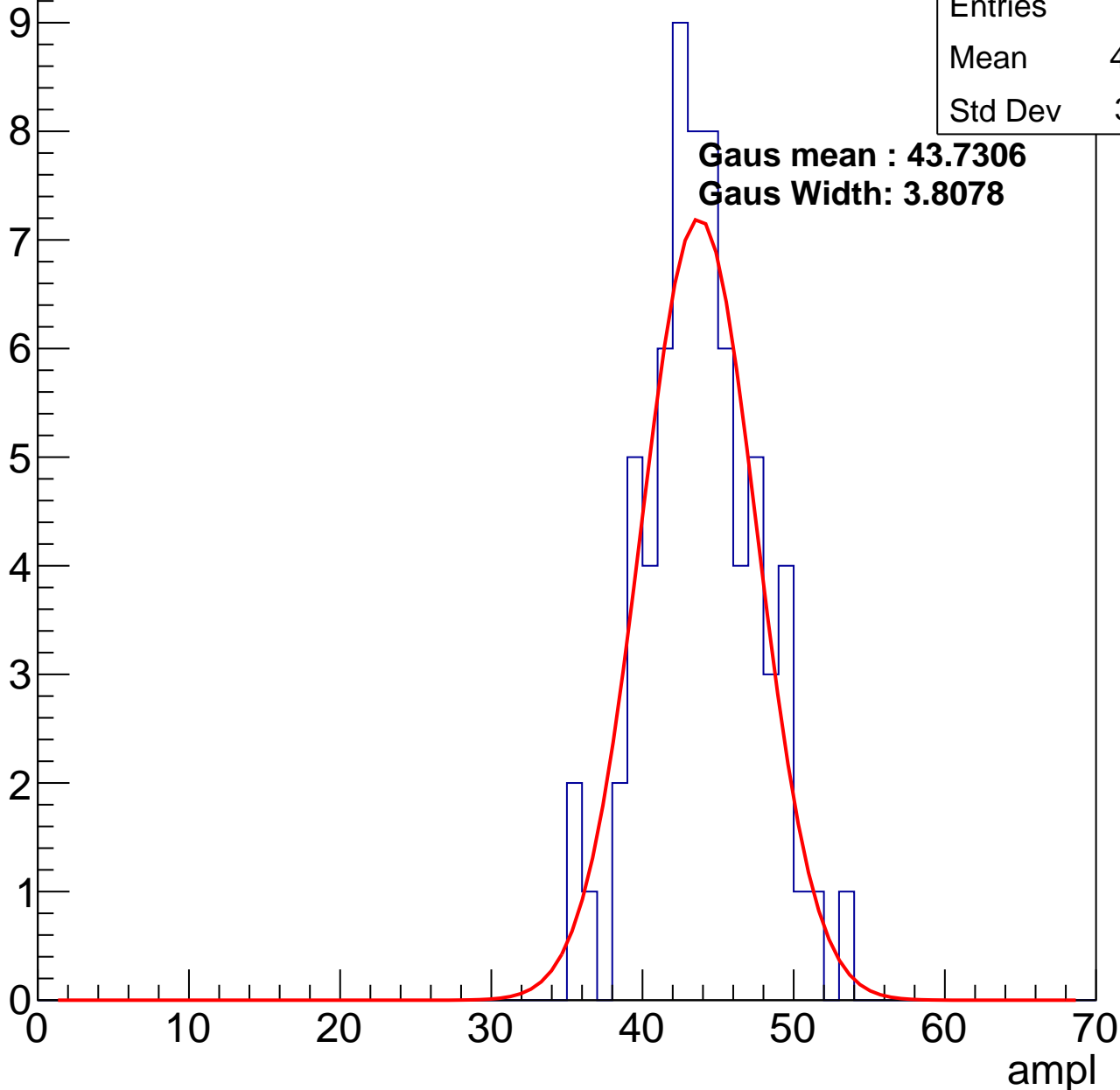
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.43
Std Dev	3.671

Gaus mean : 43.7306

Gaus Width: 3.8078



B1L103S, U14-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	50.64
Std Dev	4.019

Entry

10

8

6

4

2

0

0

10

20

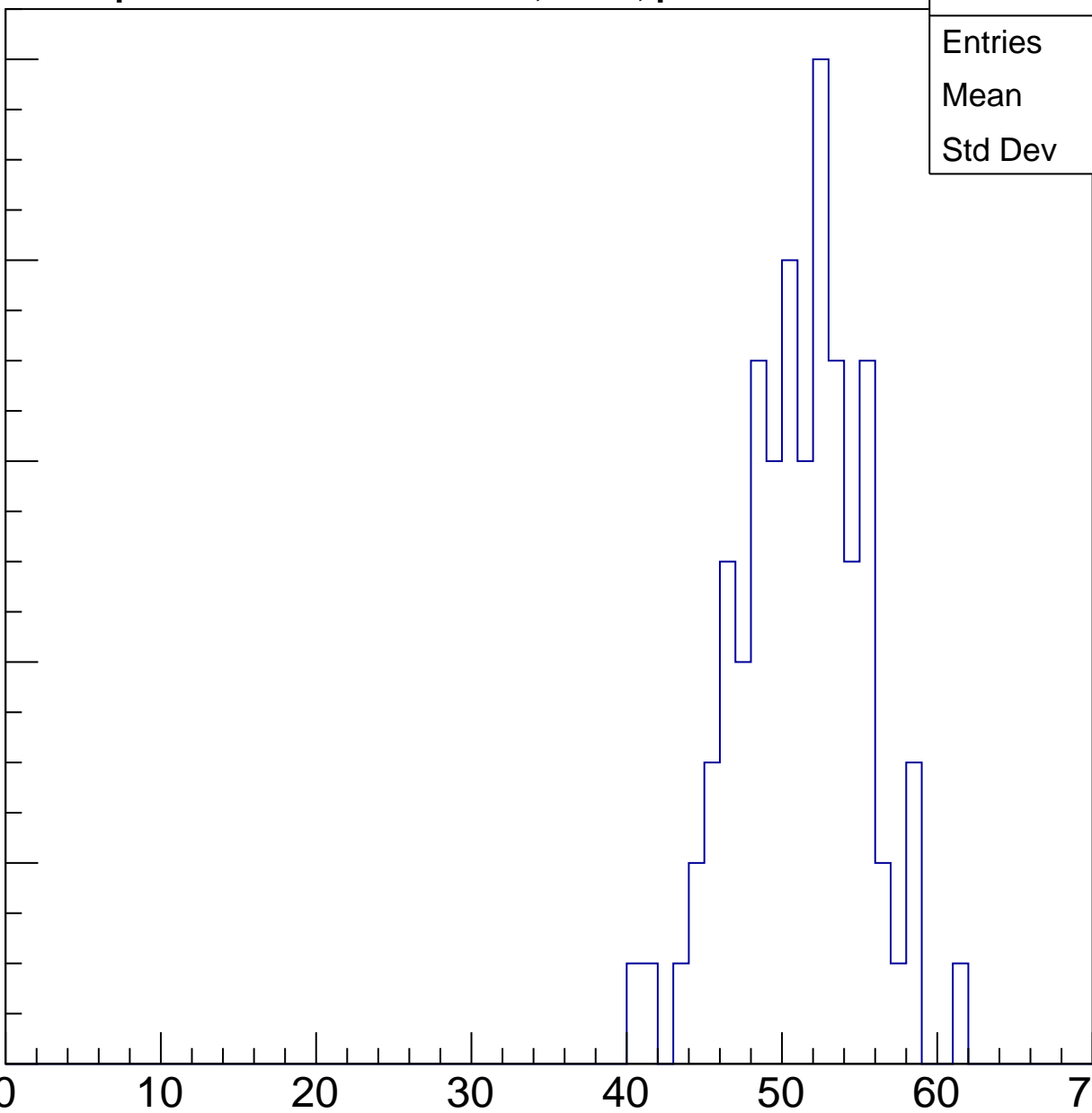
30

40

50

60

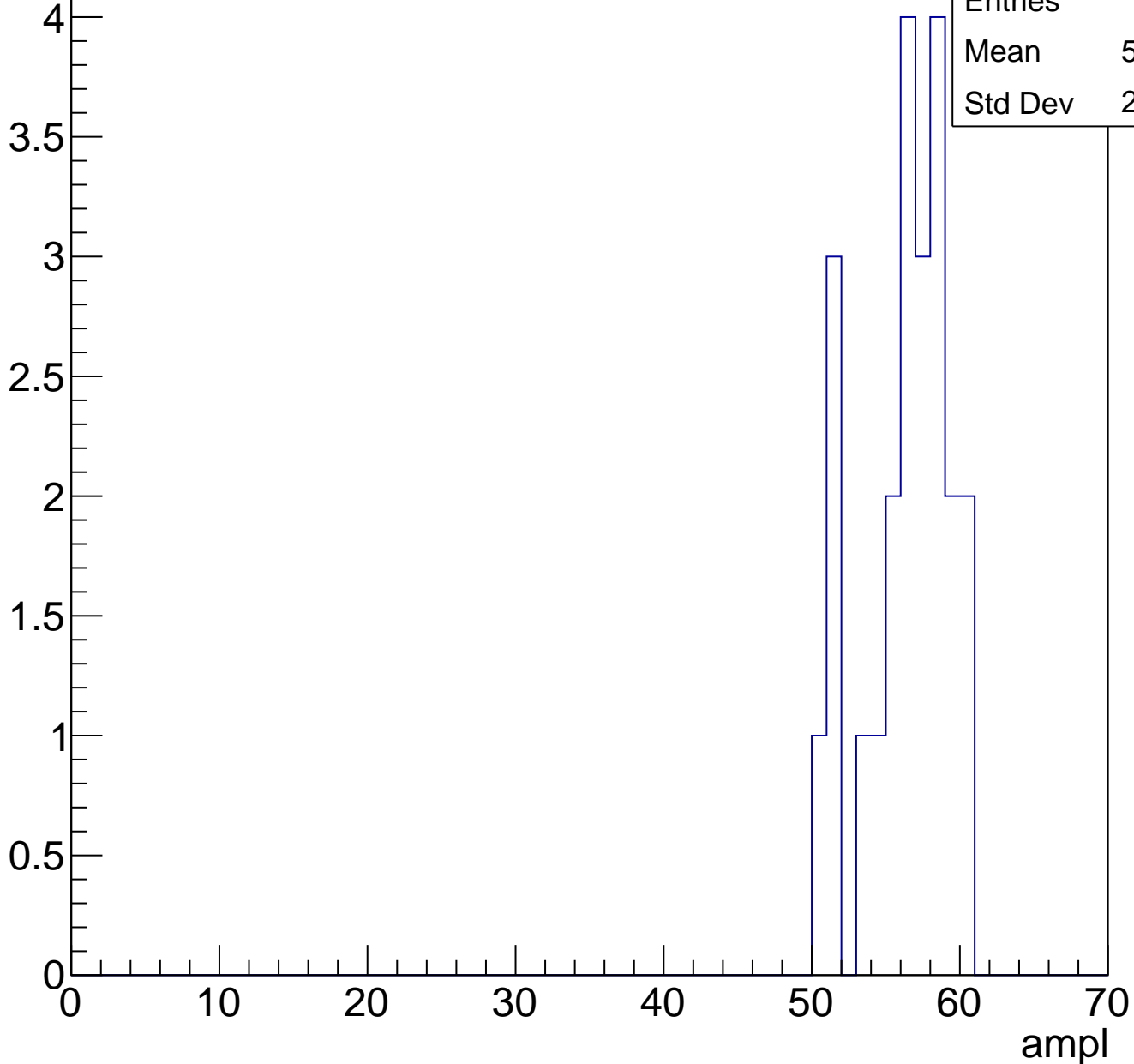
ampl



B1L103S, U14-ch25, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch25, adc5

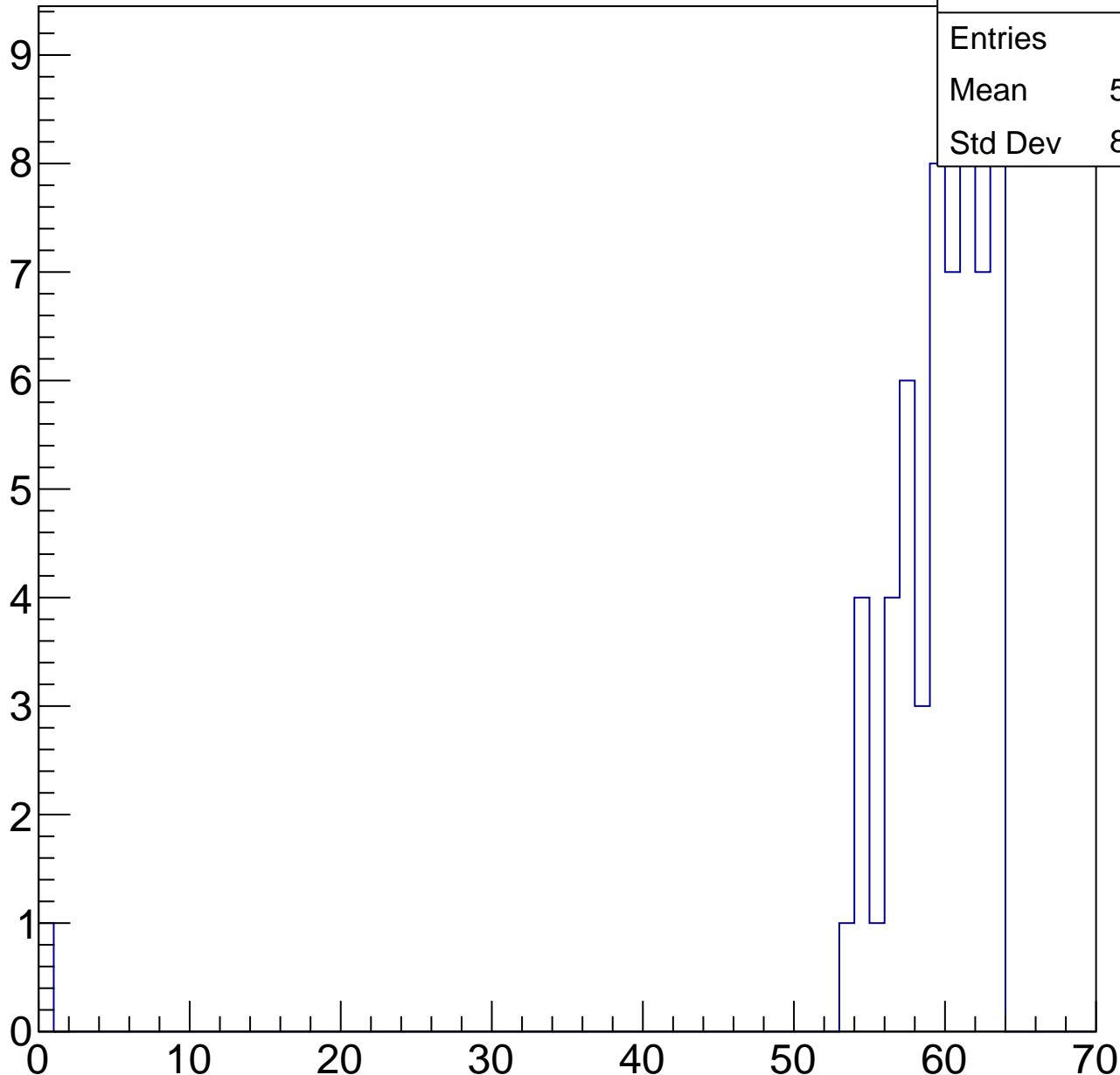
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	58.36
Std Dev	8.136

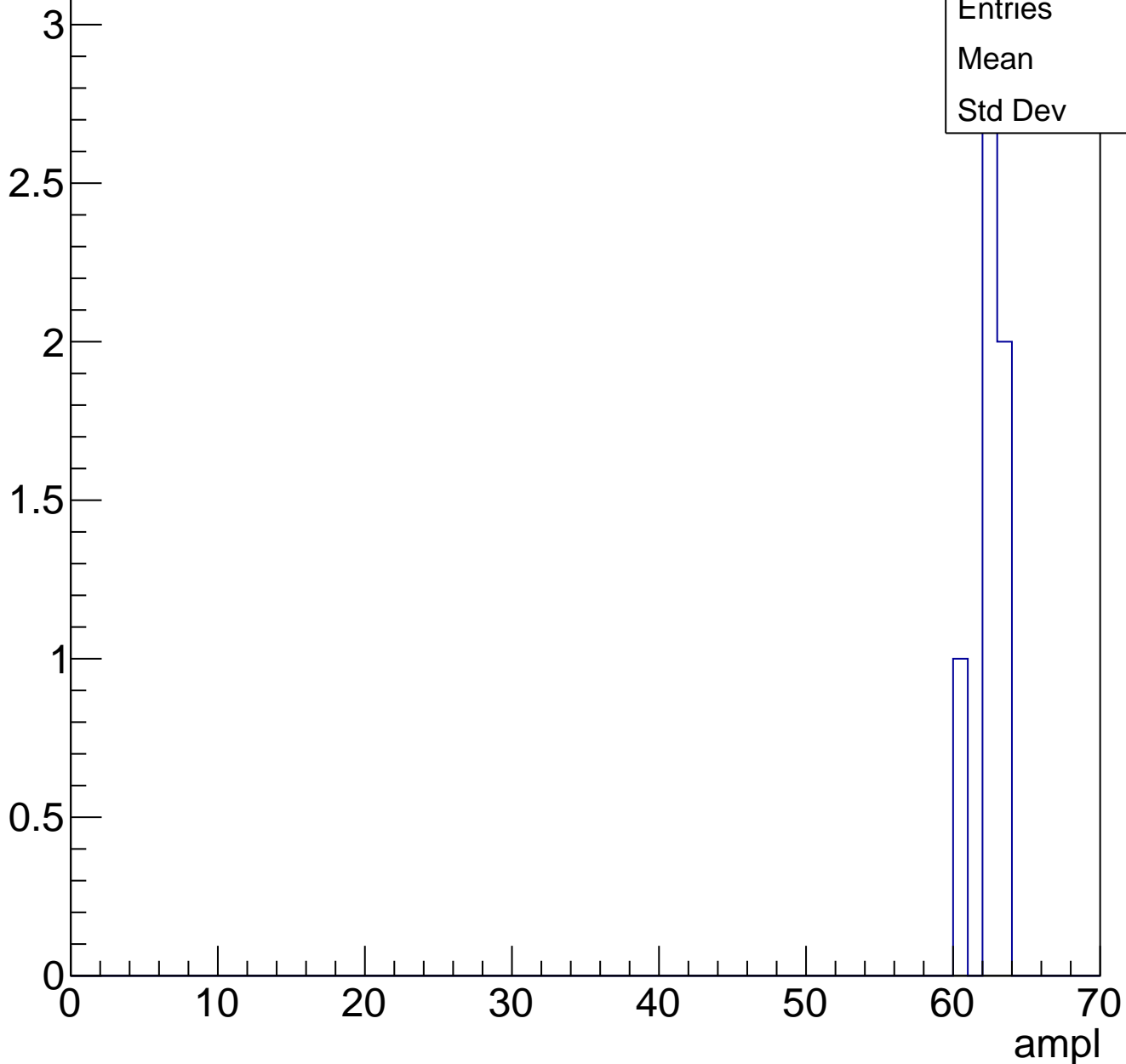
ampl



B1L103S, U14-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U14-ch26, adc0

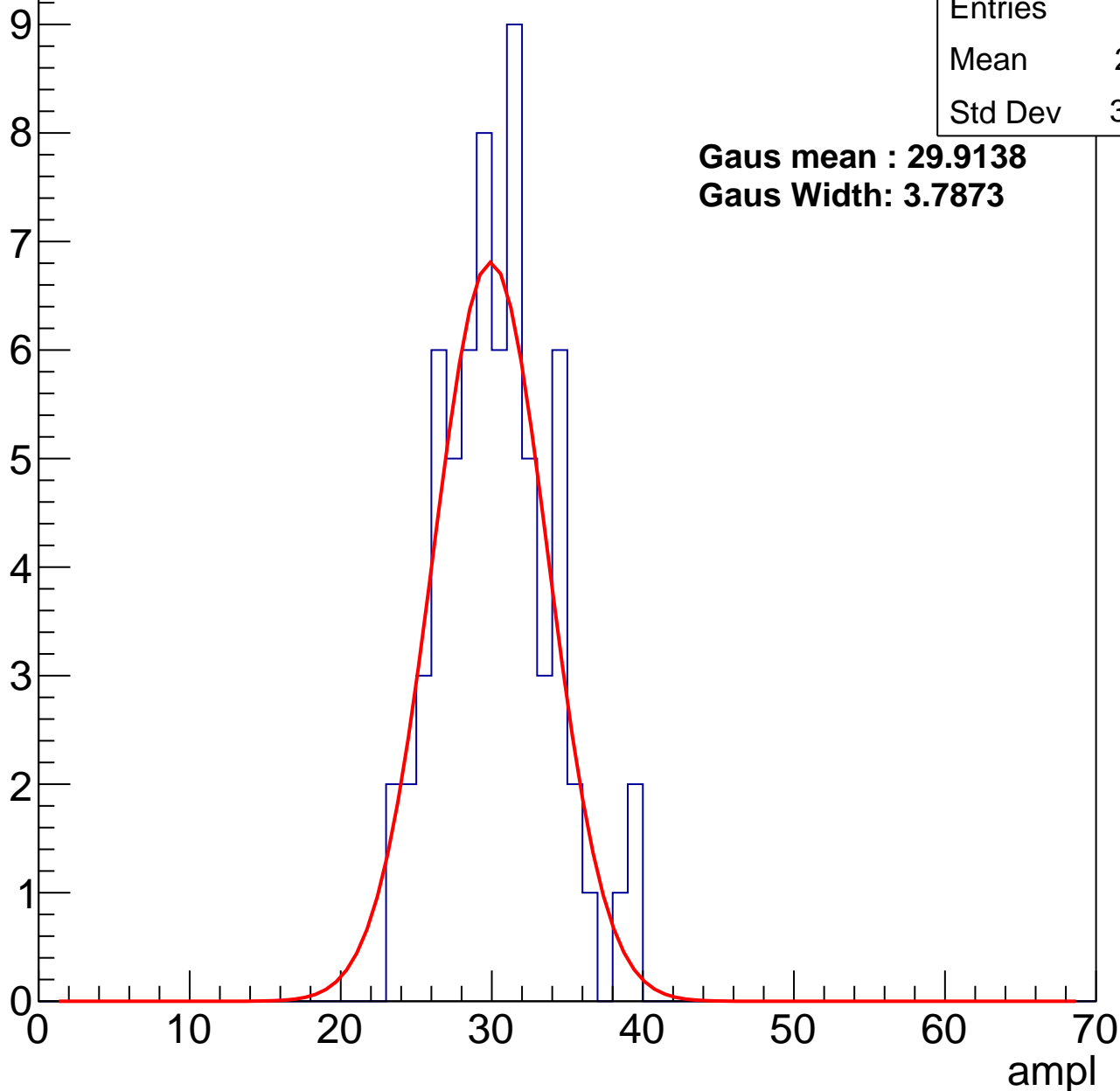
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.91
Std Dev	3.627

Gaus mean : 29.9138

Gaus Width: 3.7873



B1L103S, U14-ch26, adc1

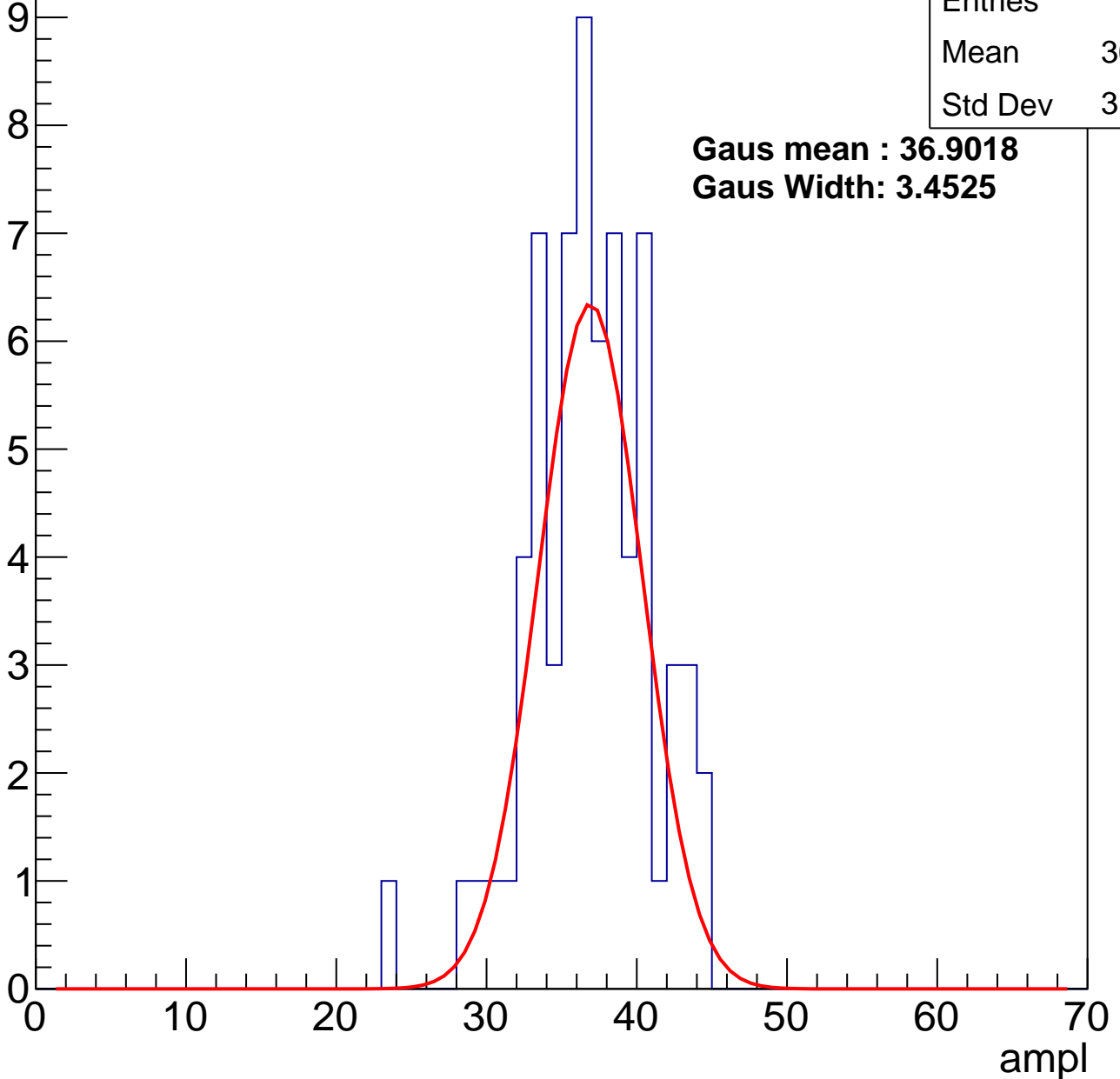
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.46
Std Dev	3.957

Gaus mean : 36.9018

Gaus Width: 3.4525



B1L103S, U14-ch26, adc2

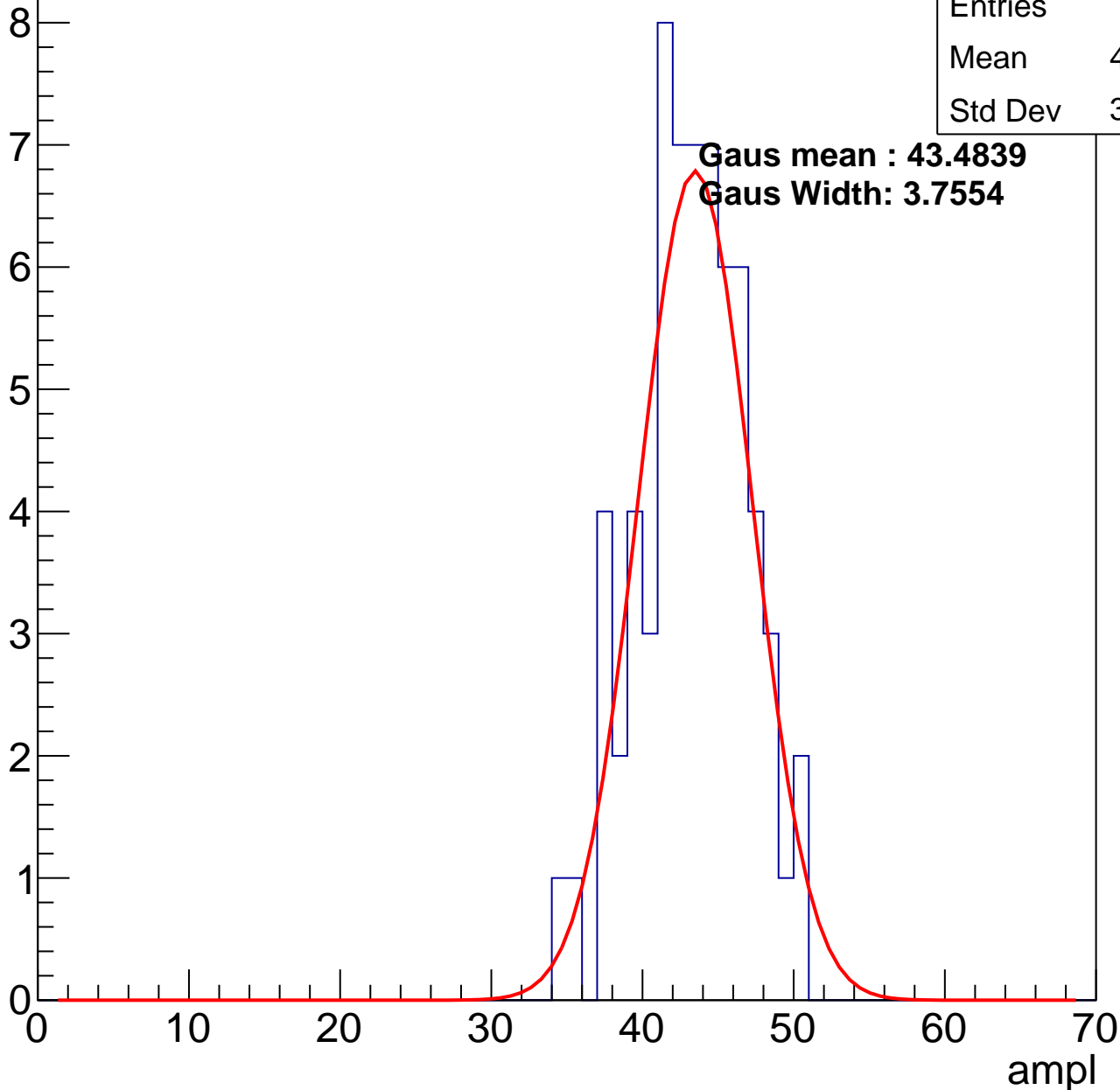
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.83
Std Dev	3.536

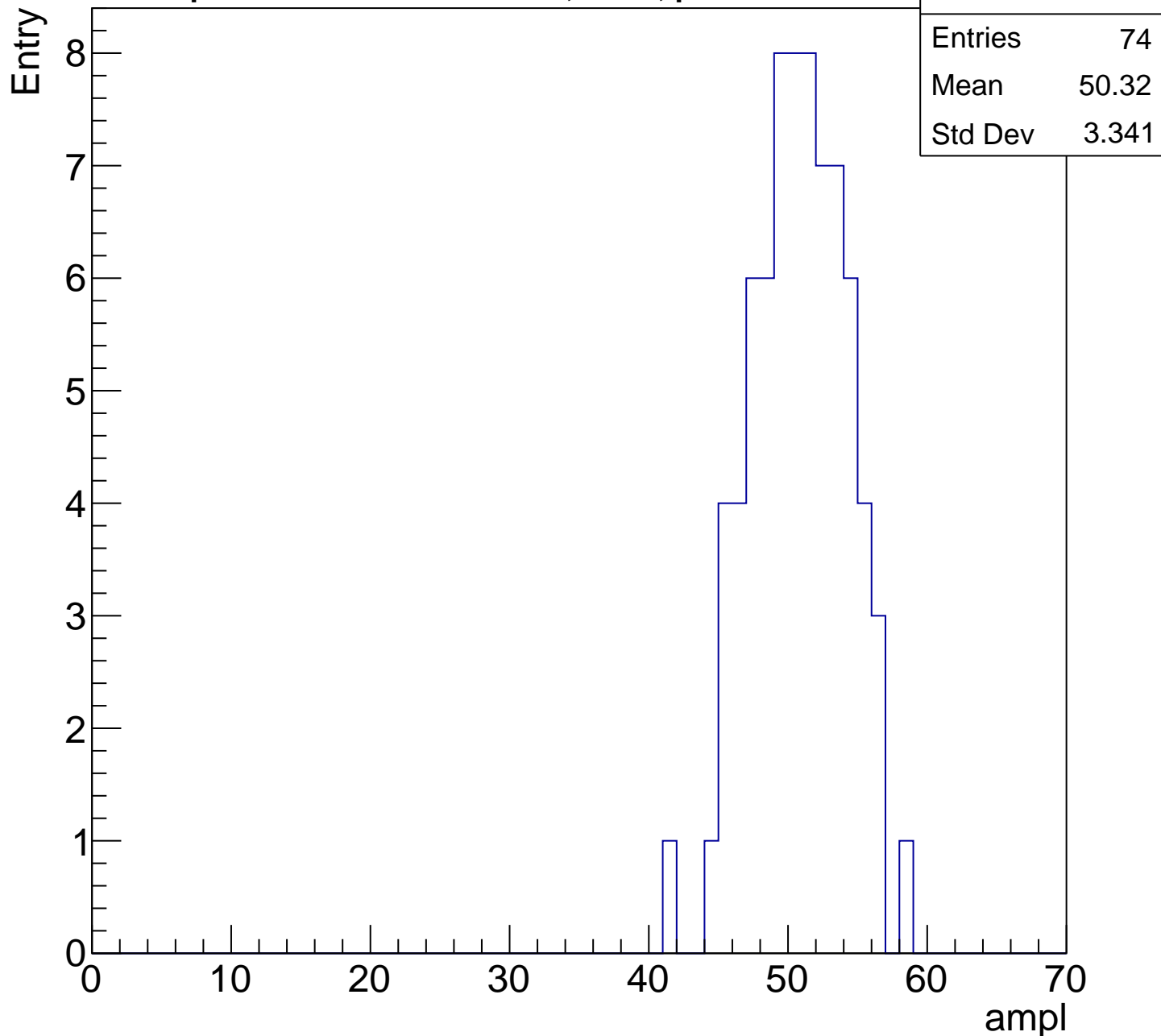
Gaus mean : 43.4839

Gaus Width: 3.7554



B1L103S, U14-ch26, adc3

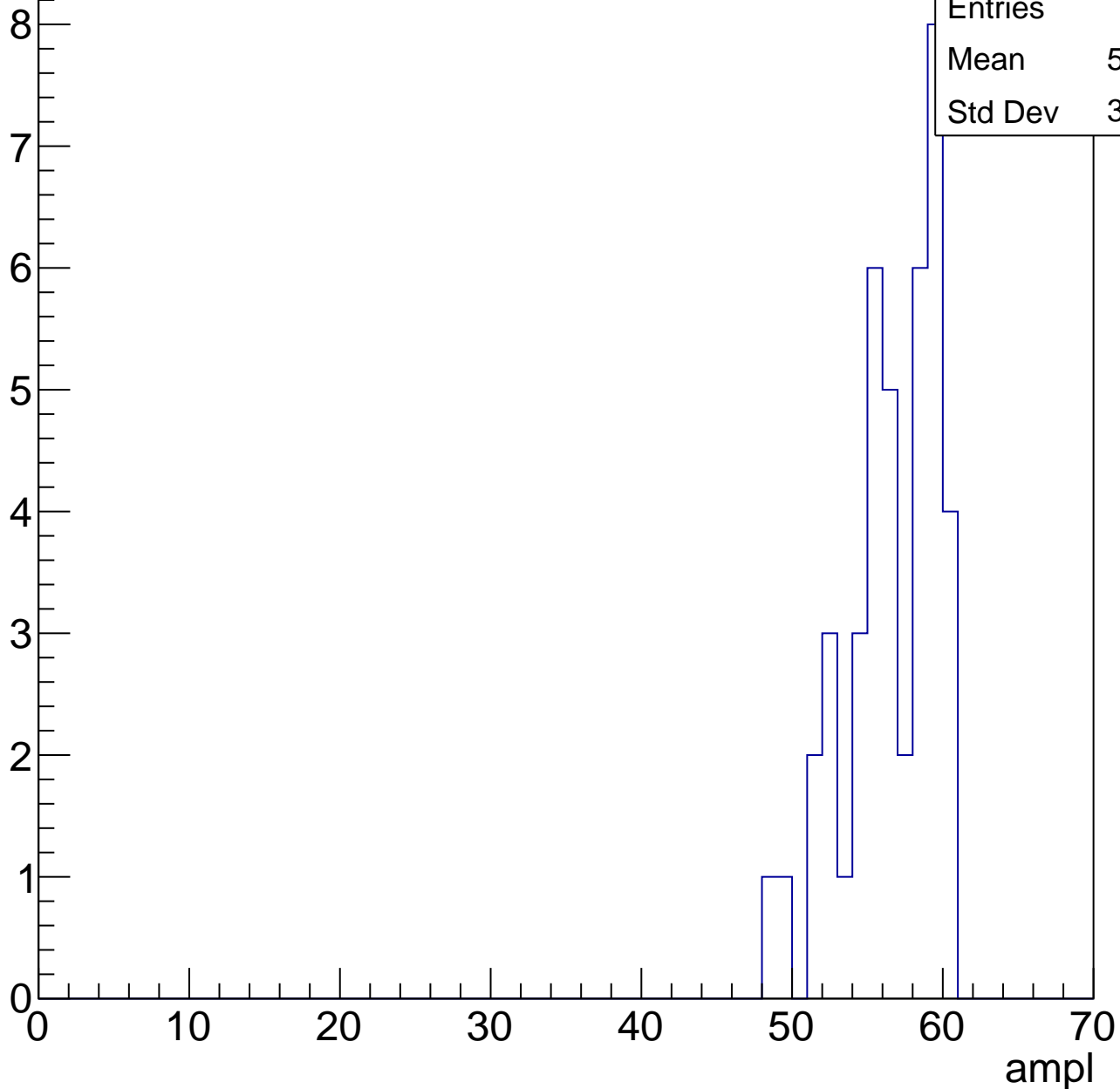
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U14-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

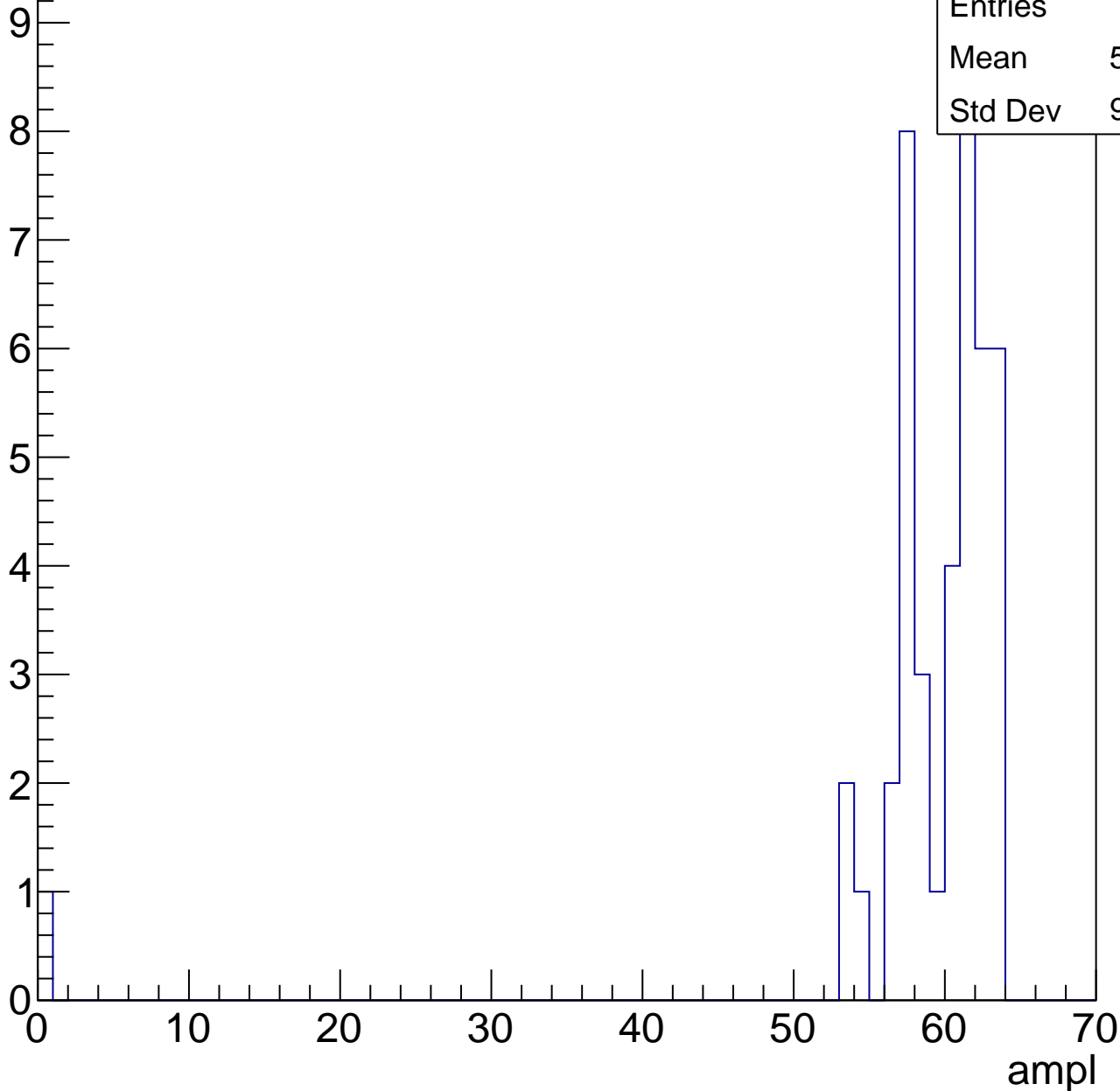


B1L103S, U14-ch26, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

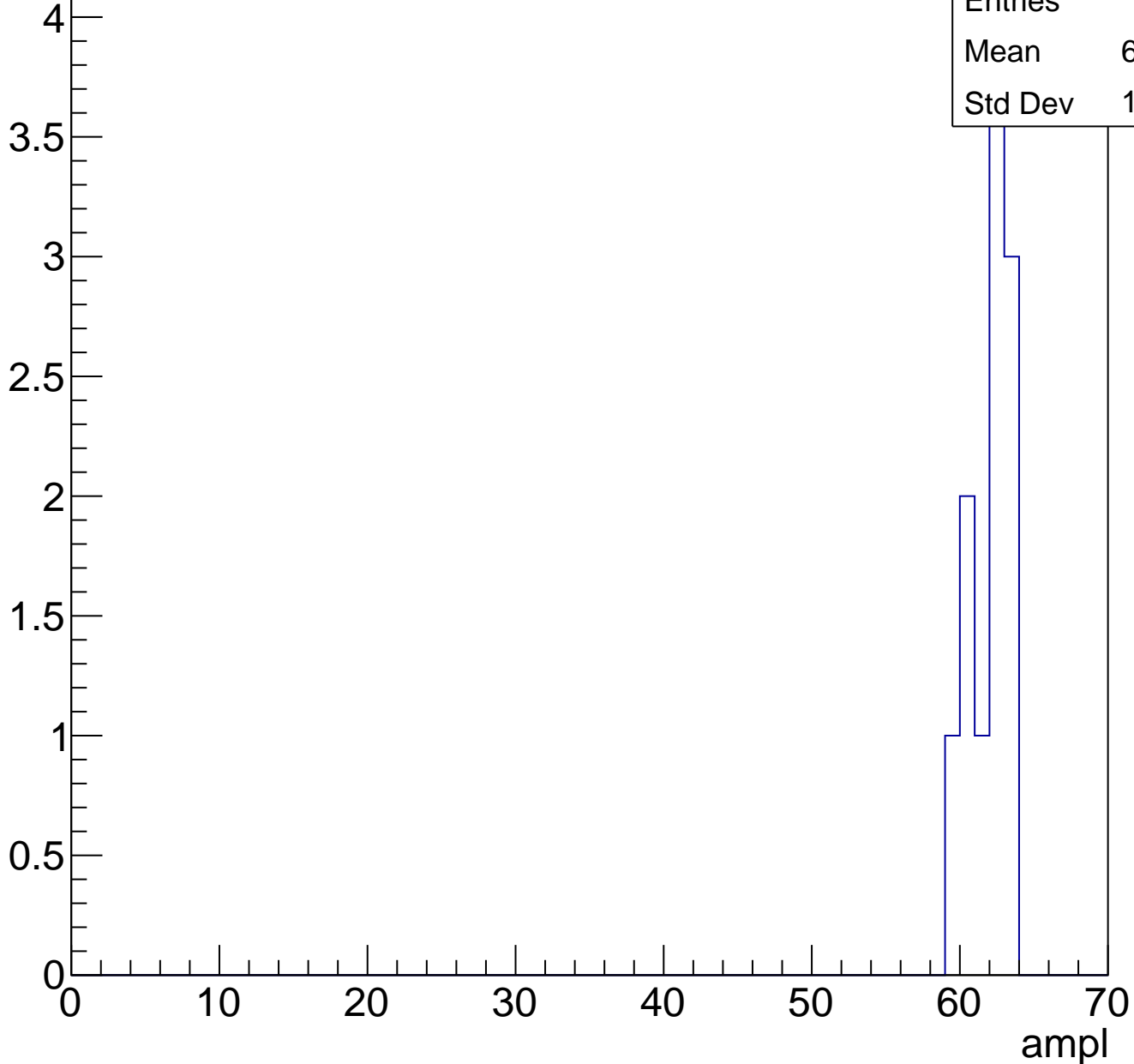
Entries	43
Mean	58.14
Std Dev	9.387



B1L103S, U14-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch27, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	29.74
Std Dev	5.706

Gaus mean : 30.9810

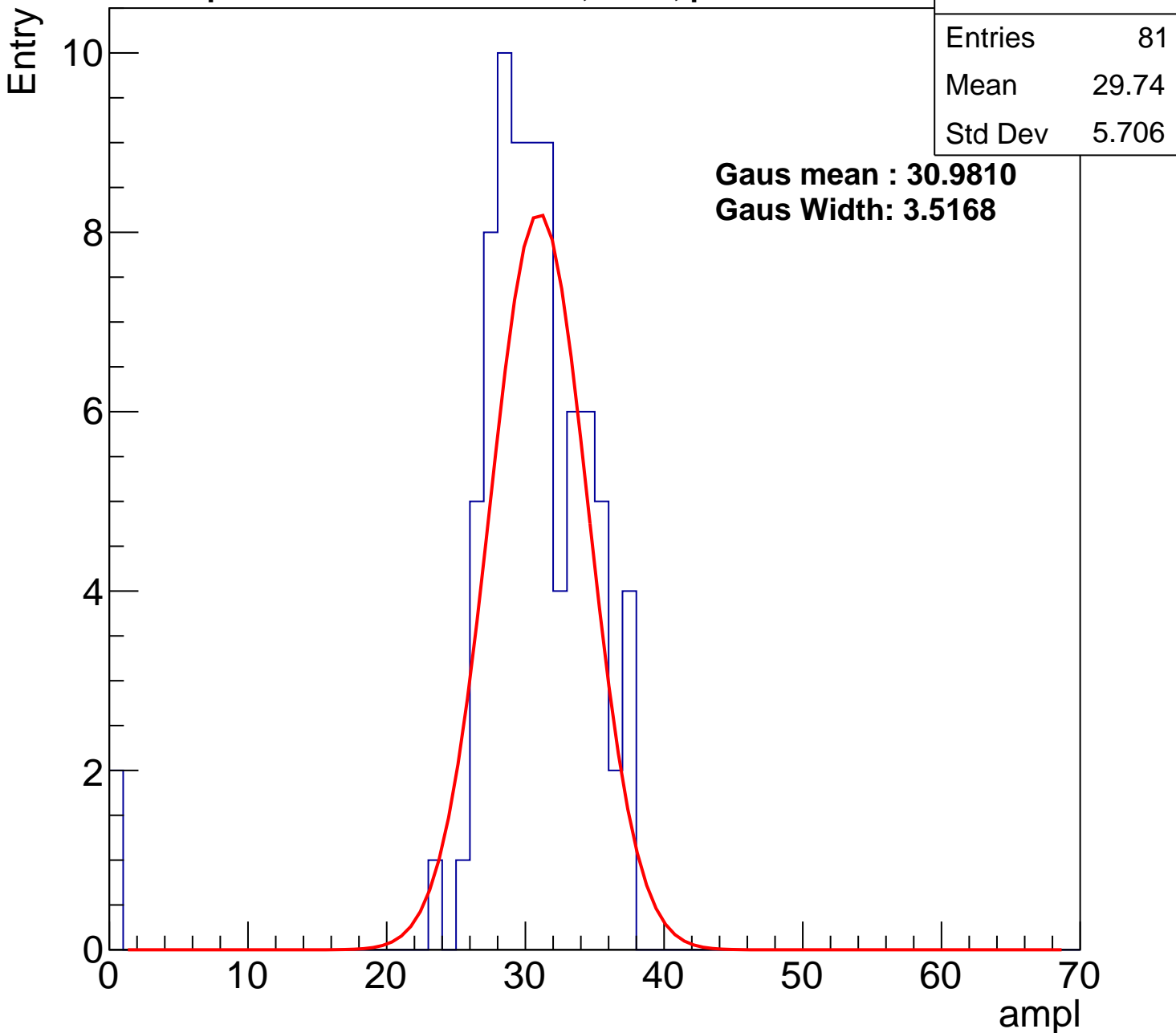
Gaus Width: 3.5168

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch27, adc1

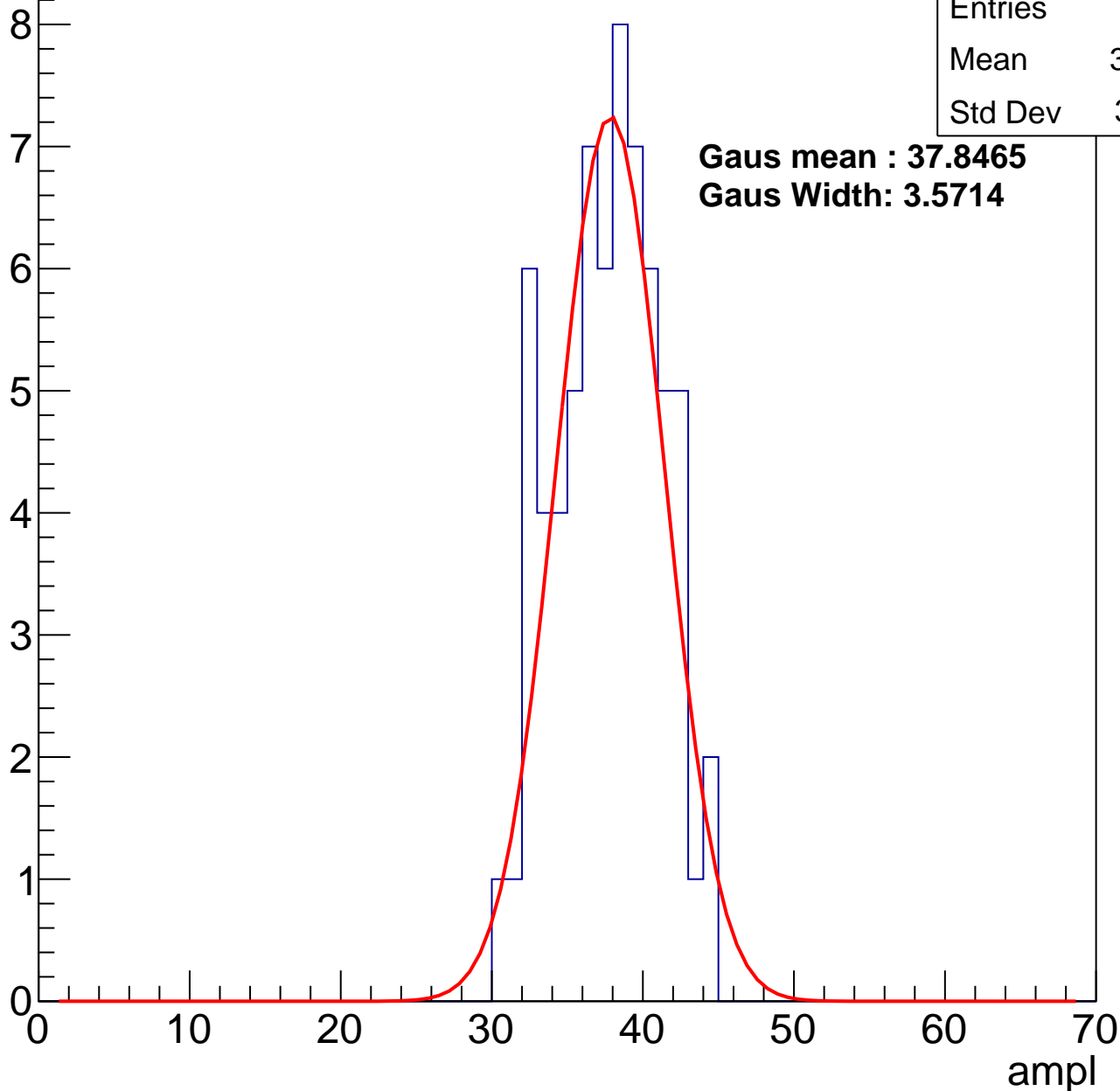
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.25
Std Dev	3.401

Gaus mean : 37.8465

Gaus Width: 3.5714



B1L103S, U14-ch27, adc2

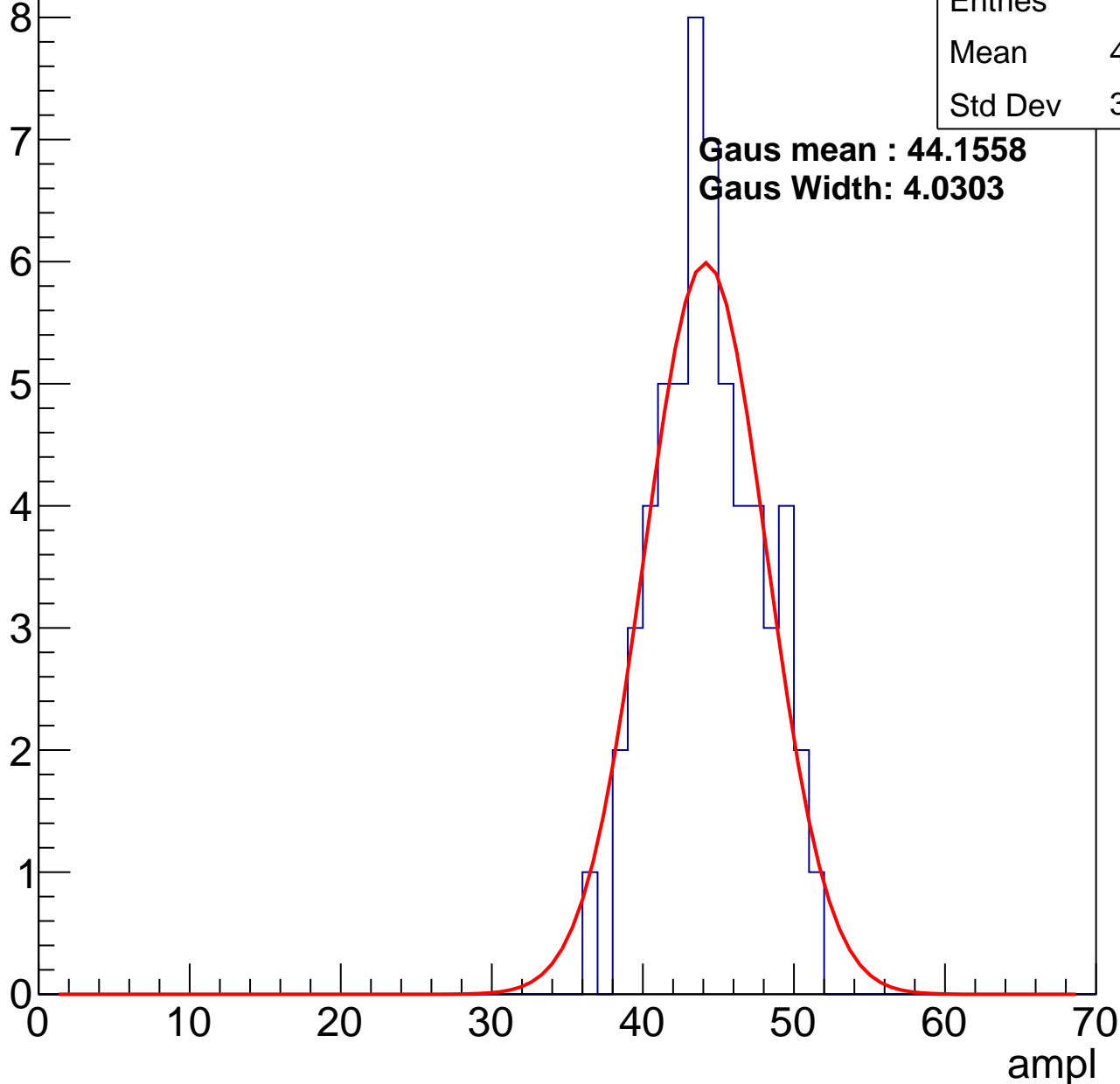
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.86
Std Dev	3.406

Gaus mean : 44.1558

Gaus Width: 4.0303

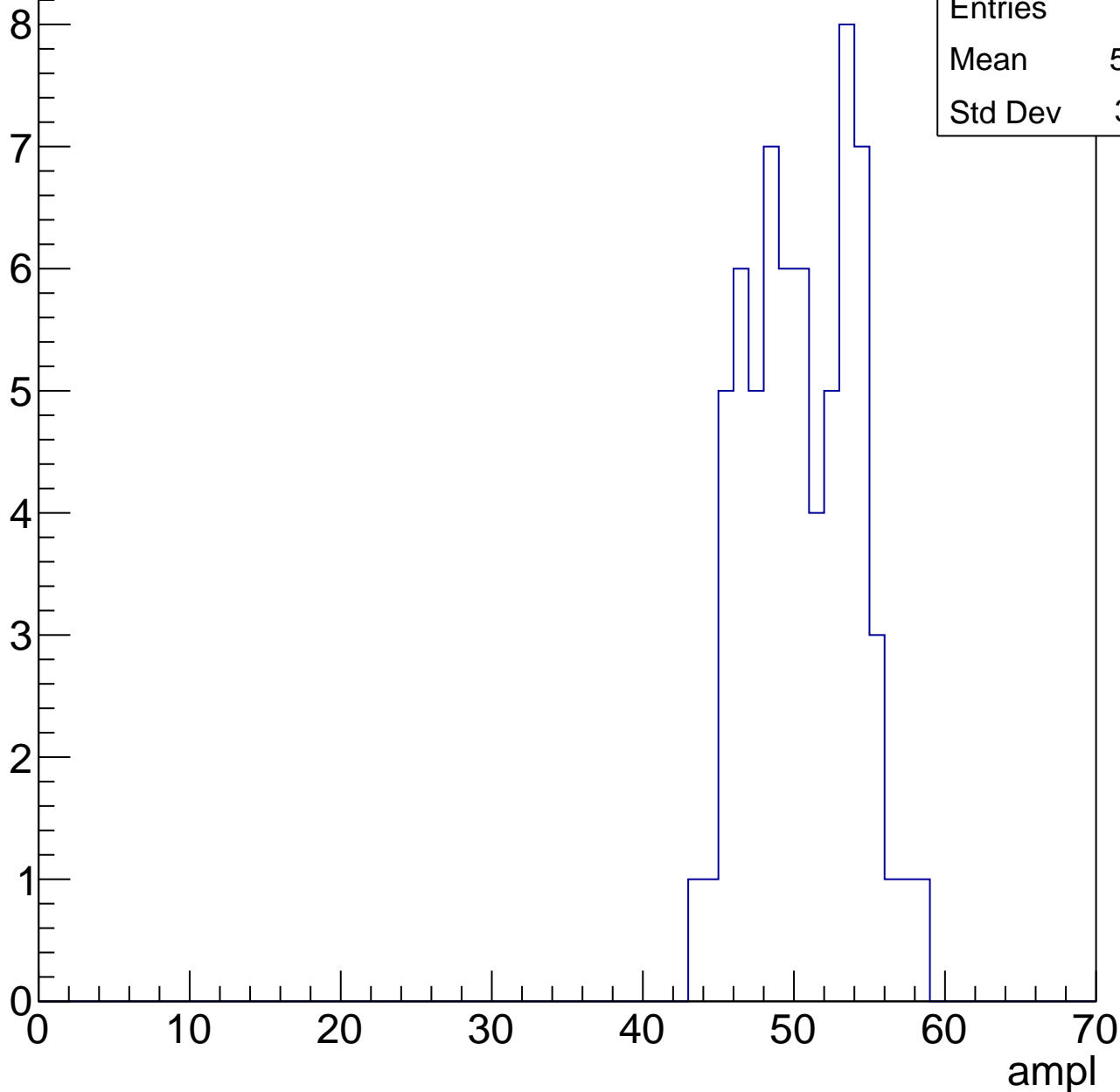


B1L103S, U14-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	50.07
Std Dev	3.491

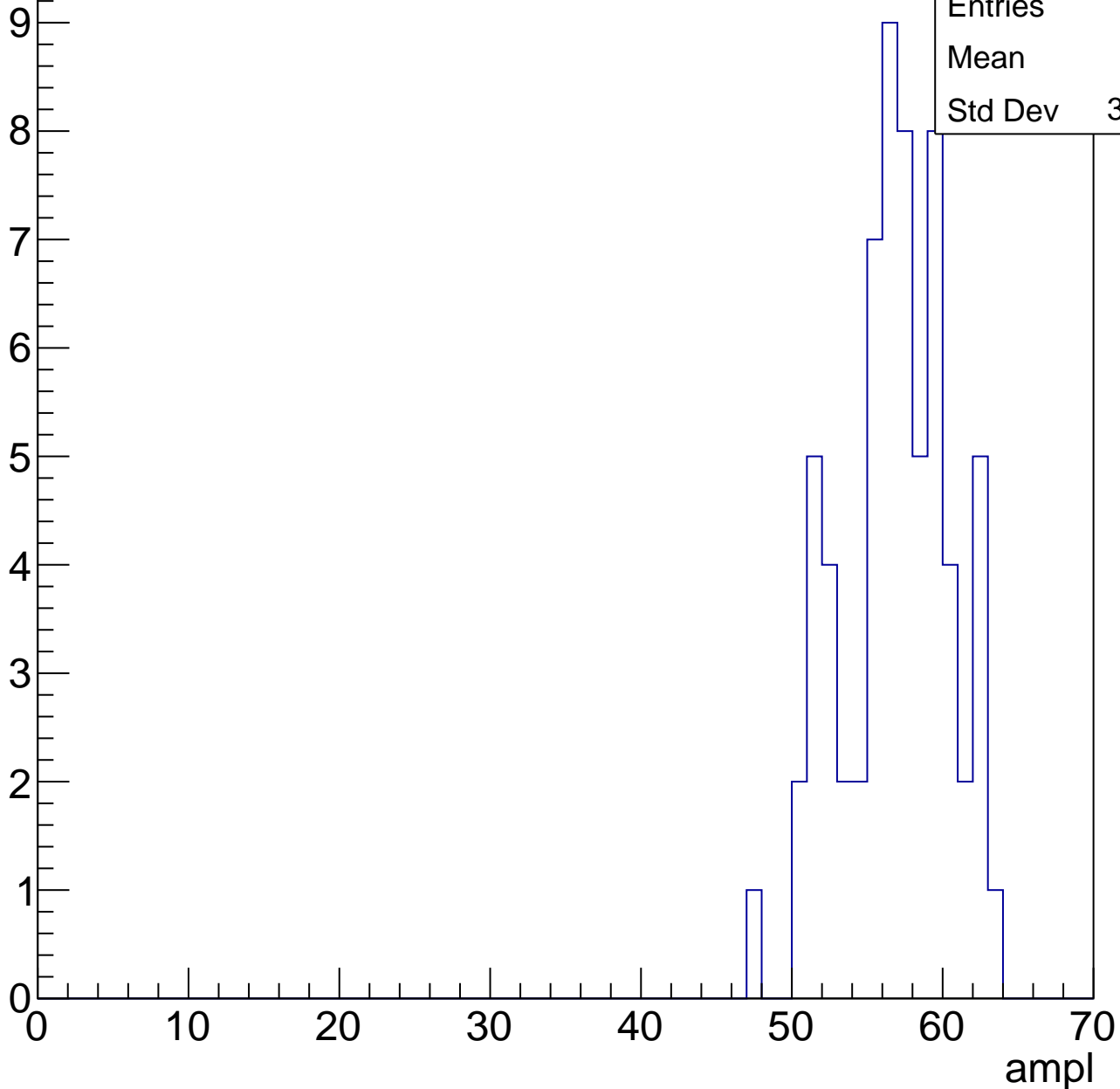


B1L103S, U14-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	56.4
Std Dev	3.525

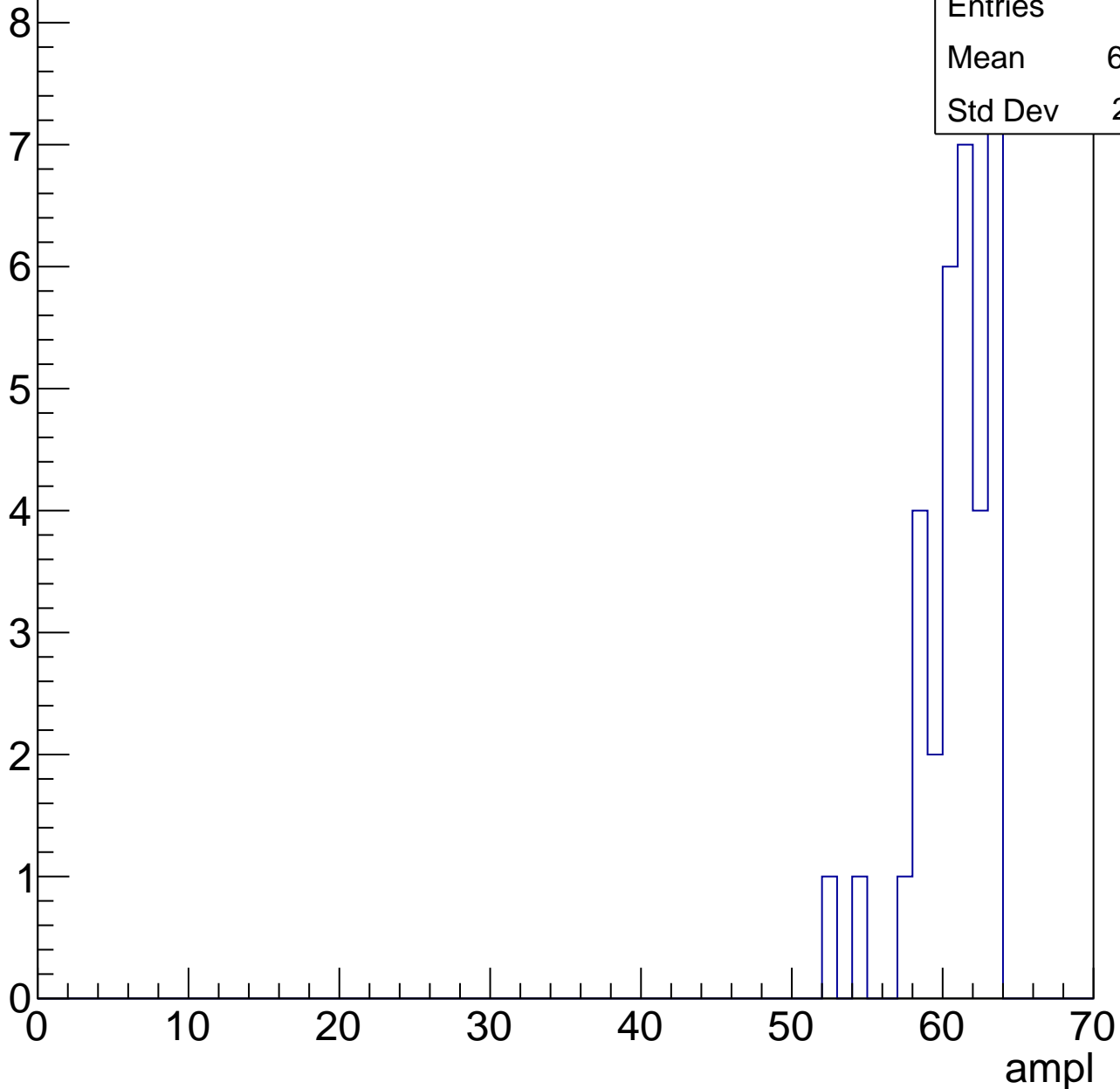


B1L103S, U14-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	60.35
Std Dev	2.531



B1L103S, U14-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L103S, U14-ch28, adc0

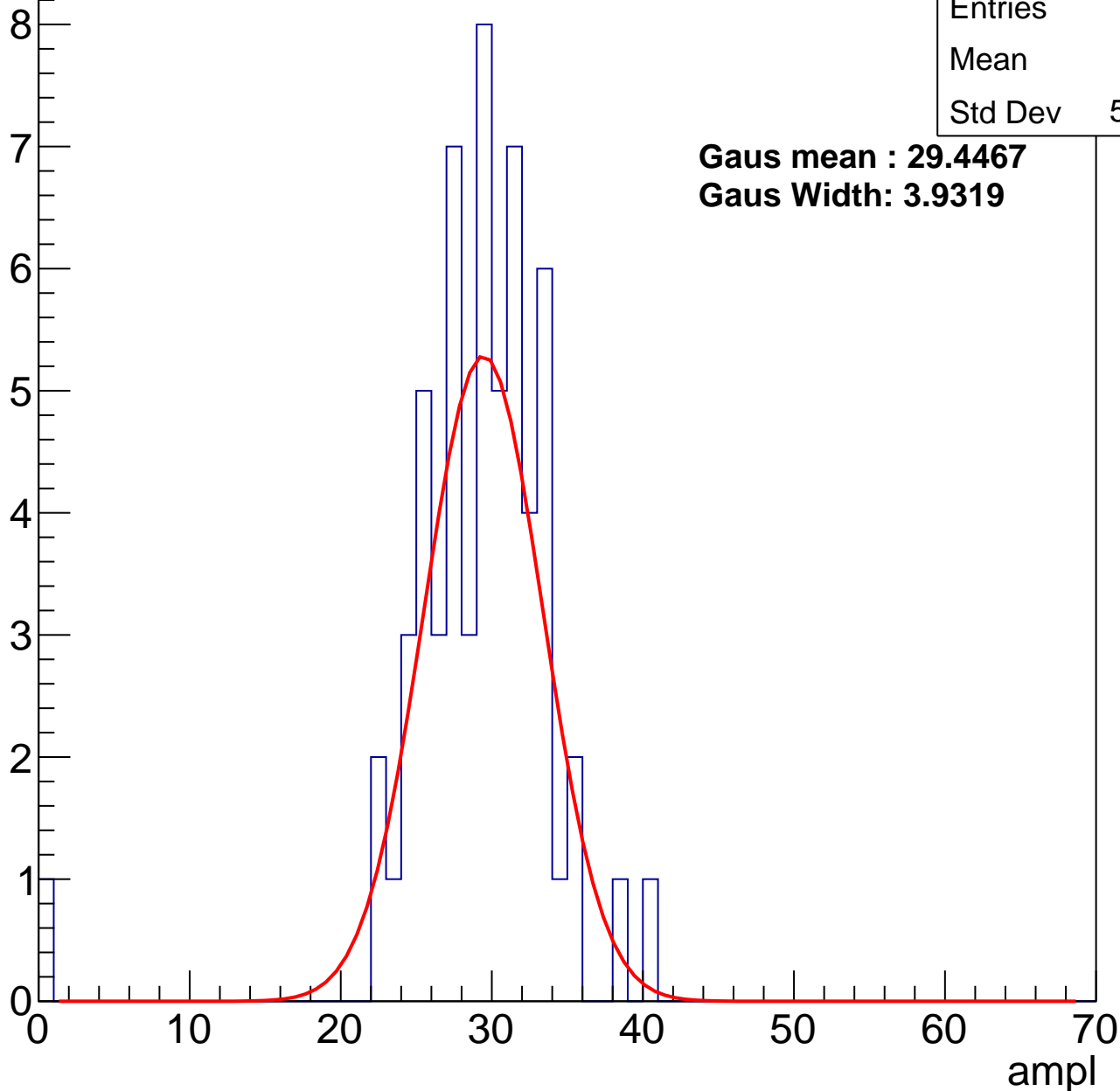
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	28.7
Std Dev	5.239

Gaus mean : 29.4467

Gaus Width: 3.9319



B1L103S, U14-ch28, adc1

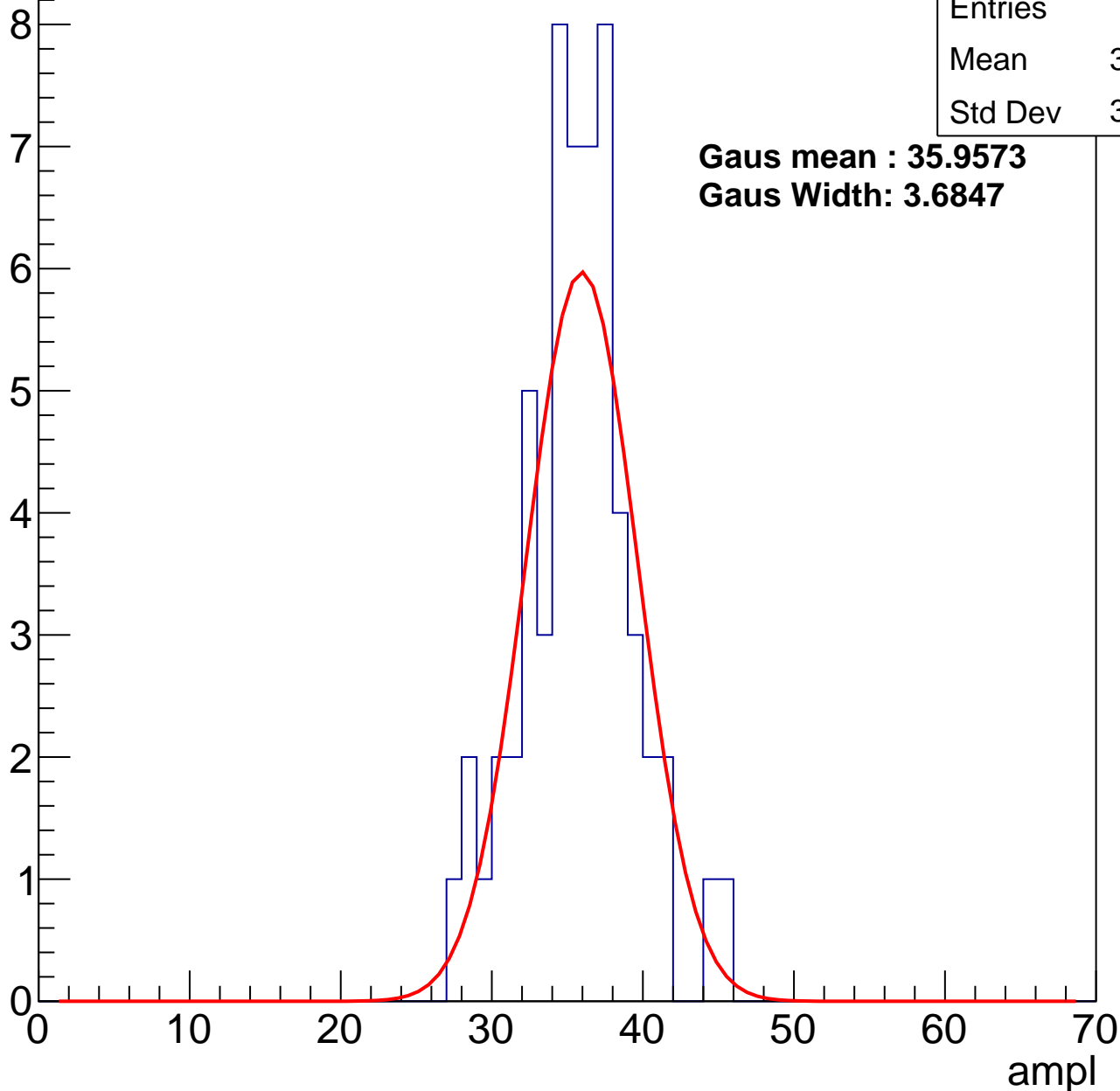
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	35.22
Std Dev	3.608

Gaus mean : 35.9573

Gaus Width: 3.6847



B1L103S, U14-ch28, adc2

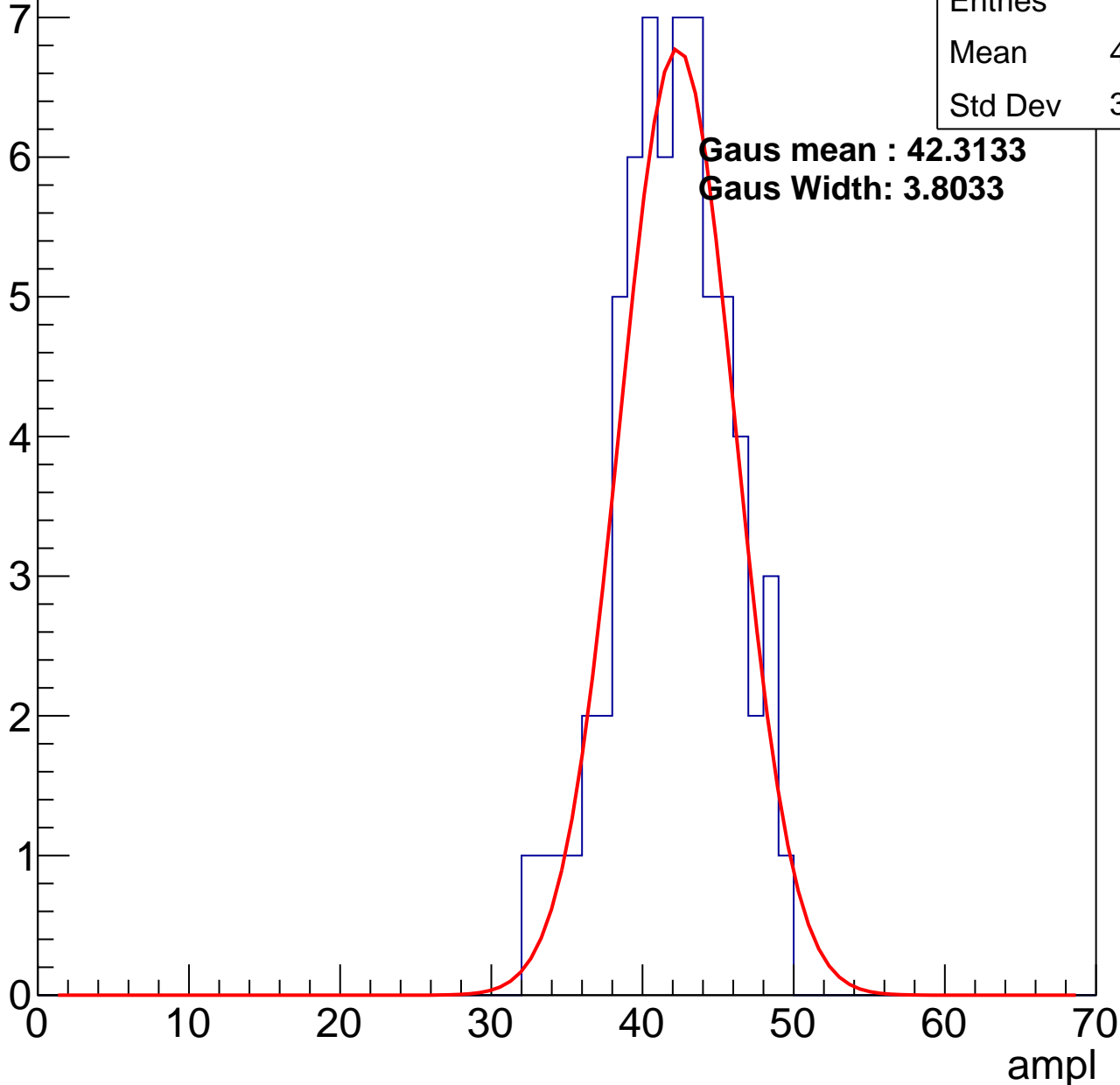
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	41.53
Std Dev	3.718

Gaus mean : 42.3133

Gaus Width: 3.8033

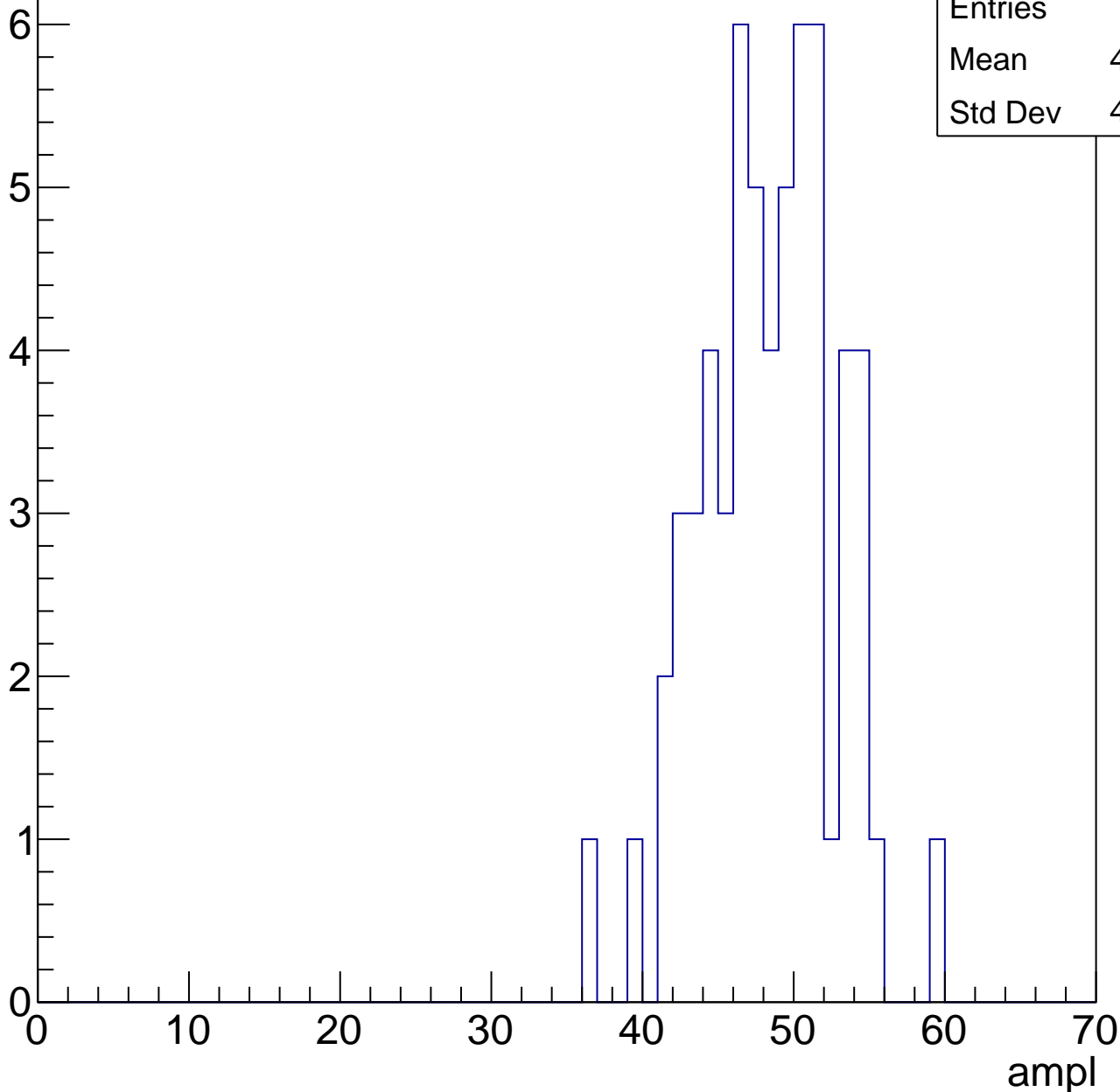


B1L103S, U14-ch28, adc3

calib_packv5_042523_0143.root, FC#7, port C2

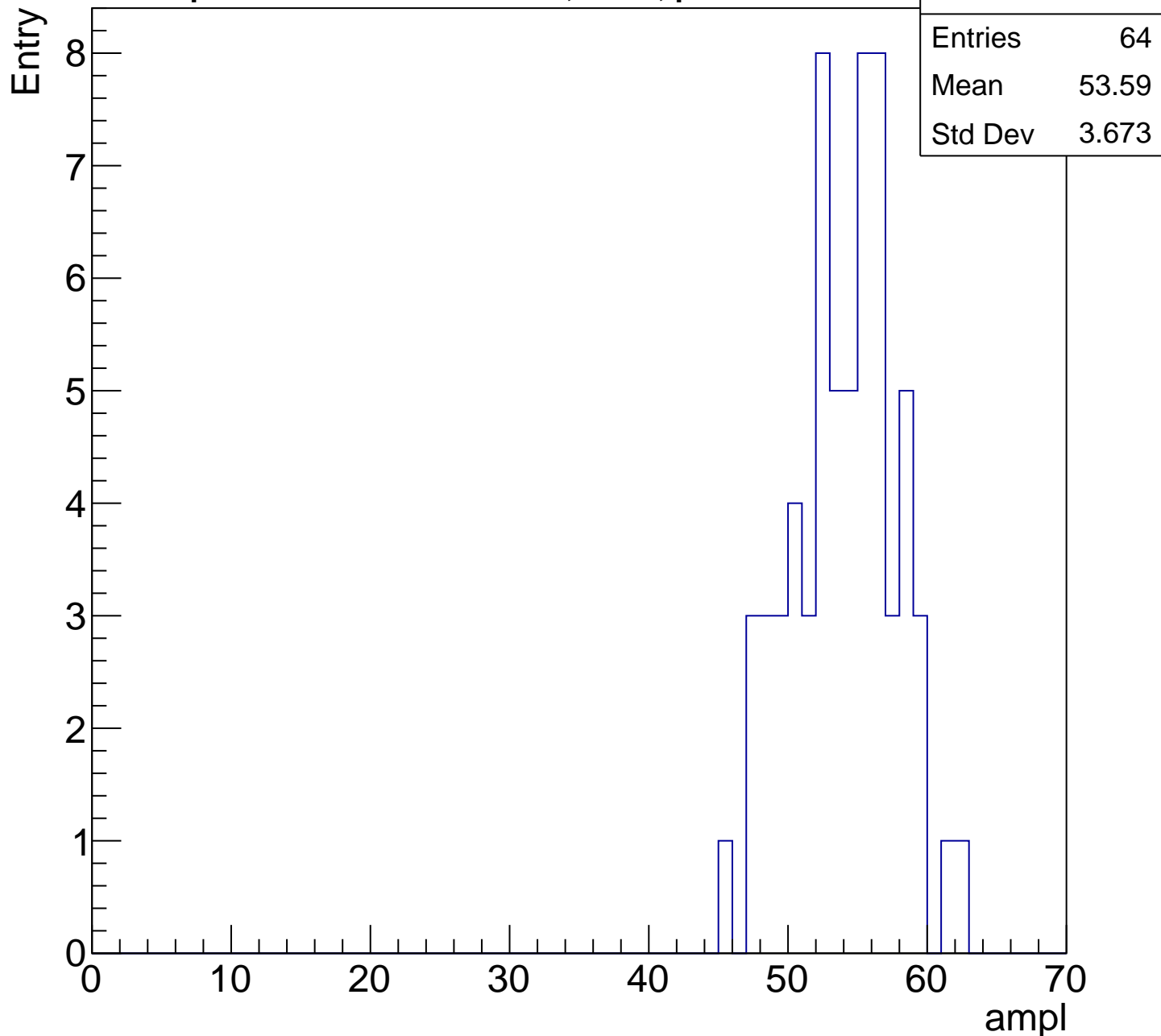
Entry

Entries	60
Mean	47.85
Std Dev	4.354



B1L103S, U14-ch28, adc4

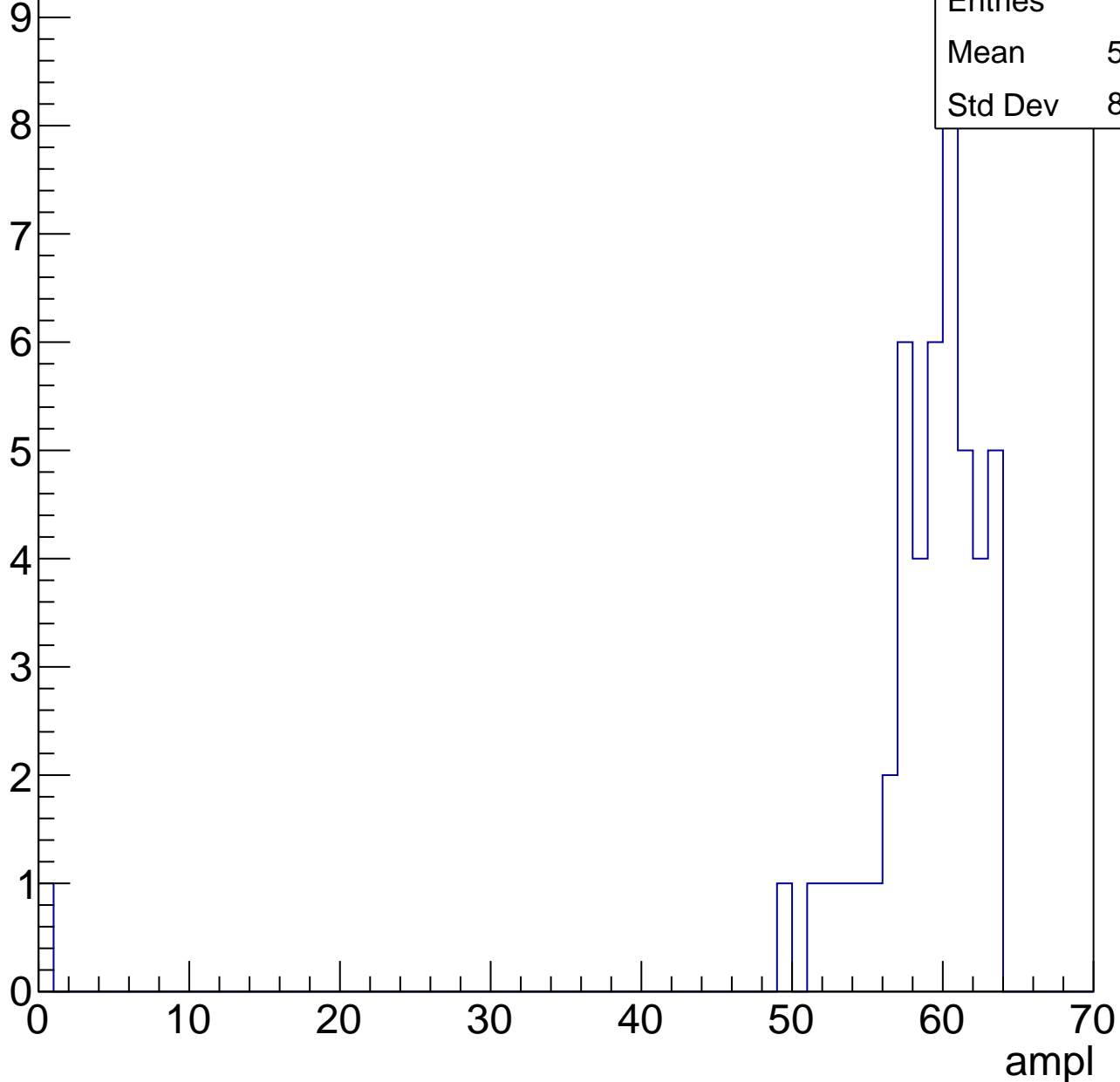
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U14-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

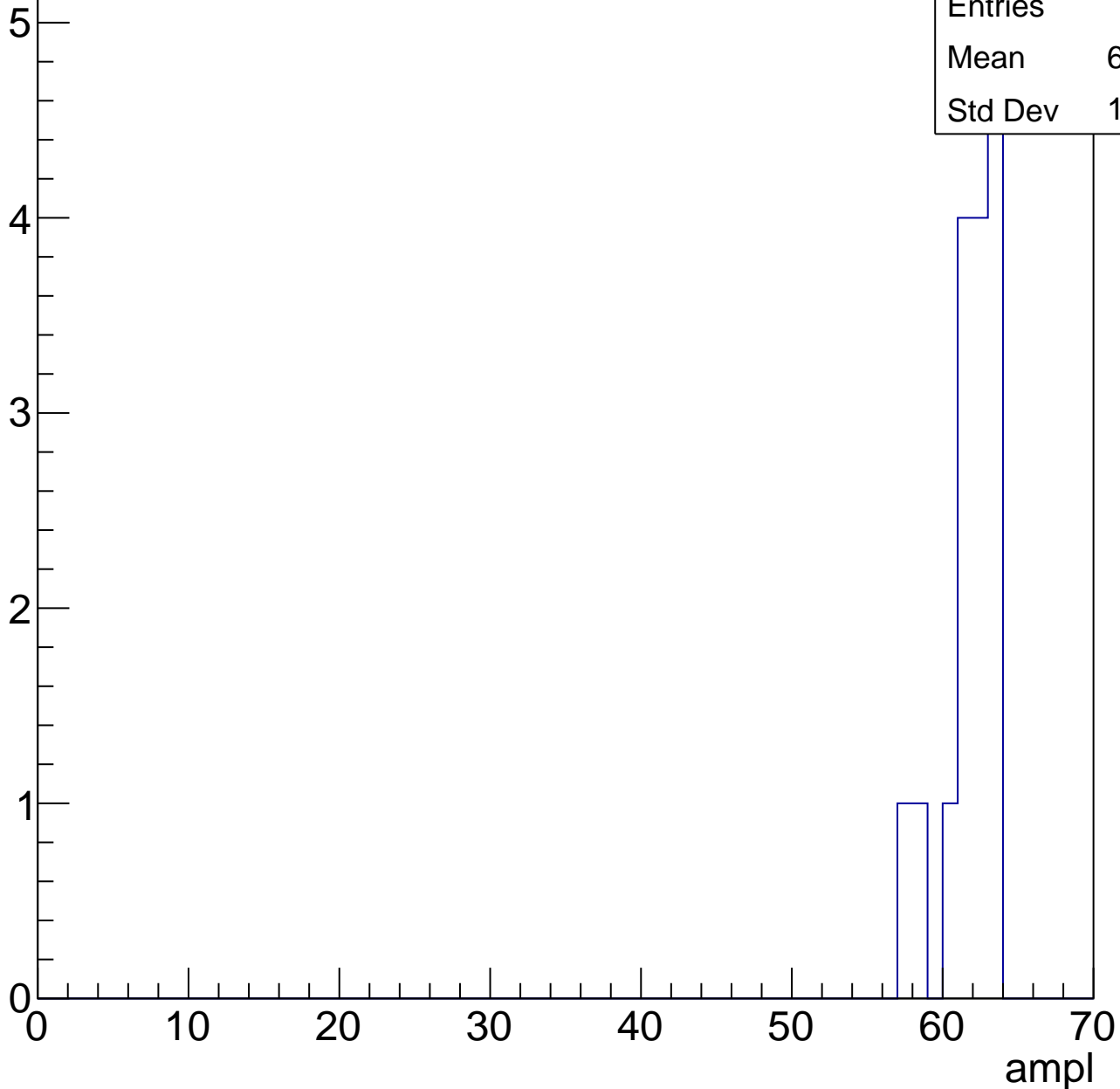


B1L103S, U14-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	61.38
Std Dev	1.728



B1L103S, U14-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch29, adc0

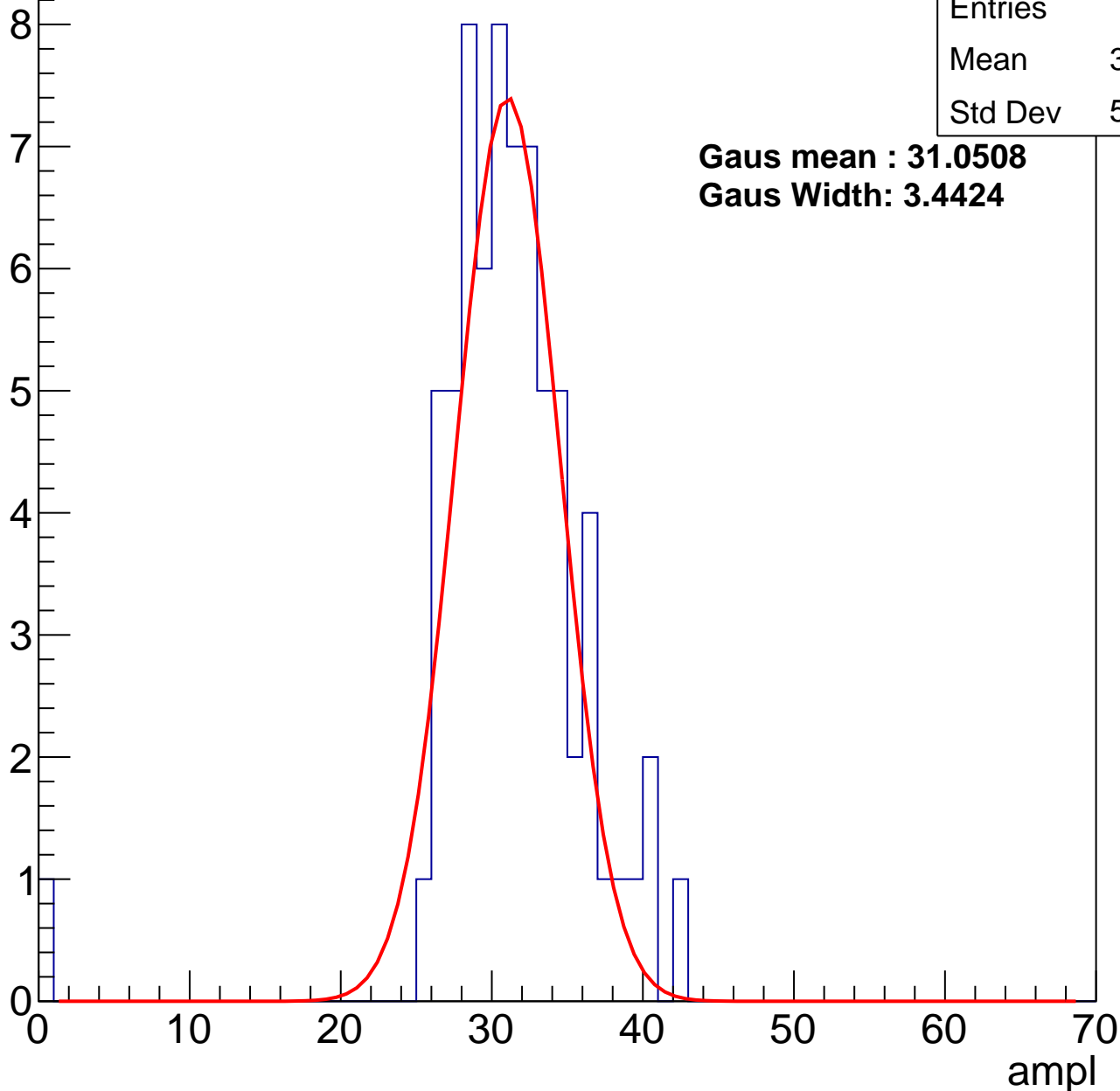
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	30.77
Std Dev	5.262

Gaus mean : 31.0508

Gaus Width: 3.4424



B1L103S, U14-ch29, adc1

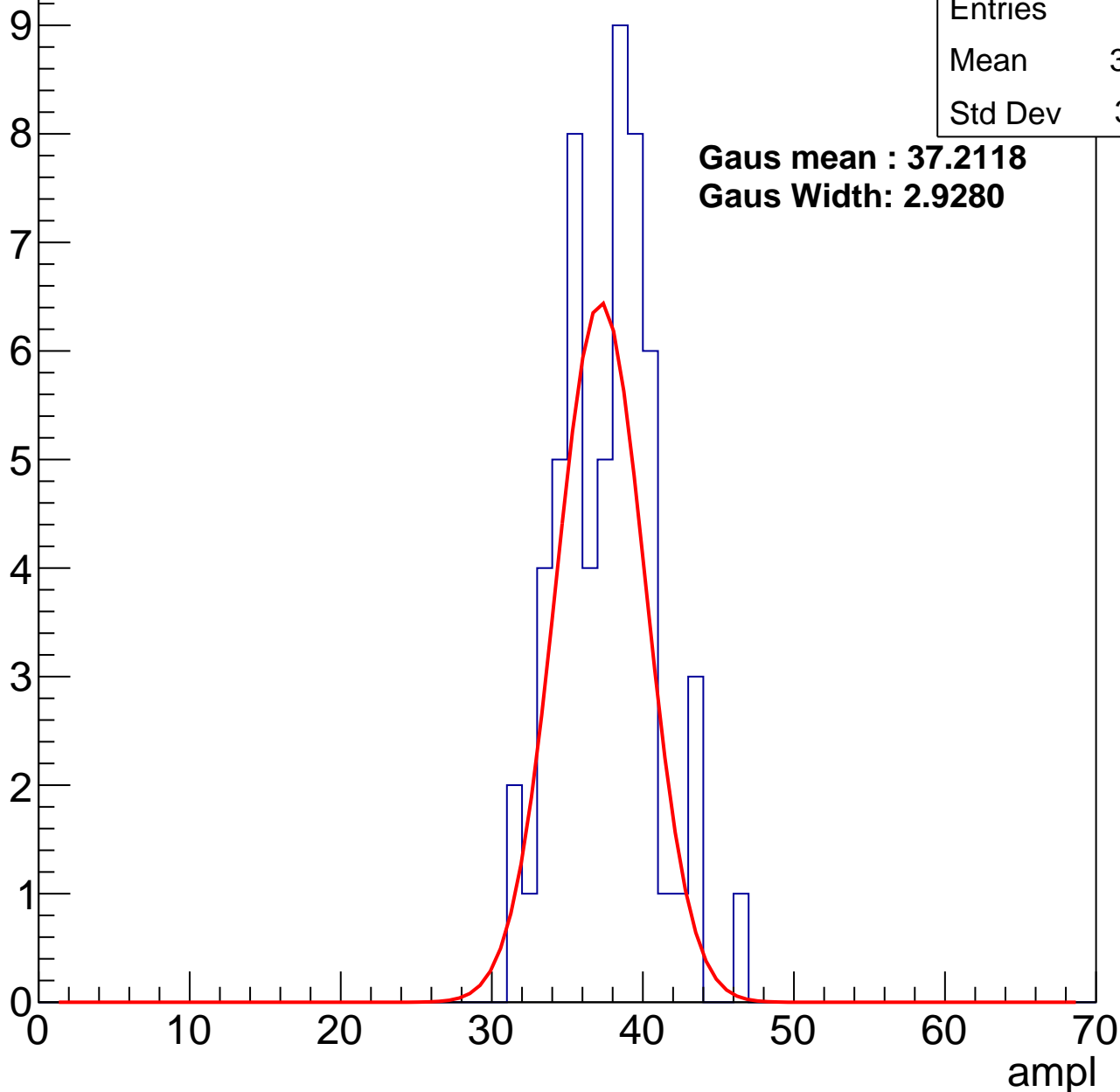
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	37.19
Std Dev	3.121

Gaus mean : 37.2118

Gaus Width: 2.9280



B1L103S, U14-ch29, adc2

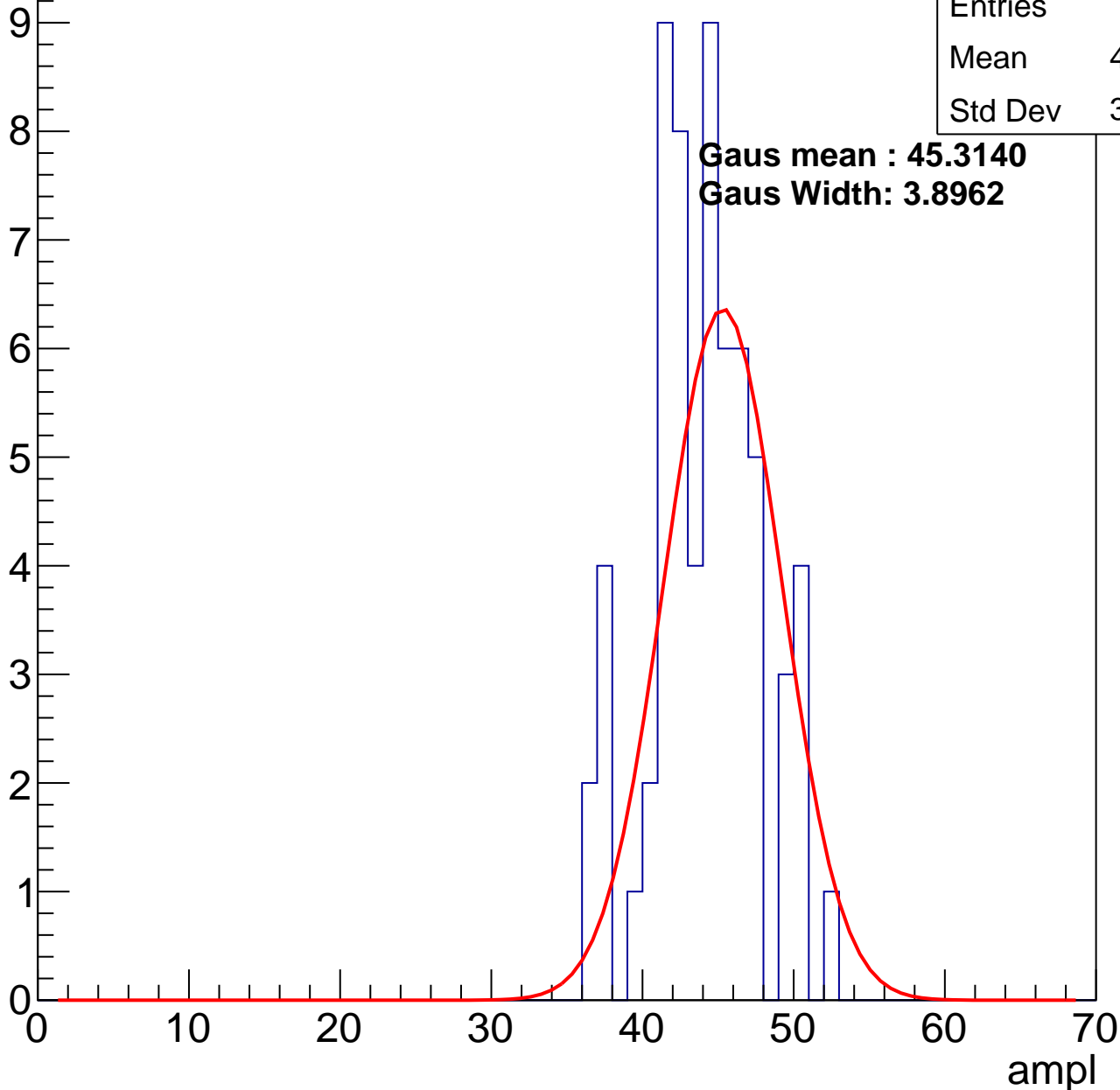
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.62
Std Dev	3.638

Gaus mean : 45.3140

Gaus Width: 3.8962

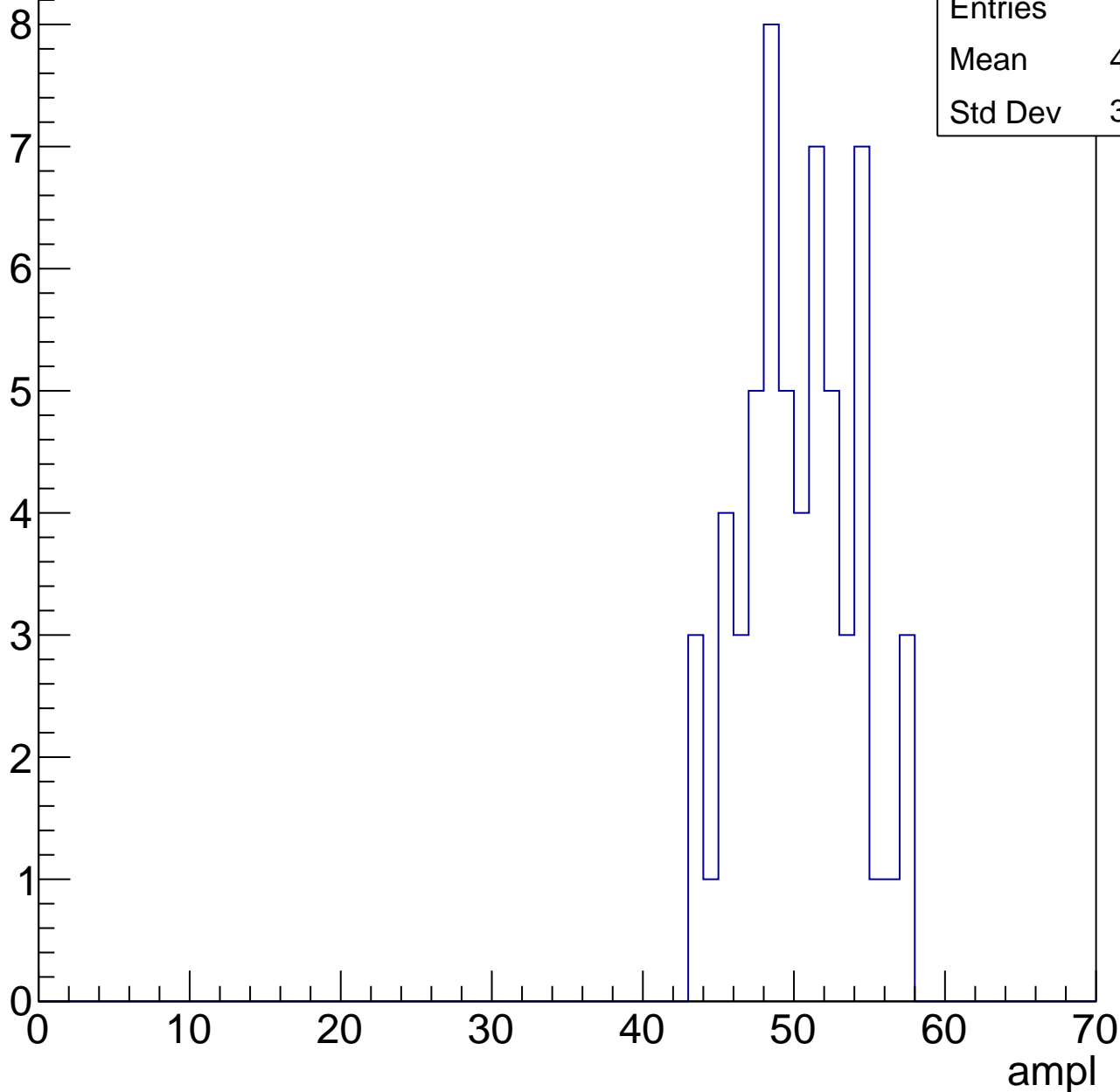


B1L103S, U14-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

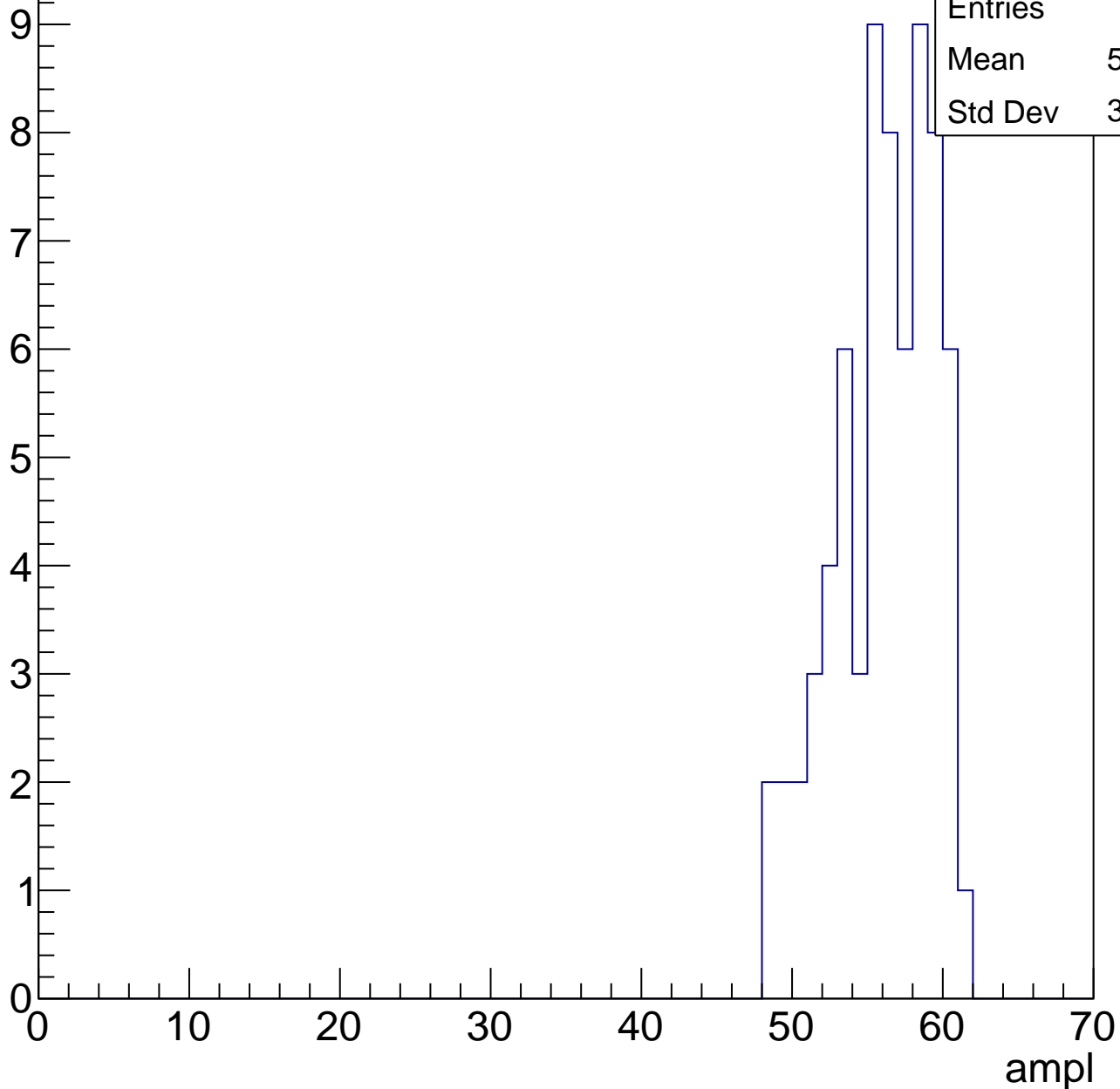
Entries	60
Mean	49.85
Std Dev	3.619



B1L103S, U14-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

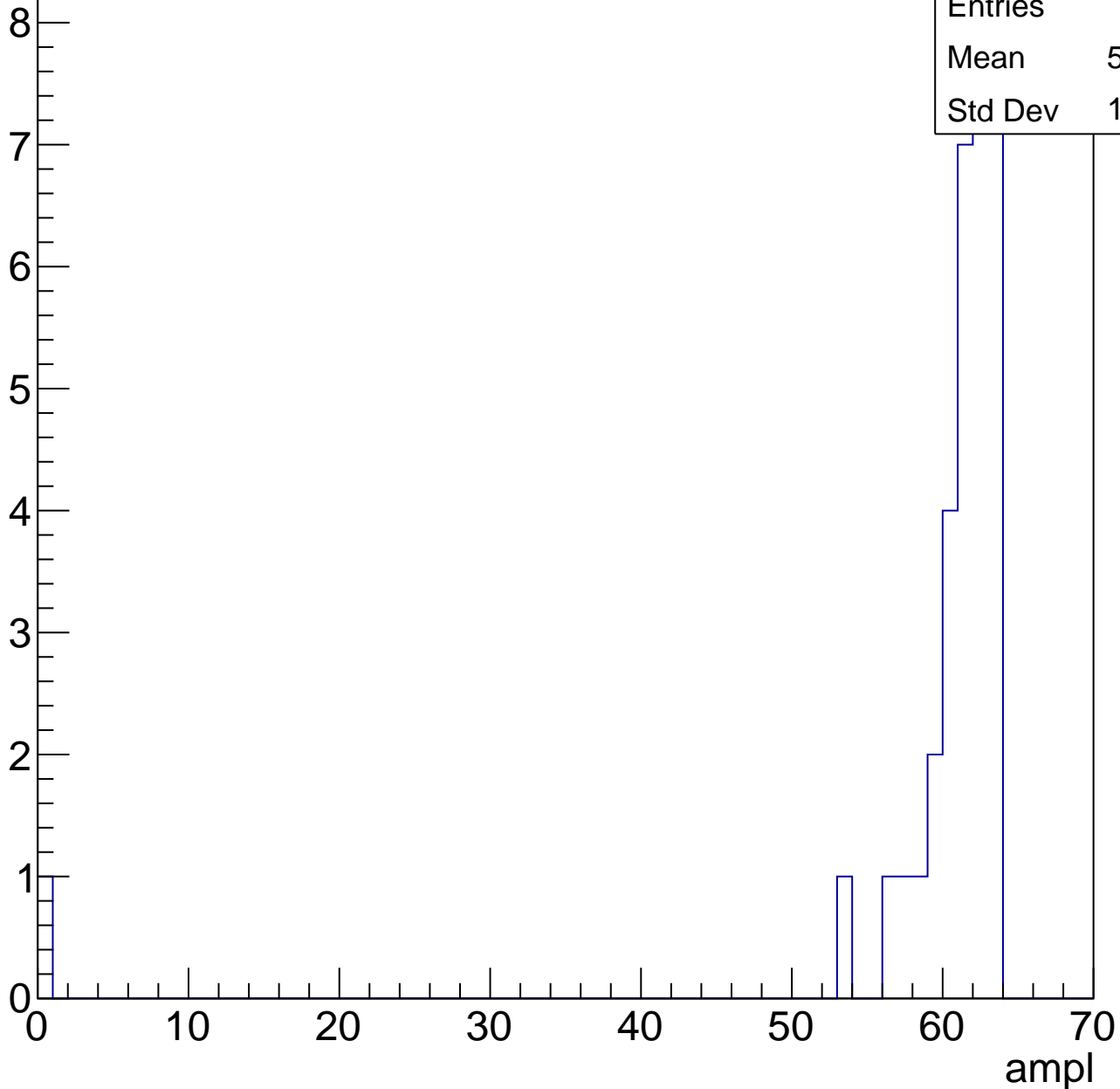


B1L103S, U14-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

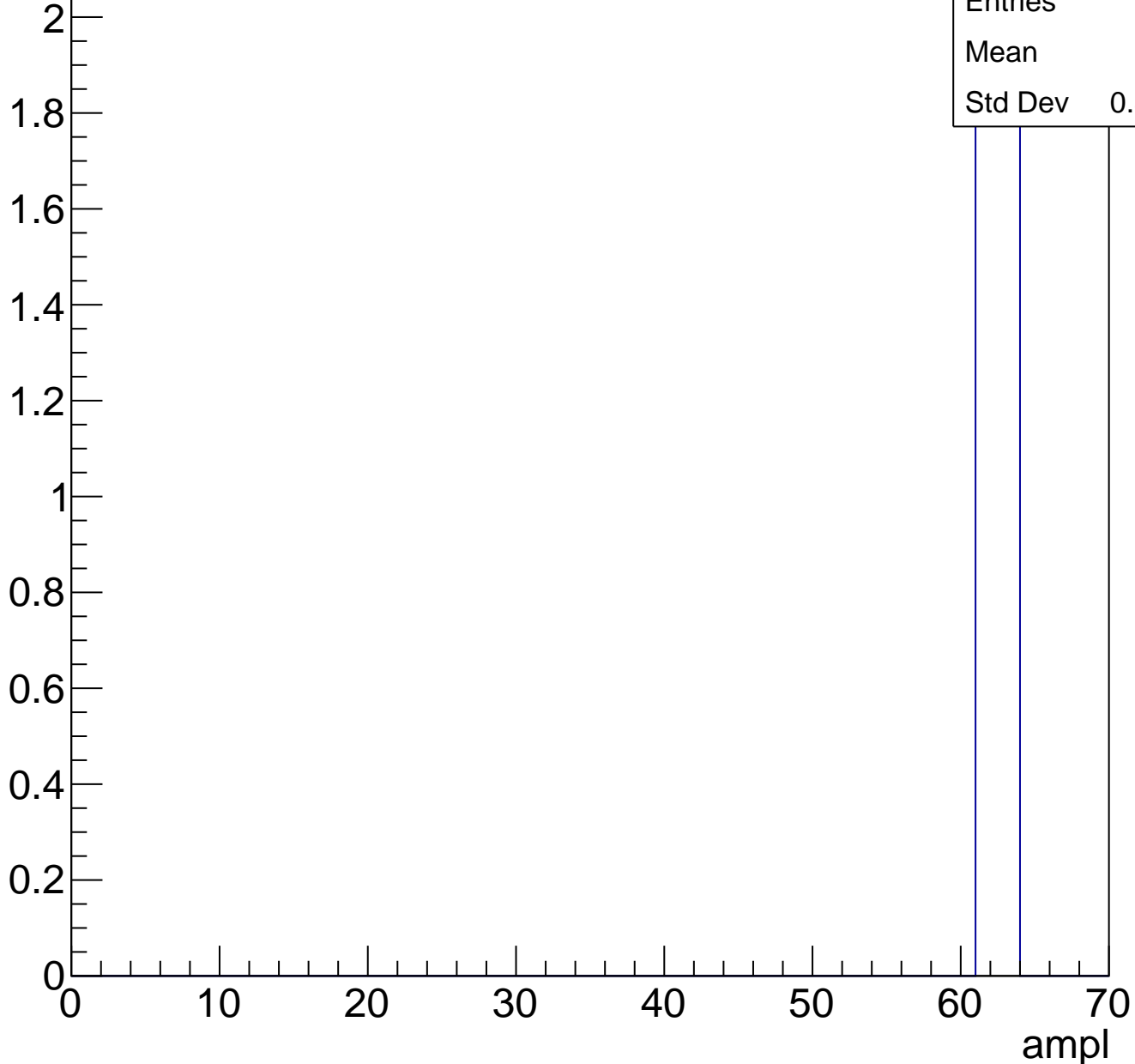
Entries	34
Mean	59.09
Std Dev	10.52



B1L103S, U14-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

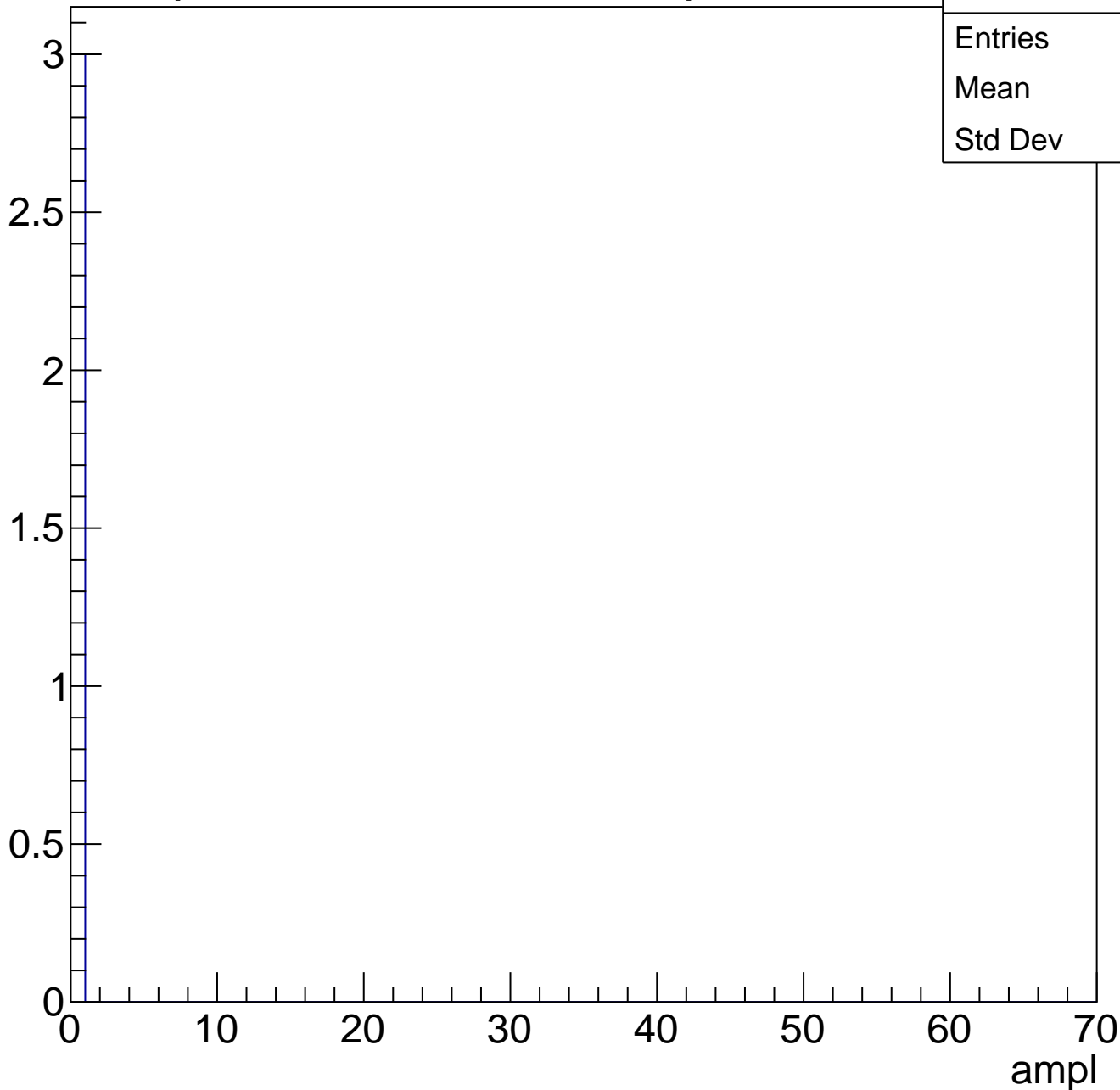
Entry



B1L103S, U14-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U14-ch30, adc0

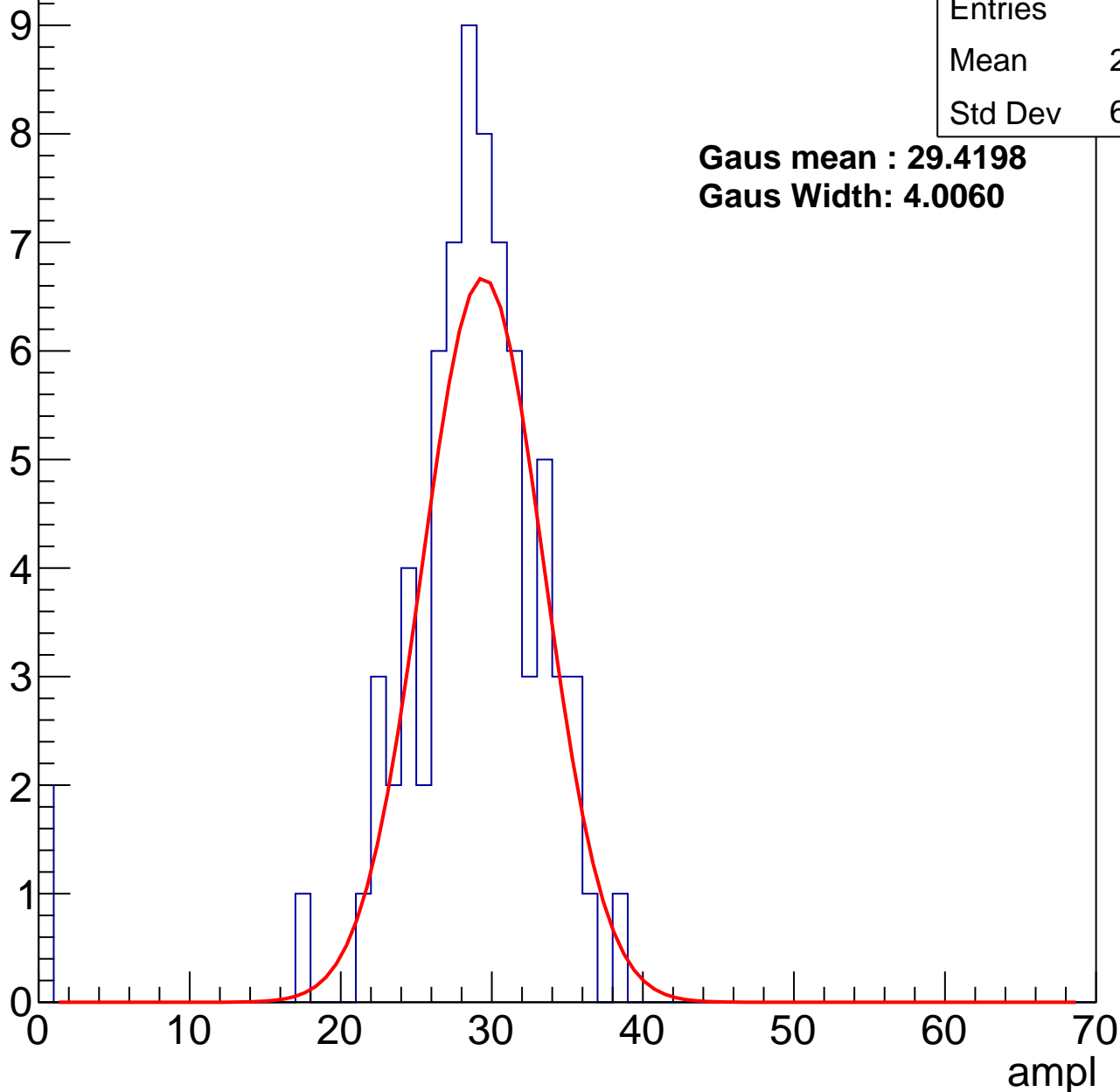
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.88
Std Dev	6.029

Gaus mean : 29.4198

Gaus Width: 4.0060



B1L103S, U14-ch30, adc1

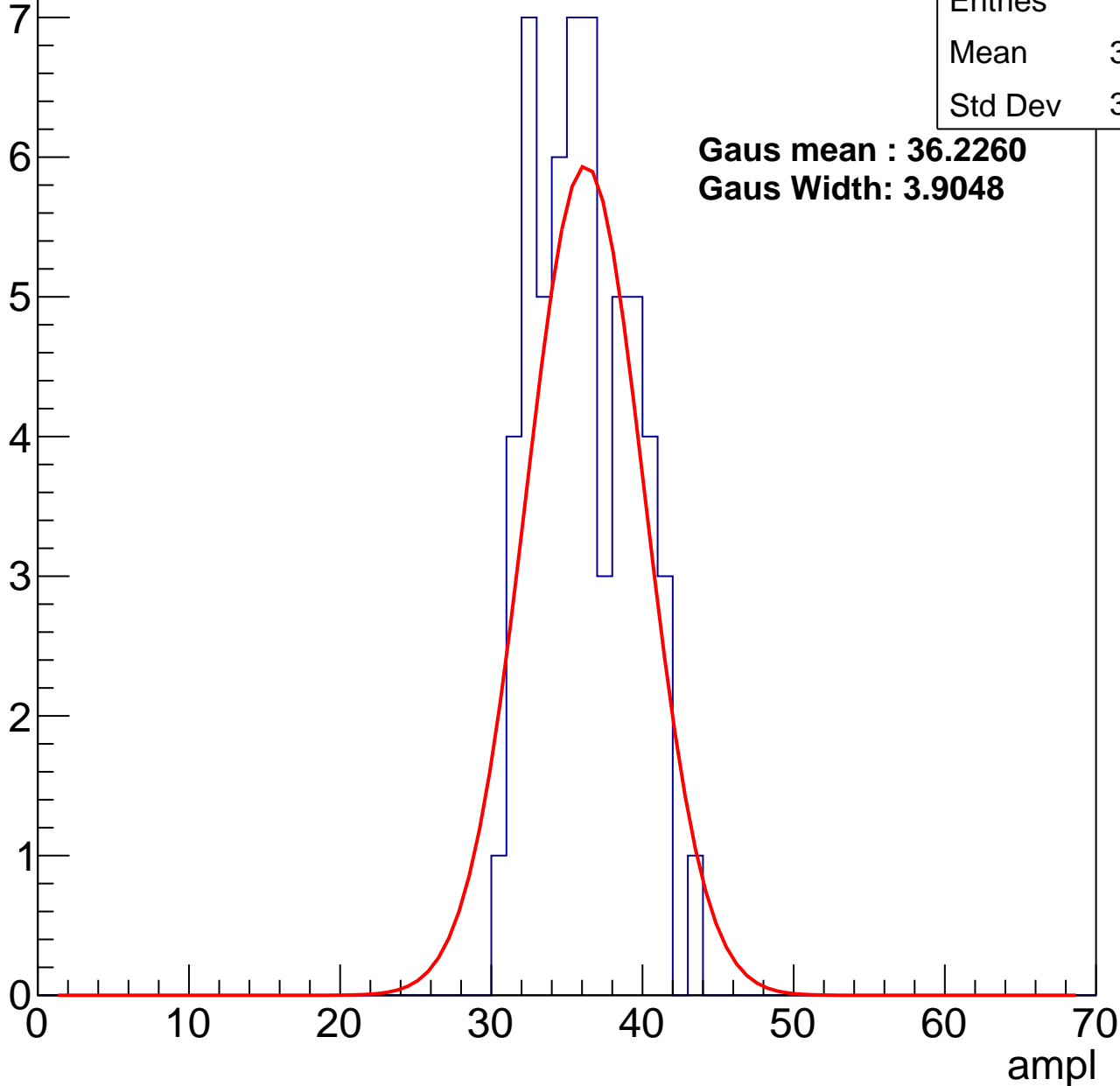
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	35.62
Std Dev	3.139

Gaus mean : 36.2260

Gaus Width: 3.9048



B1L103S, U14-ch30, adc2

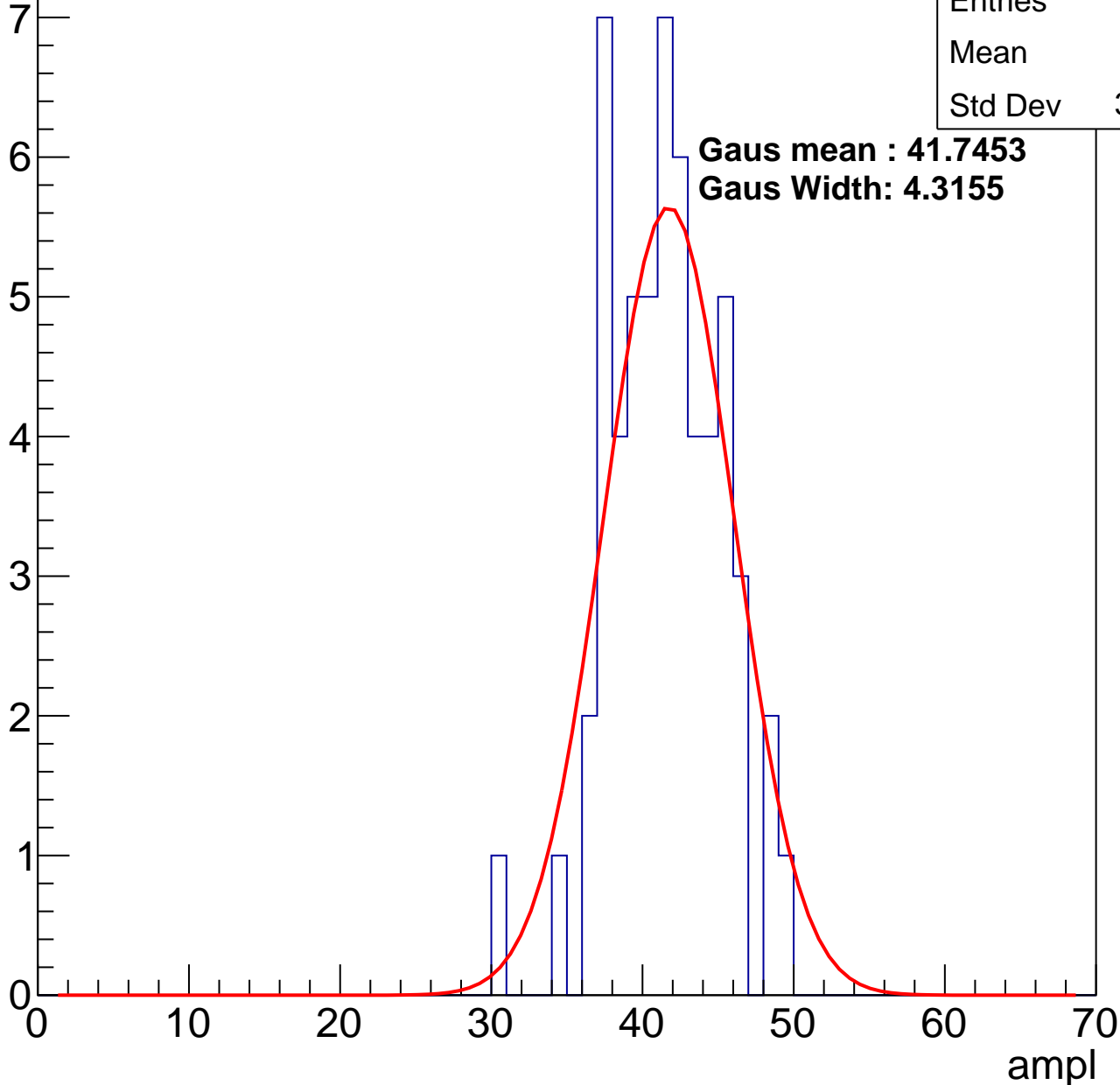
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41
Std Dev	3.671

Gaus mean : 41.7453

Gaus Width: 4.3155



B1L103S, U14-ch30, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	47.87
Std Dev	3.518

Entry

10

8

6

4

2

0

0

10

20

30

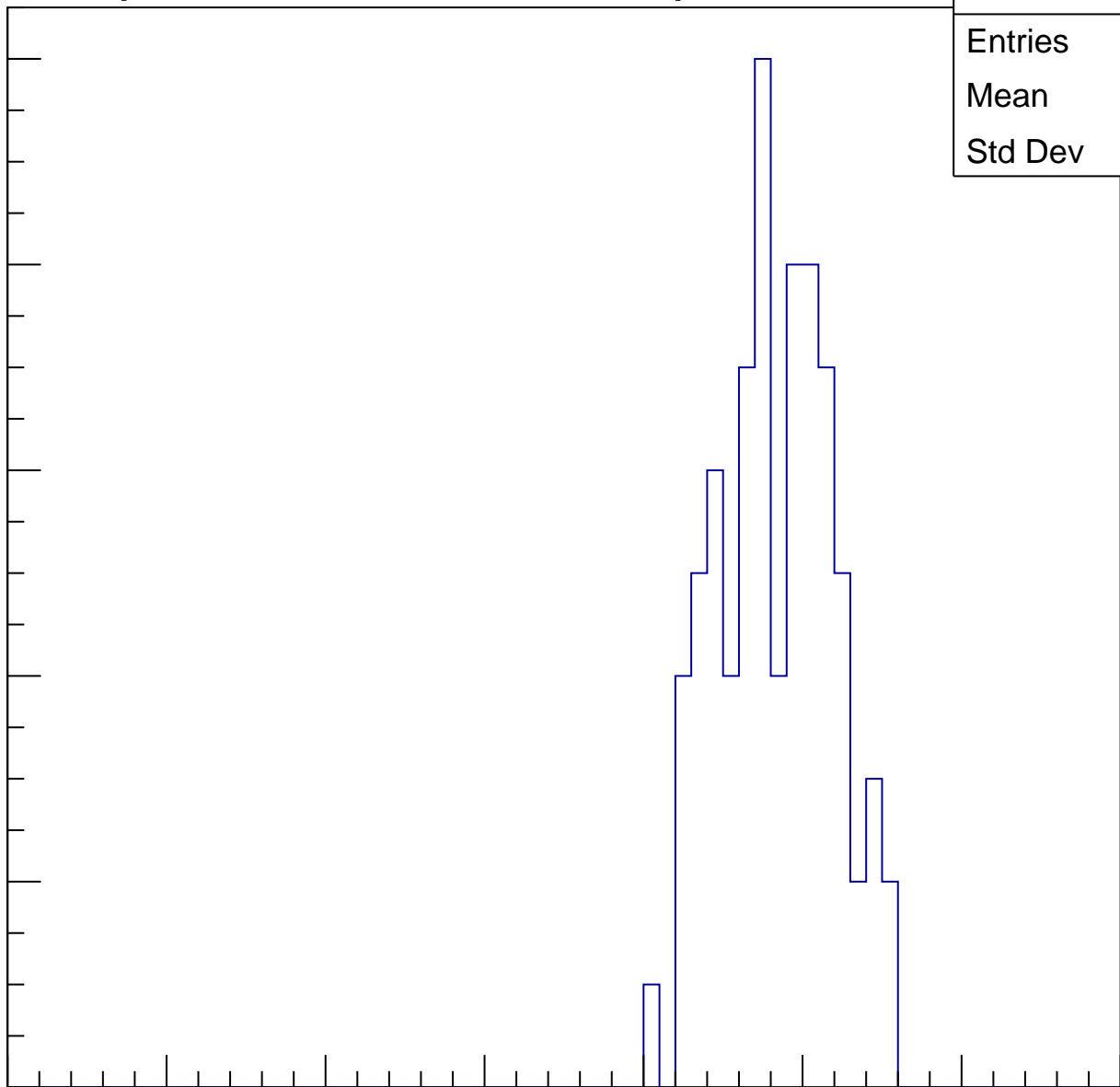
40

50

60

70

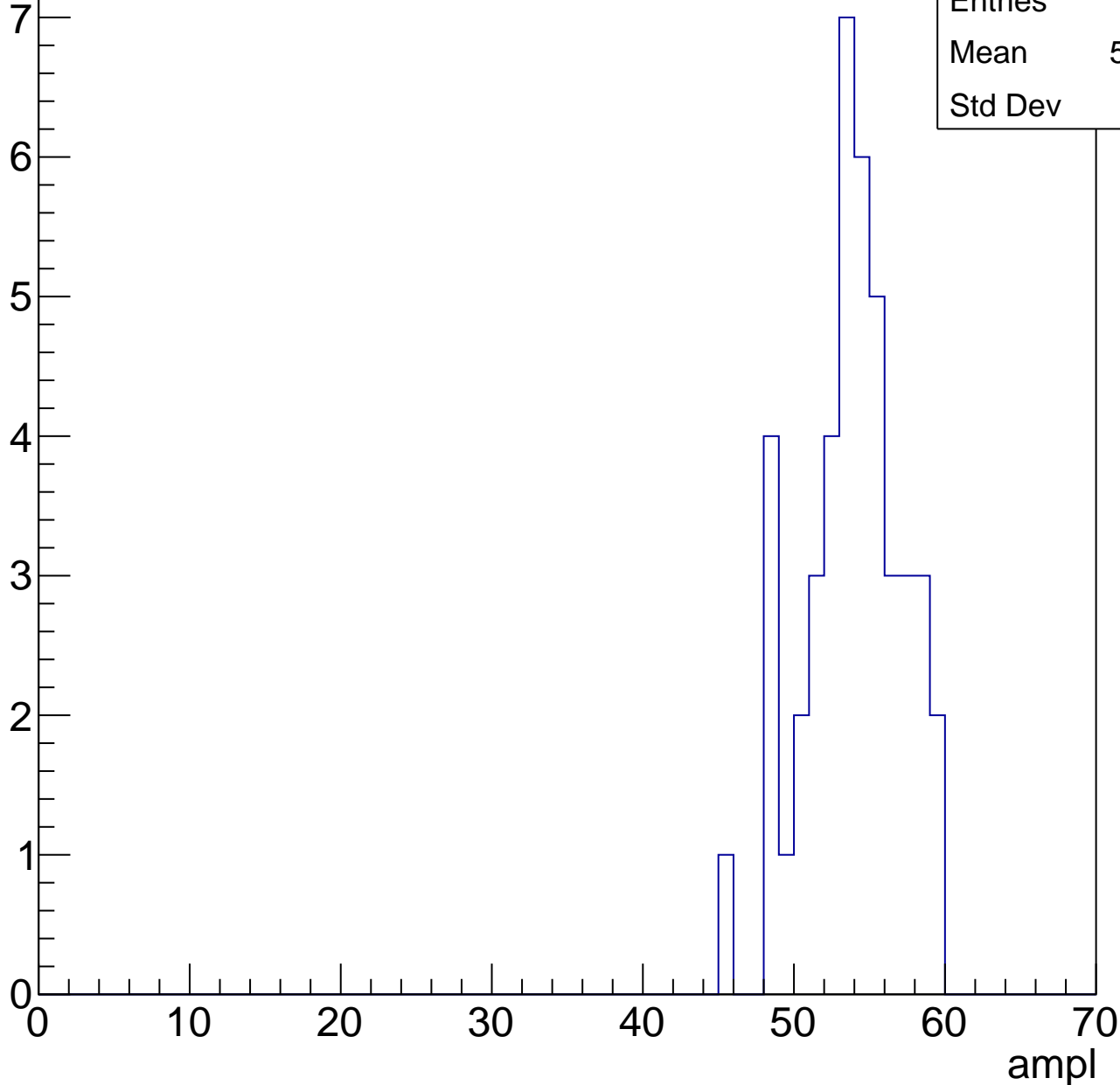
ampl



B1L103S, U14-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

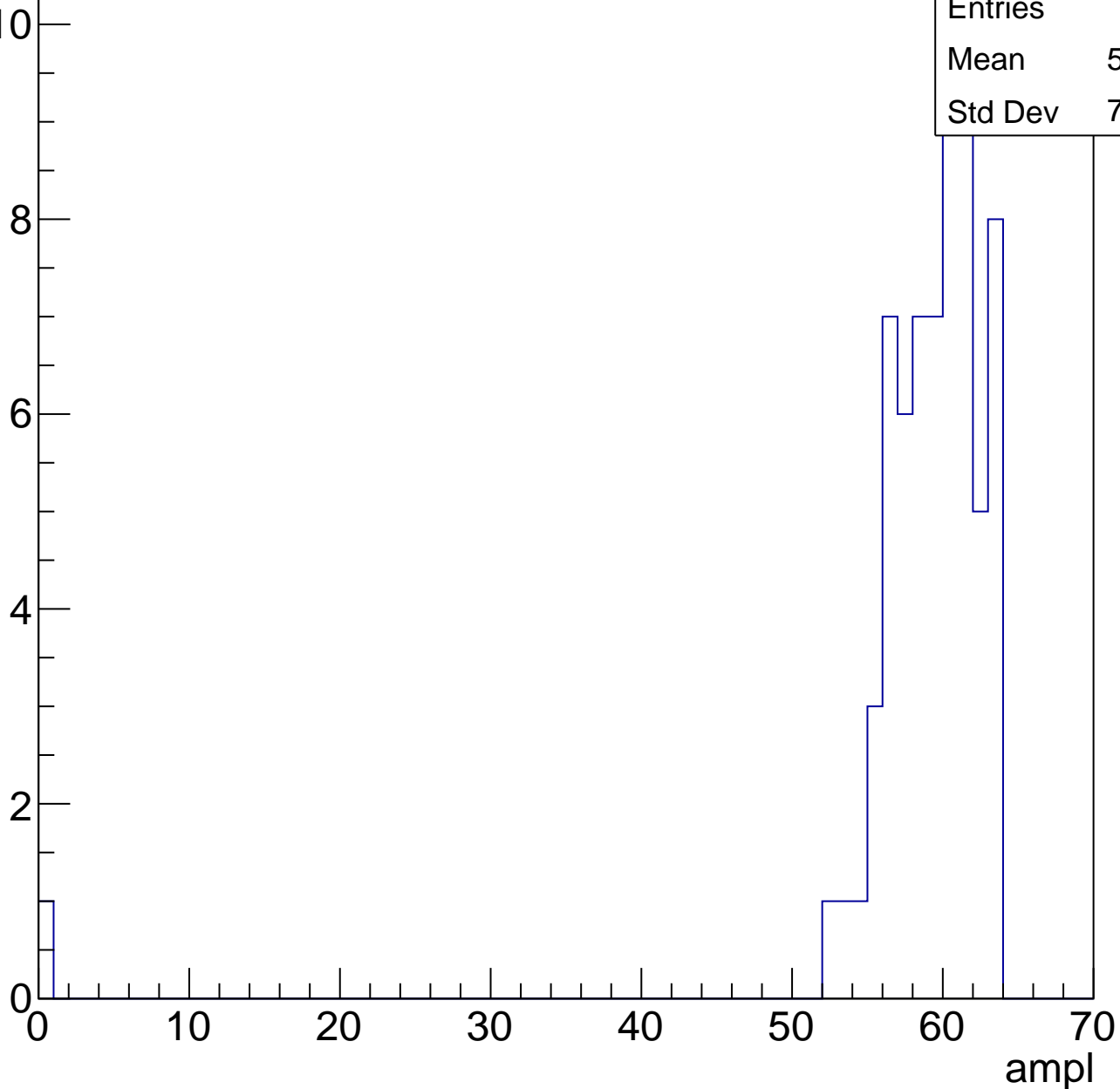


B1L103S, U14-ch30, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	58.18
Std Dev	7.695

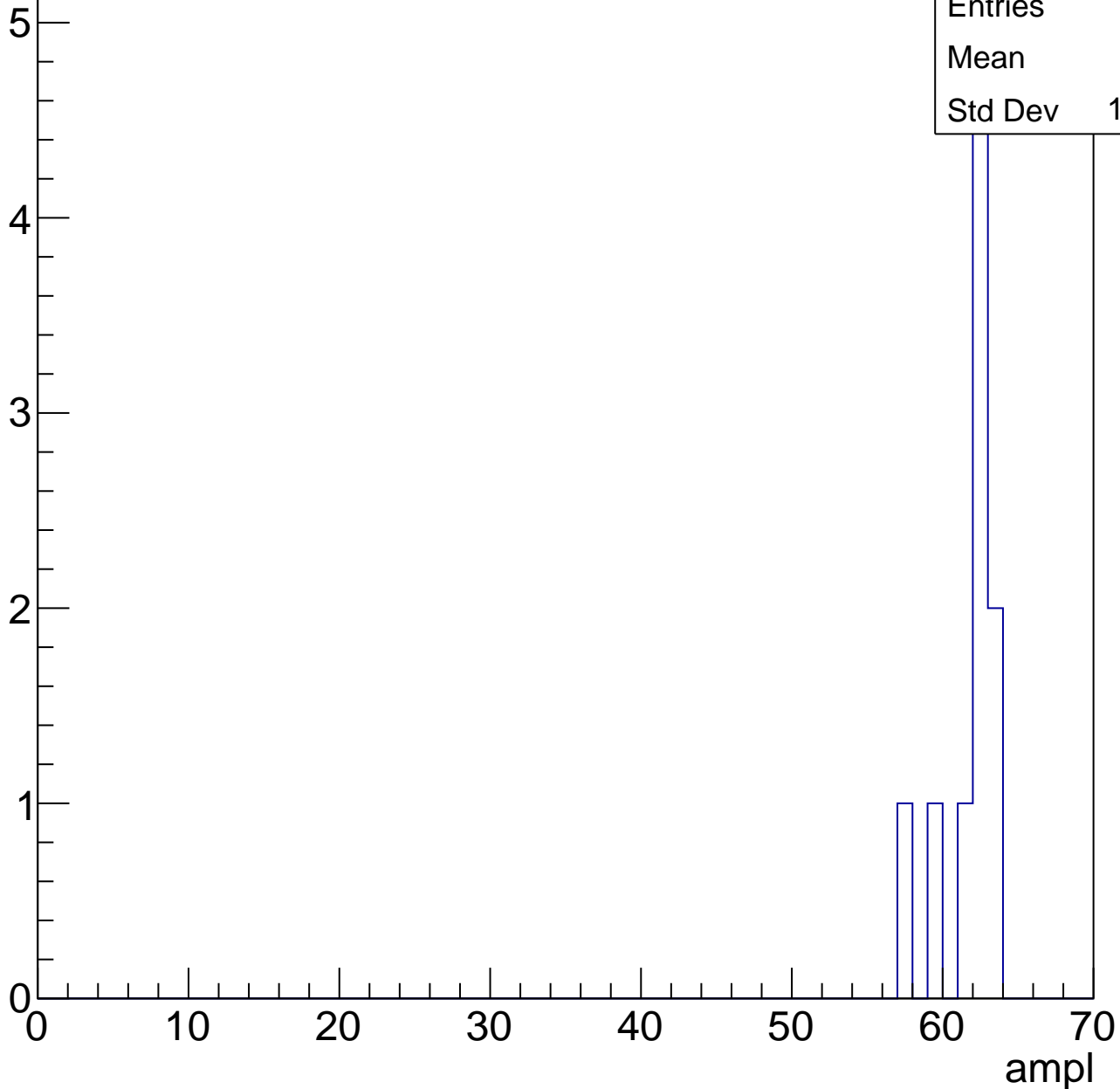


B1L103S, U14-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	10
Mean	61.3
Std Dev	1.792



B1L103S, U14-ch30, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch31, adc0

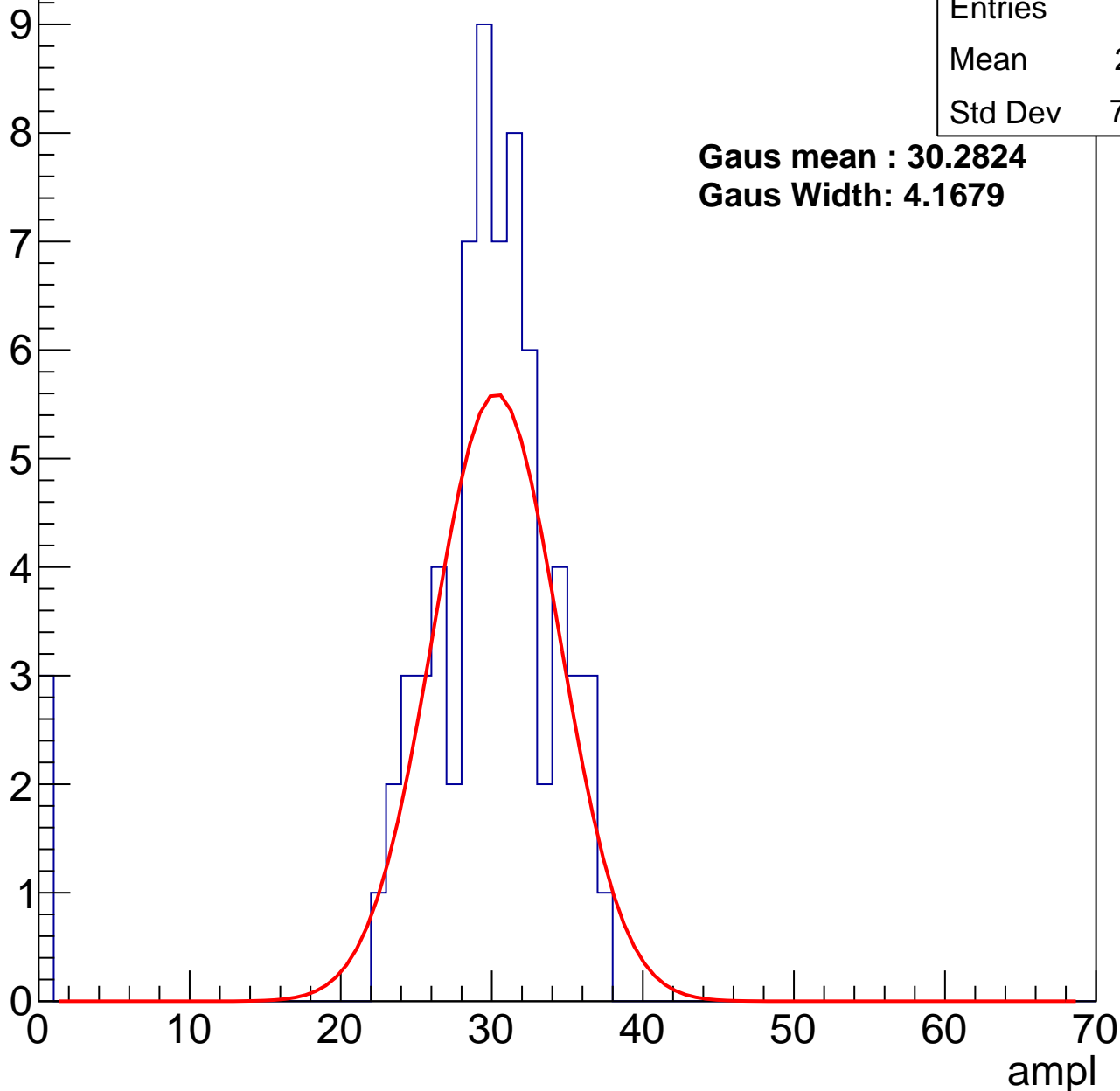
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.41
Std Dev	7.007

Gaus mean : 30.2824

Gaus Width: 4.1679



B1L103S, U14-ch31, adc1

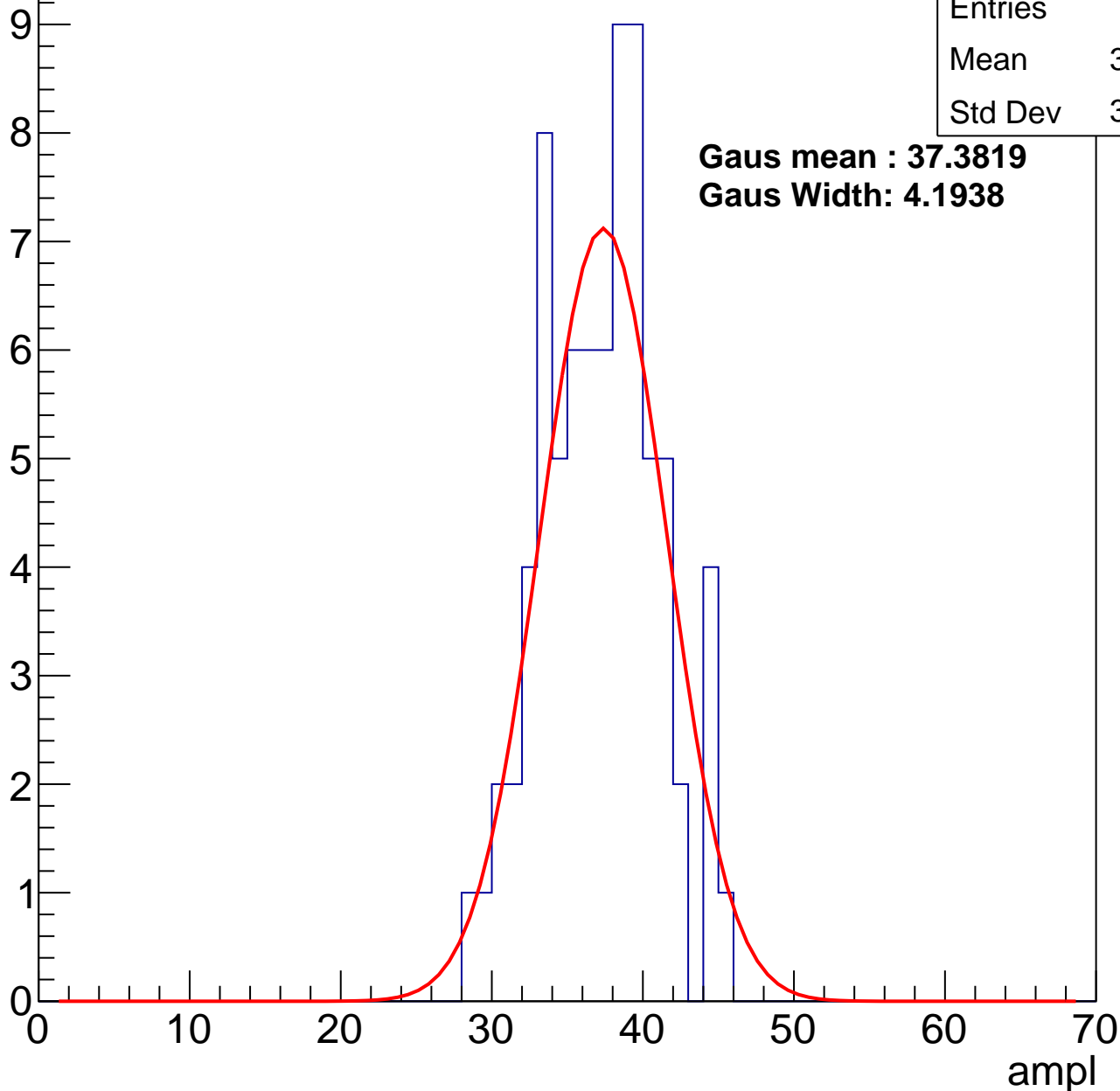
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.74
Std Dev	3.792

Gaus mean : 37.3819

Gaus Width: 4.1938



B1L103S, U14-ch31, adc2

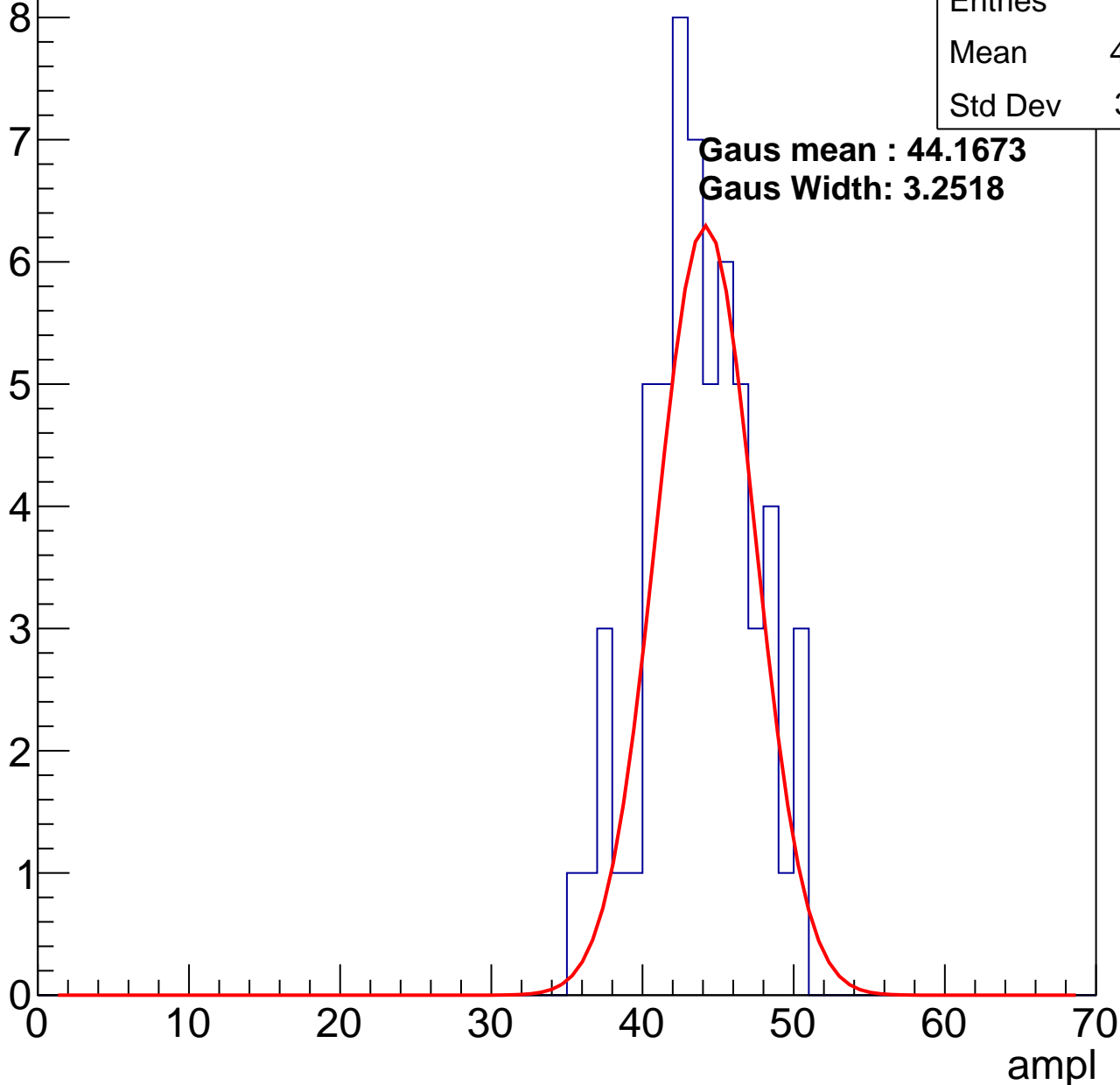
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.27
Std Dev	3.531

Gaus mean : 44.1673

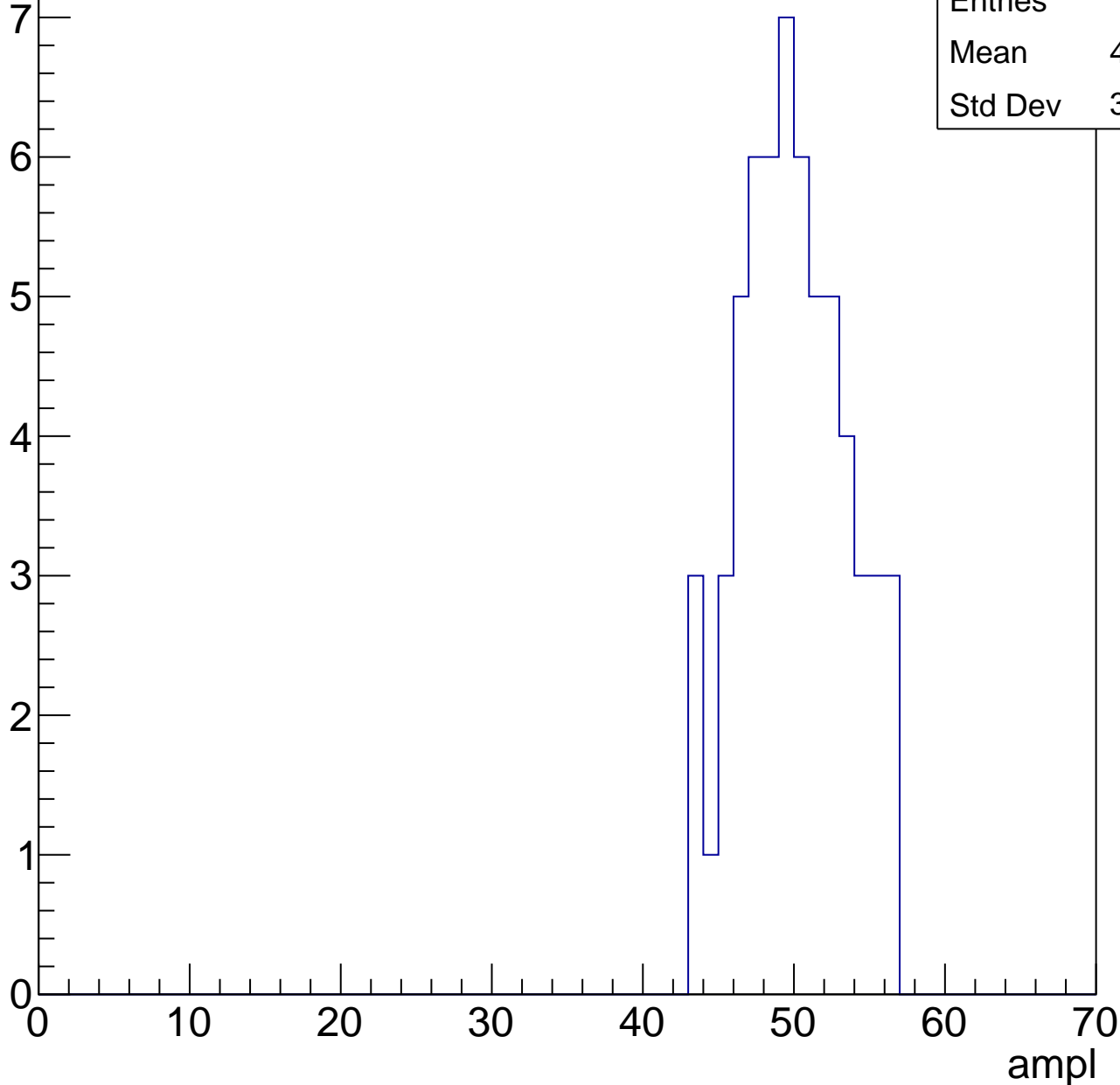
Gaus Width: 3.2518



B1L103S, U14-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



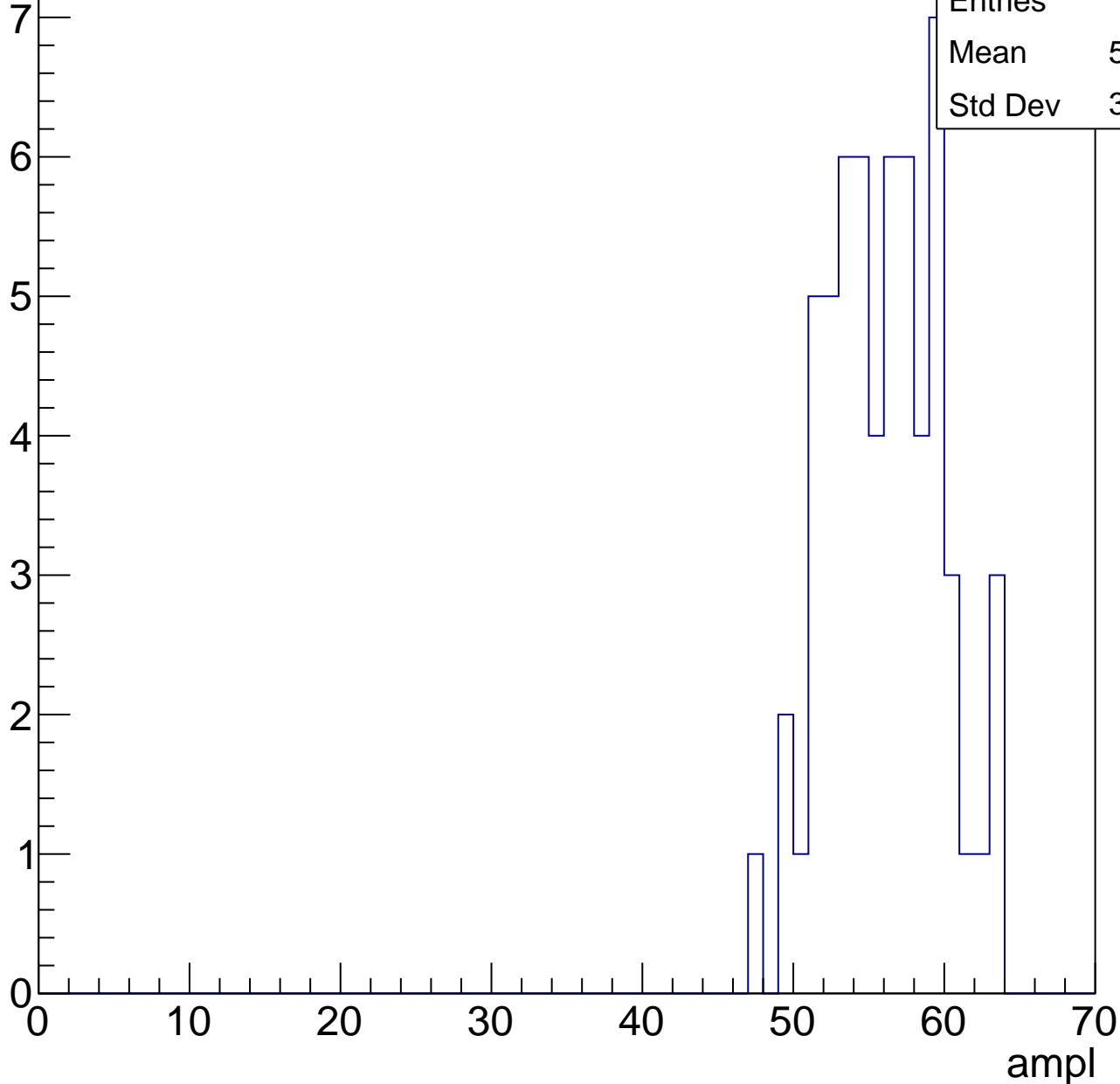
Entries	60
Mean	49.55
Std Dev	3.423

B1L103S, U14-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.52
Std Dev	3.687

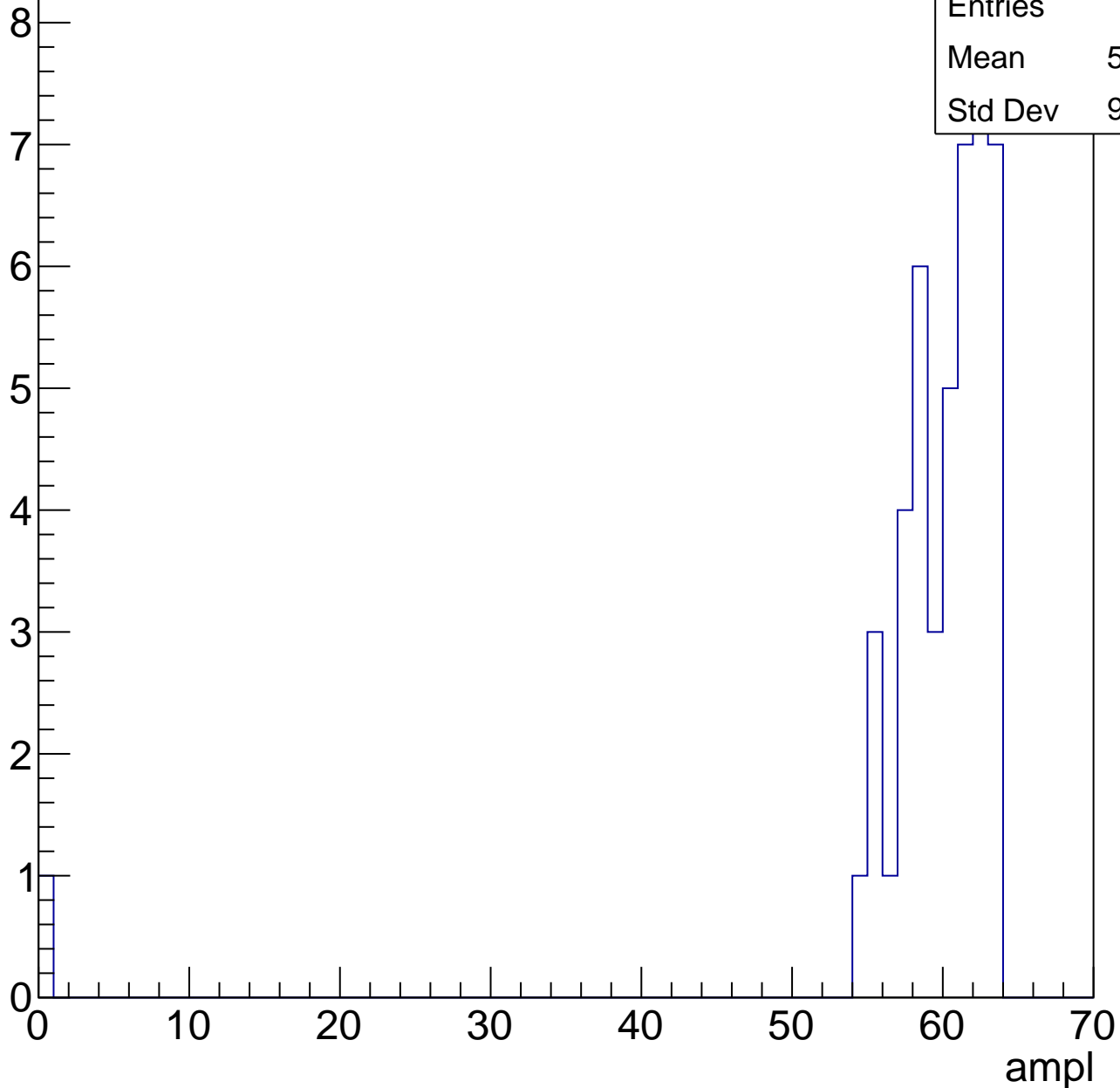


B1L103S, U14-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.52
Std Dev	9.079



B1L103S, U14-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	60.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L103S, U14-ch31, adc7

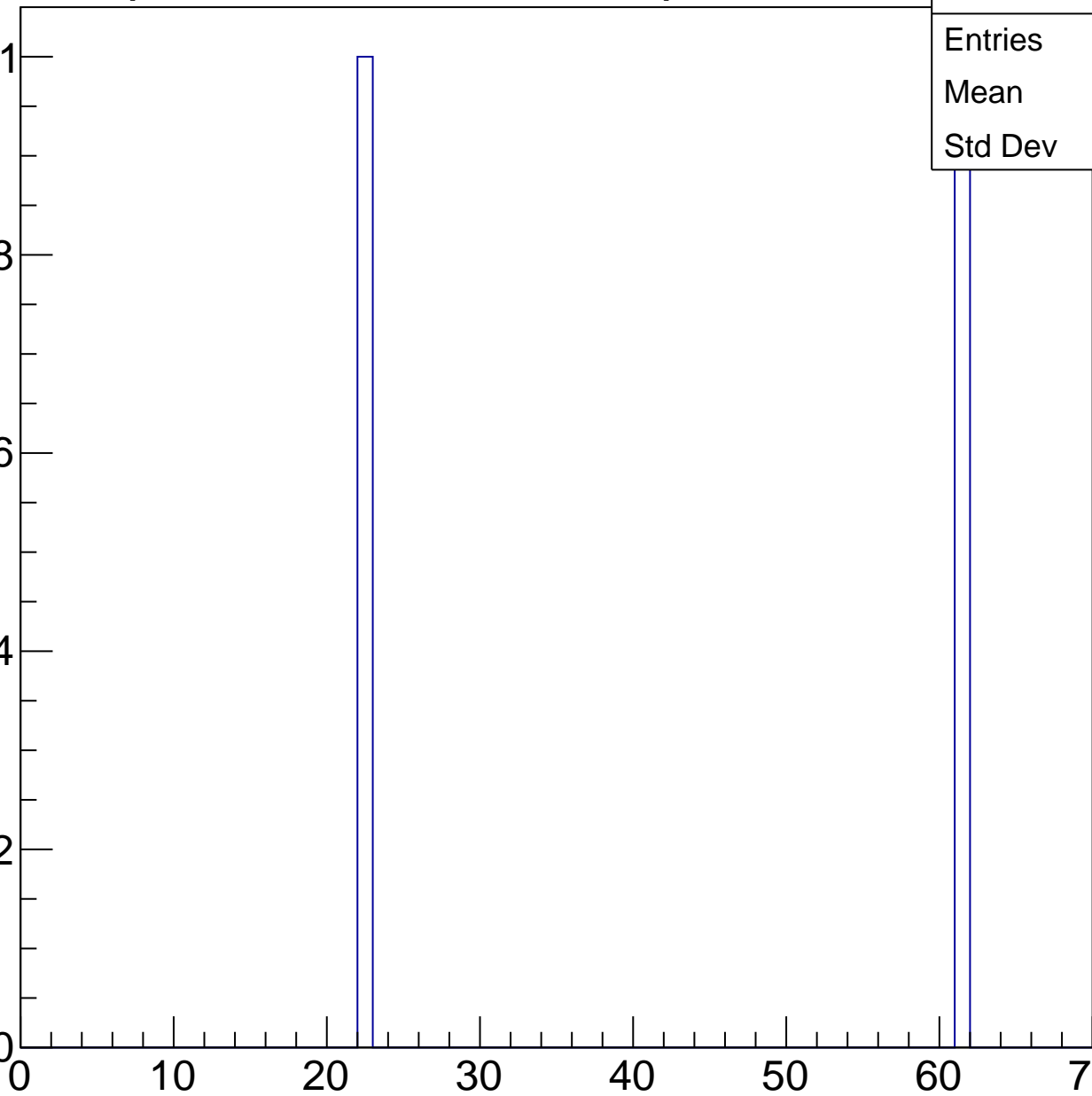
calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	41.5
Std Dev	19.5

ampl



B1L103S, U14-ch32, adc0

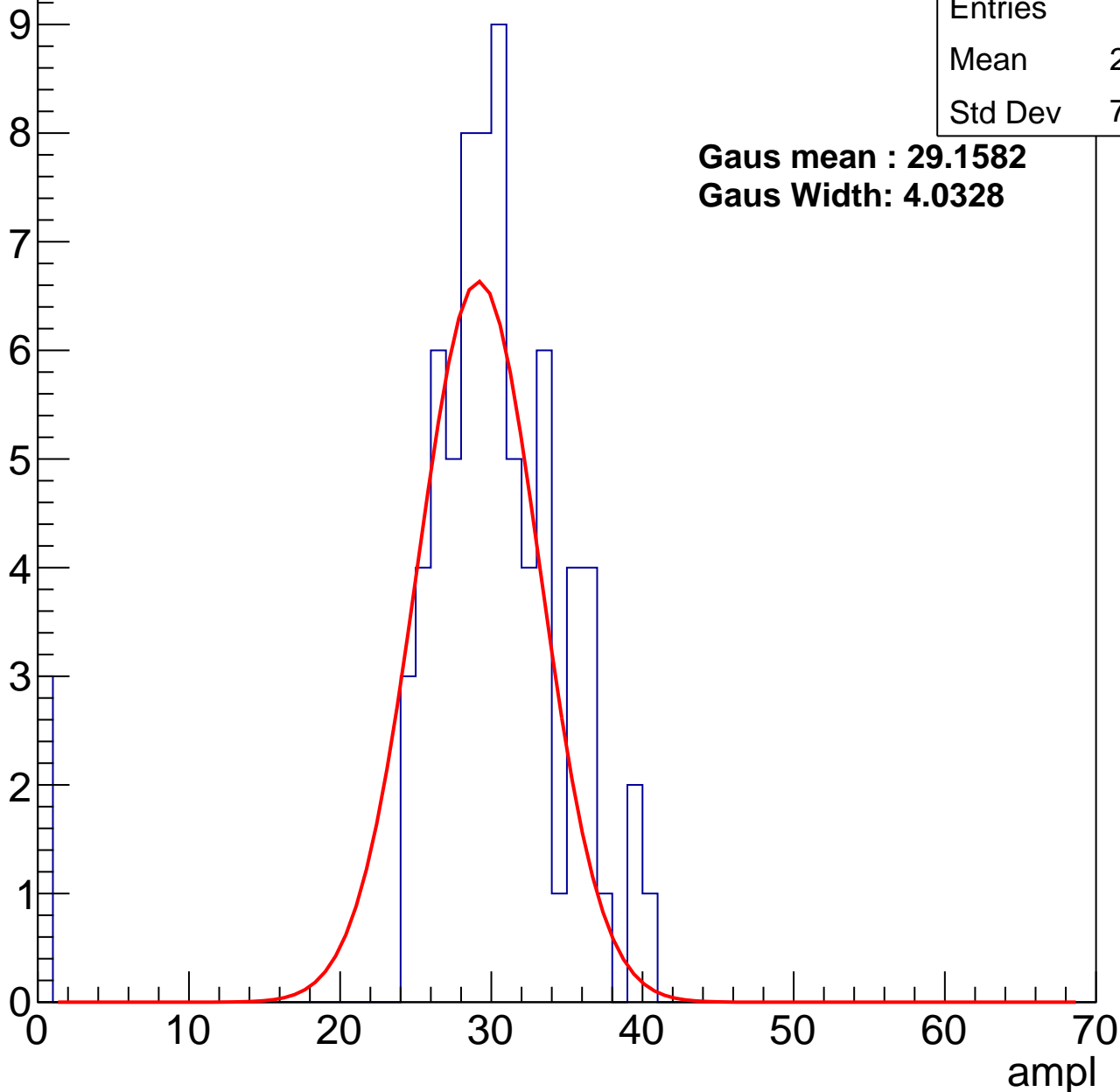
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.96
Std Dev	7.028

Gaus mean : 29.1582

Gaus Width: 4.0328



B1L103S, U14-ch32, adc1

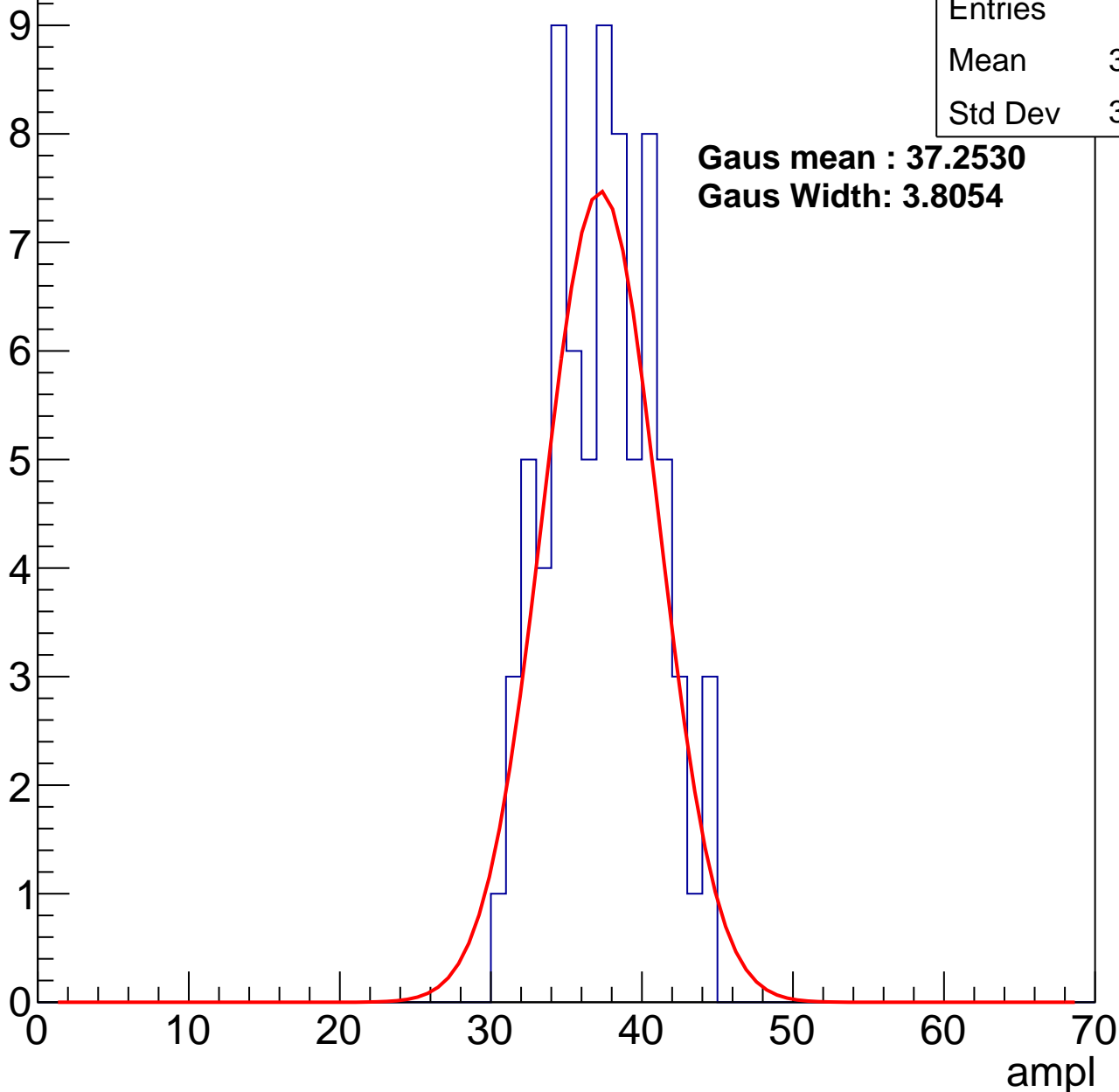
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.92
Std Dev	3.452

Gaus mean : 37.2530

Gaus Width: 3.8054



B1L103S, U14-ch32, adc2

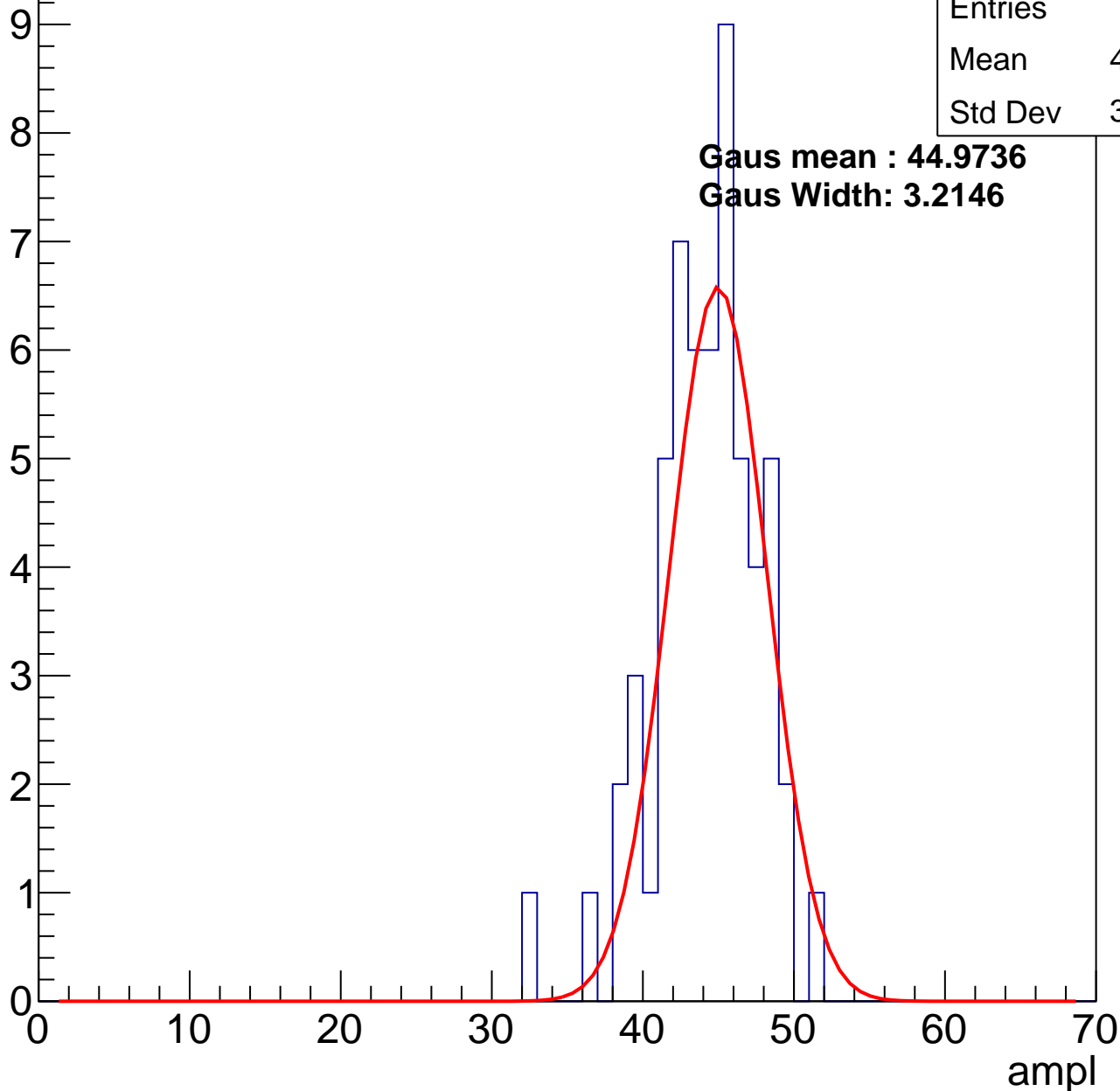
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.69
Std Dev	3.445

Gaus mean : 44.9736

Gaus Width: 3.2146

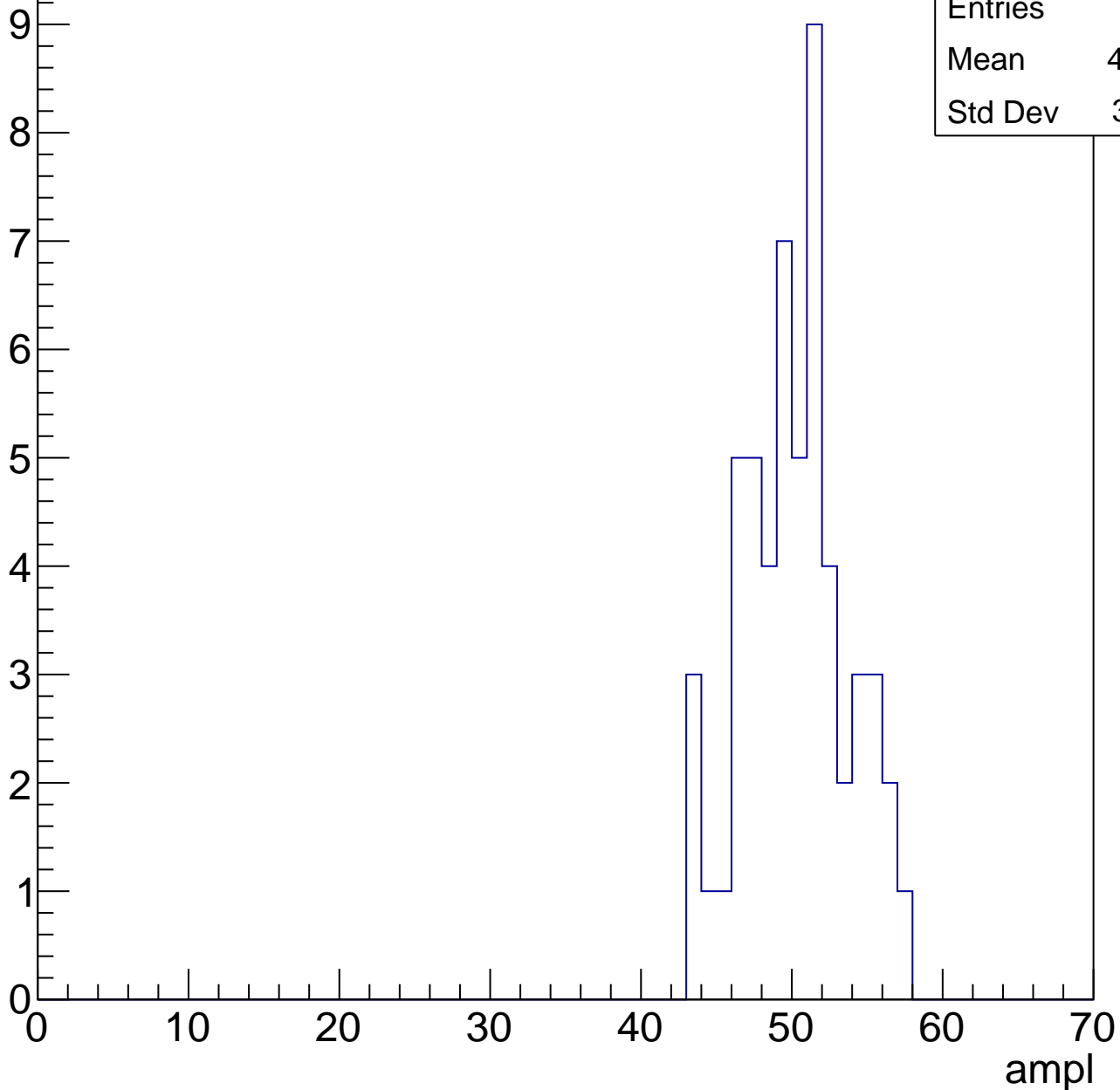


B1L103S, U14-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	49.76
Std Dev	3.411

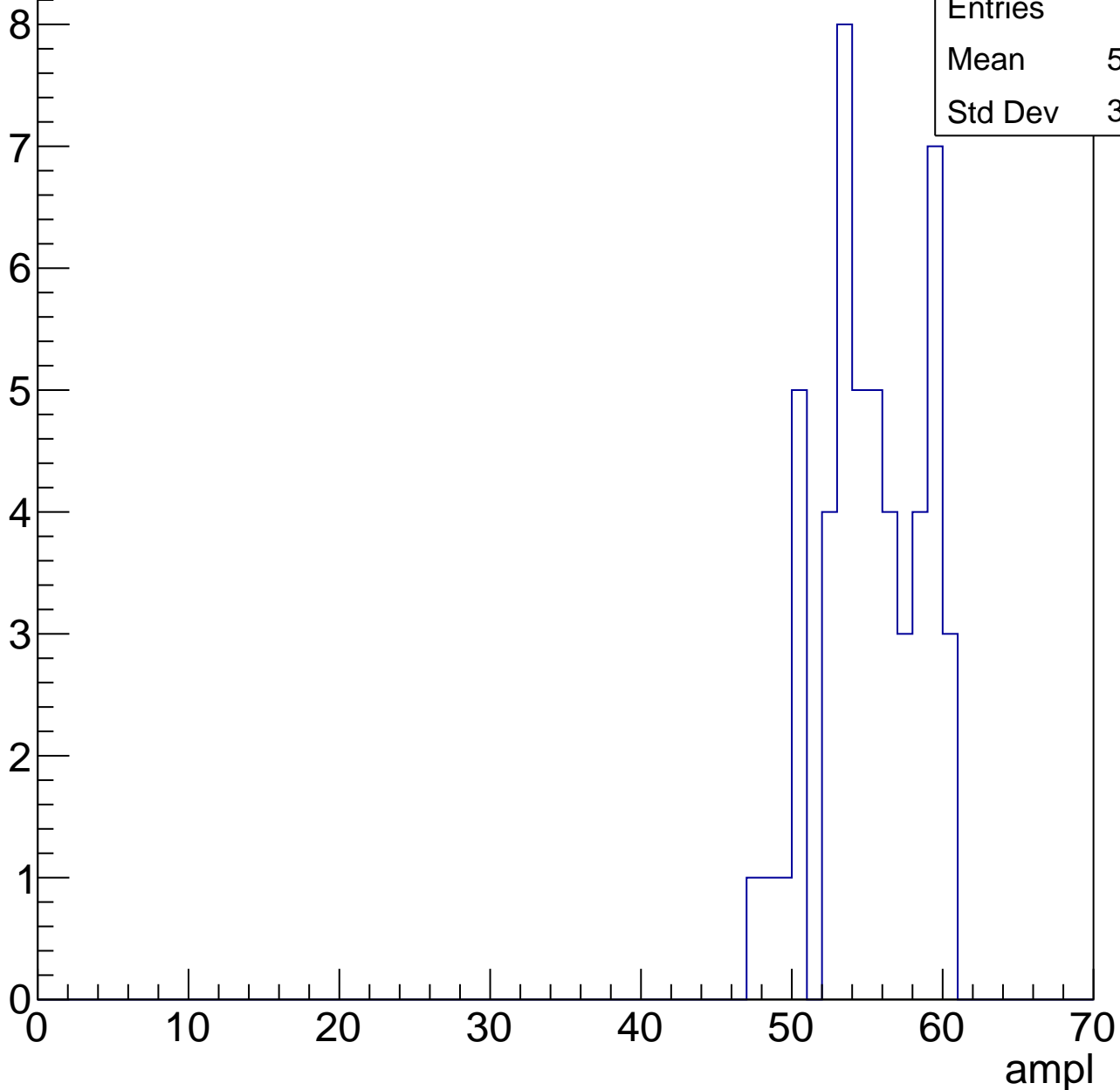


B1L103S, U14-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	54.73
Std Dev	3.379

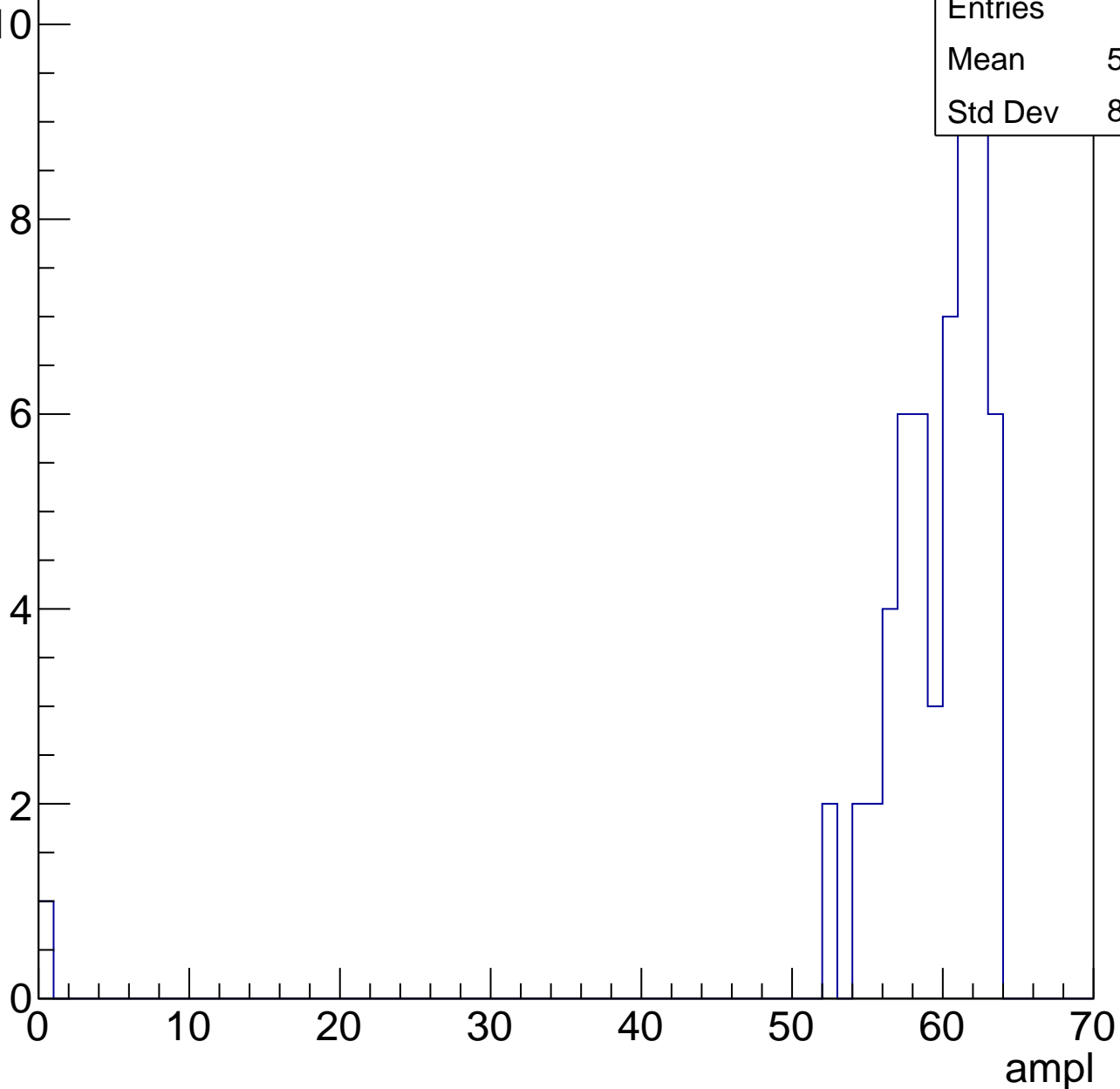


B1L103S, U14-ch32, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	58.32
Std Dev	8.164



B1L103S, U14-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L103S, U14-ch33, adc0

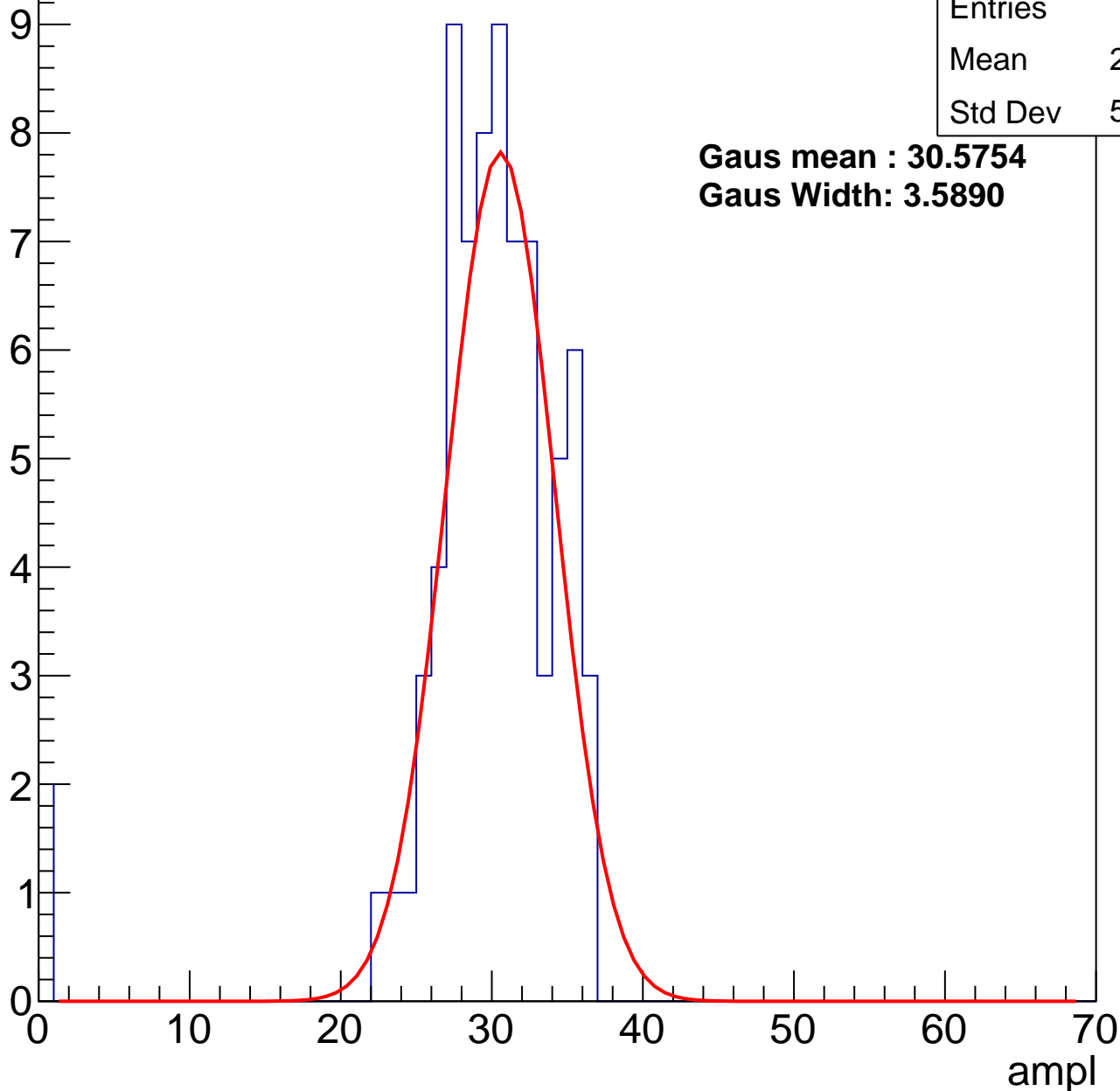
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	29.17
Std Dev	5.796

Gaus mean : 30.5754

Gaus Width: 3.5890



B1L103S, U14-ch33, adc1

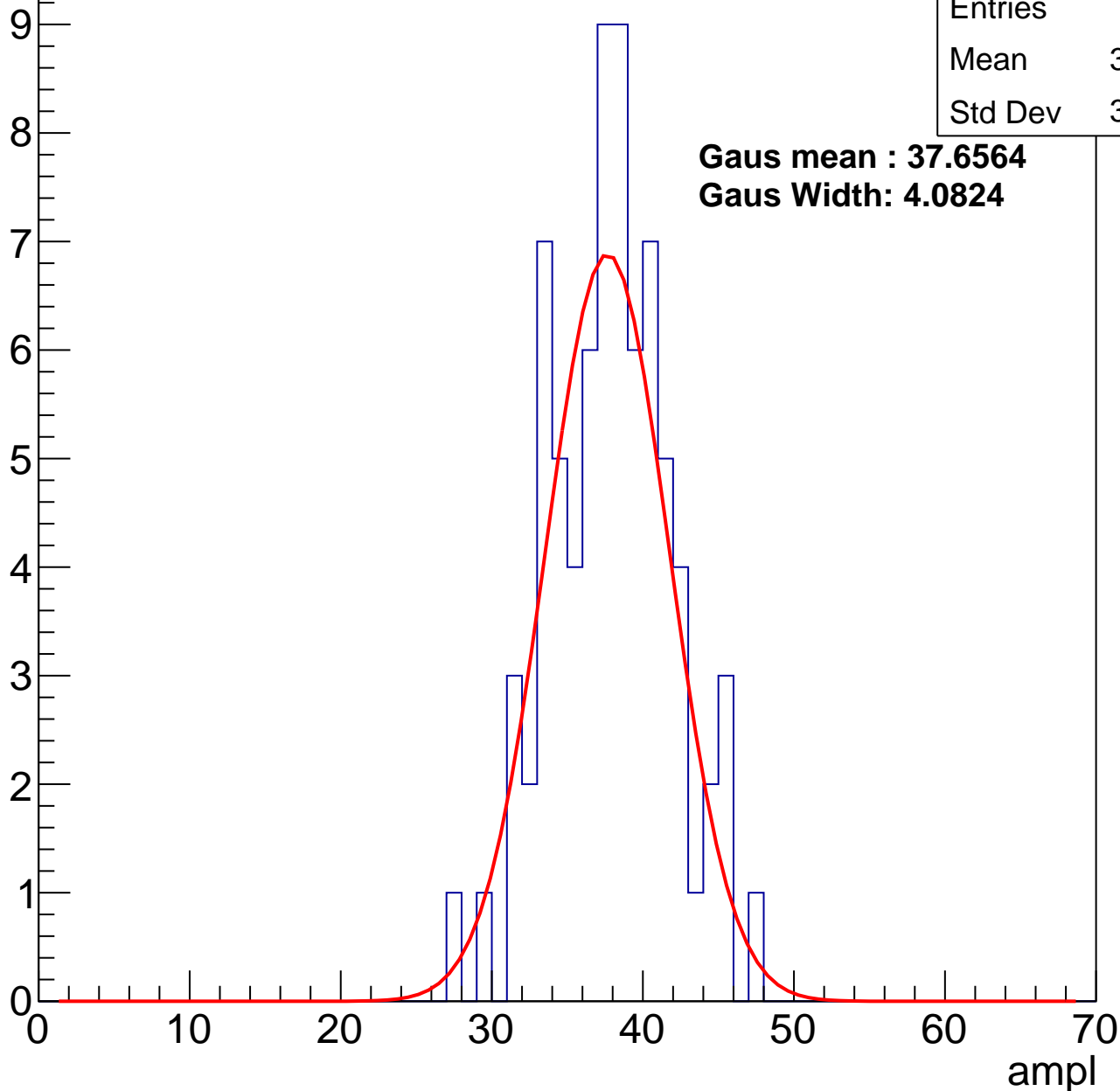
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	37.43
Std Dev	3.955

Gaus mean : 37.6564

Gaus Width: 4.0824



B1L103S, U14-ch33, adc2

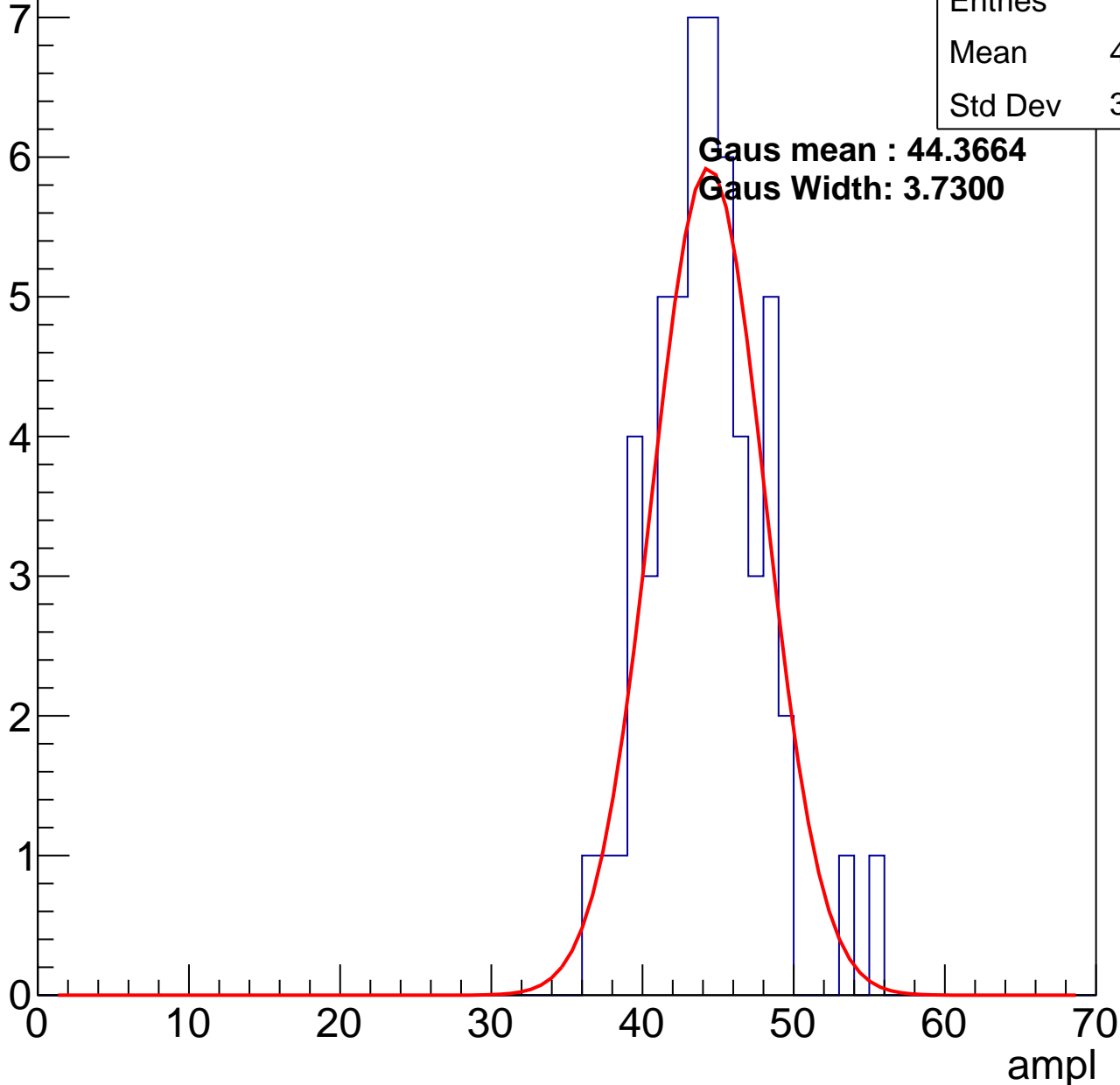
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.79
Std Dev	3.653

Gaus mean : 44.3664

Gaus Width: 3.7300

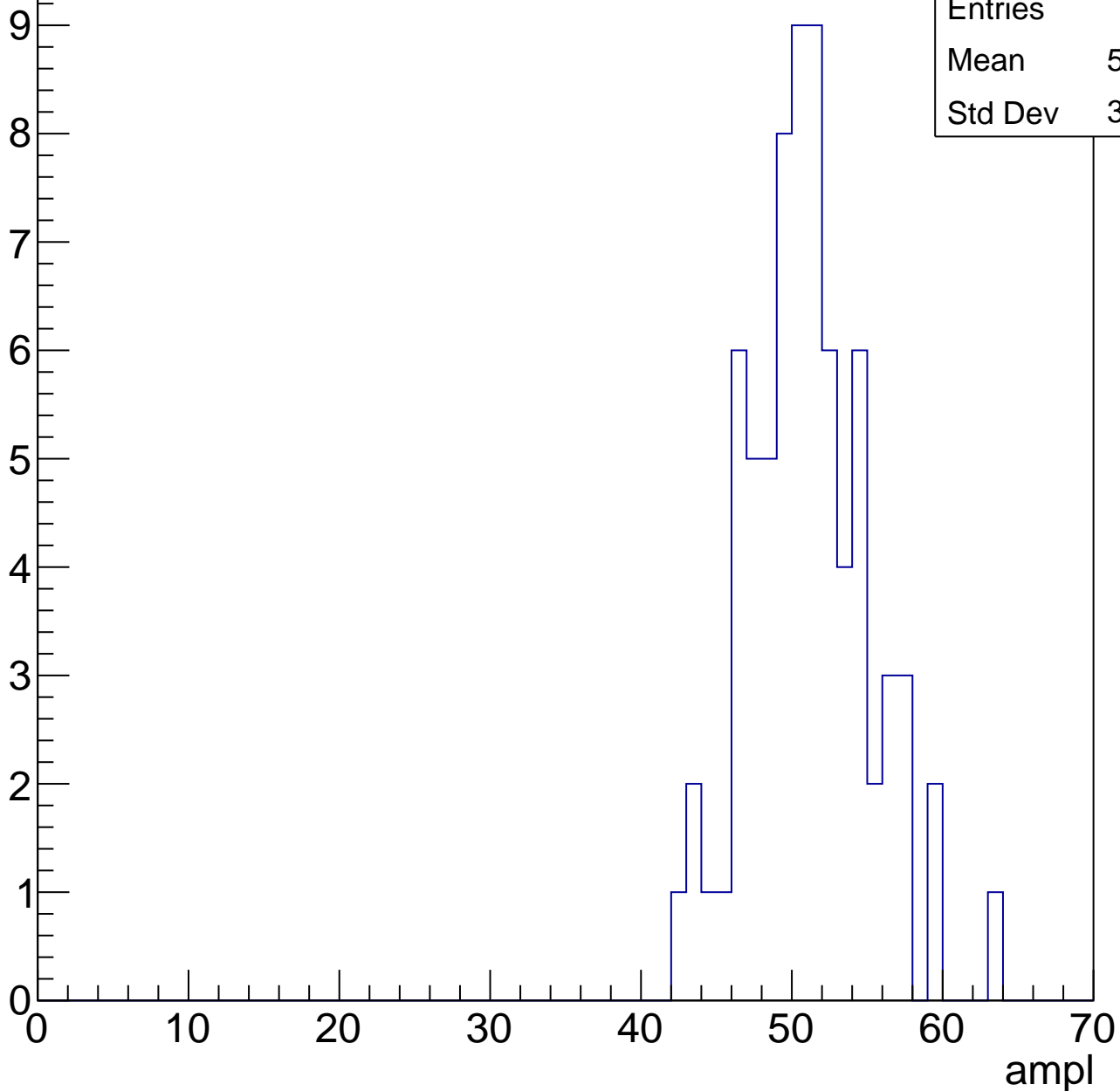


B1L103S, U14-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	50.64
Std Dev	3.965

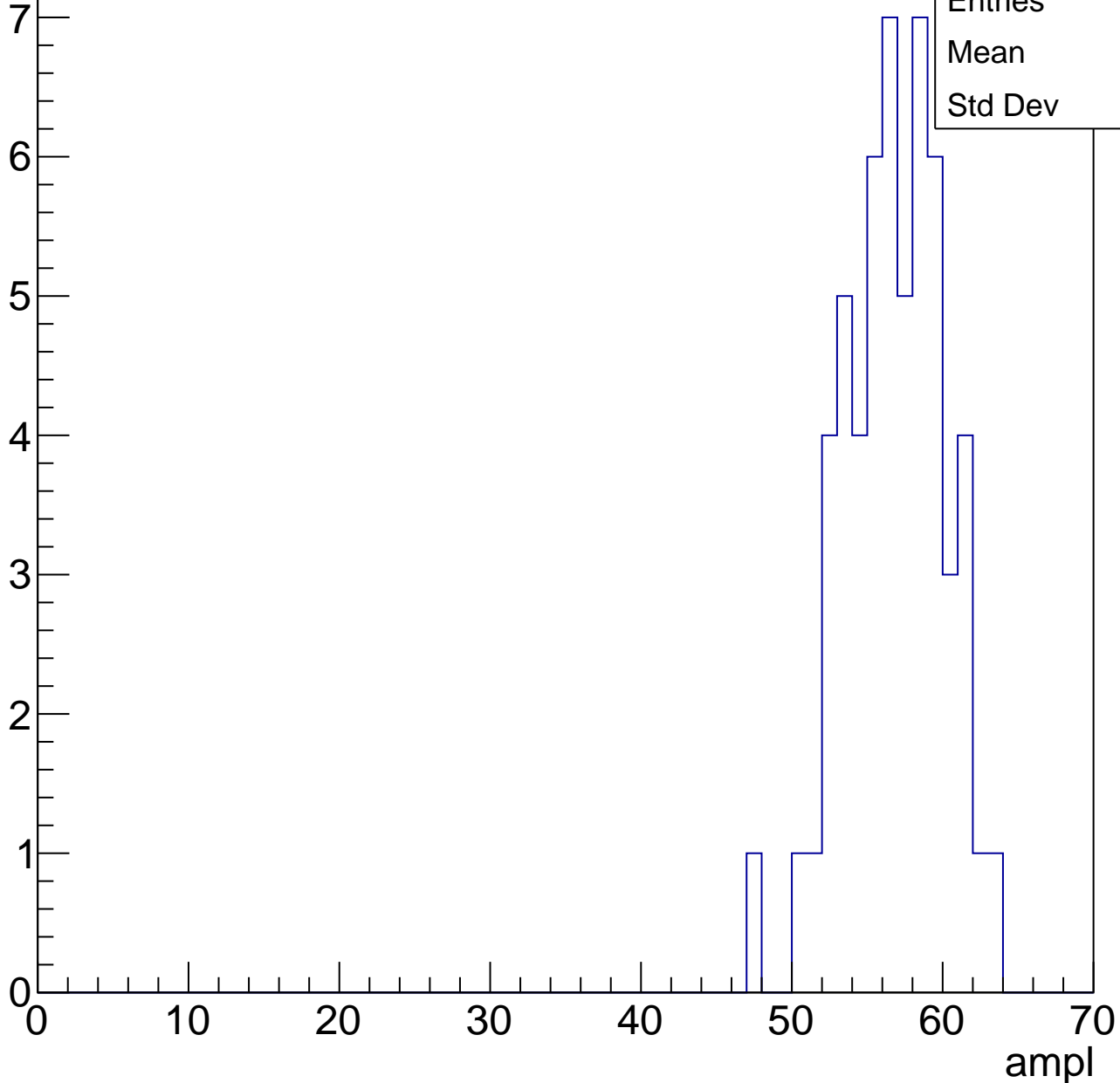


B1L103S, U14-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	56.3
Std Dev	3.24



B1L103S, U14-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 33

Mean 58.64

Std Dev 10.55

0

10

20

30

40

50

60

70

ampl

0

10

20

30

40

50

60

70

0

10

20

30

40

50

60

70

B1L103S, U14-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch34, adc0

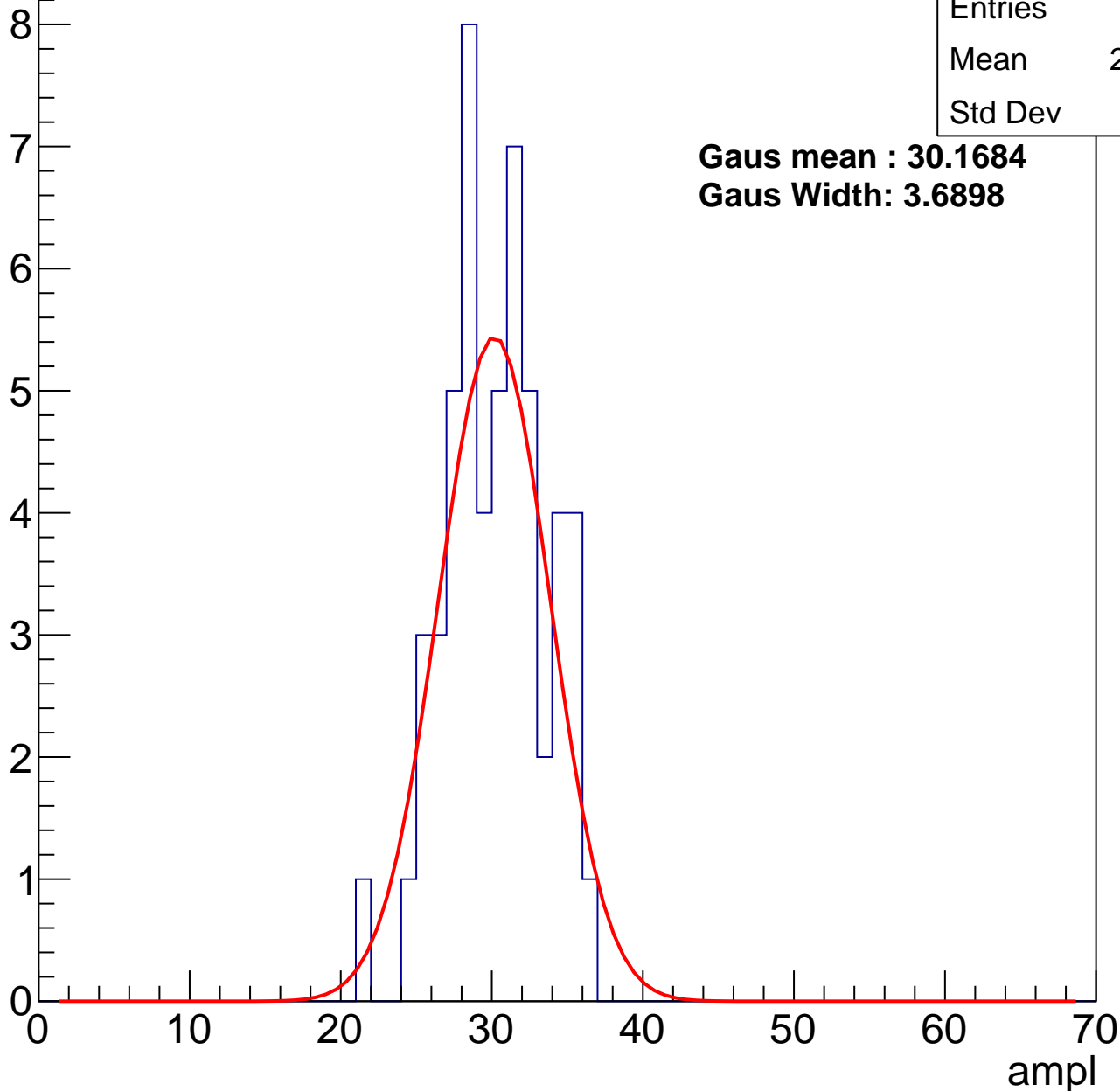
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	29.77
Std Dev	3.26

Gaus mean : 30.1684

Gaus Width: 3.6898



B1L103S, U14-ch34, adc1

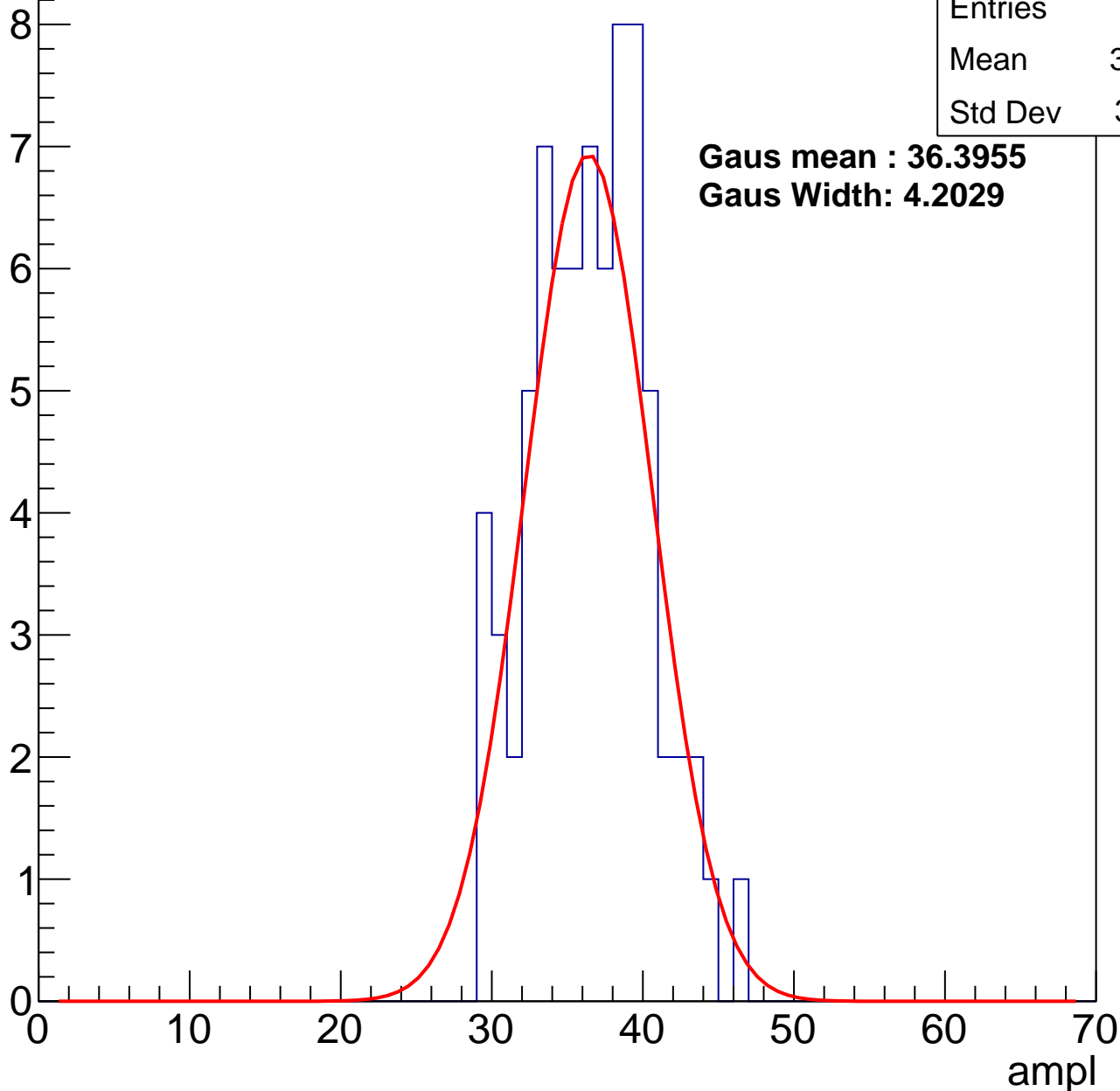
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.07
Std Dev	3.831

Gaus mean : 36.3955

Gaus Width: 4.2029



B1L103S, U14-ch34, adc2

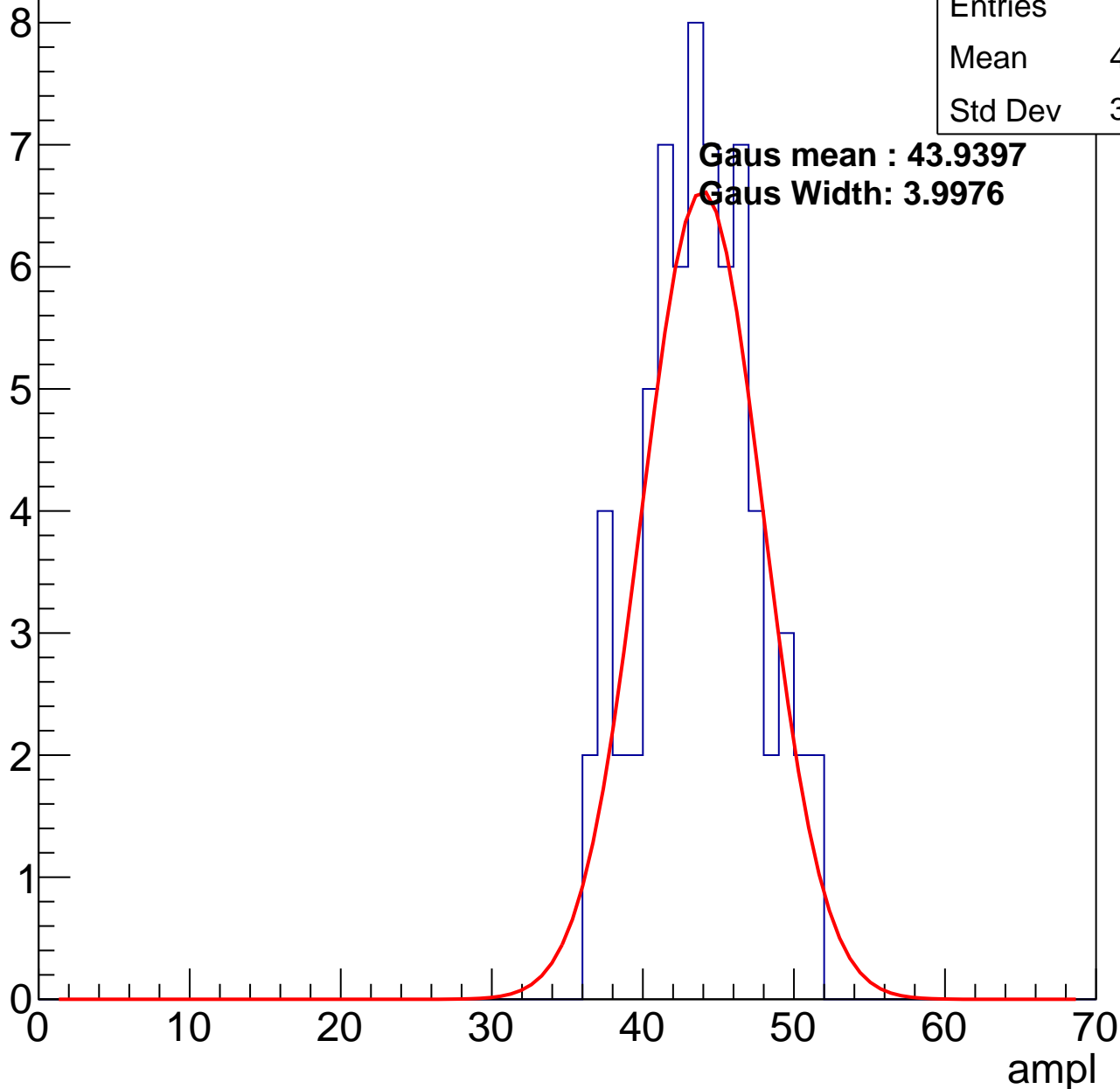
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	43.33
Std Dev	3.686

Gaus mean : 43.9397

Gaus Width: 3.9976

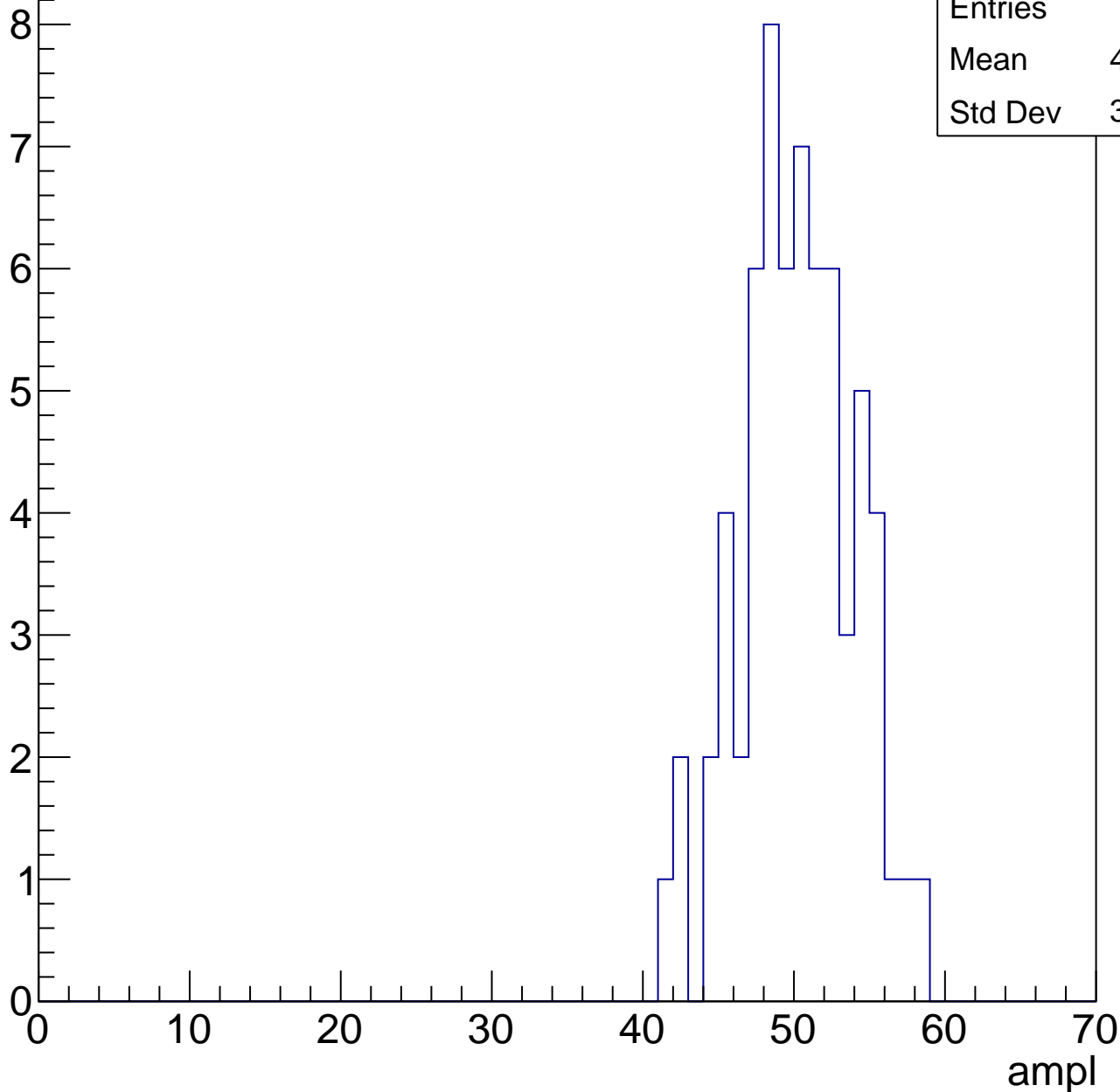


B1L103S, U14-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.74
Std Dev	3.693

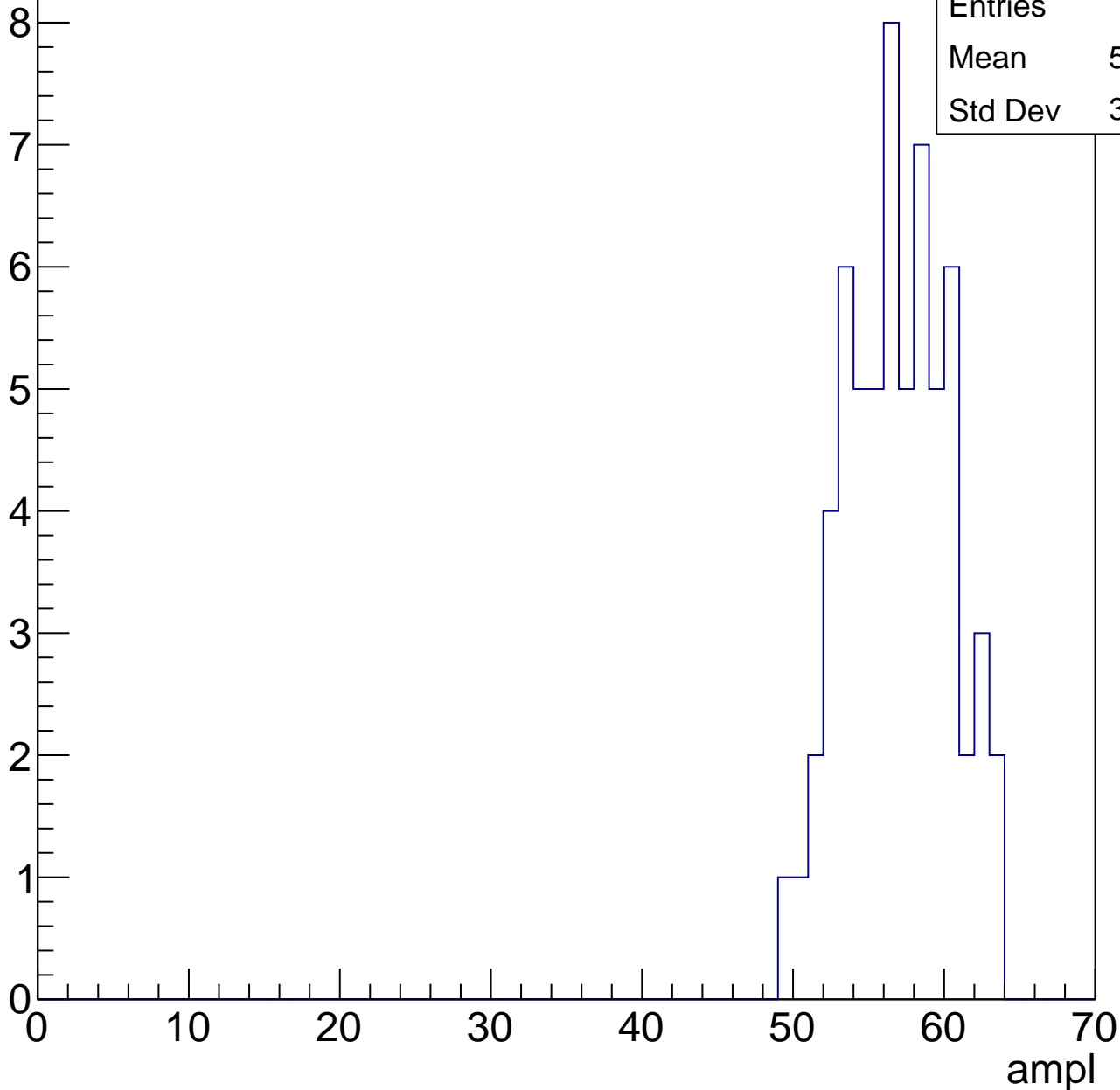


B1L103S, U14-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	56.45
Std Dev	3.349

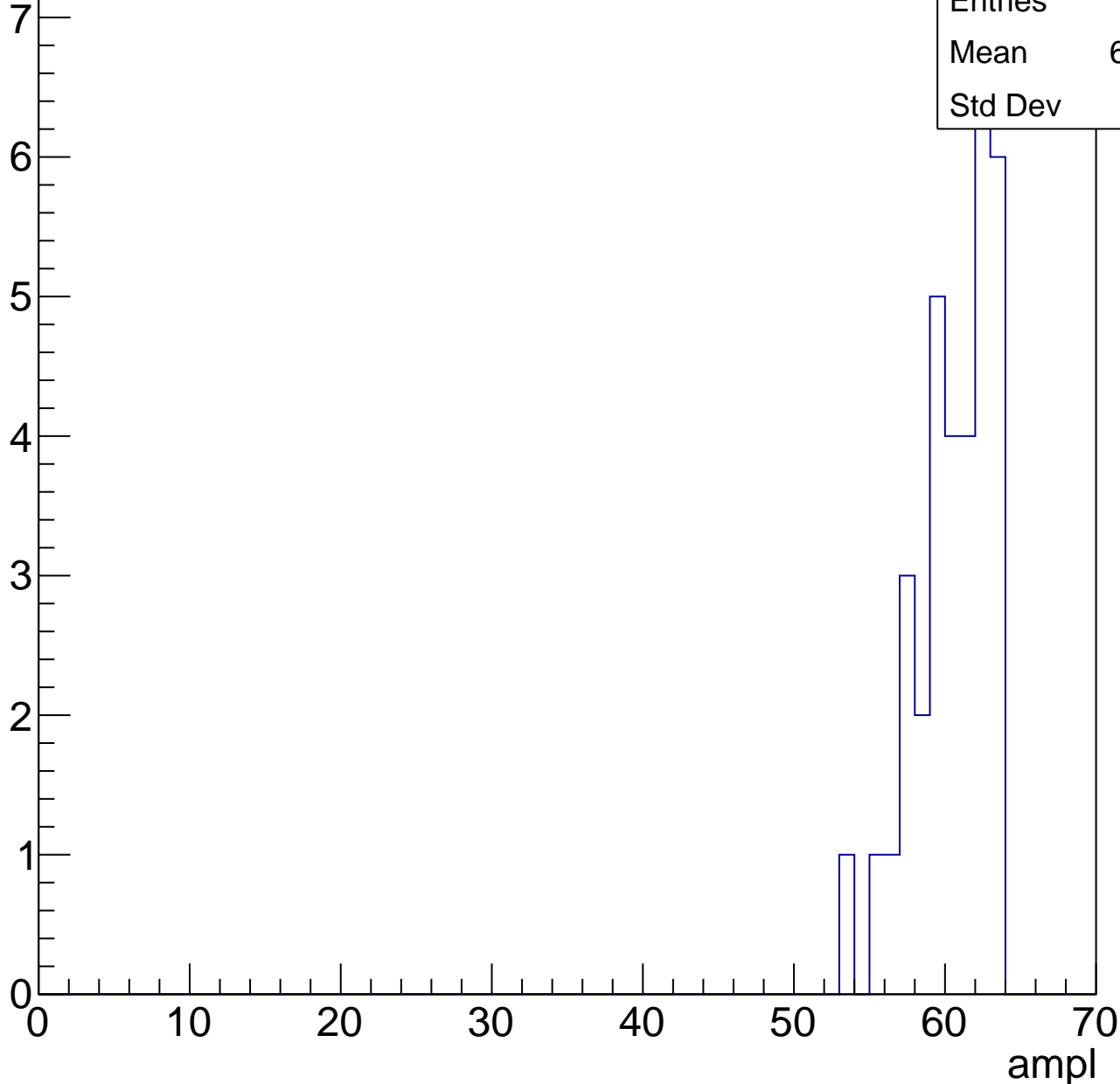


B1L103S, U14-ch34, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

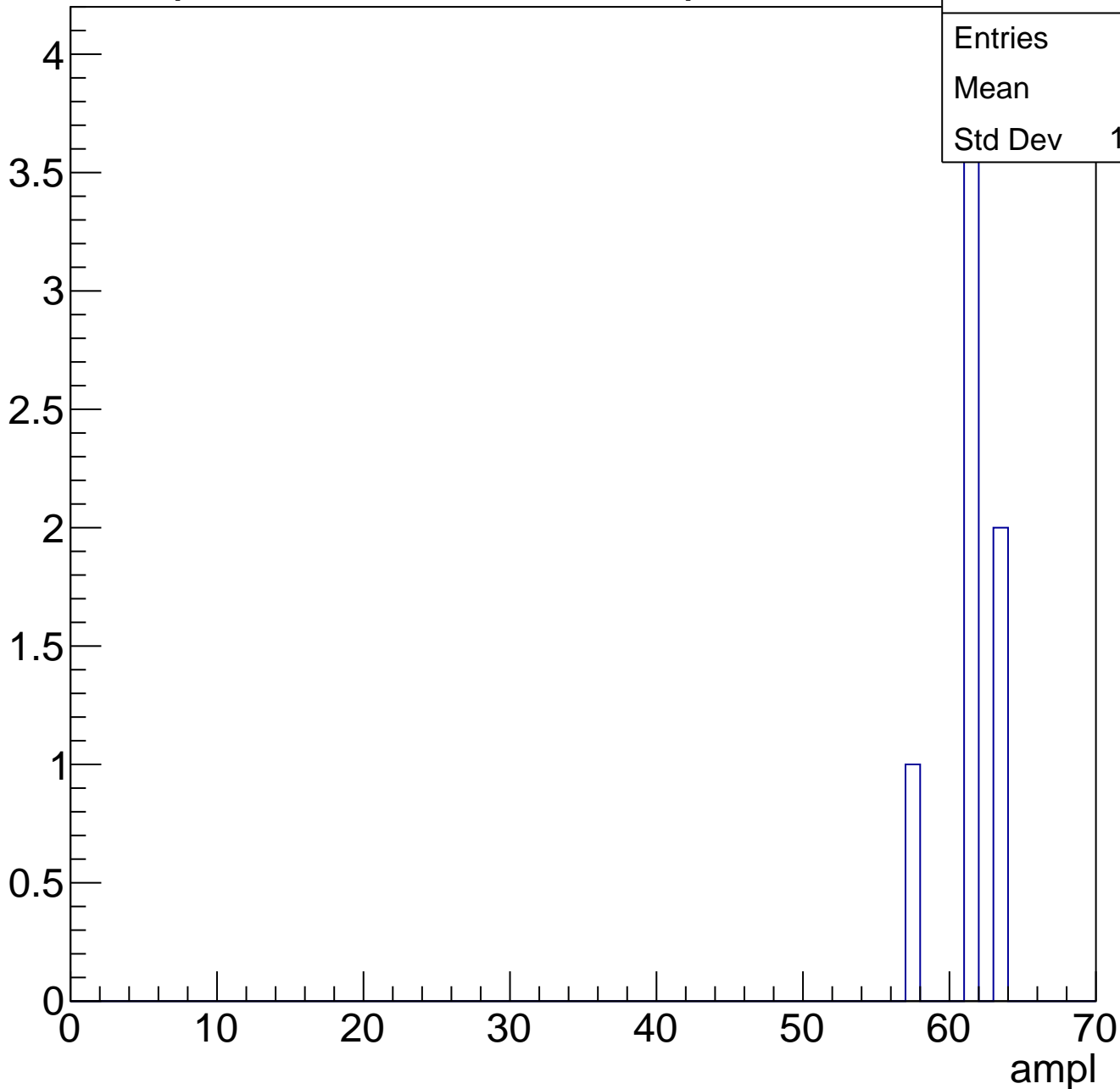
Entries	34
Mean	60.06
Std Dev	2.52



B1L103S, U14-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch35, adc0

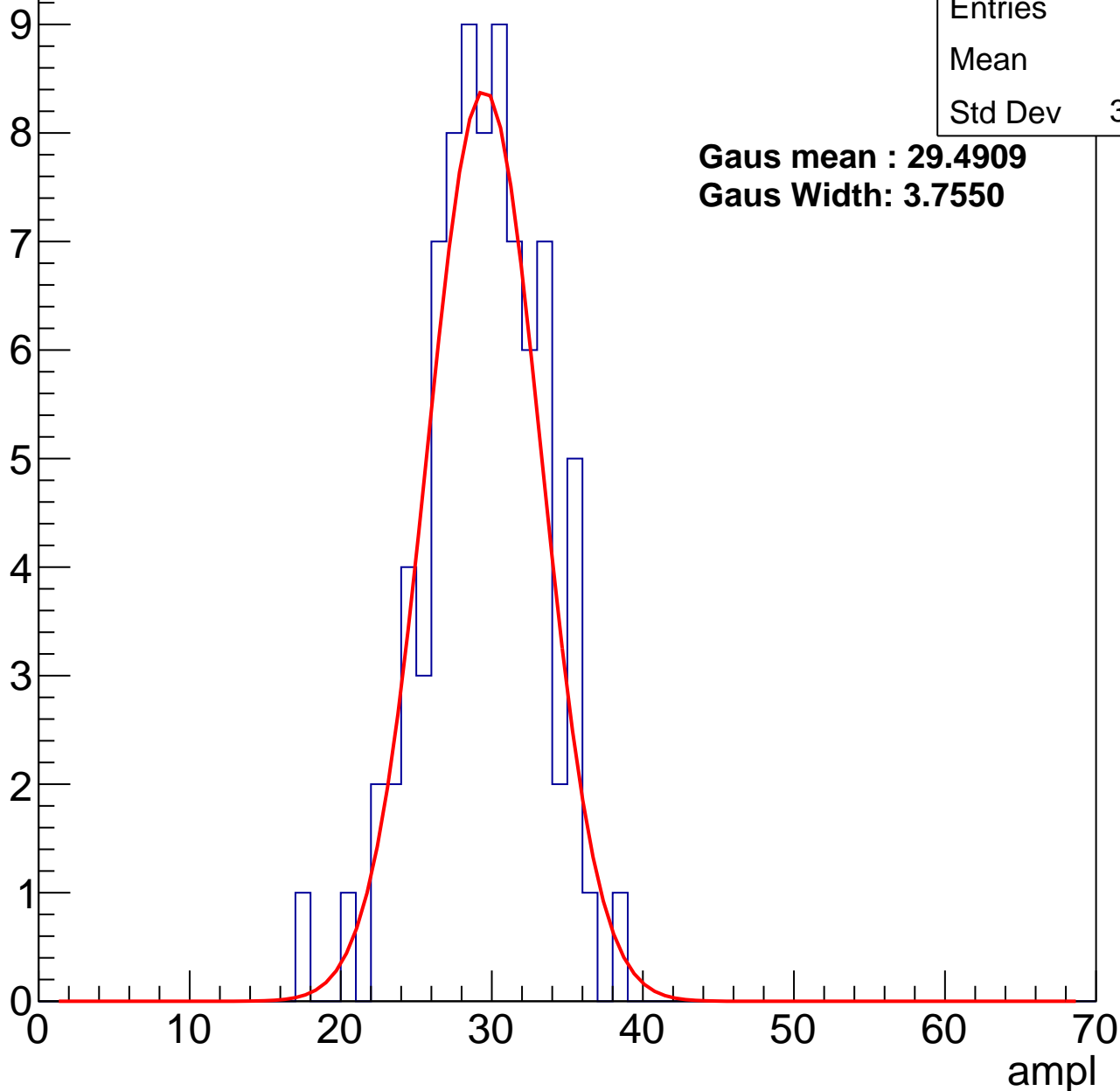
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	29
Std Dev	3.818

Gaus mean : 29.4909

Gaus Width: 3.7550



B1L103S, U14-ch35, adc1

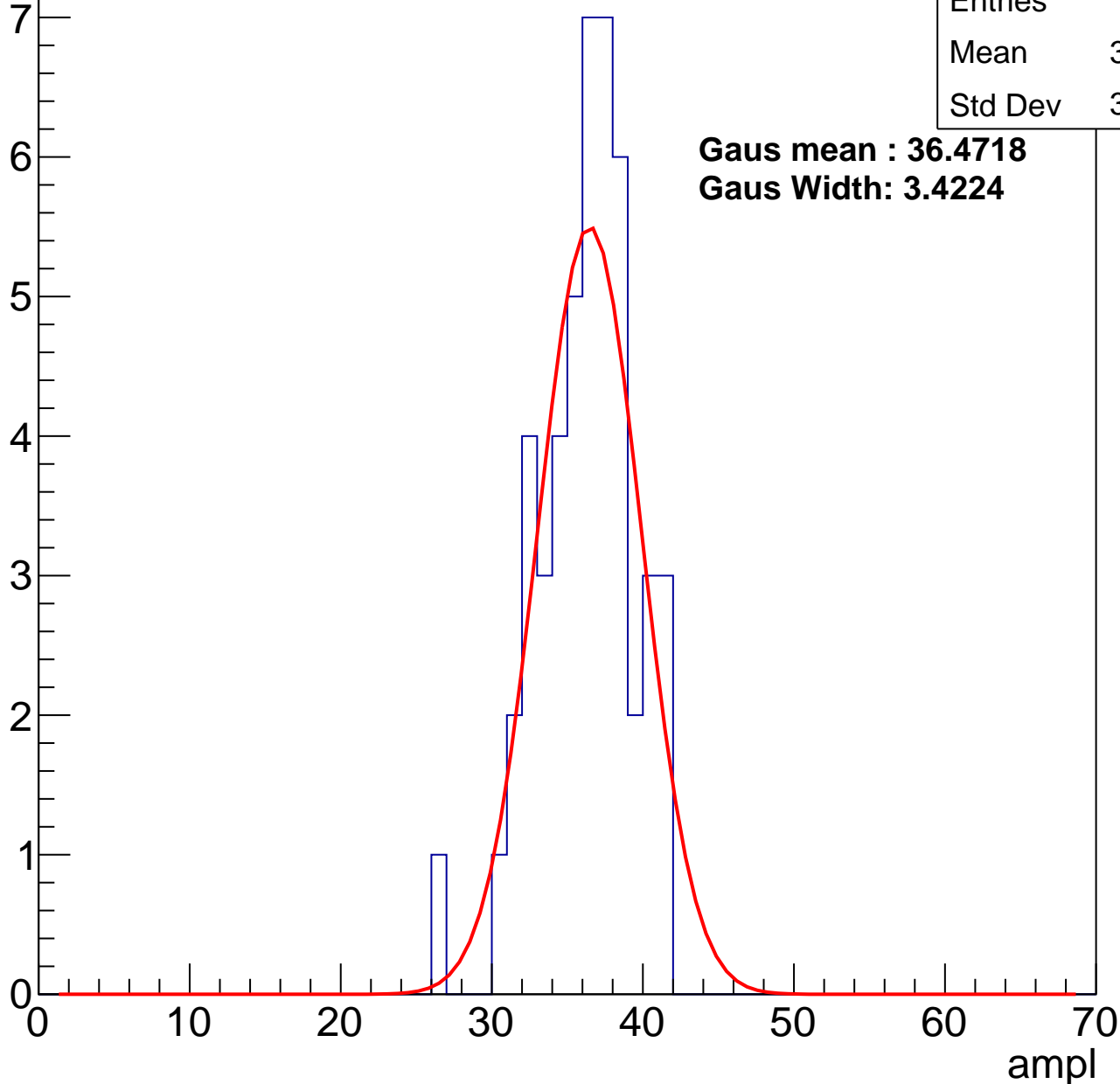
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	35.75
Std Dev	3.119

Gaus mean : 36.4718

Gaus Width: 3.4224



B1L103S, U14-ch35, adc2

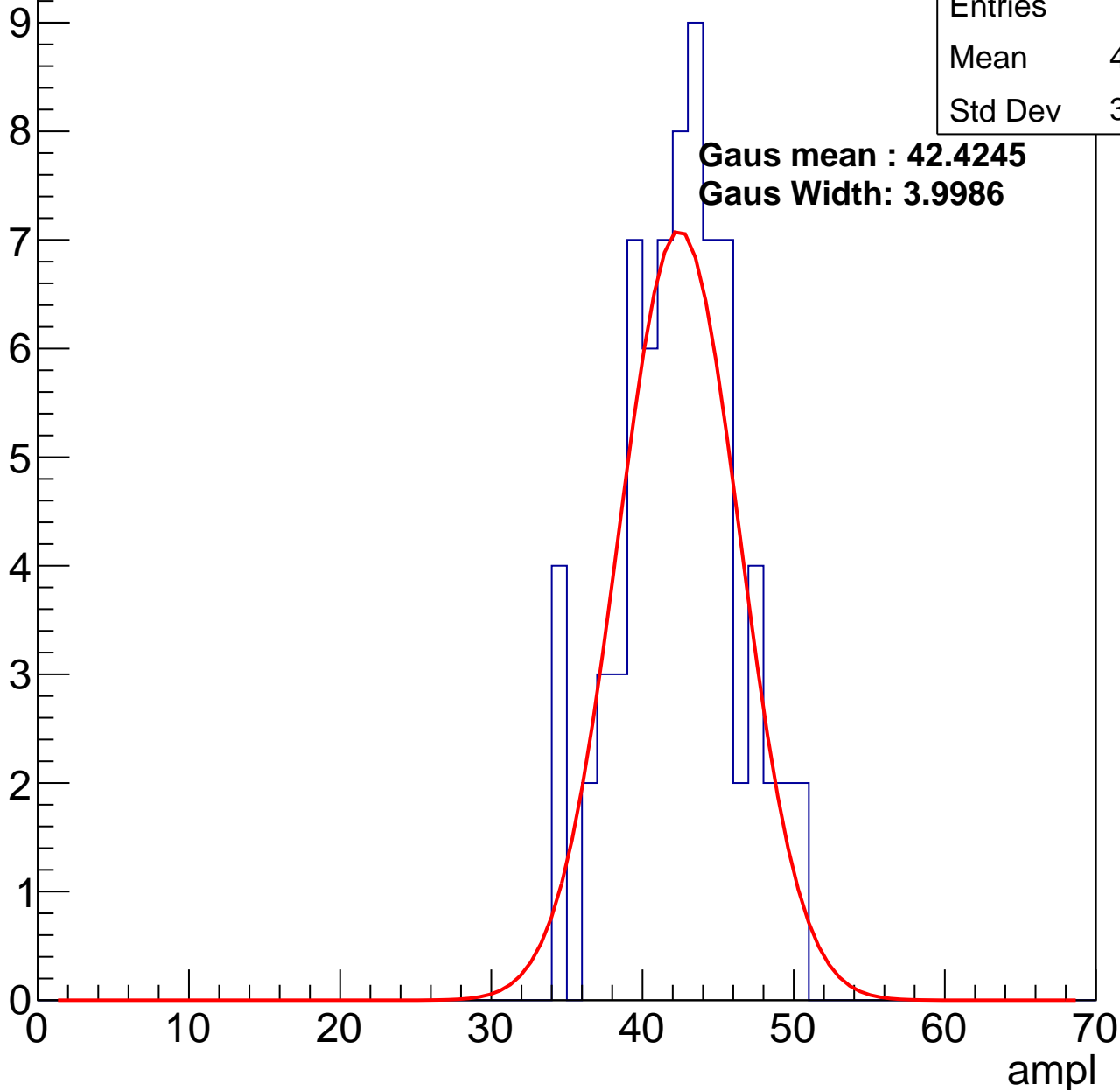
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	42.04
Std Dev	3.786

Gaus mean : 42.4245

Gaus Width: 3.9986

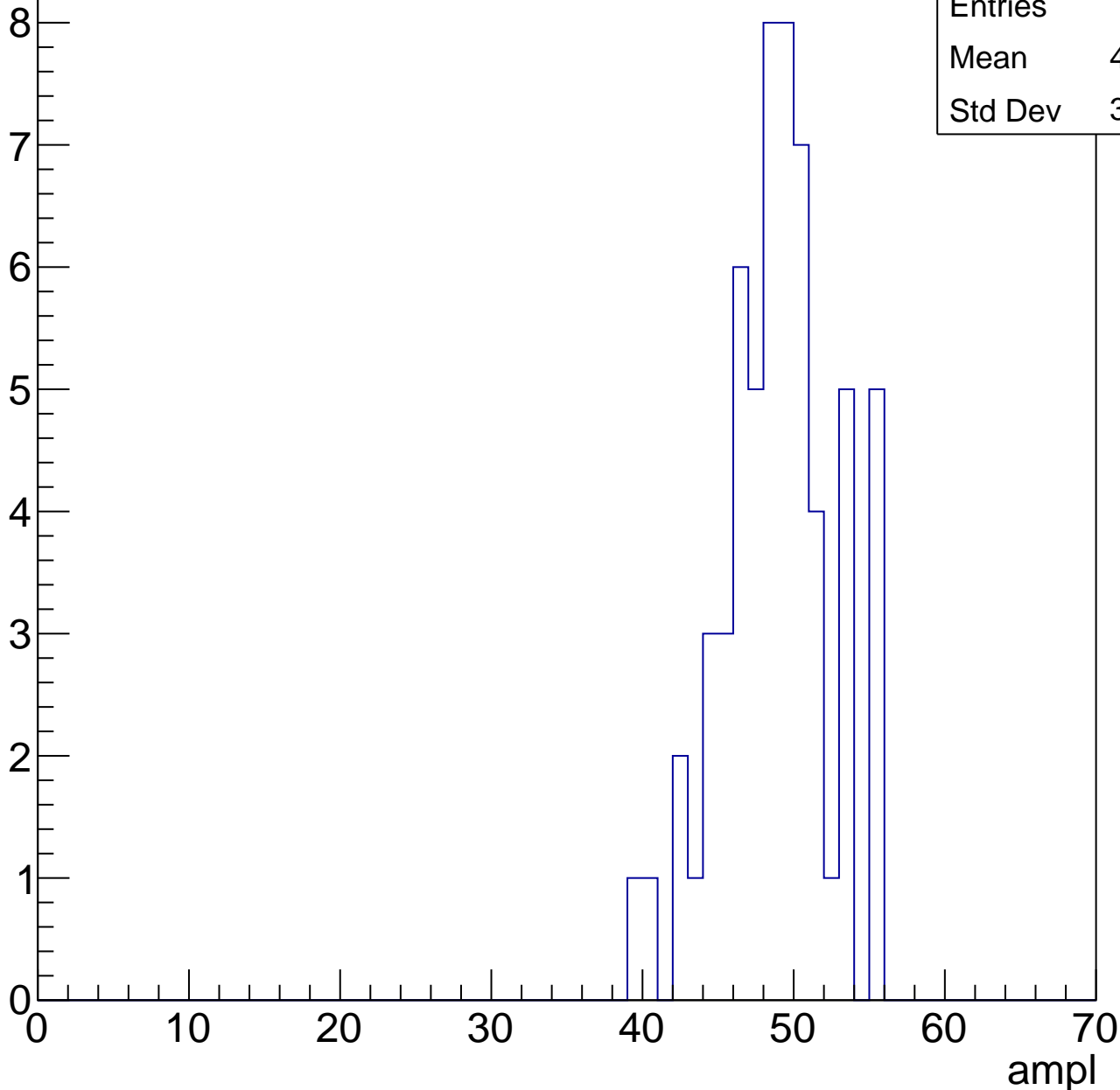


B1L103S, U14-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	48.43
Std Dev	3.635



B1L103S, U14-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

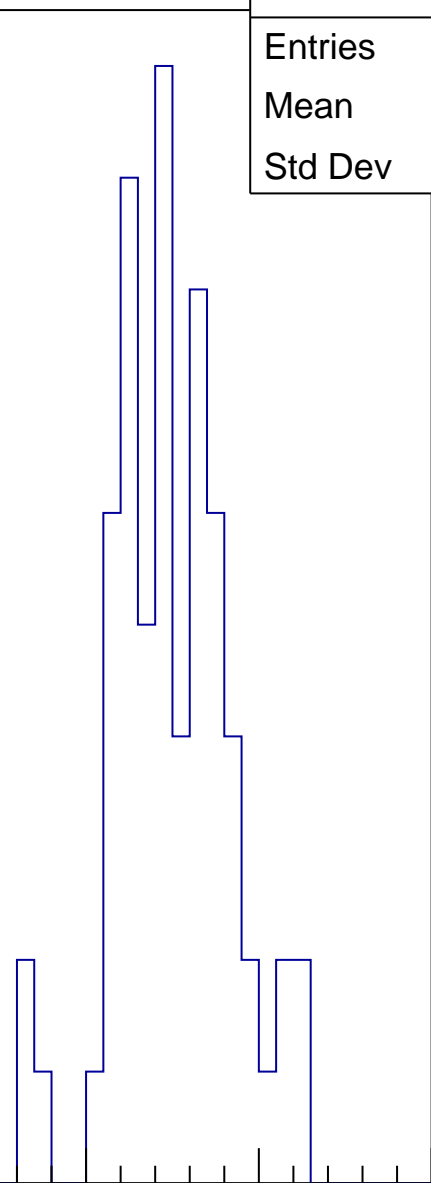
Entries	63
Mean	54.51
Std Dev	3.422

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

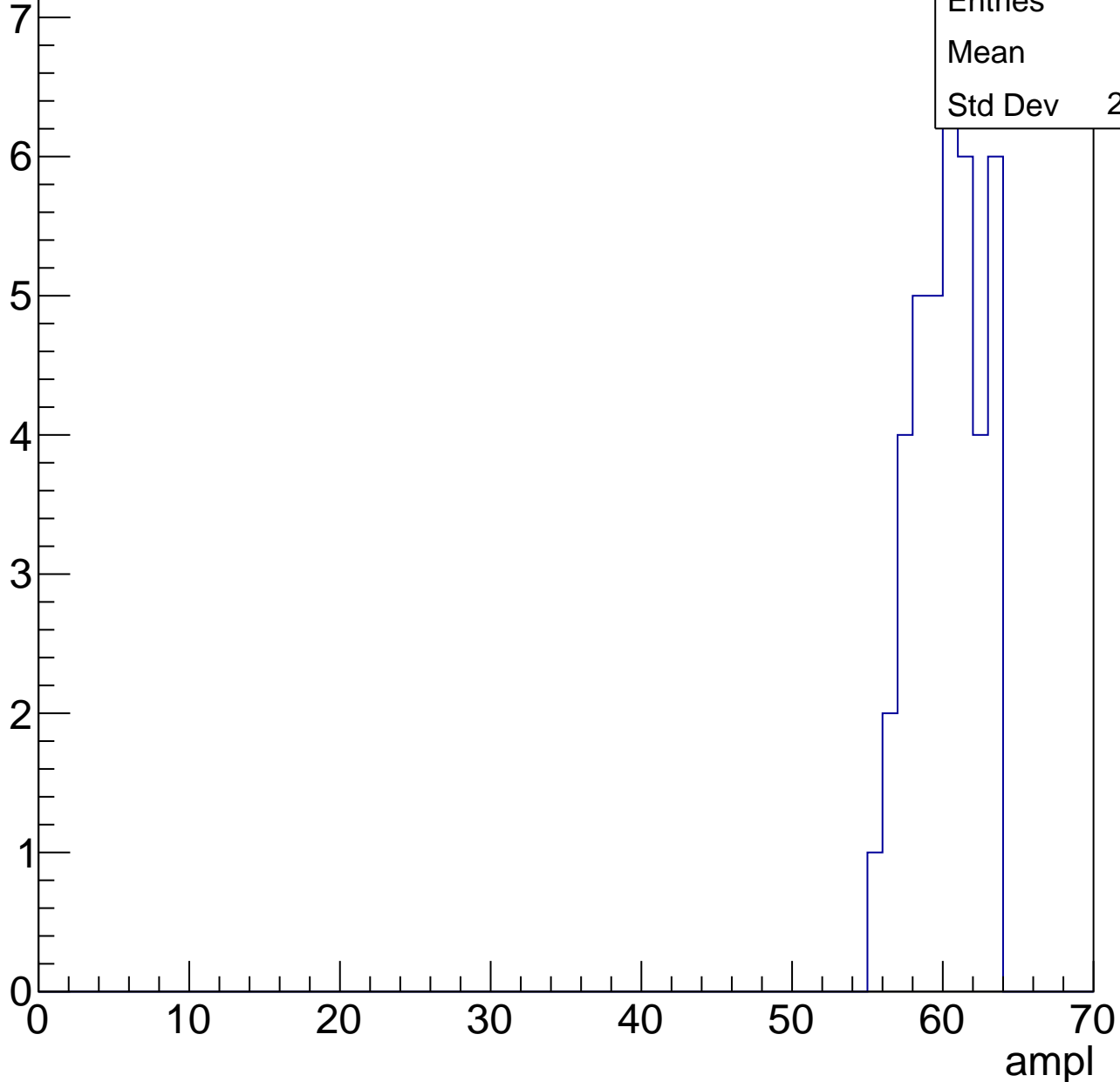
ampl



B1L103S, U14-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

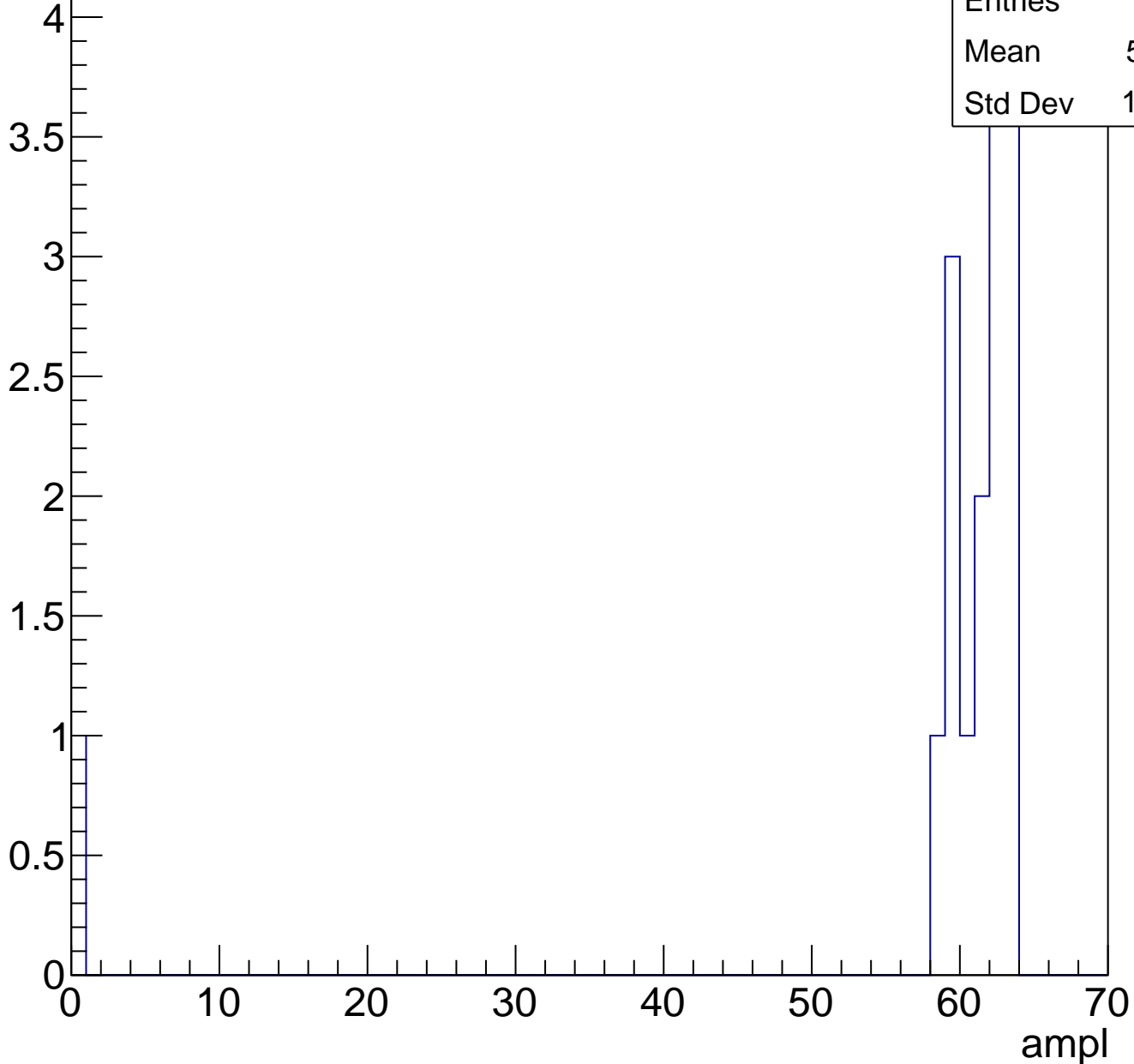


Entries	40
Mean	59.8
Std Dev	2.193

B1L103S, U14-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch36, adc0

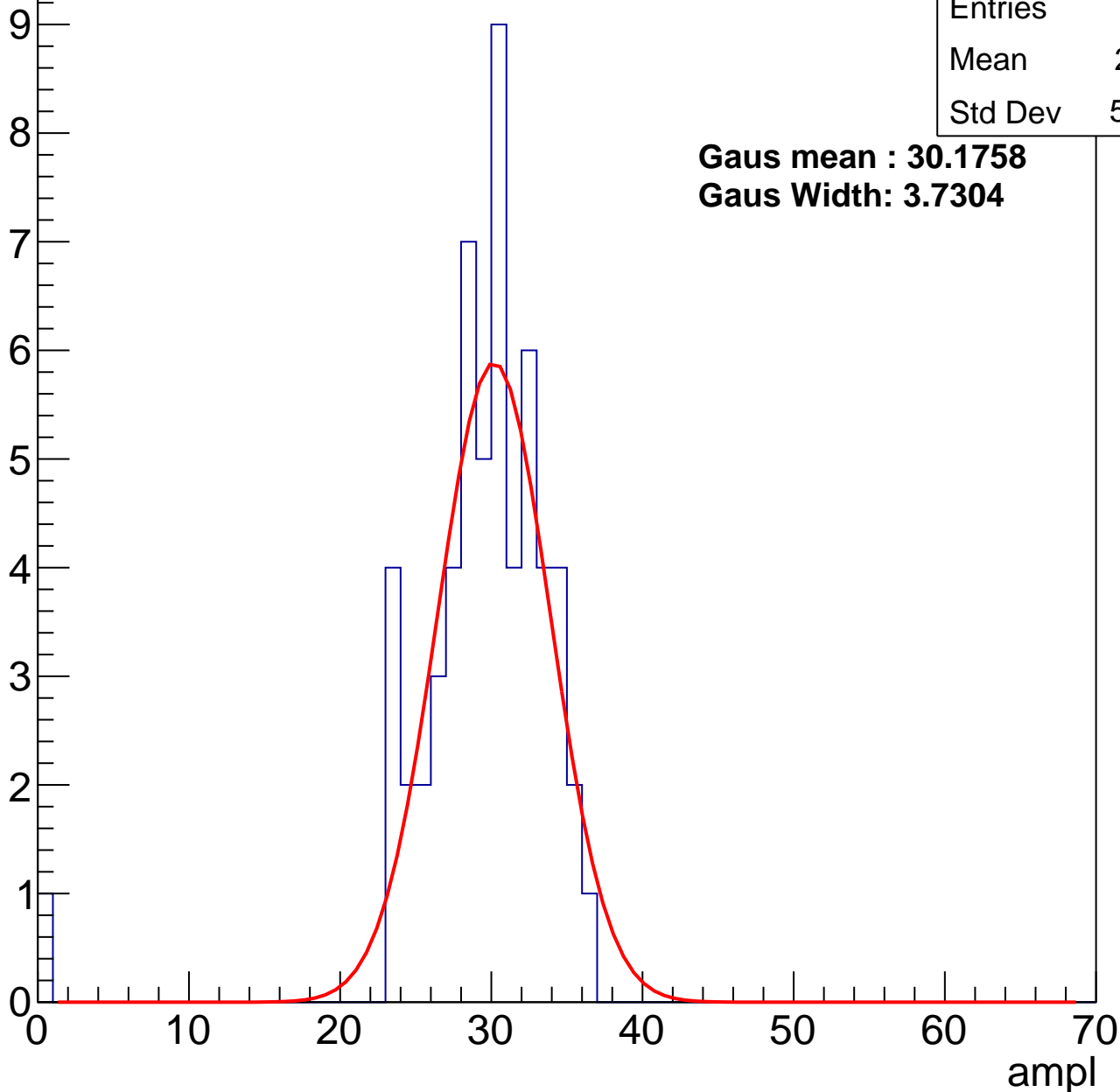
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	28.91
Std Dev	5.052

Gaus mean : 30.1758

Gaus Width: 3.7304



B1L103S, U14-ch36, adc1

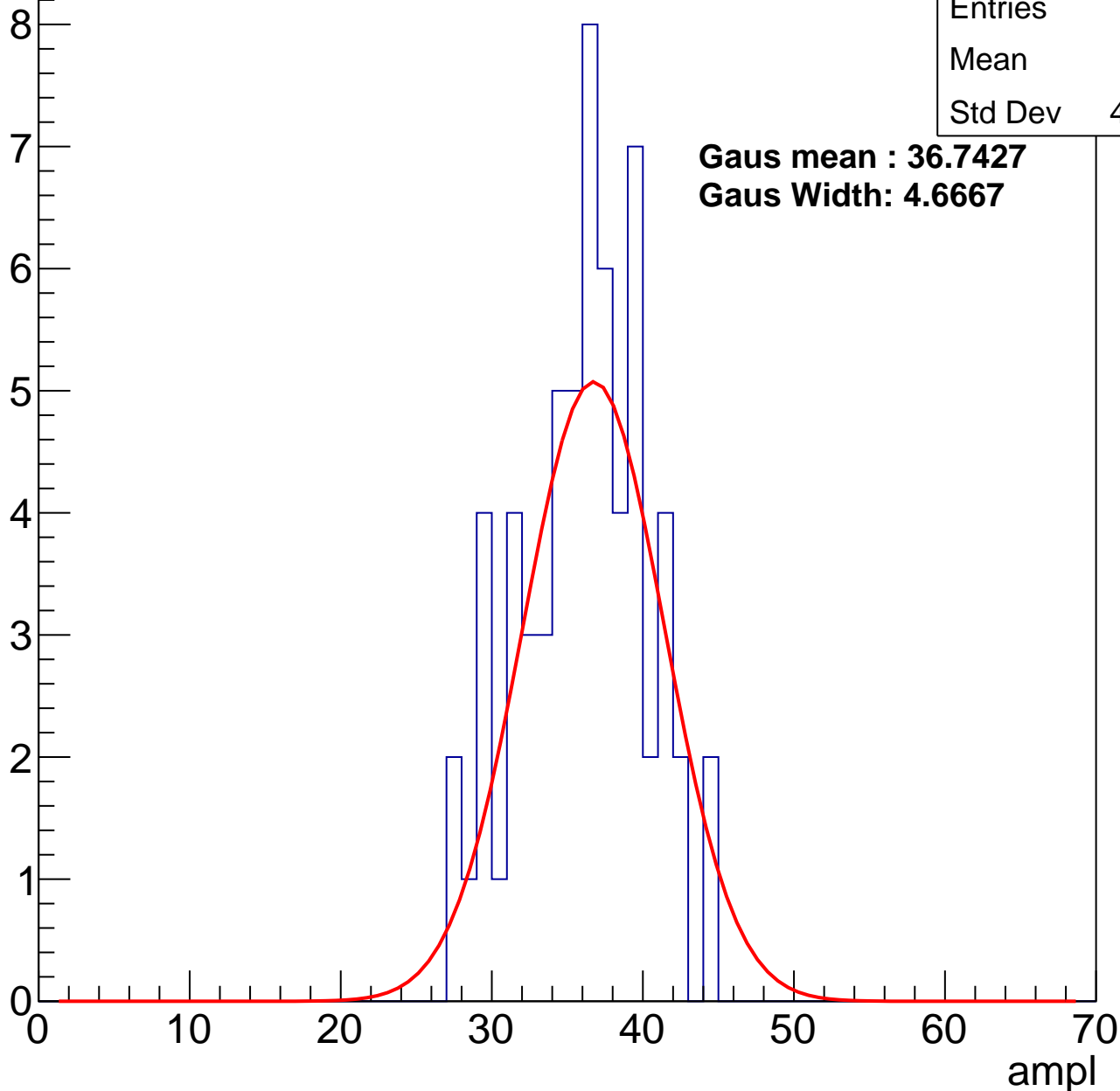
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.6
Std Dev	4.112

Gaus mean : 36.7427

Gaus Width: 4.6667



B1L103S, U14-ch36, adc2

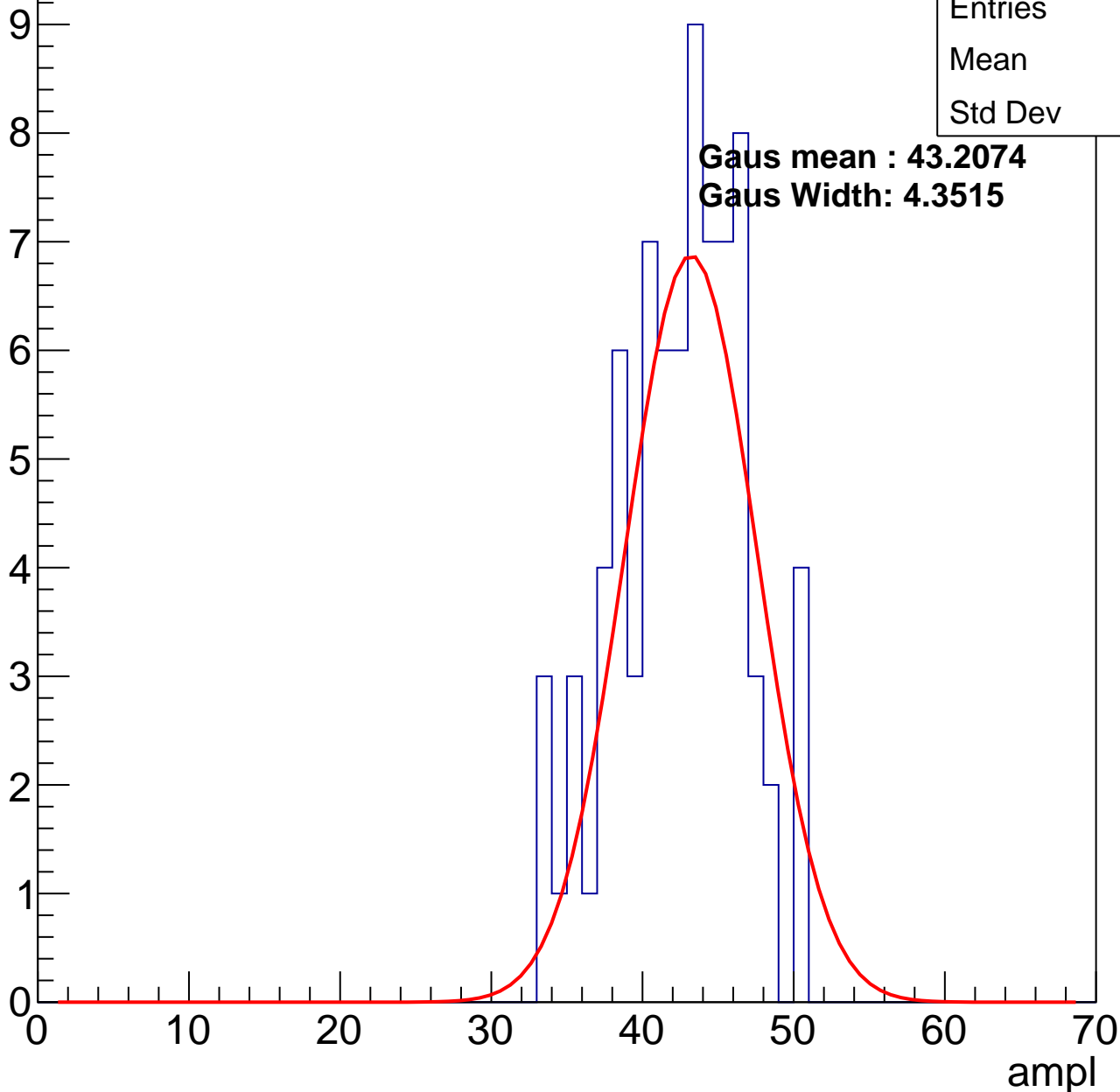
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	42
Std Dev	4.15

Gaus mean : 43.2074

Gaus Width: 4.3515

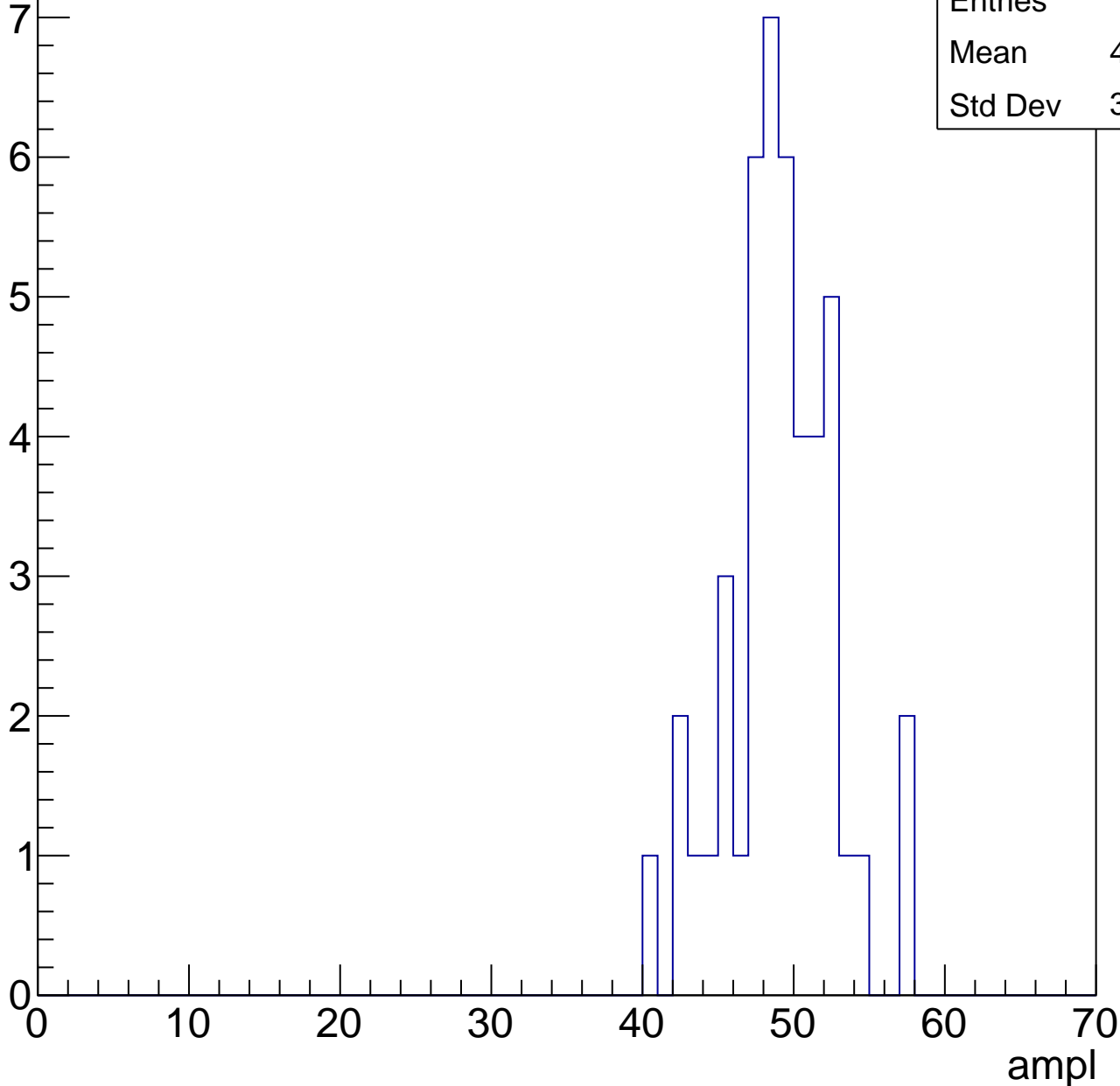


B1L103S, U14-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	48.64
Std Dev	3.516

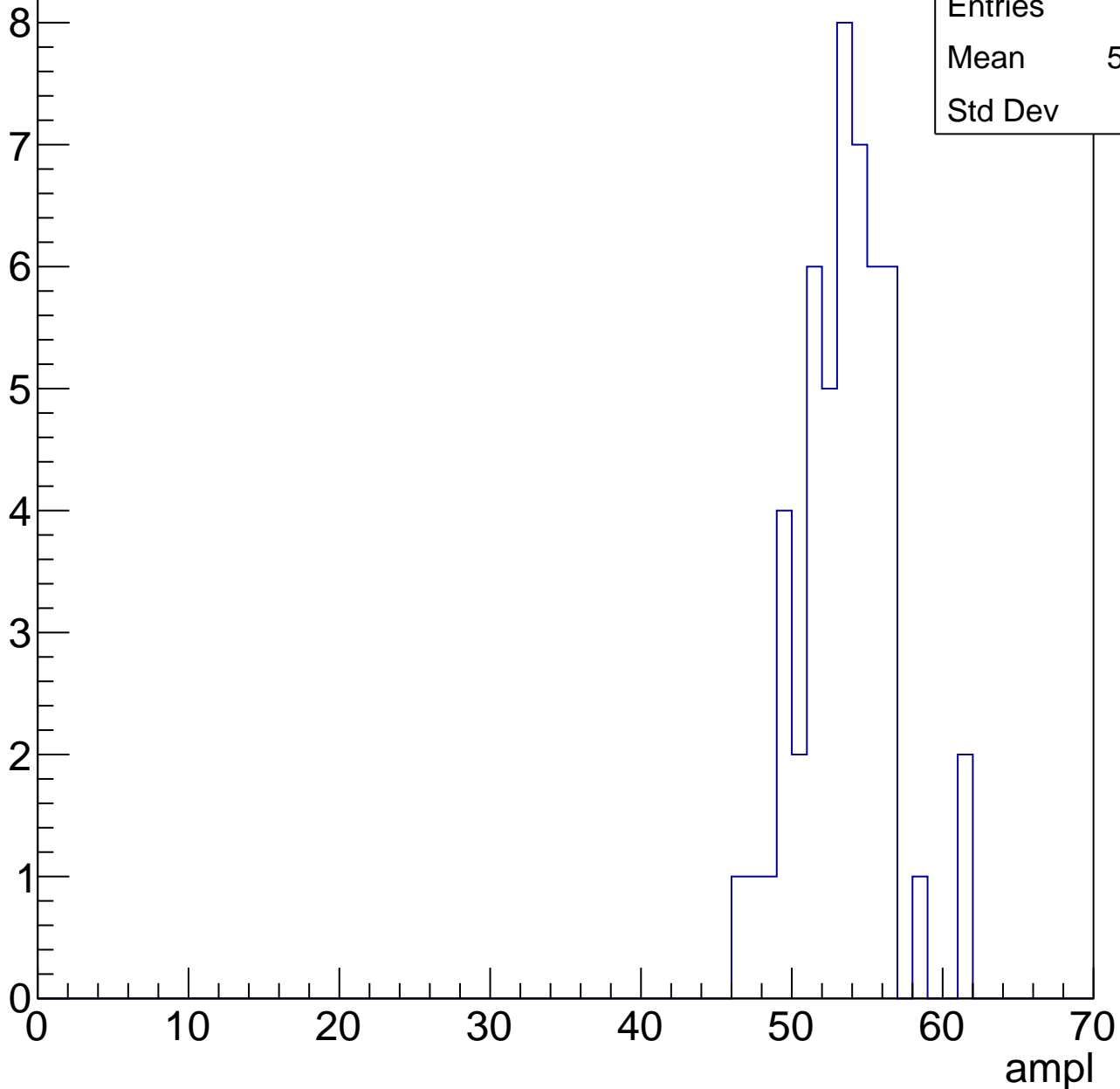


B1L103S, U14-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

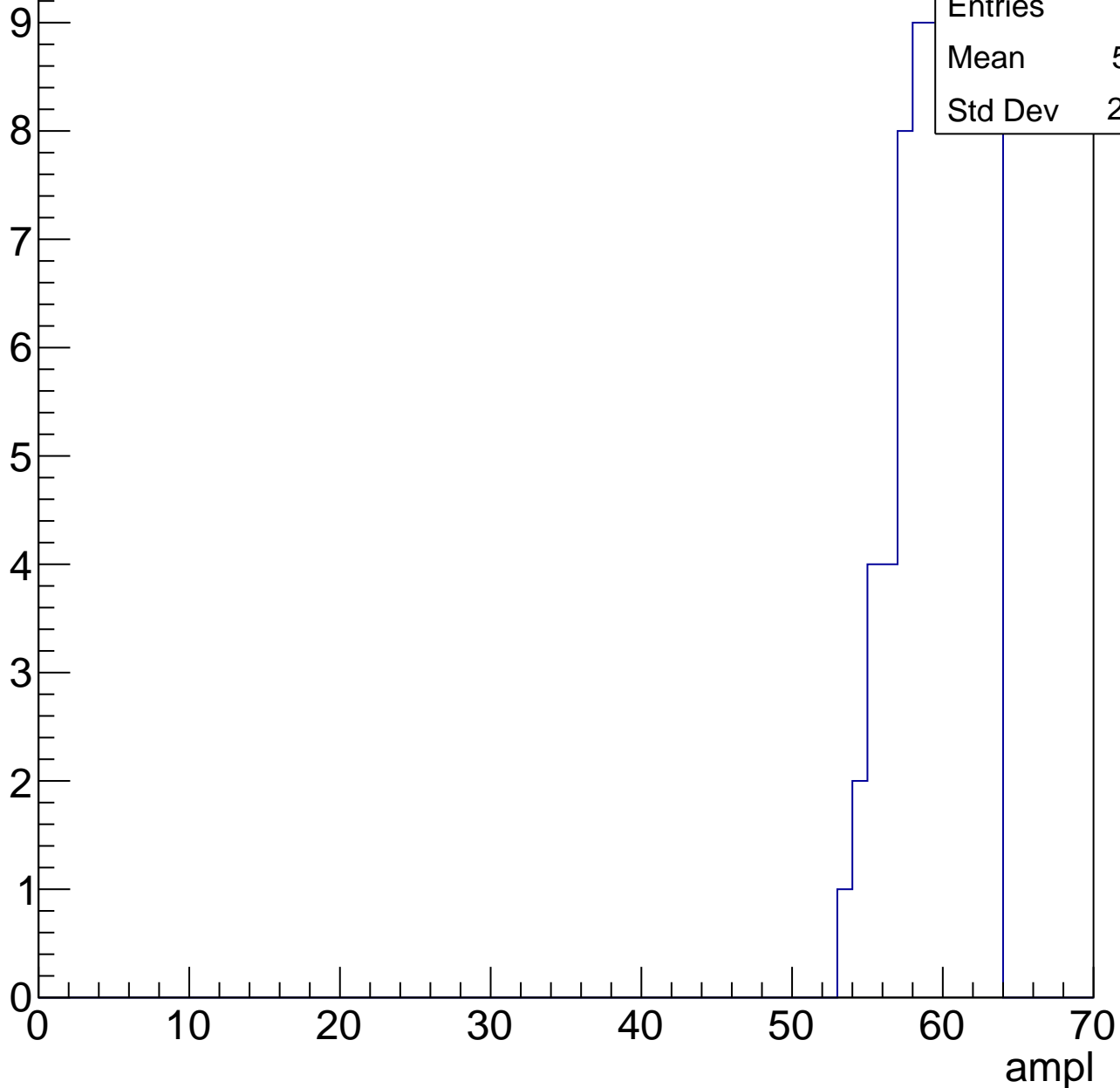
Entries	50
Mean	53.02
Std Dev	3.03



B1L103S, U14-ch36, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

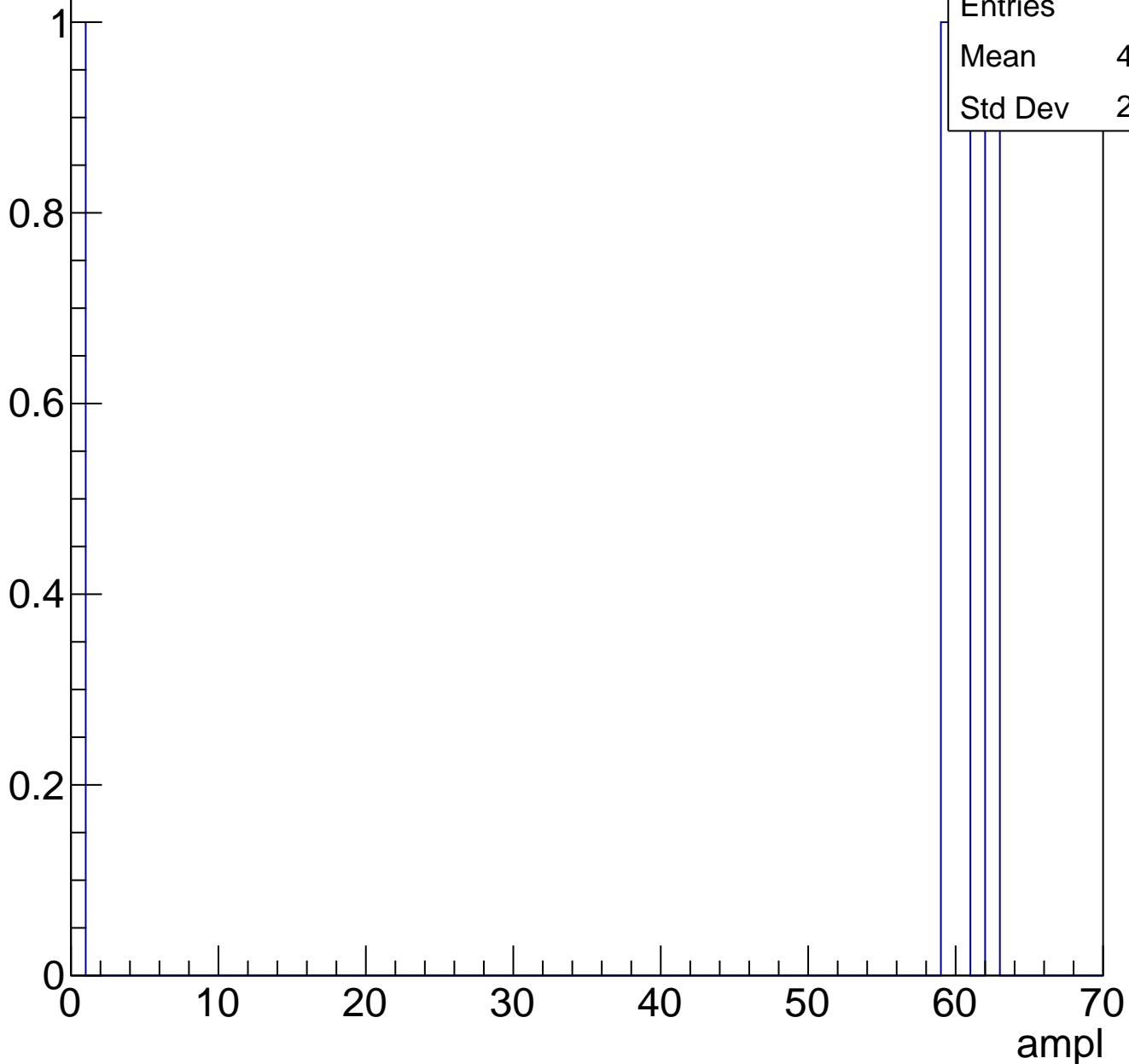


Entries	71
Mean	59.21
Std Dev	2.583

B1L103S, U14-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch37, adc0

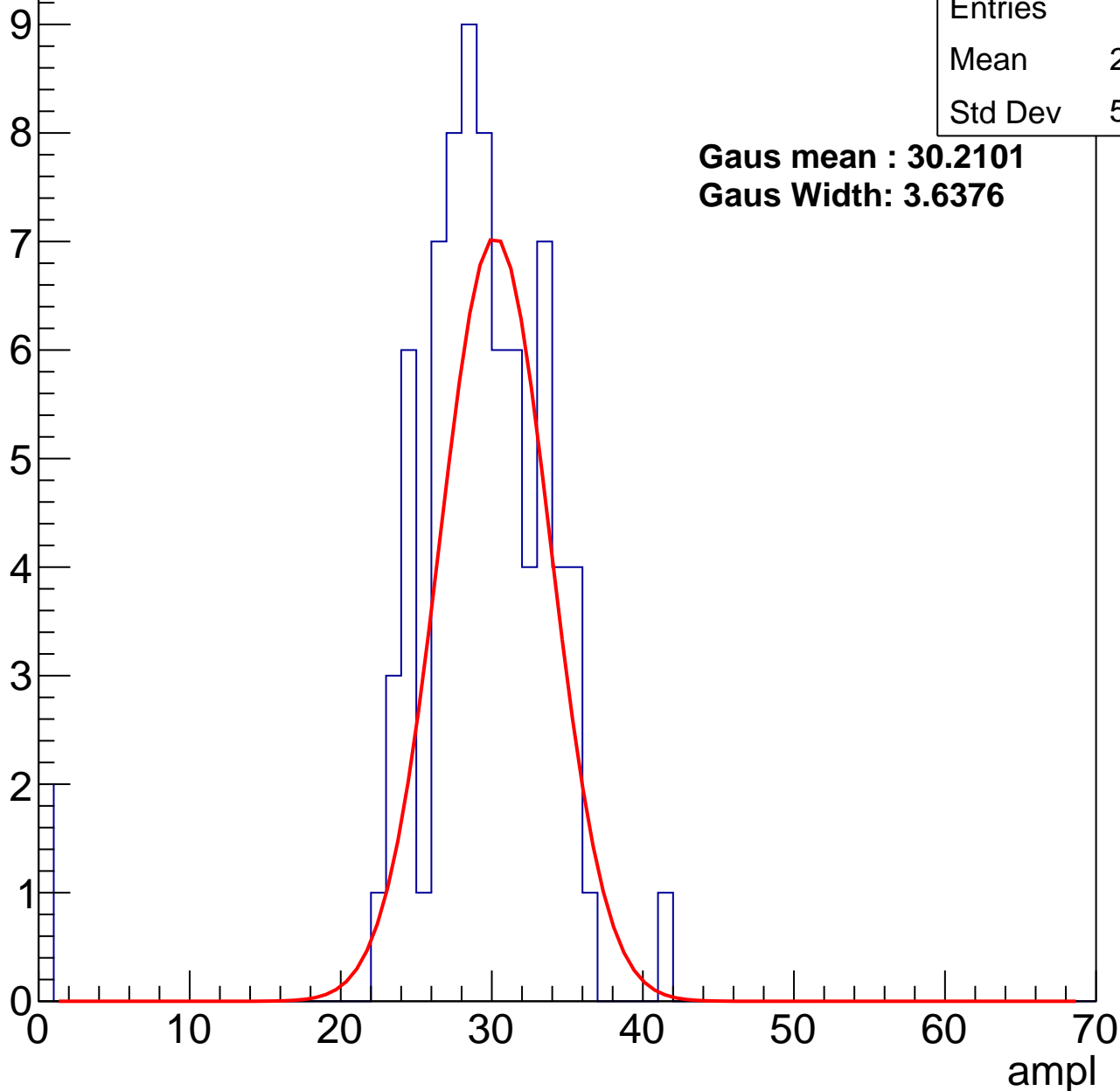
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	28.46
Std Dev	5.885

Gaus mean : 30.2101

Gaus Width: 3.6376



B1L103S, U14-ch37, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	36.72
Std Dev	3.546

Gaus mean : 37.1444

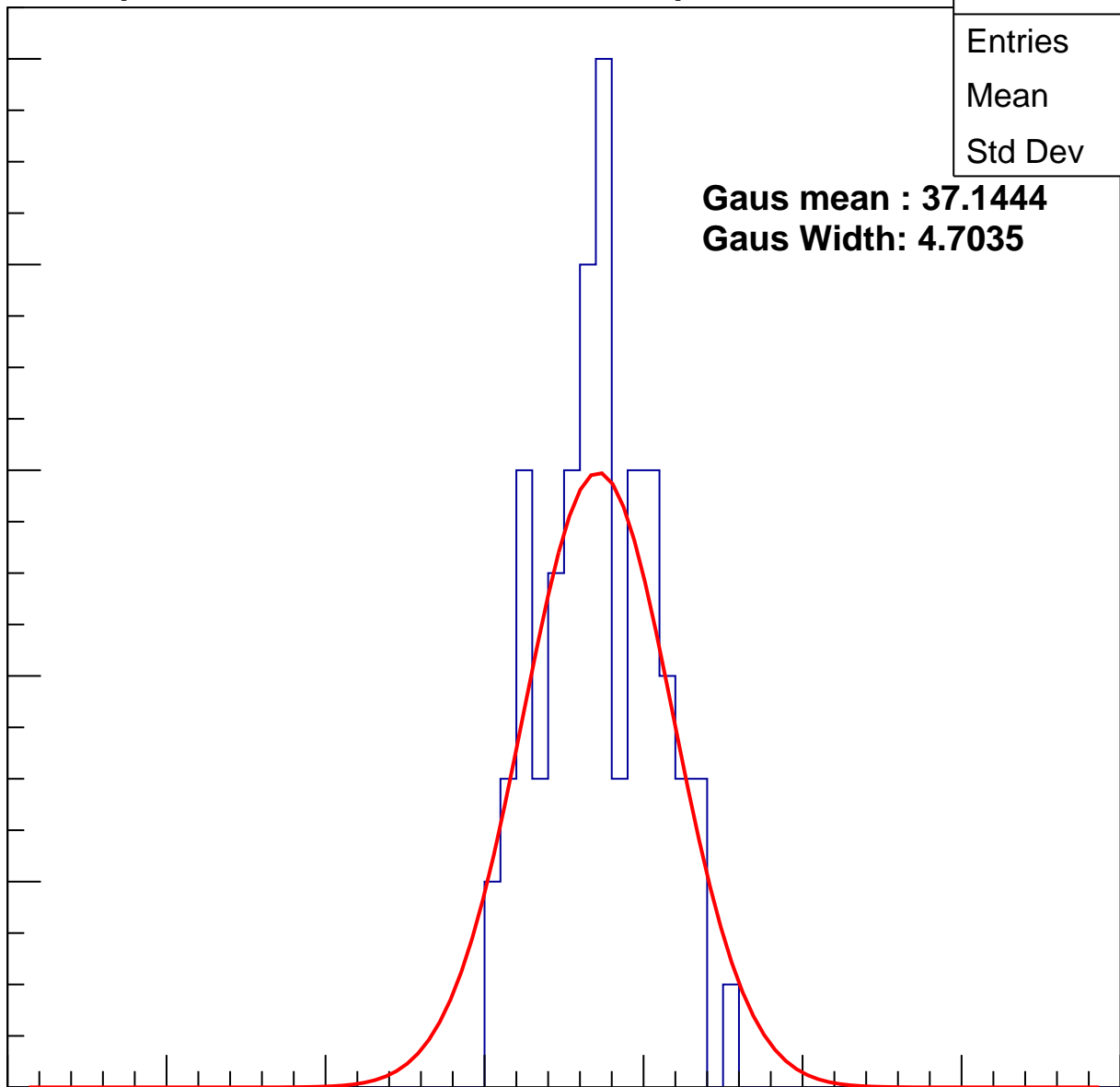
Gaus Width: 4.7035

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

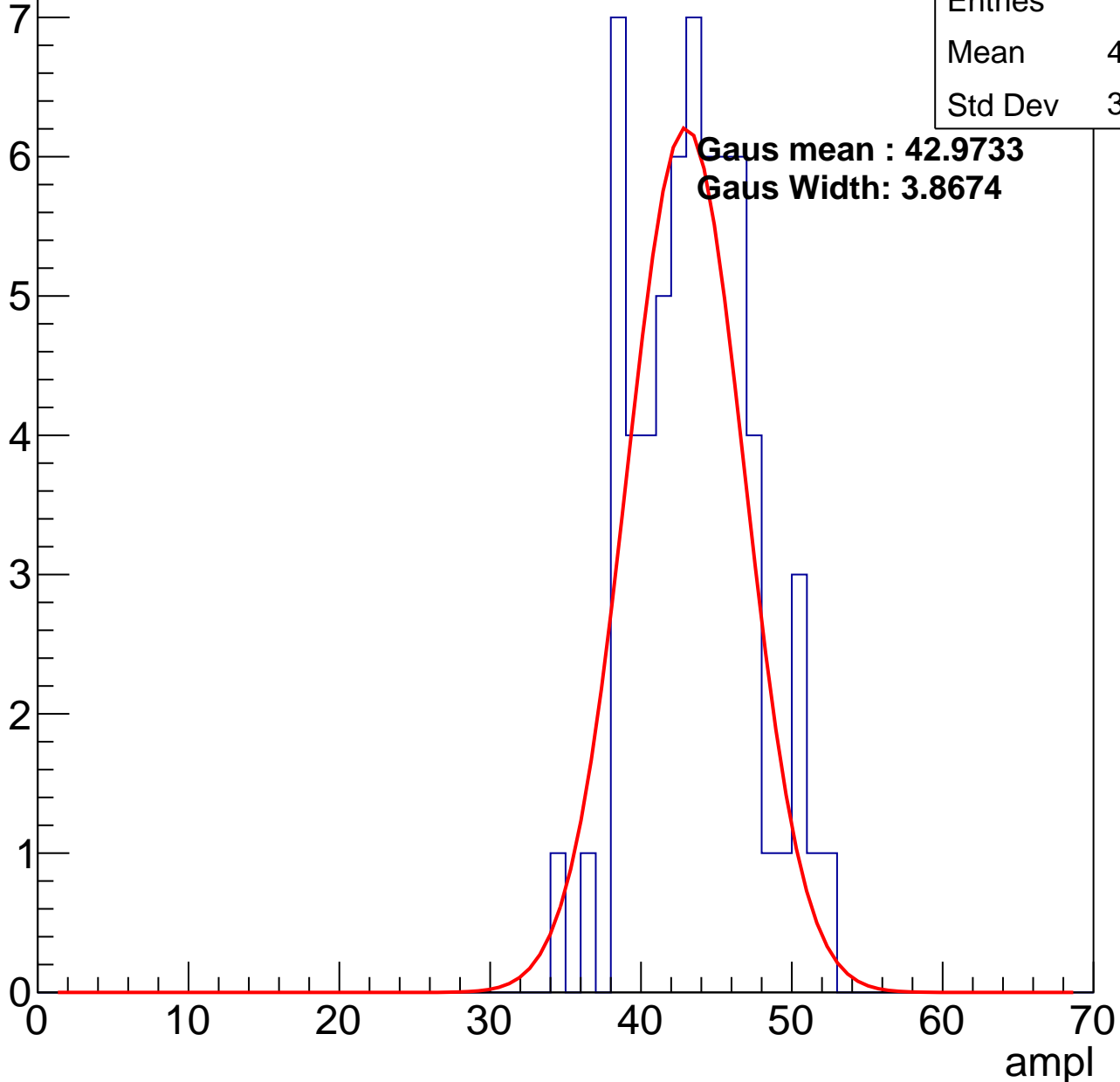


B1L103S, U14-ch37, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.09
Std Dev	3.815

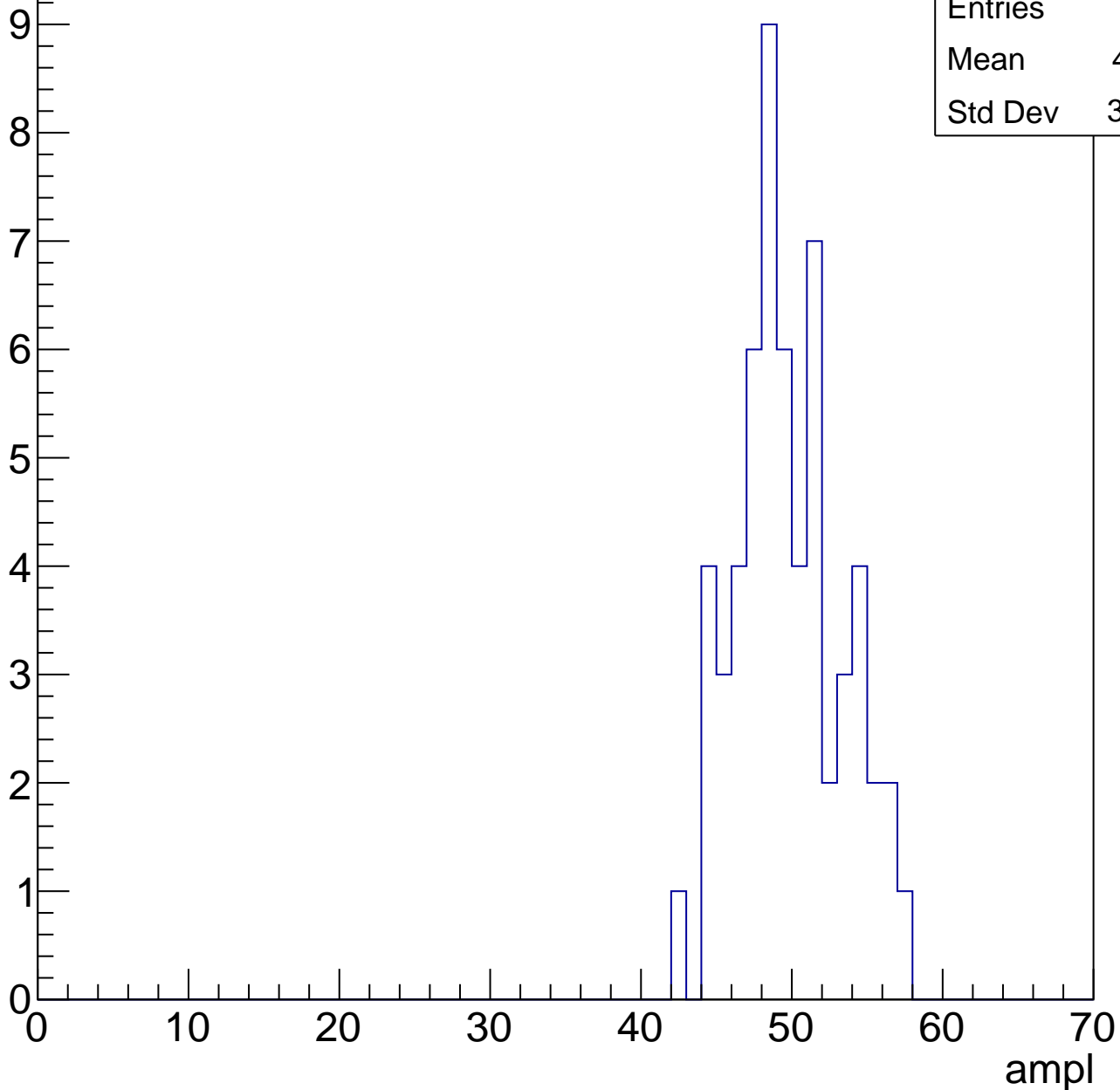


B1L103S, U14-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.31
Std Dev	3.455

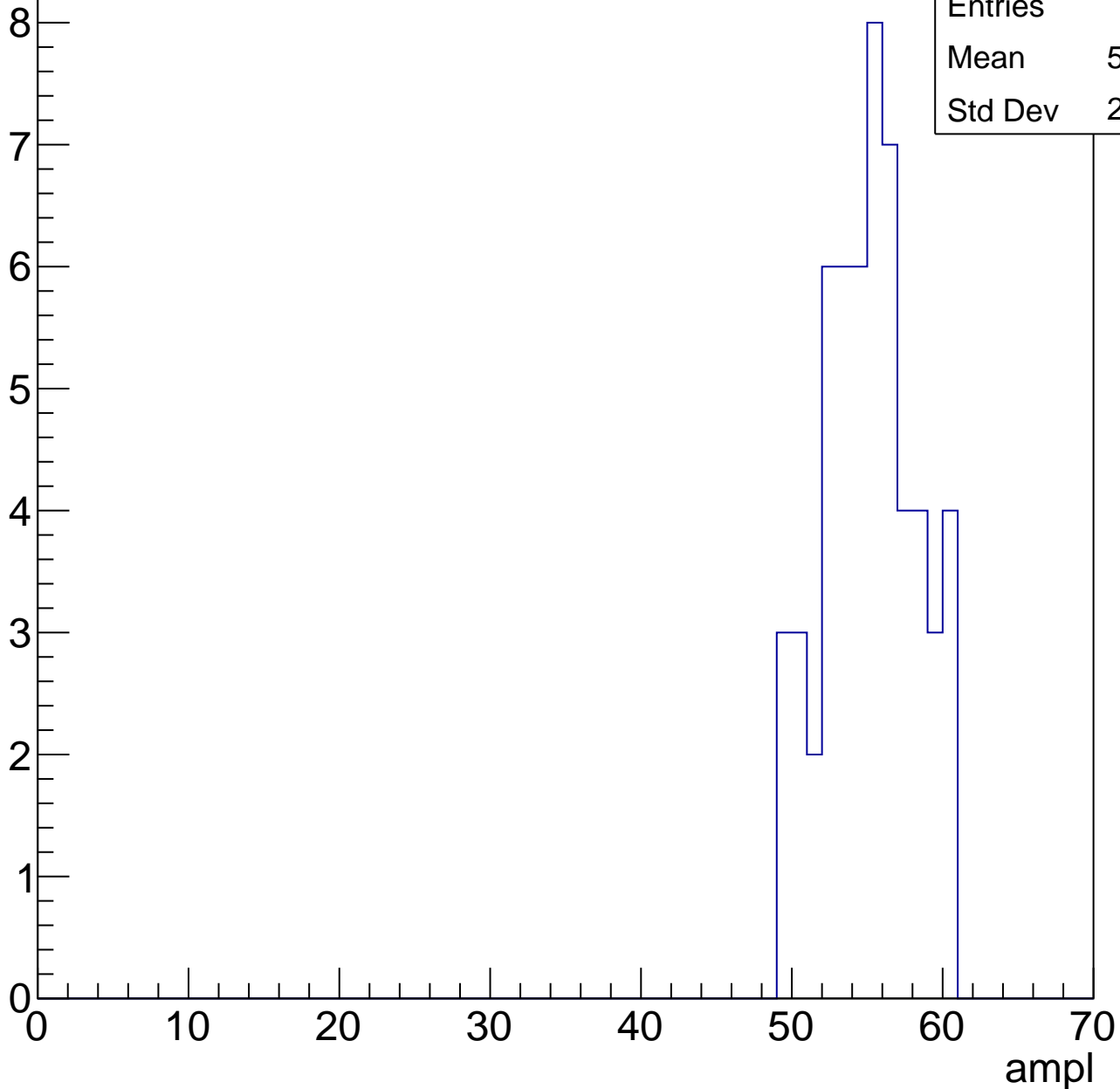


B1L103S, U14-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	54.68
Std Dev	2.989

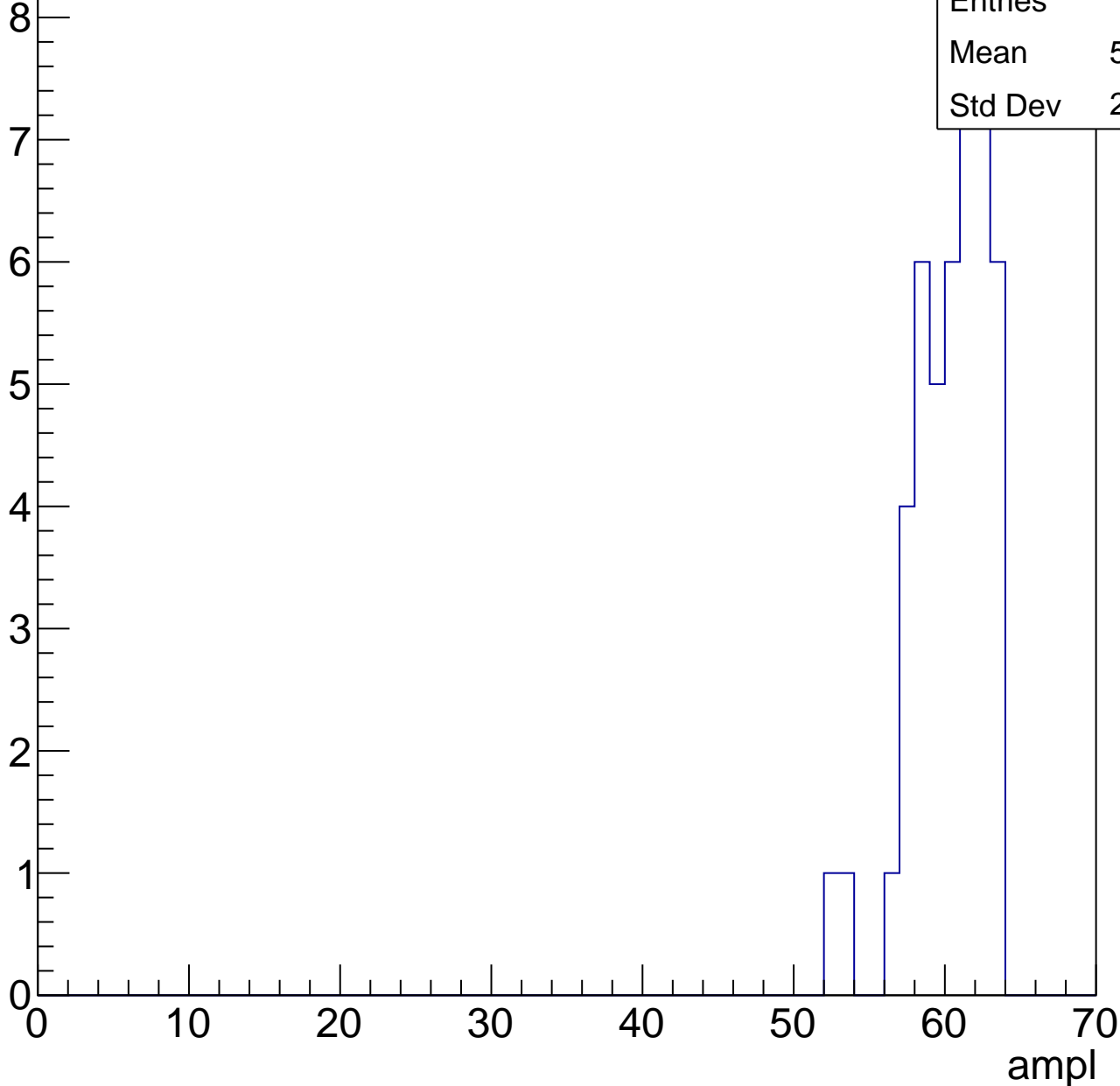


B1L103S, U14-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

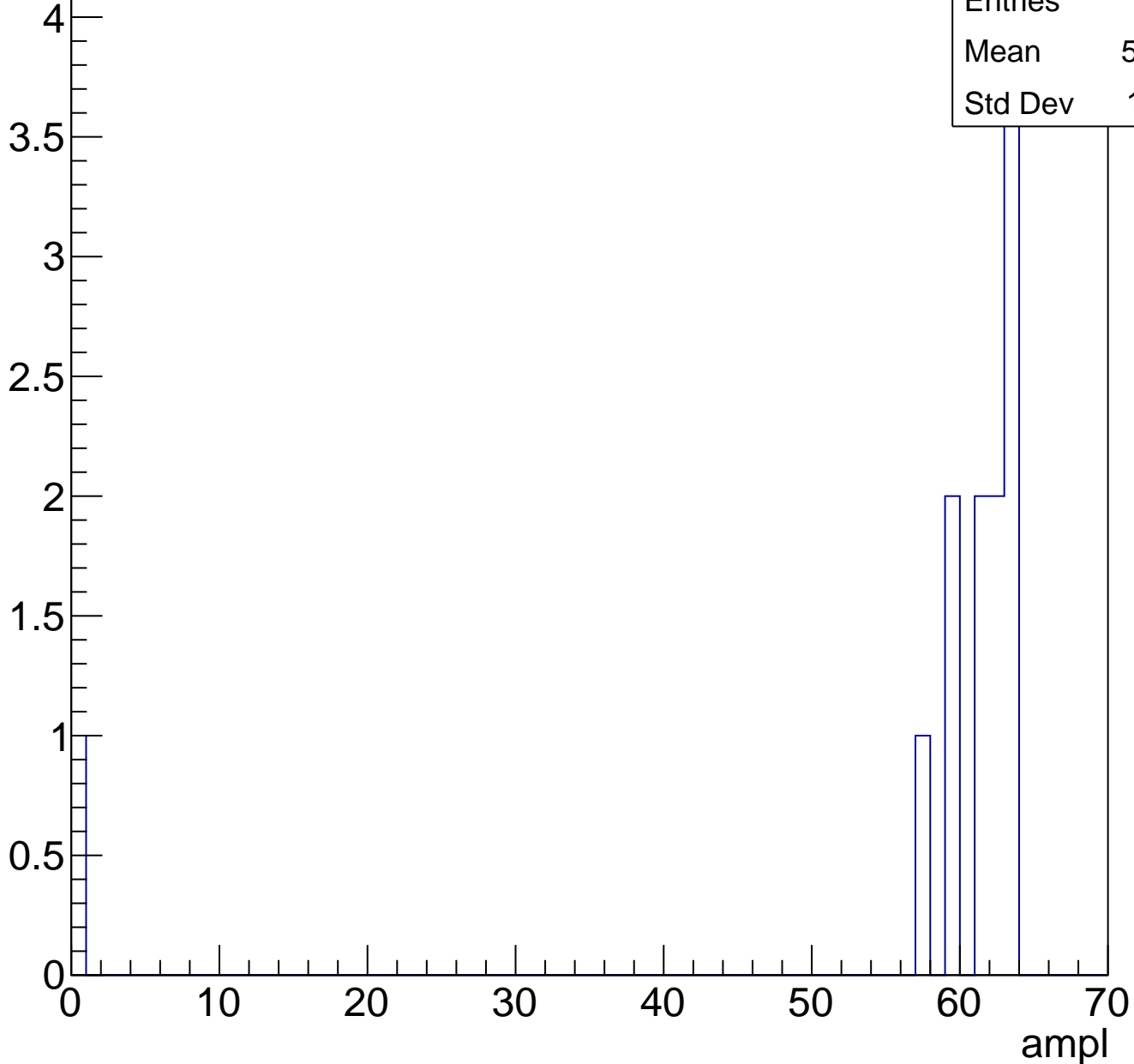
Entries	46
Mean	59.87
Std Dev	2.499



B1L103S, U14-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch38, adc0

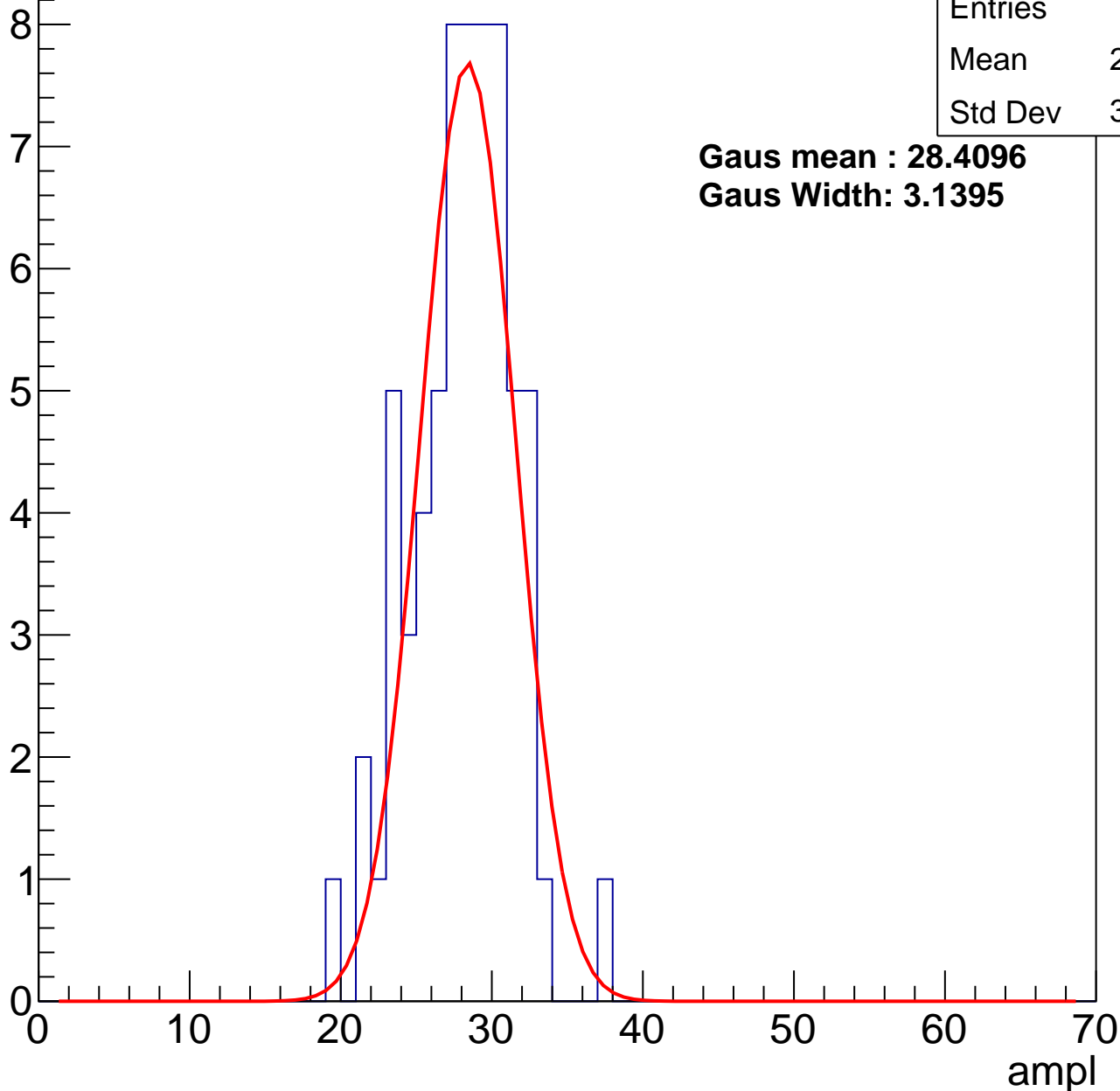
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	27.65
Std Dev	3.316

Gaus mean : 28.4096

Gaus Width: 3.1395



B1L103S, U14-ch38, adc1

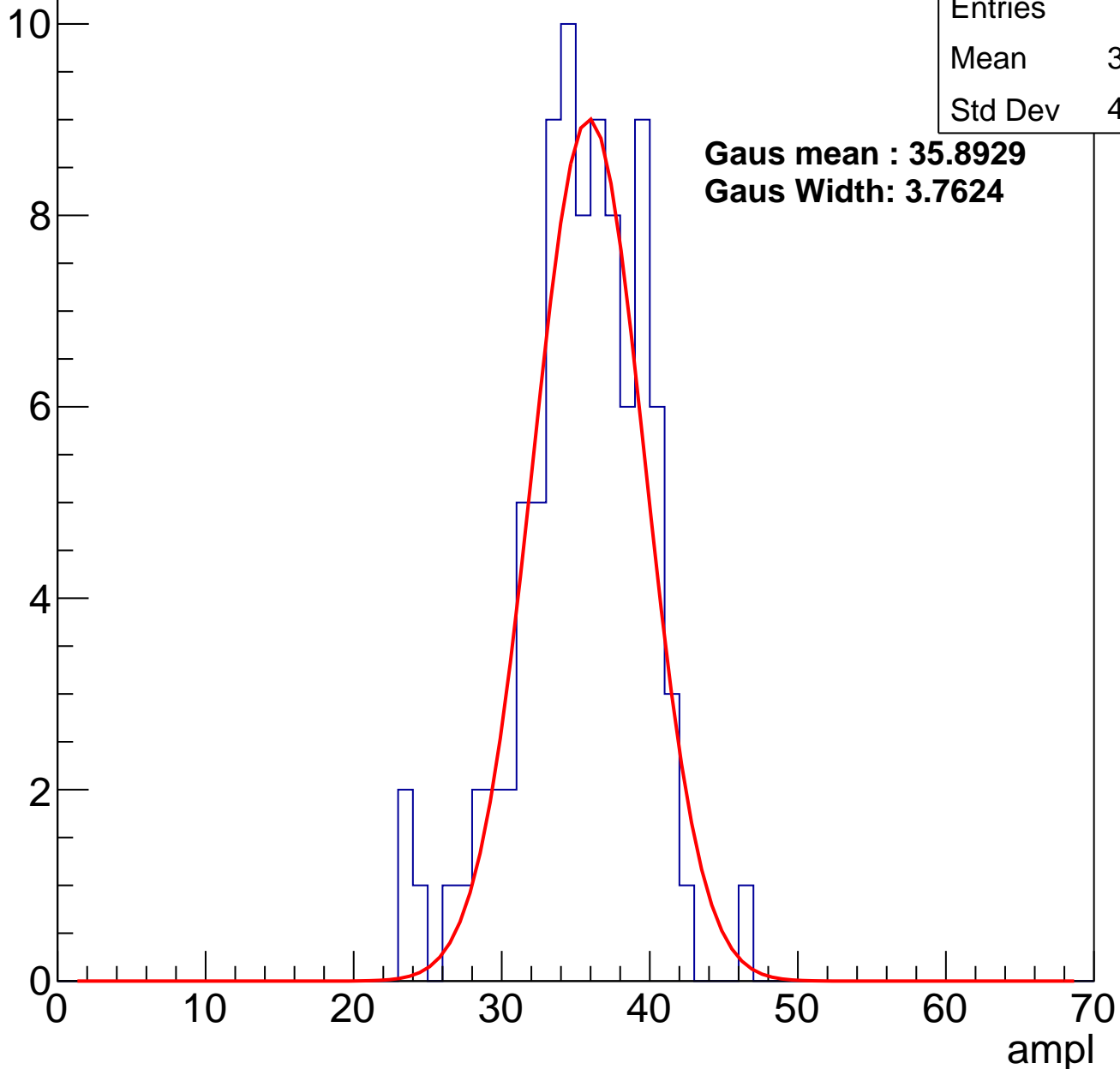
calib_packv5_042523_0143.root, FC#7, port C2

Entries	91
Mean	34.93
Std Dev	4.208

Gaus mean : 35.8929

Gaus Width: 3.7624

Entry



B1L103S, U14-ch38, adc2

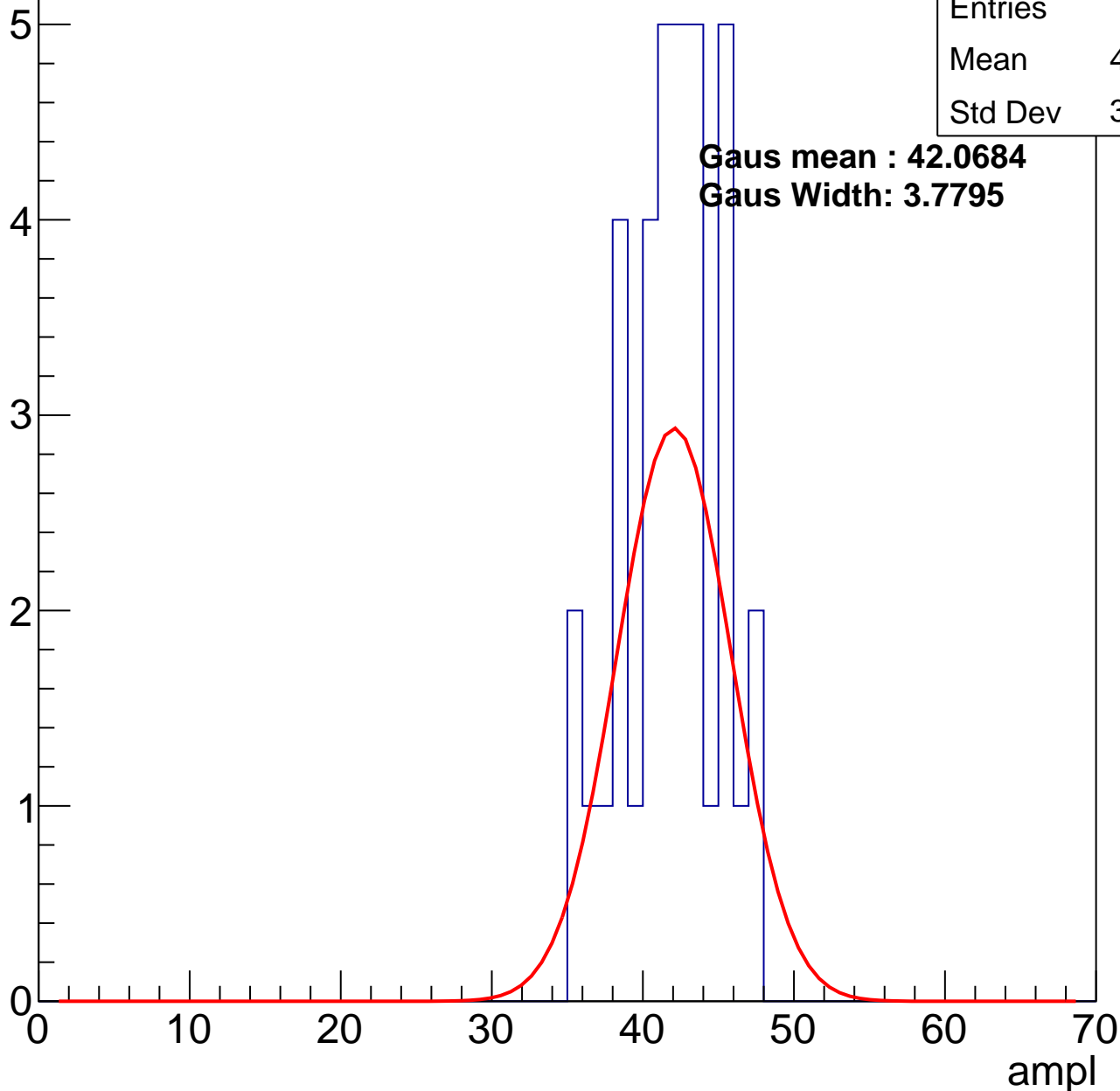
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	41.43
Std Dev	3.124

Gaus mean : 42.0684

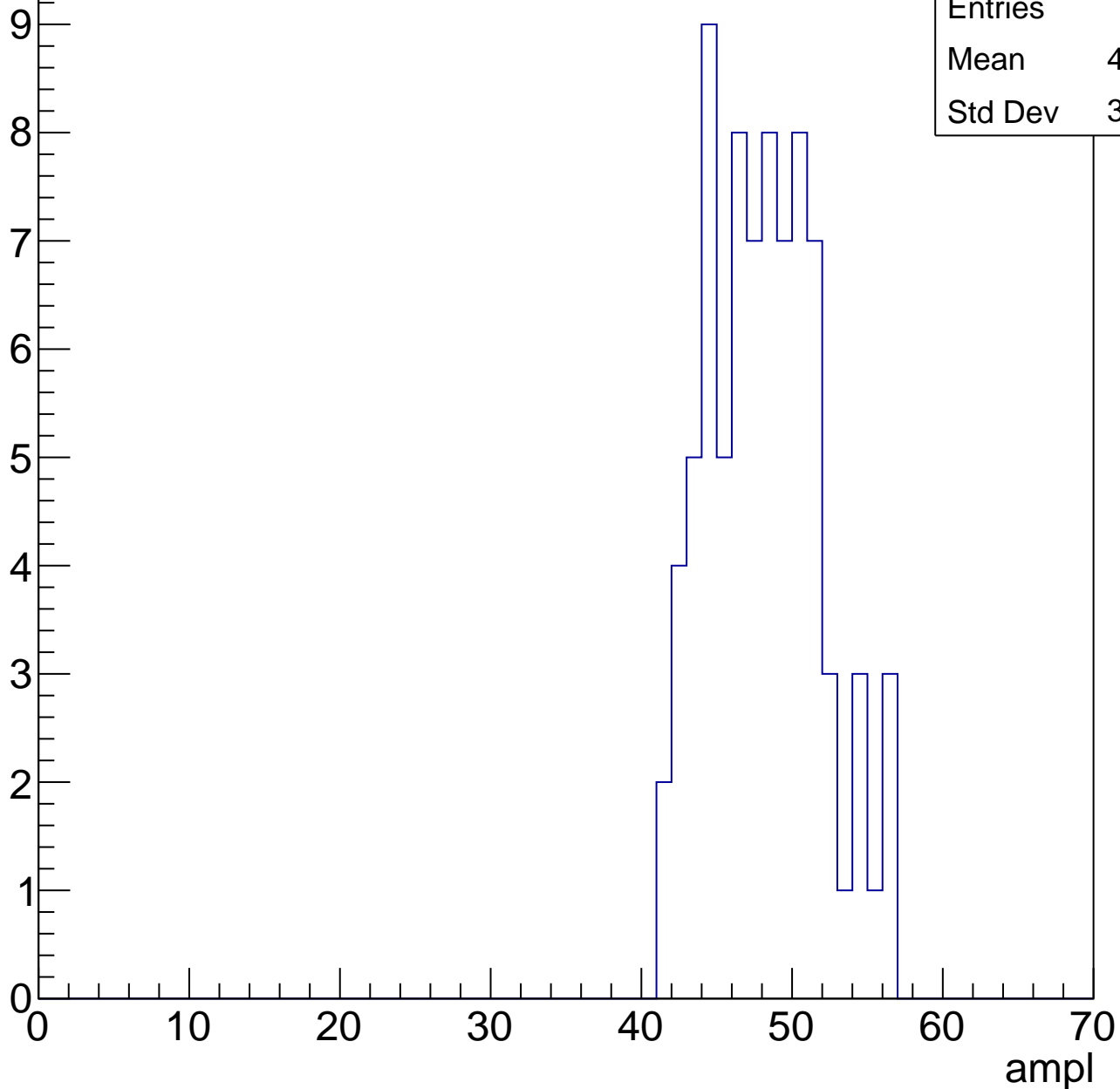
Gaus Width: 3.7795



B1L103S, U14-ch38, adc3

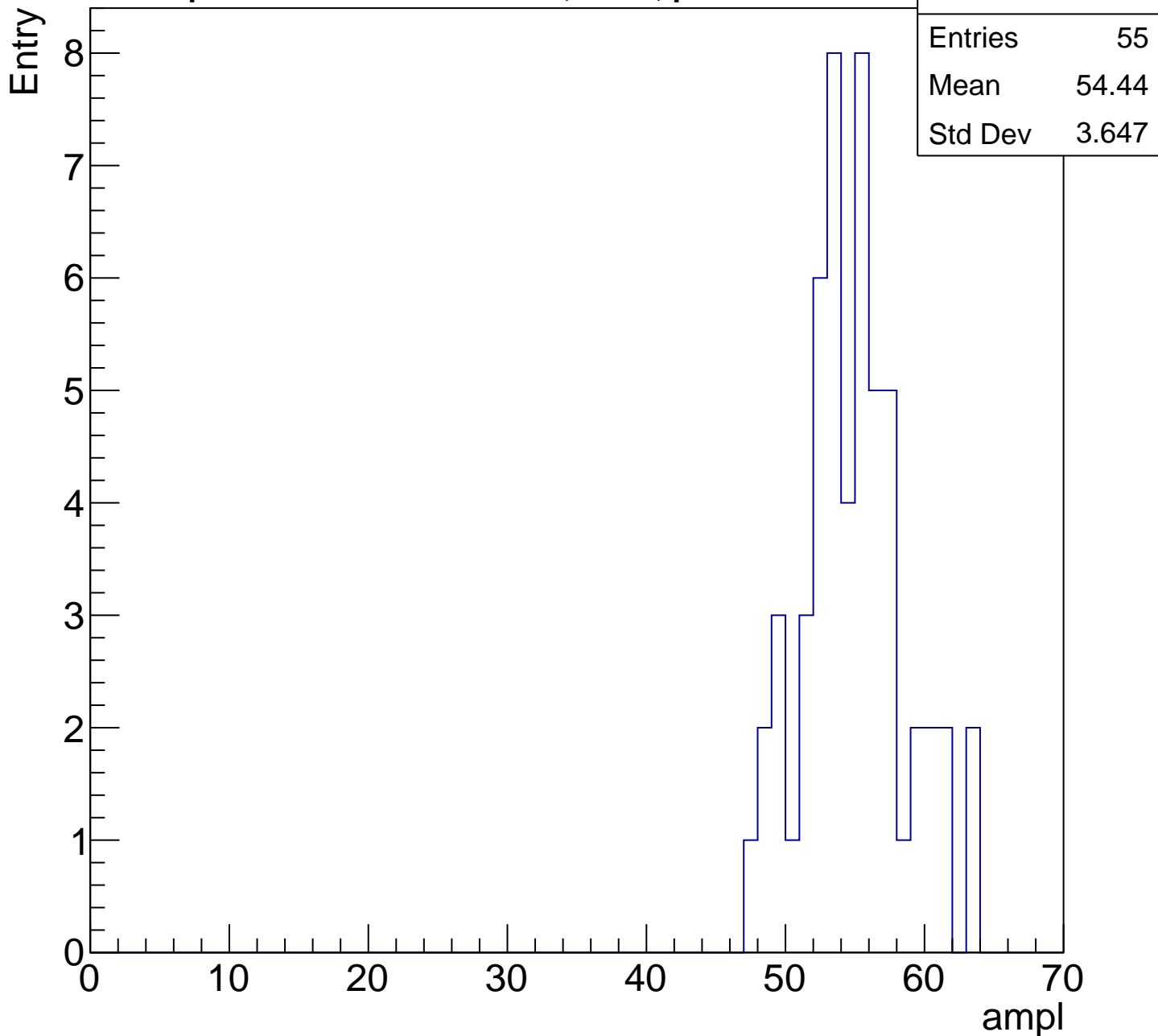
calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

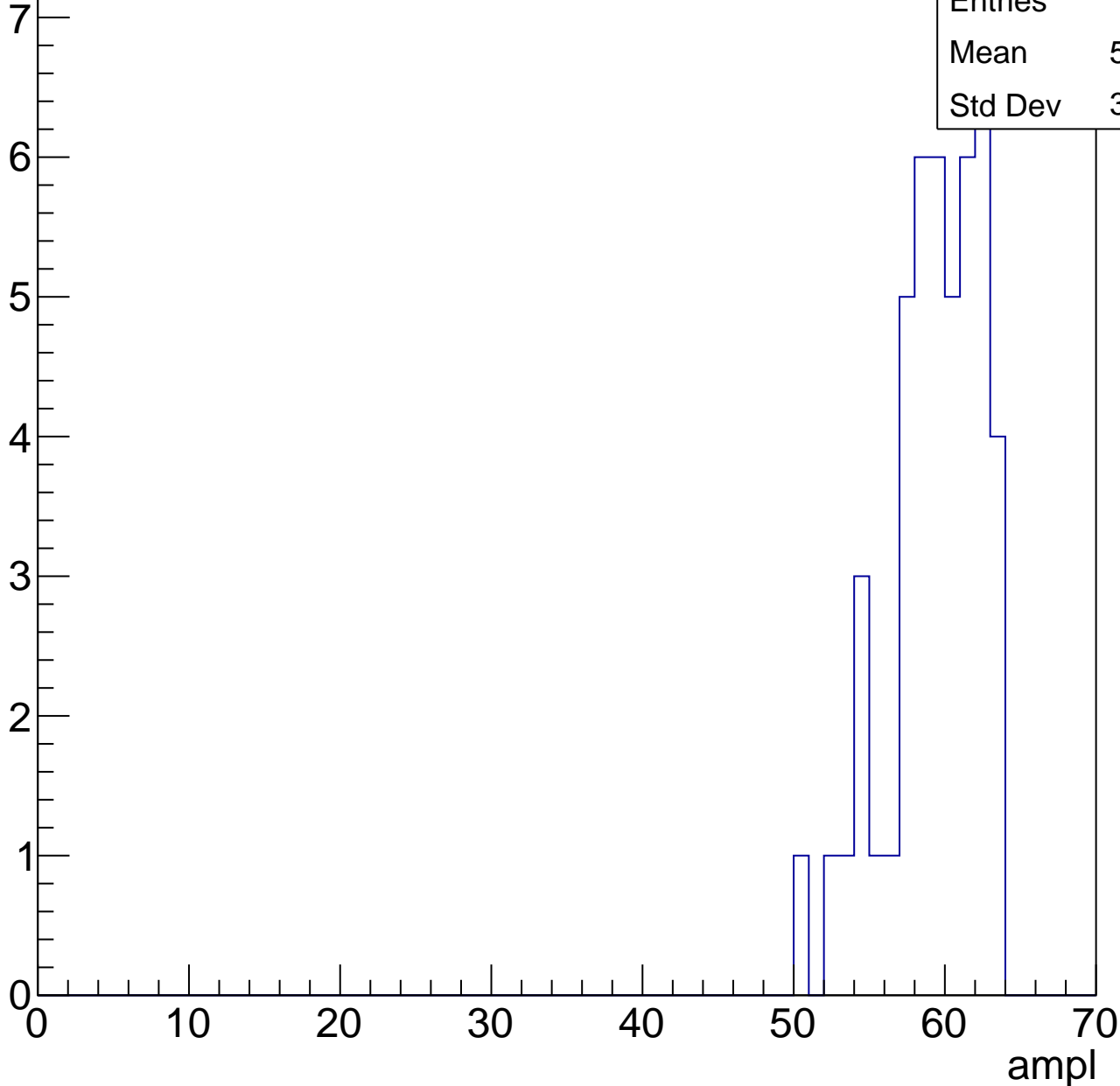


B1L103S, U14-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

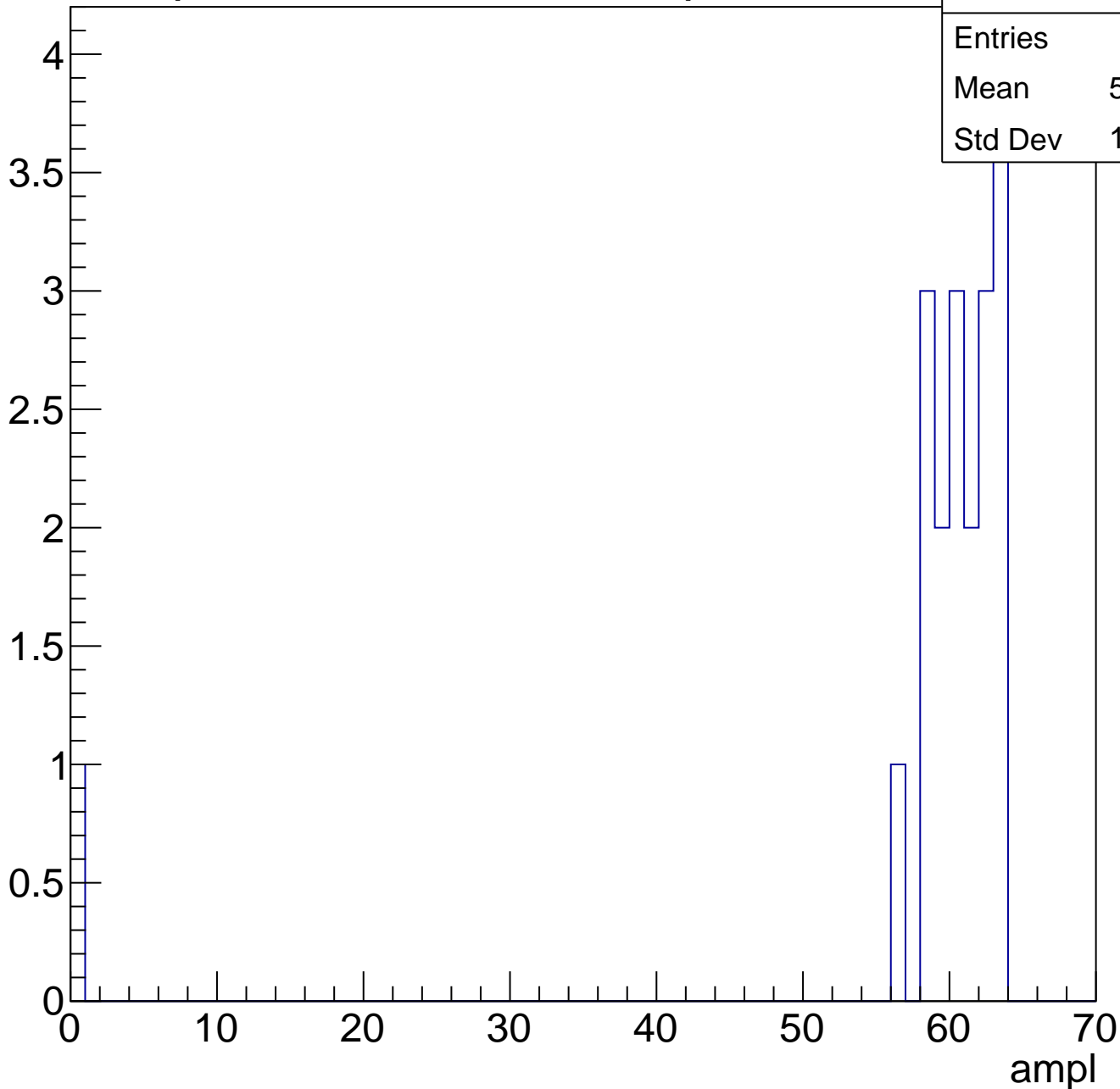
Entries	47
Mean	58.87
Std Dev	3.085



B1L103S, U14-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	9
Std Dev	9

B1L103S, U14-ch39, adc0

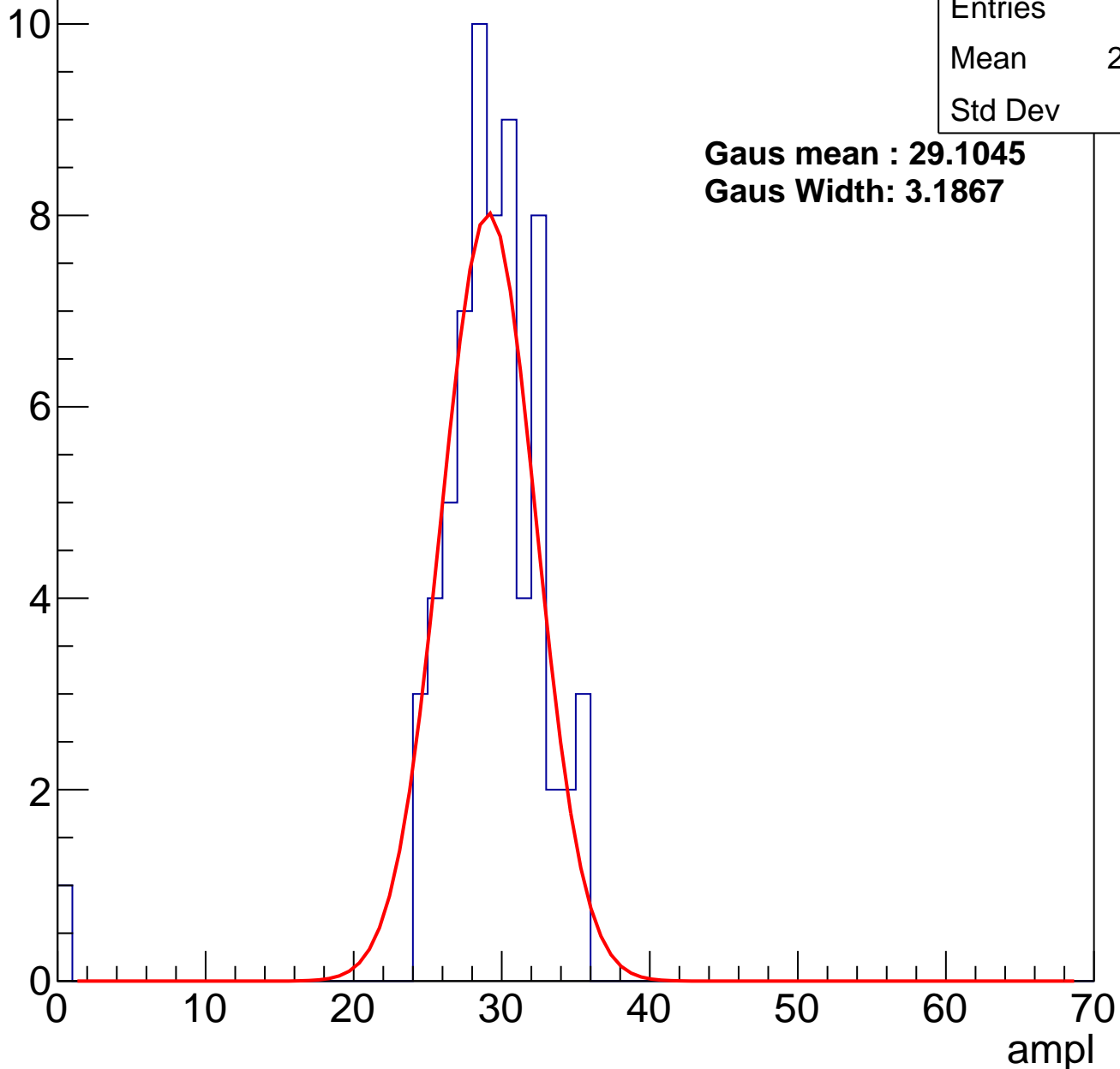
calib_packv5_042523_0143.root, FC#7, port C2

Entries	66
Mean	28.67
Std Dev	4.51

Gaus mean : 29.1045

Gaus Width: 3.1867

Entry



B1L103S, U14-ch39, adc1

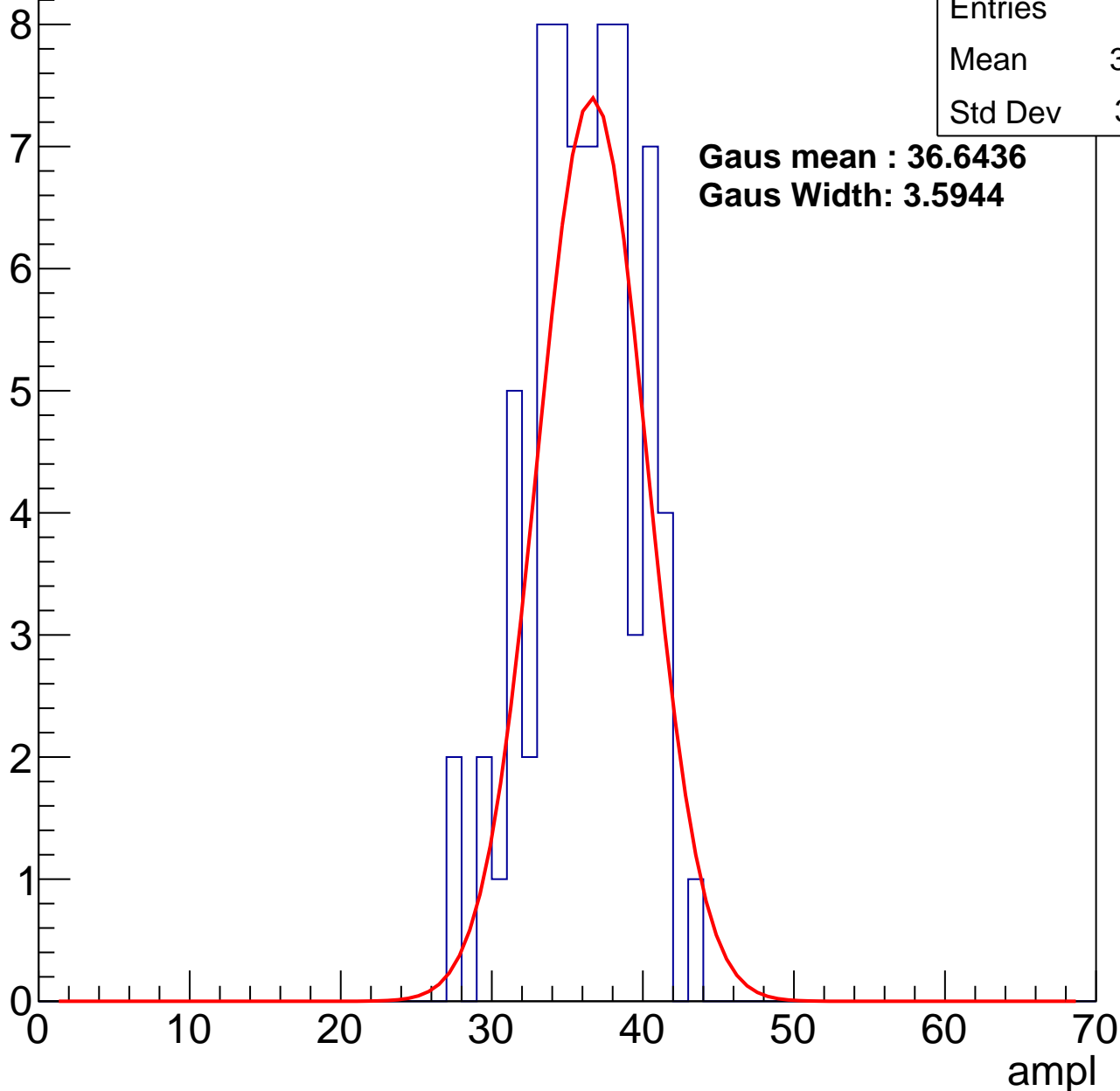
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.59
Std Dev	3.471

Gaus mean : 36.6436

Gaus Width: 3.5944



B1L103S, U14-ch39, adc2

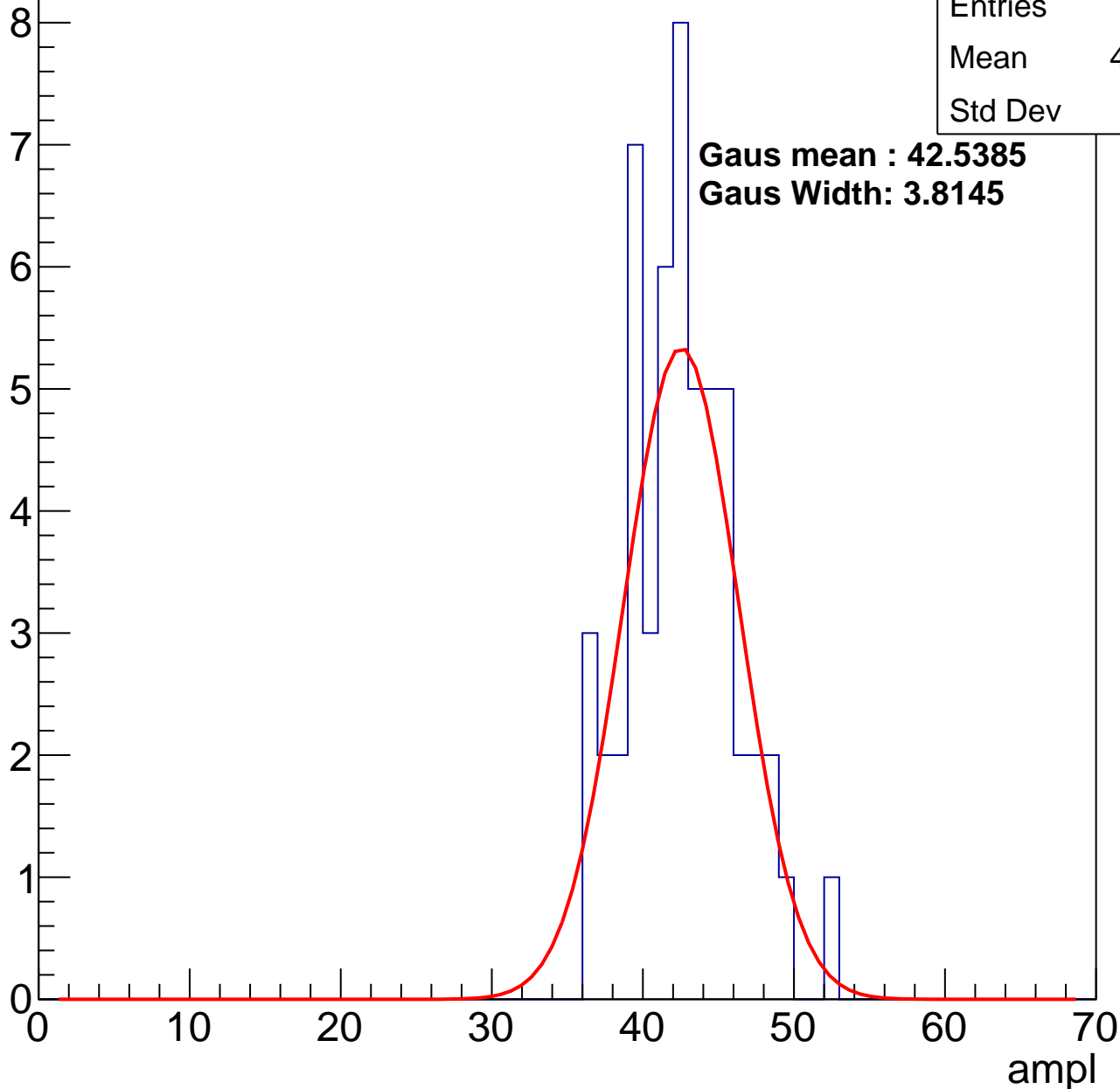
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	42.15
Std Dev	3.45

Gaus mean : 42.5385

Gaus Width: 3.8145

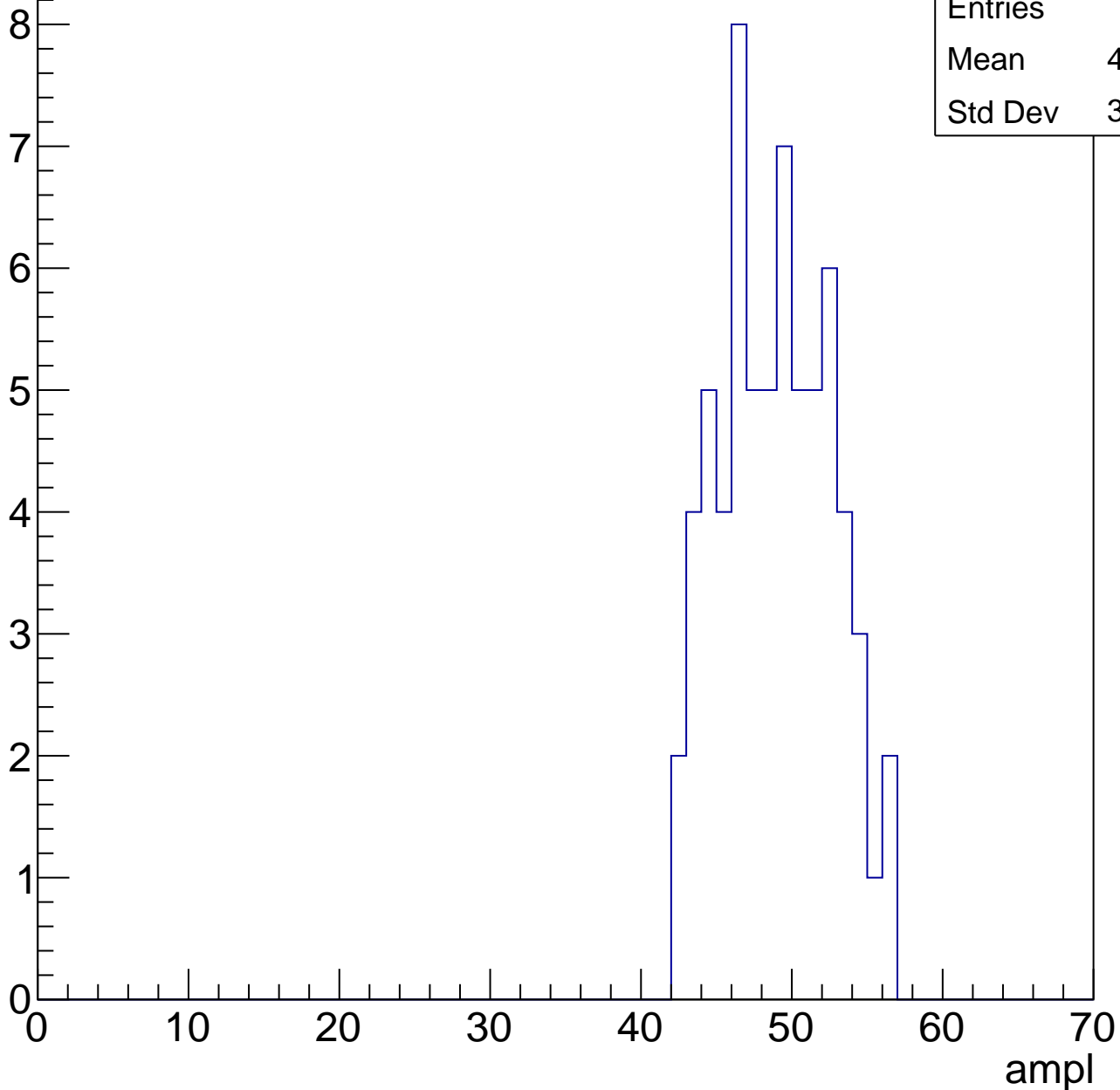


B1L103S, U14-ch39, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

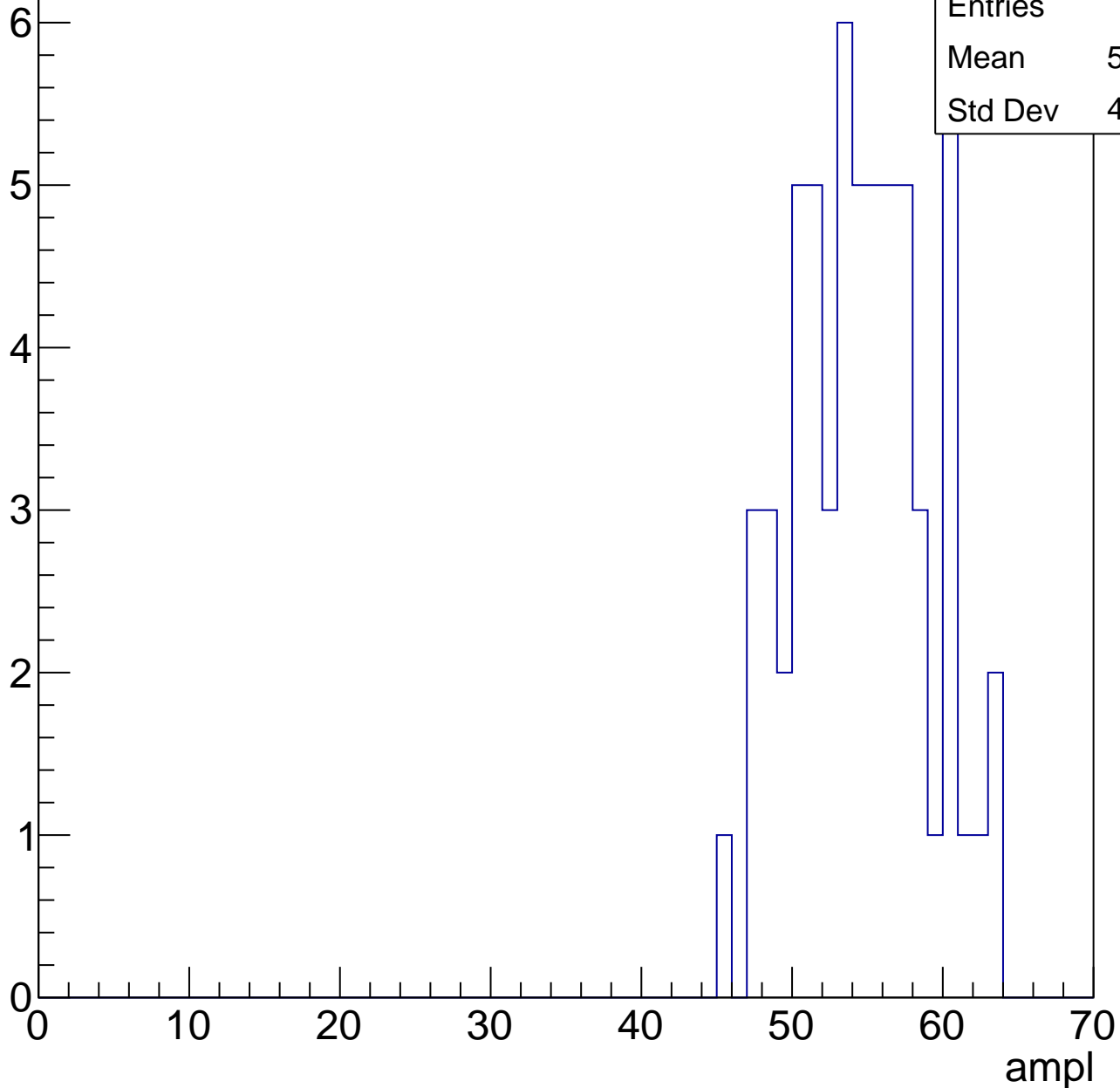
Entries	66
Mean	48.48
Std Dev	3.615



B1L103S, U14-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

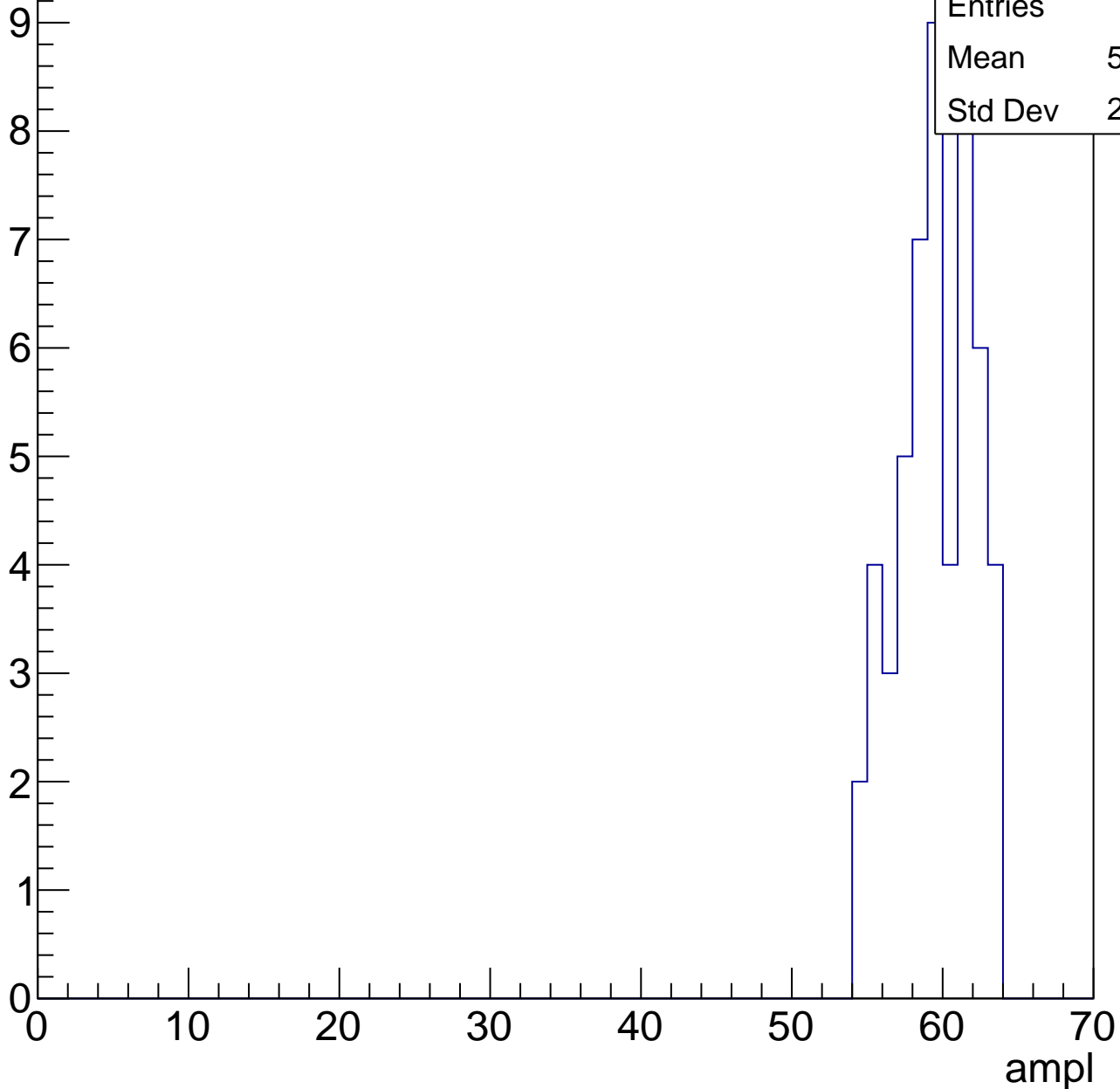


Entries	62
Mean	54.18
Std Dev	4.327

B1L103S, U14-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

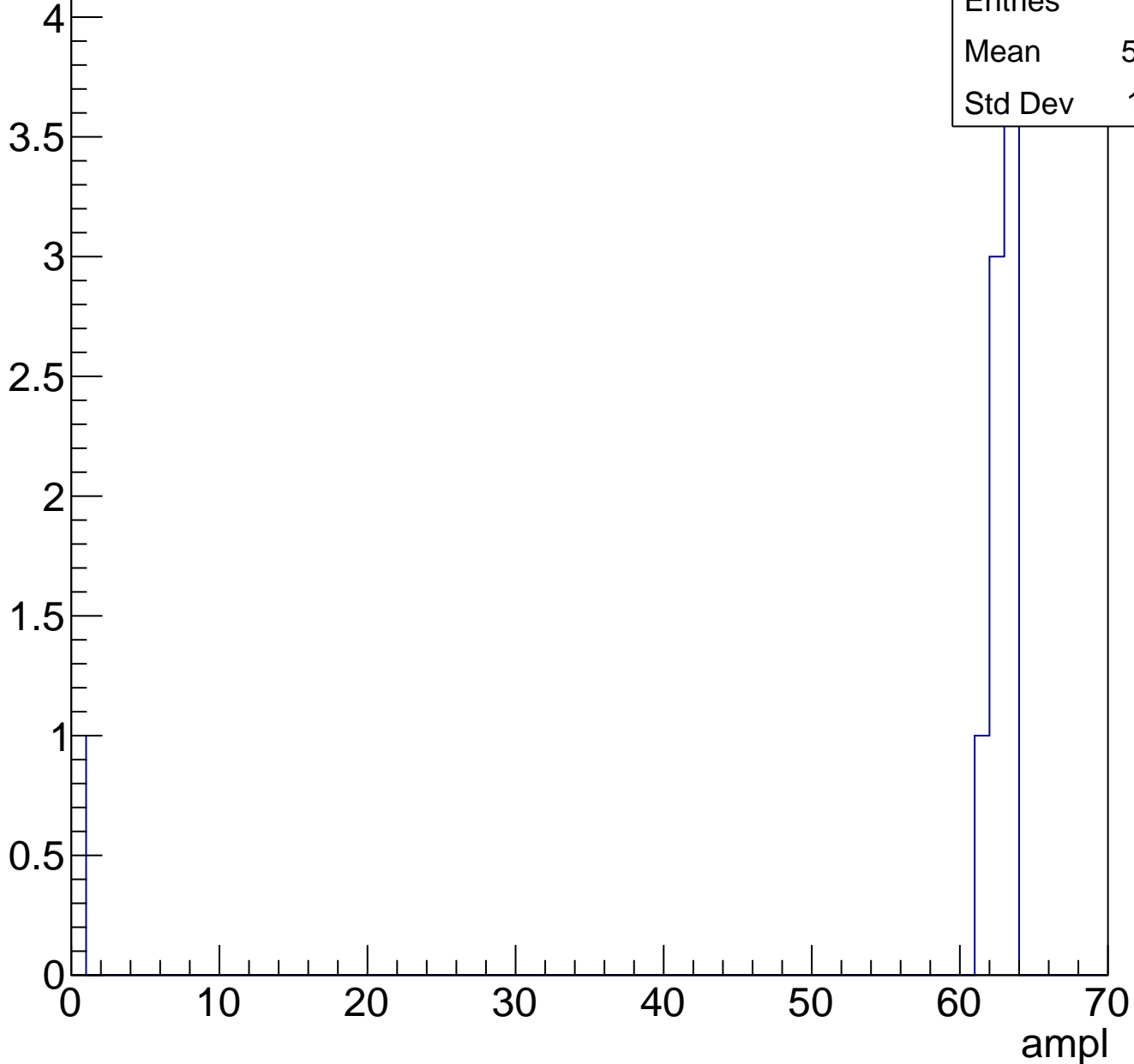
Entry



B1L103S, U14-ch39, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U14-ch40, adc0

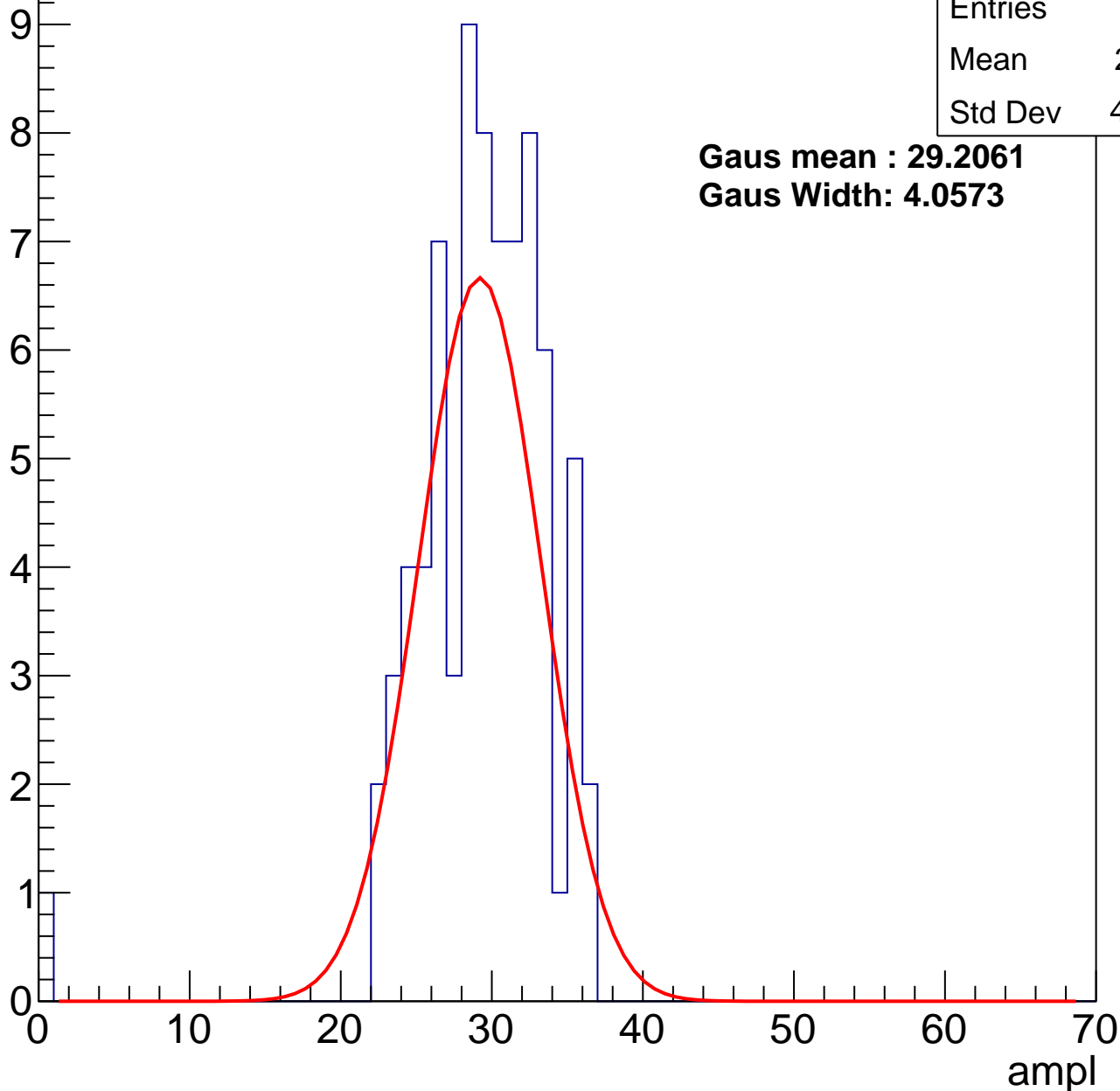
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	28.81
Std Dev	4.832

Gaus mean : 29.2061

Gaus Width: 4.0573



B1L103S, U14-ch40, adc1

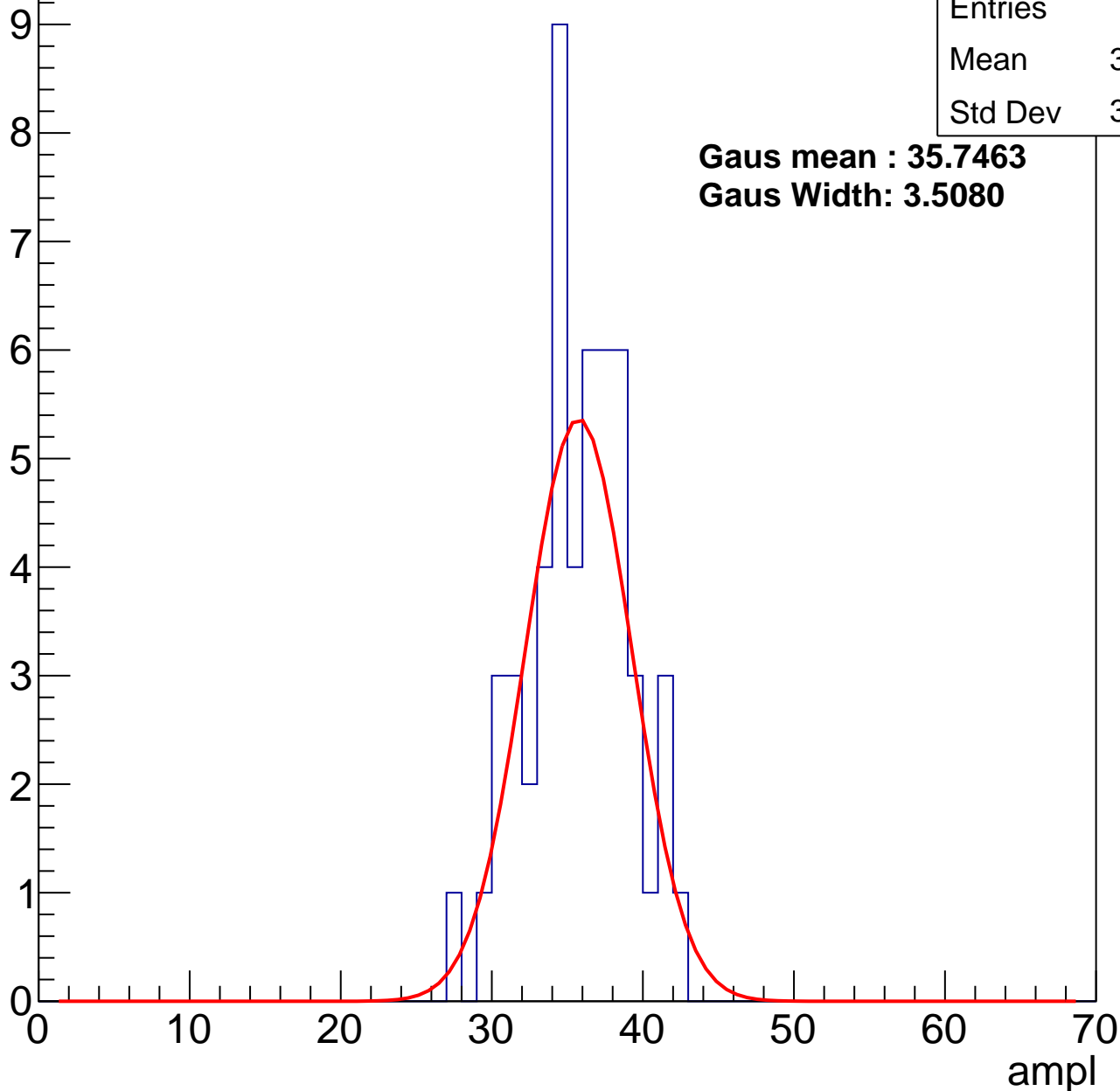
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	35.26
Std Dev	3.298

Gaus mean : 35.7463

Gaus Width: 3.5080



B1L103S, U14-ch40, adc2

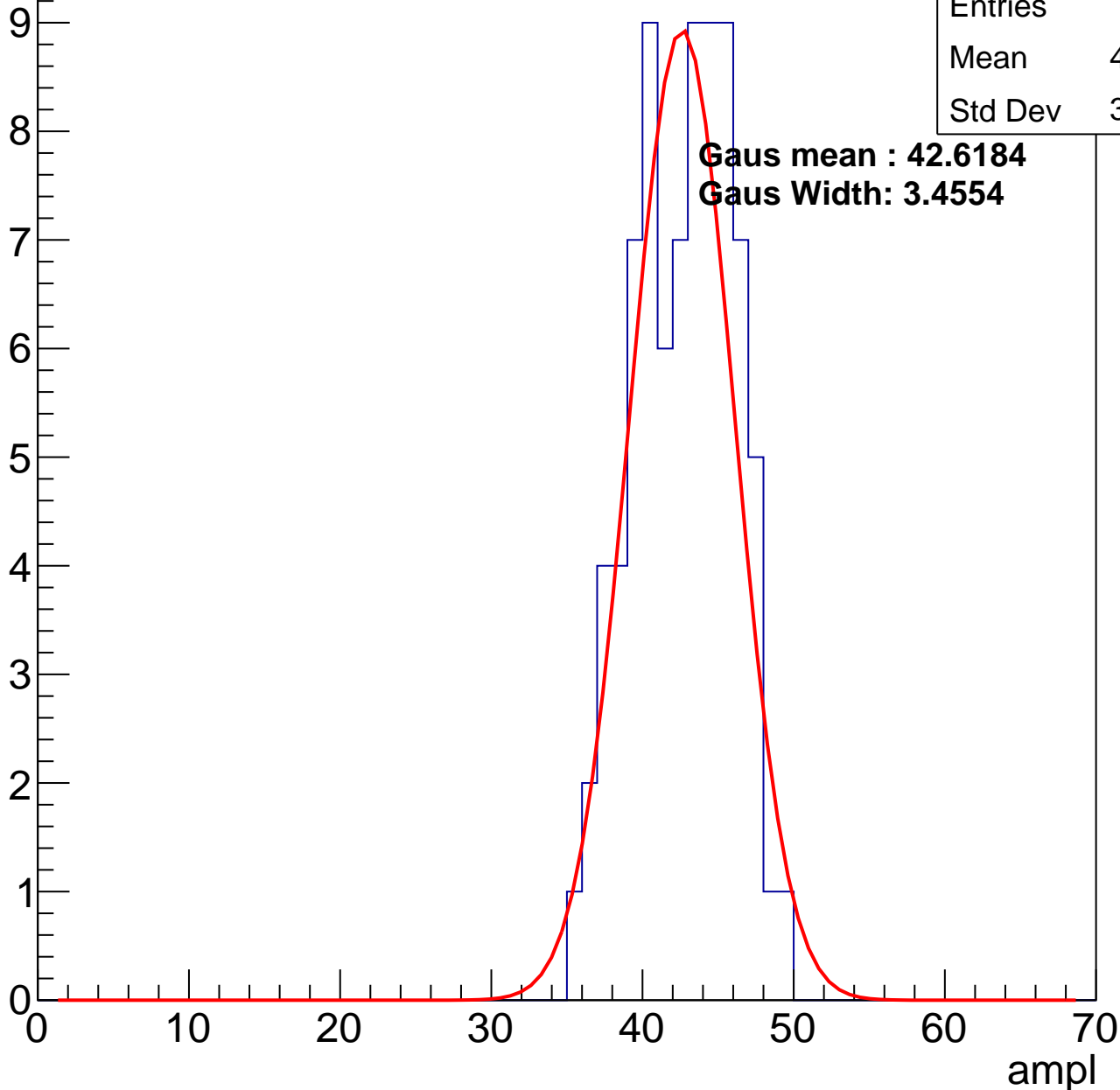
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	42.25
Std Dev	3.207

Gaus mean : 42.6184

Gaus Width: 3.4554

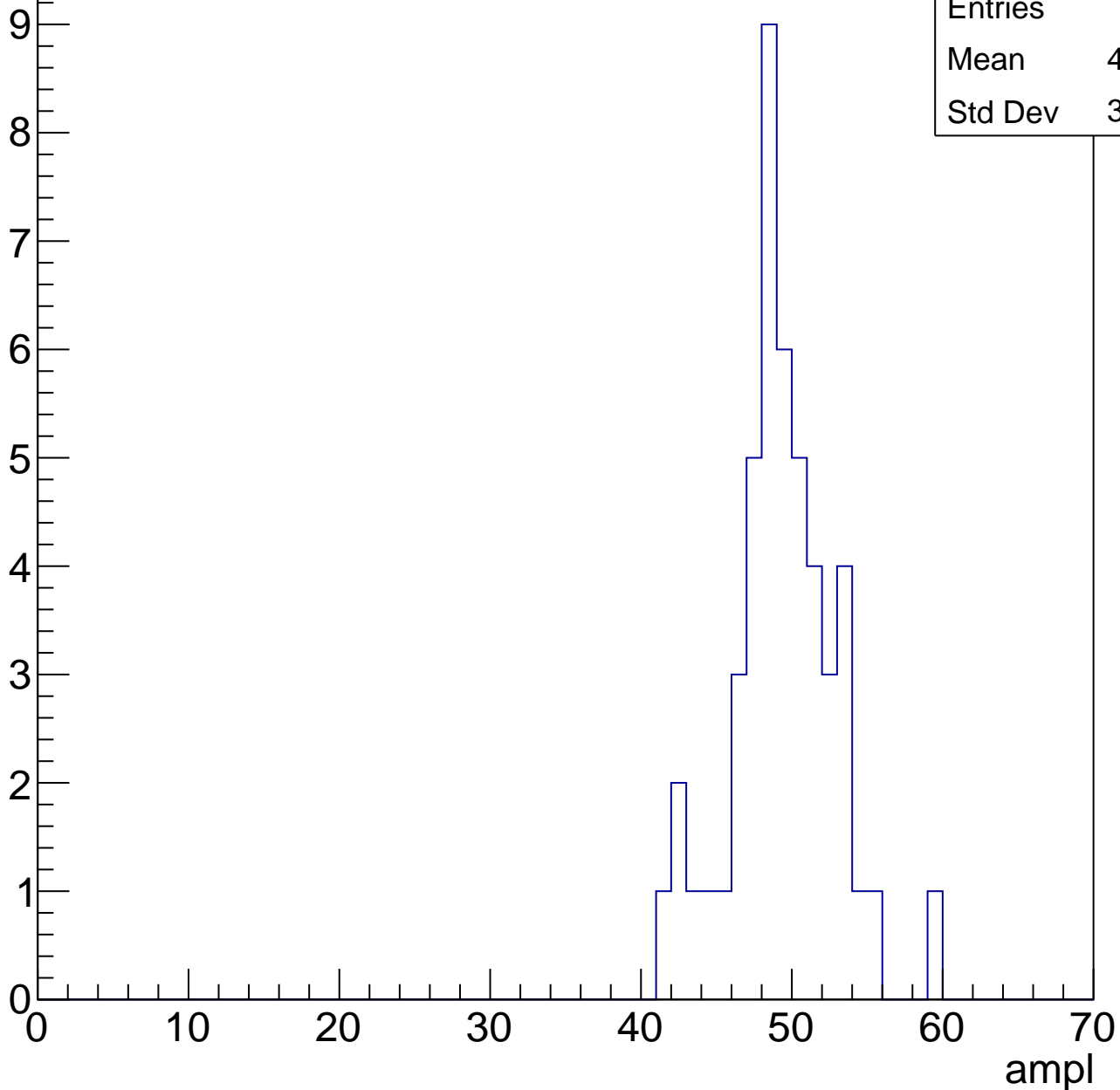


B1L103S, U14-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

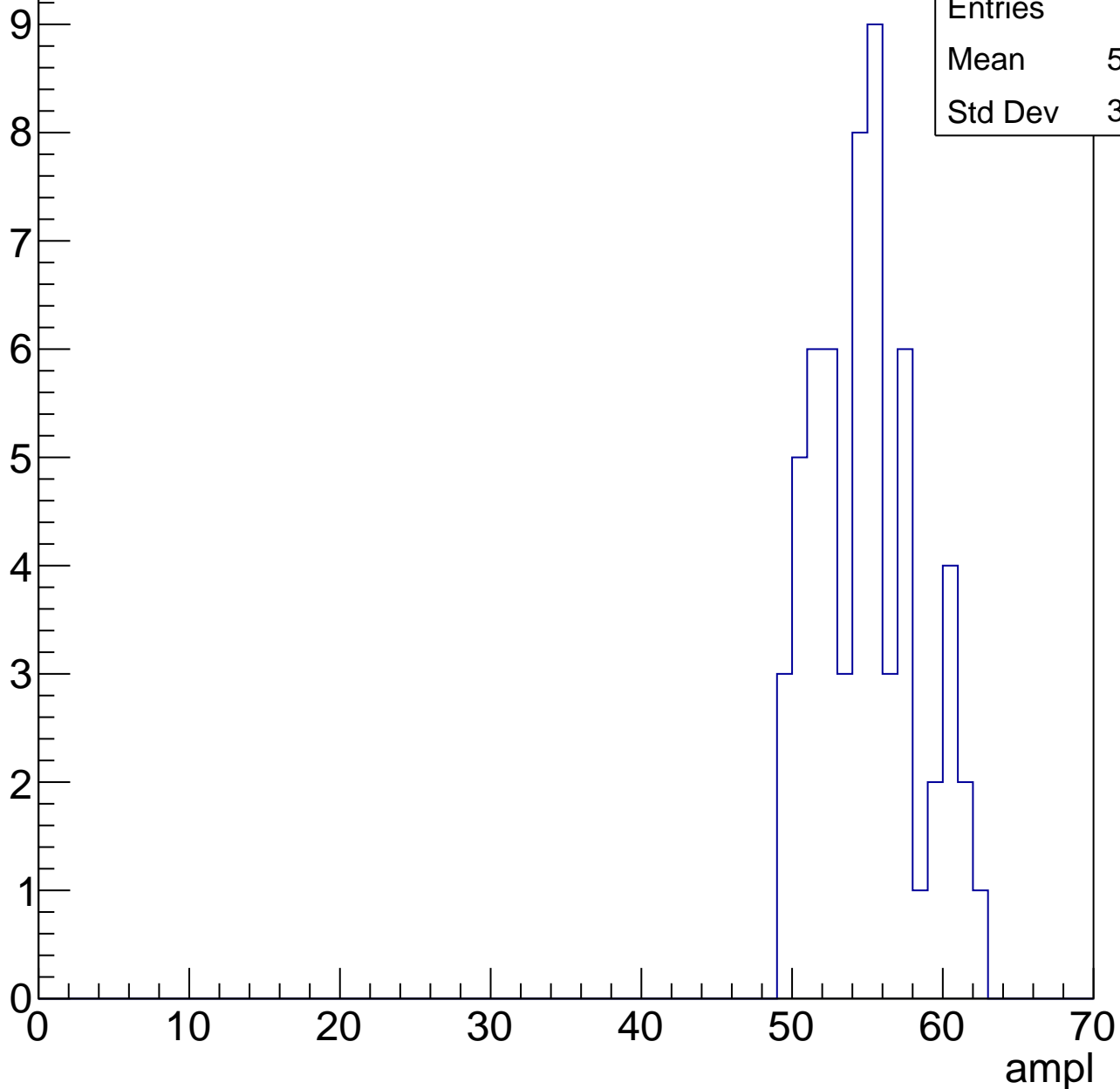
Entries	48
Mean	48.88
Std Dev	3.438



B1L103S, U14-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

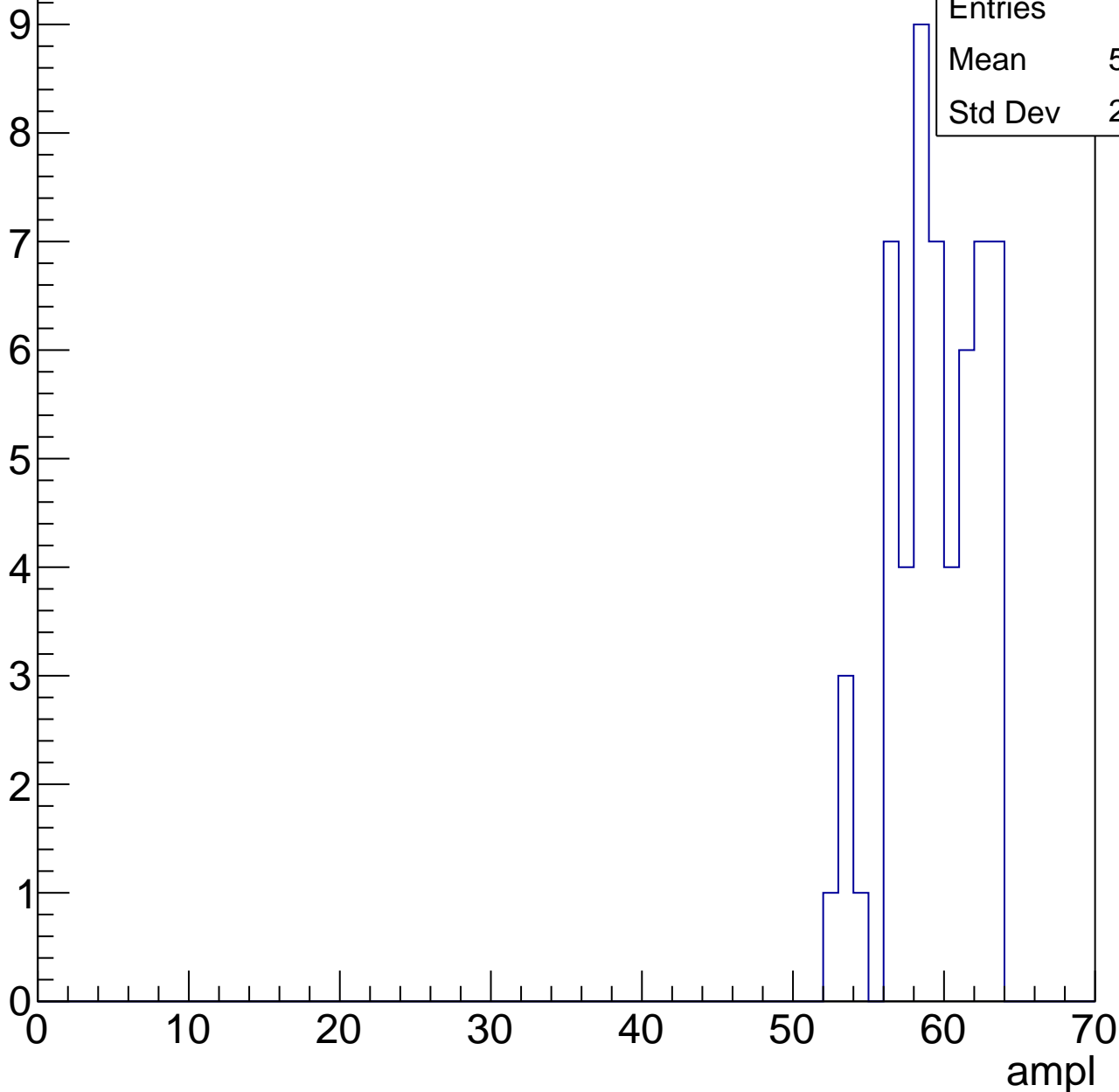


B1L103S, U14-ch40, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	58.95
Std Dev	2.906



B1L103S, U14-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	54.67
Std Dev	19.36

0 10 20 30 40 50 60 70

ampl

B1L103S, U14-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch41, adc0

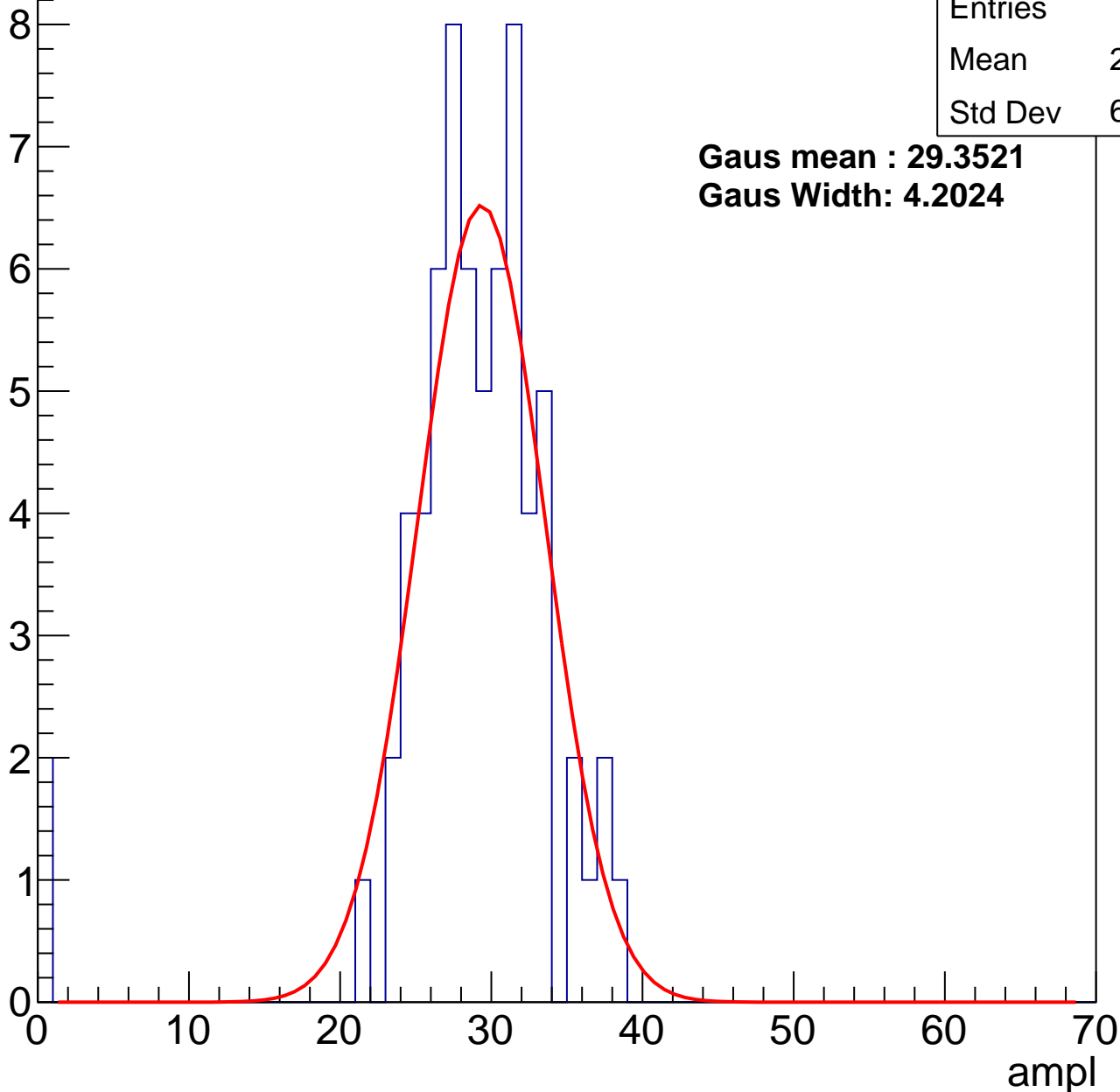
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.16
Std Dev	6.122

Gaus mean : 29.3521

Gaus Width: 4.2024



B1L103S, U14-ch41, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	34.84
Std Dev	2.981

Gaus mean : 35.8909

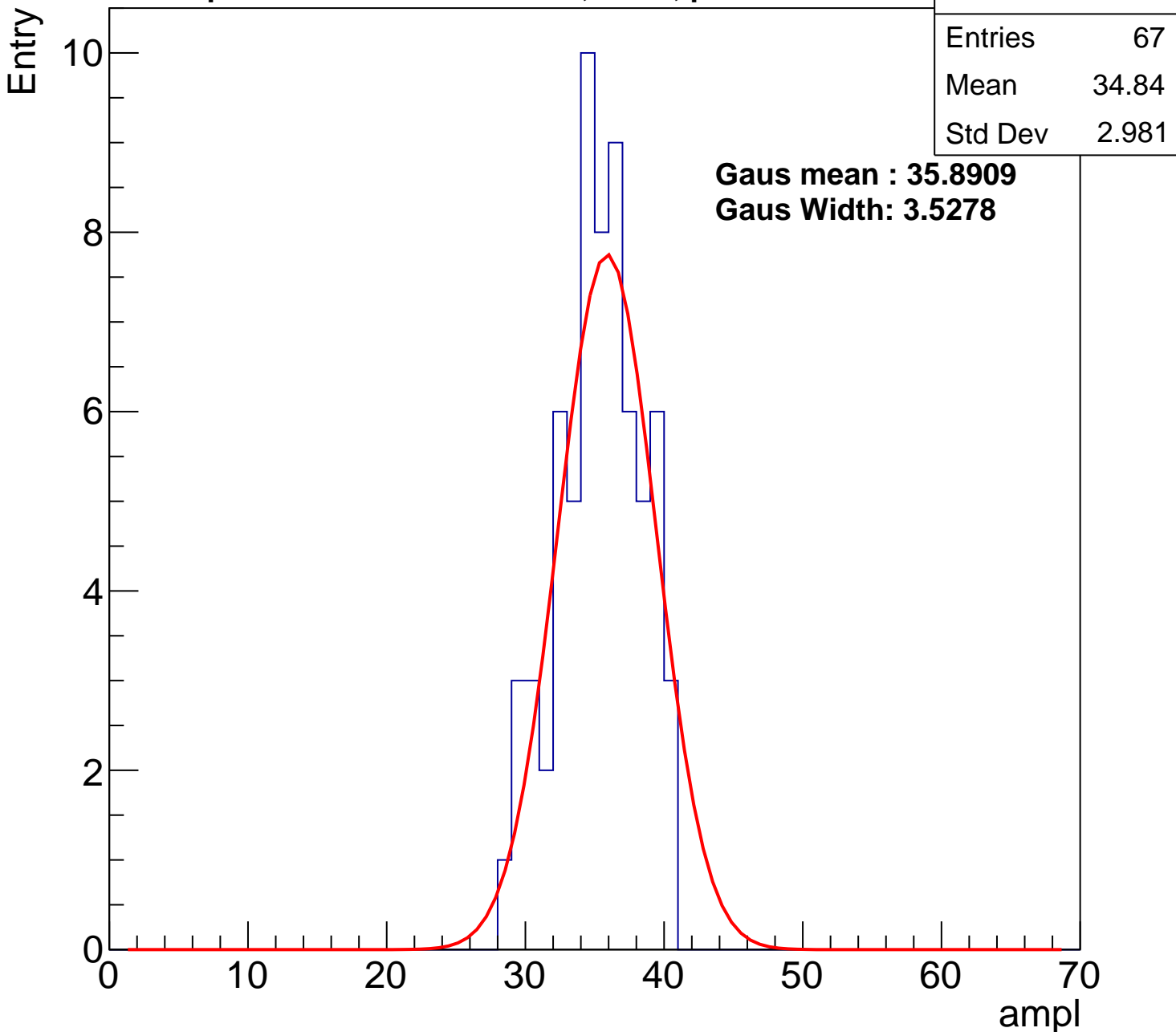
Gaus Width: 3.5278

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U14-ch41, adc2

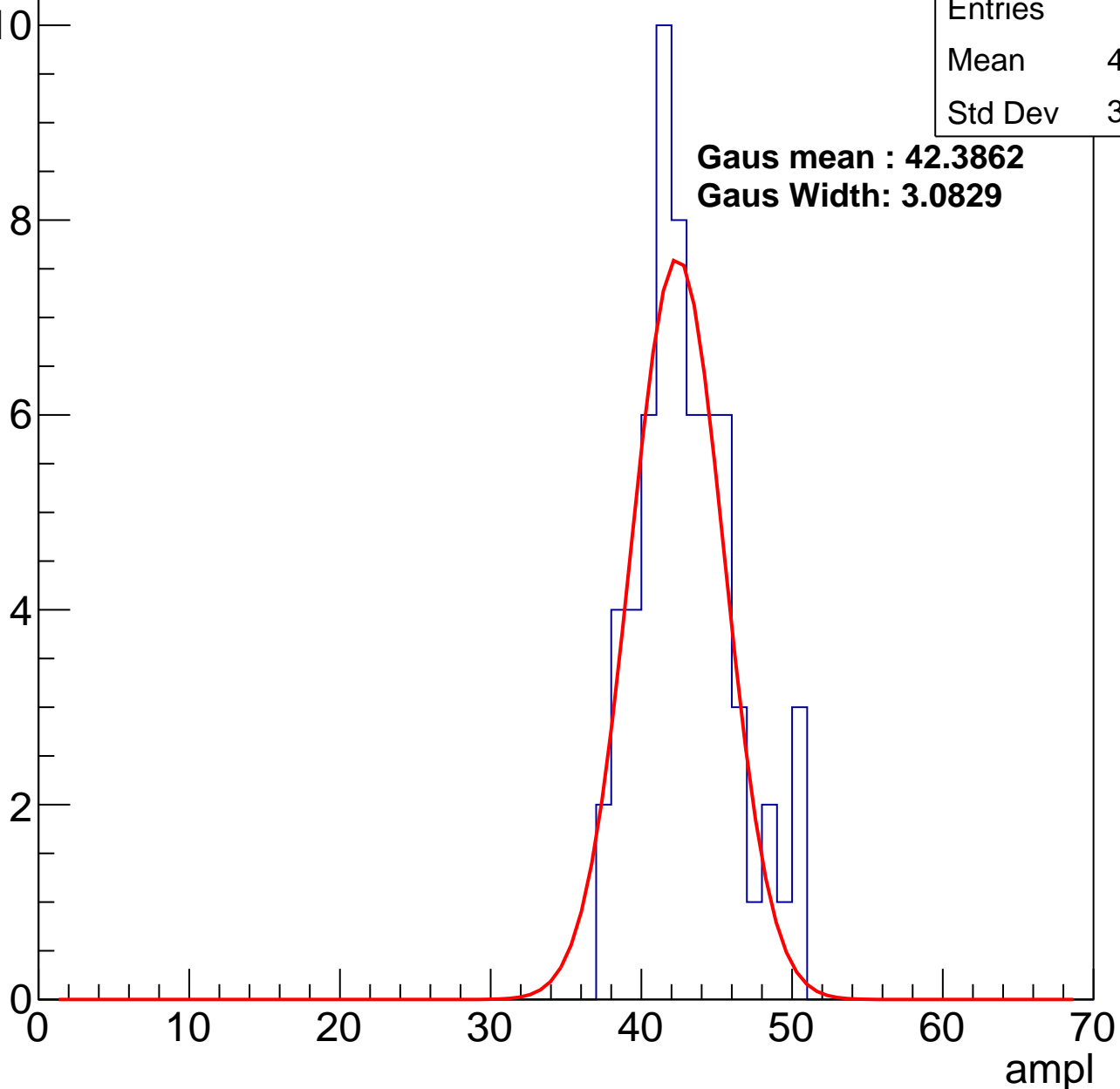
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.58
Std Dev	3.196

Gaus mean : 42.3862

Gaus Width: 3.0829

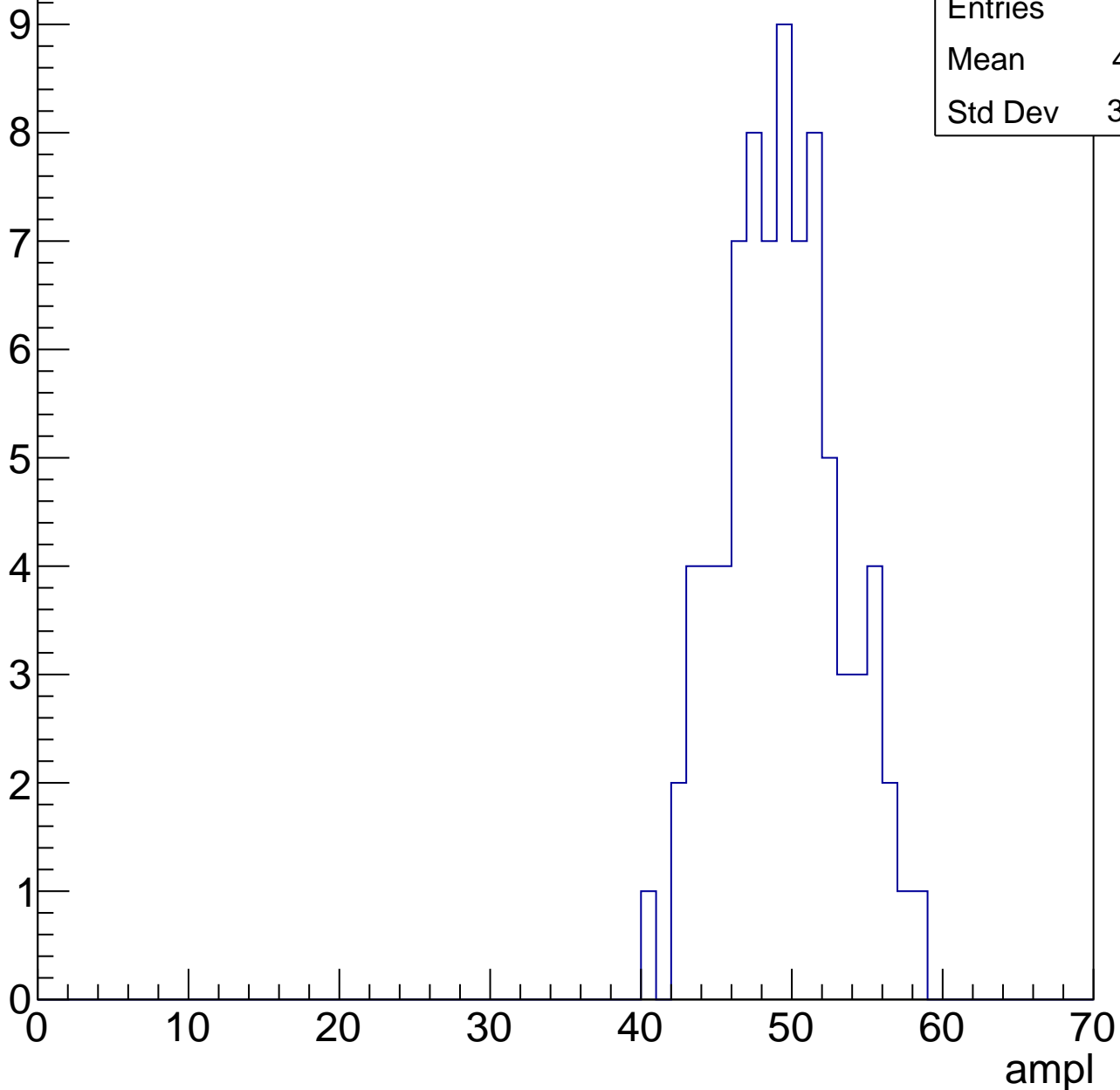


B1L103S, U14-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	48.91
Std Dev	3.844

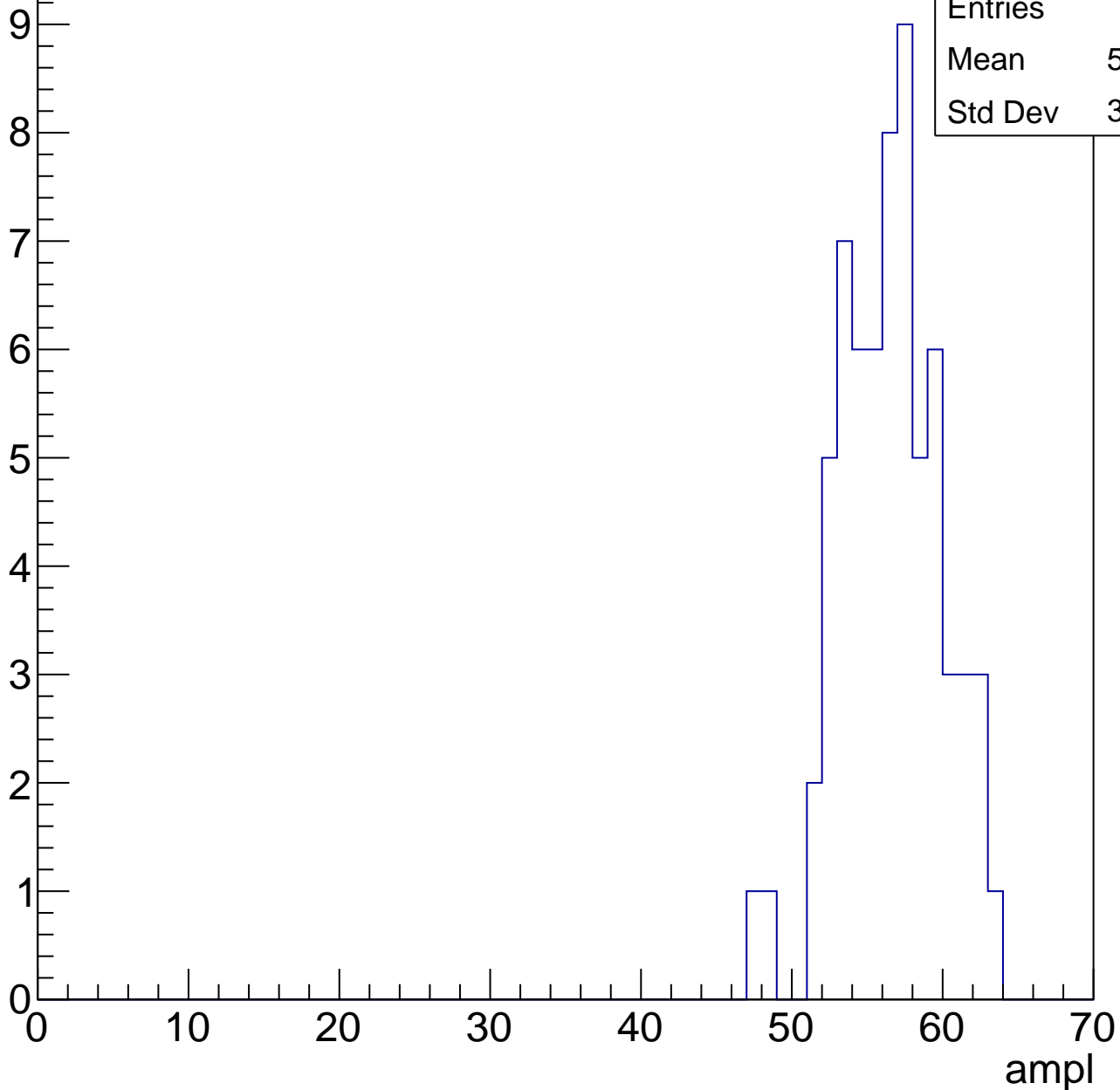


B1L103S, U14-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	56.05
Std Dev	3.328

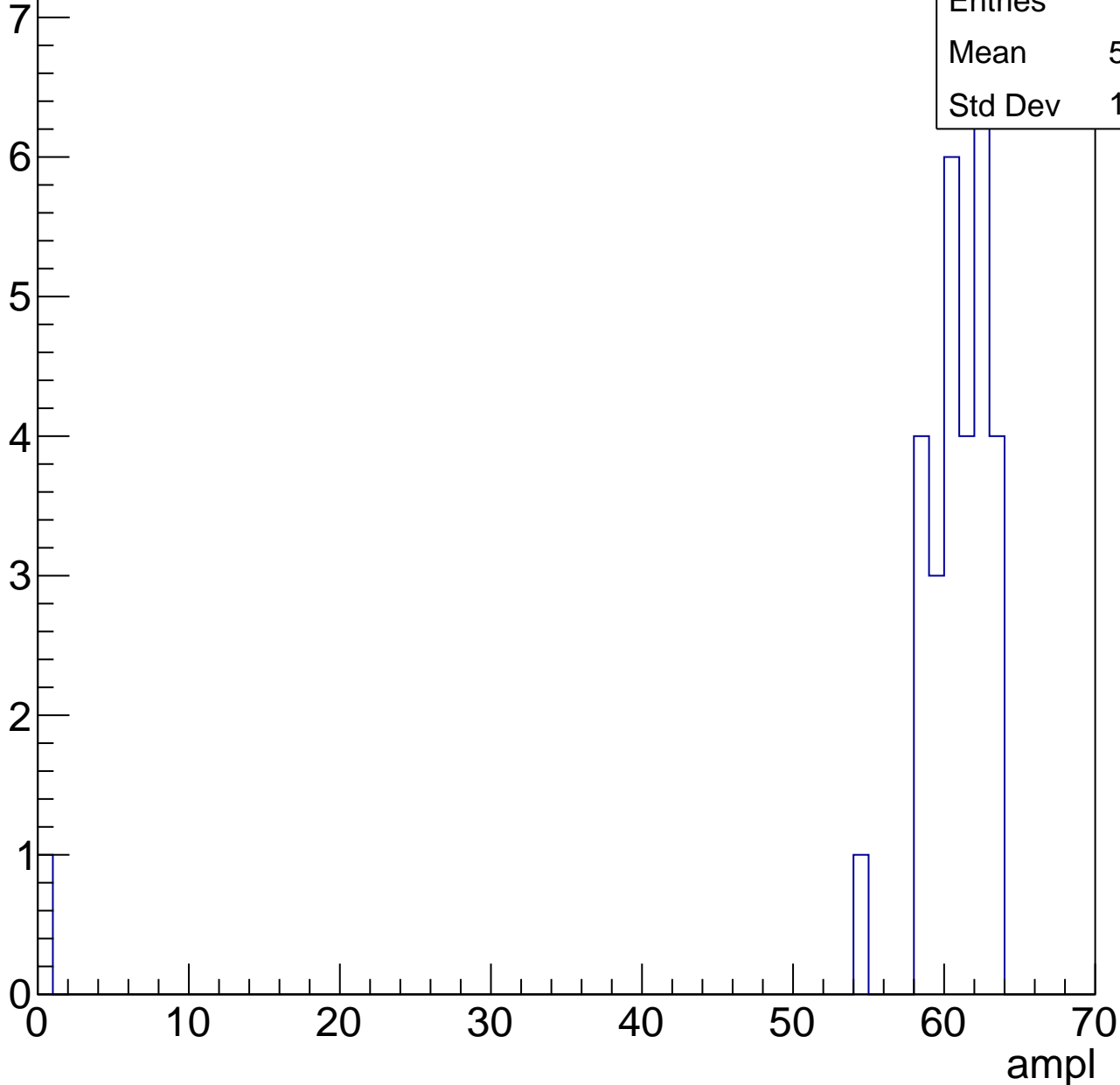


B1L103S, U14-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	58.43
Std Dev	11.03

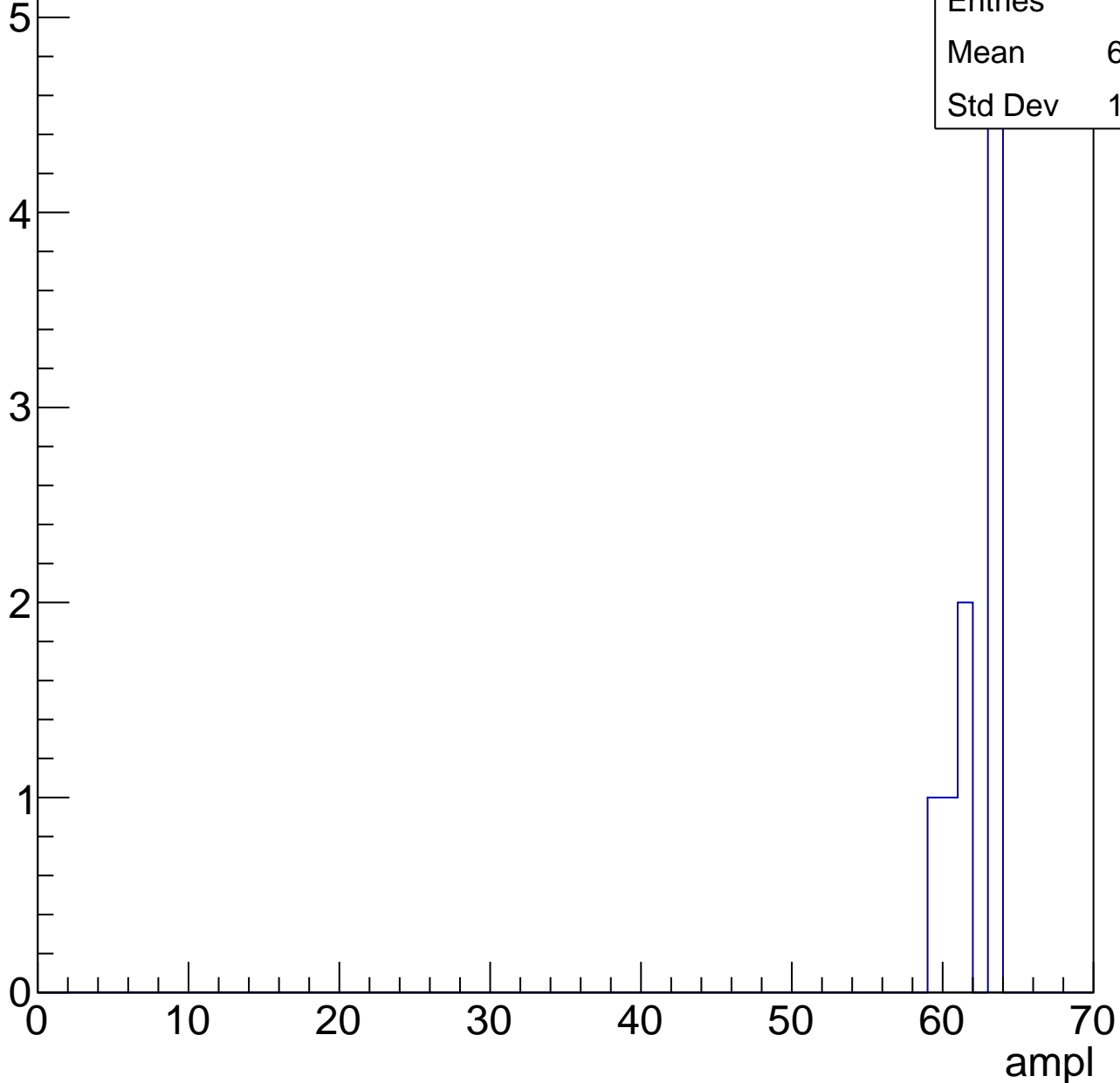


B1L103S, U14-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	61.78
Std Dev	1.474



B1L103S, U14-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch42, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	28.34
Std Dev	5.821

Gaus mean : 29.5380

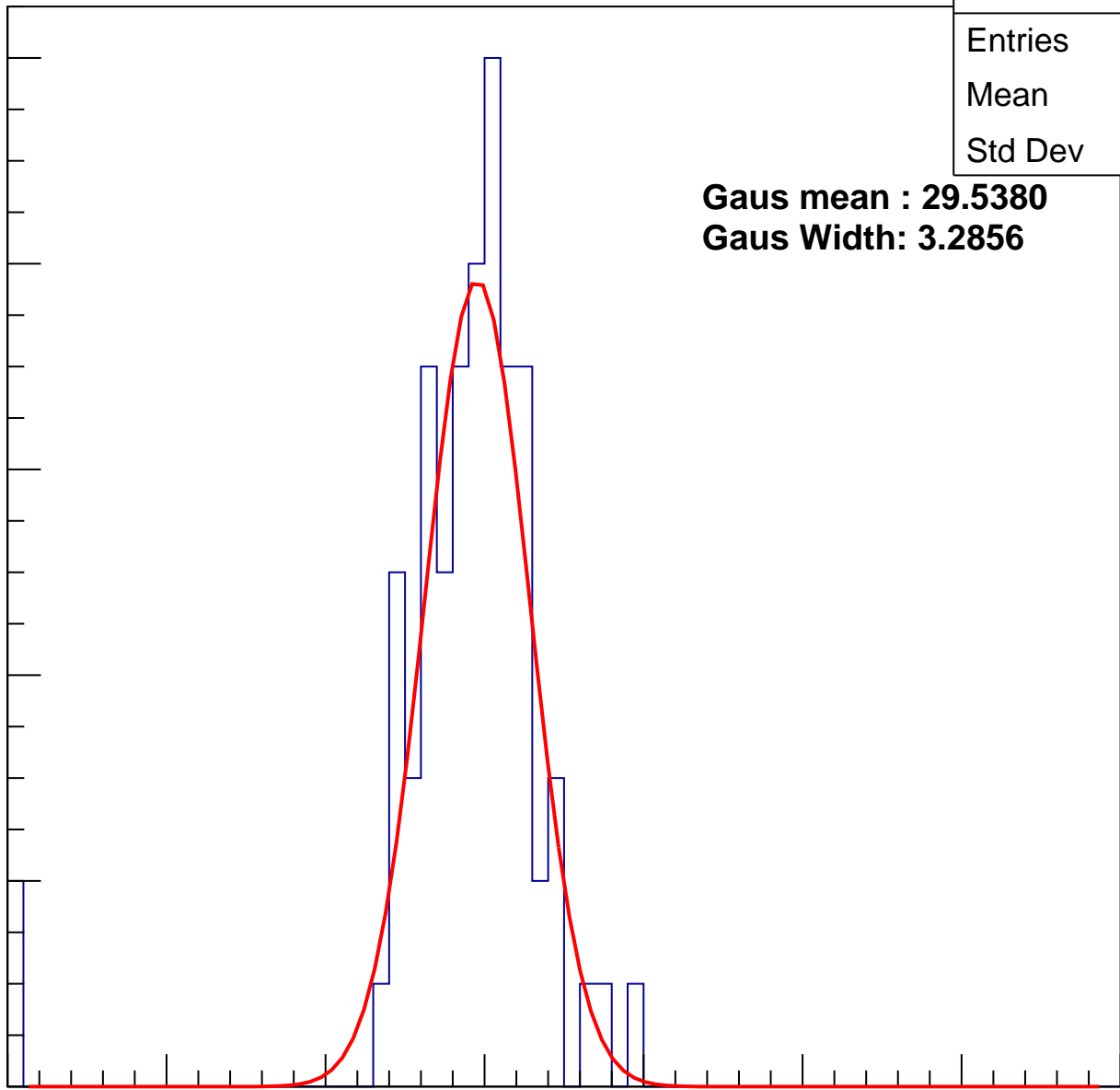
Gaus Width: 3.2856

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch42, adc1

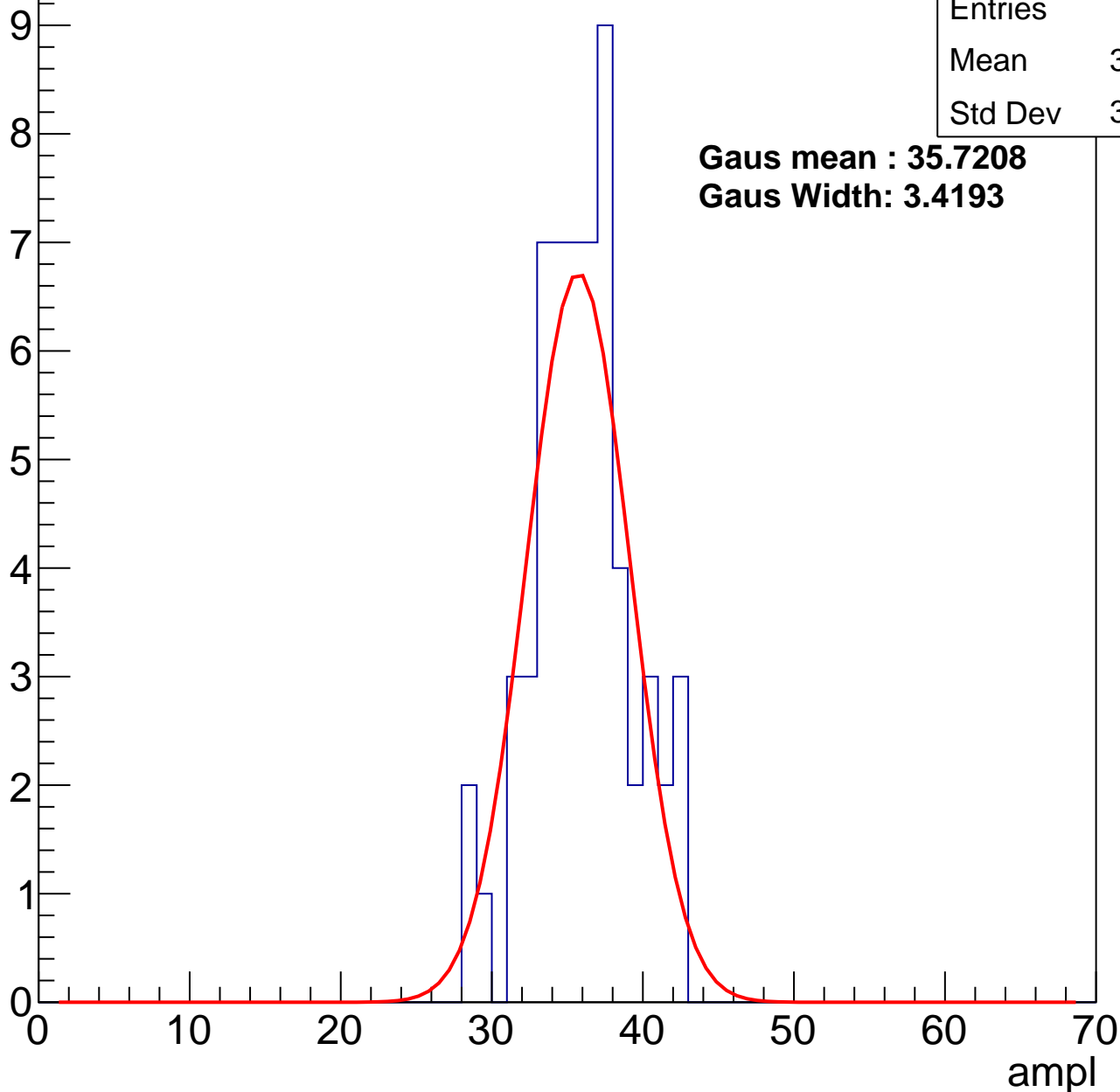
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.52
Std Dev	3.248

Gaus mean : 35.7208

Gaus Width: 3.4193



B1L103S, U14-ch42, adc2

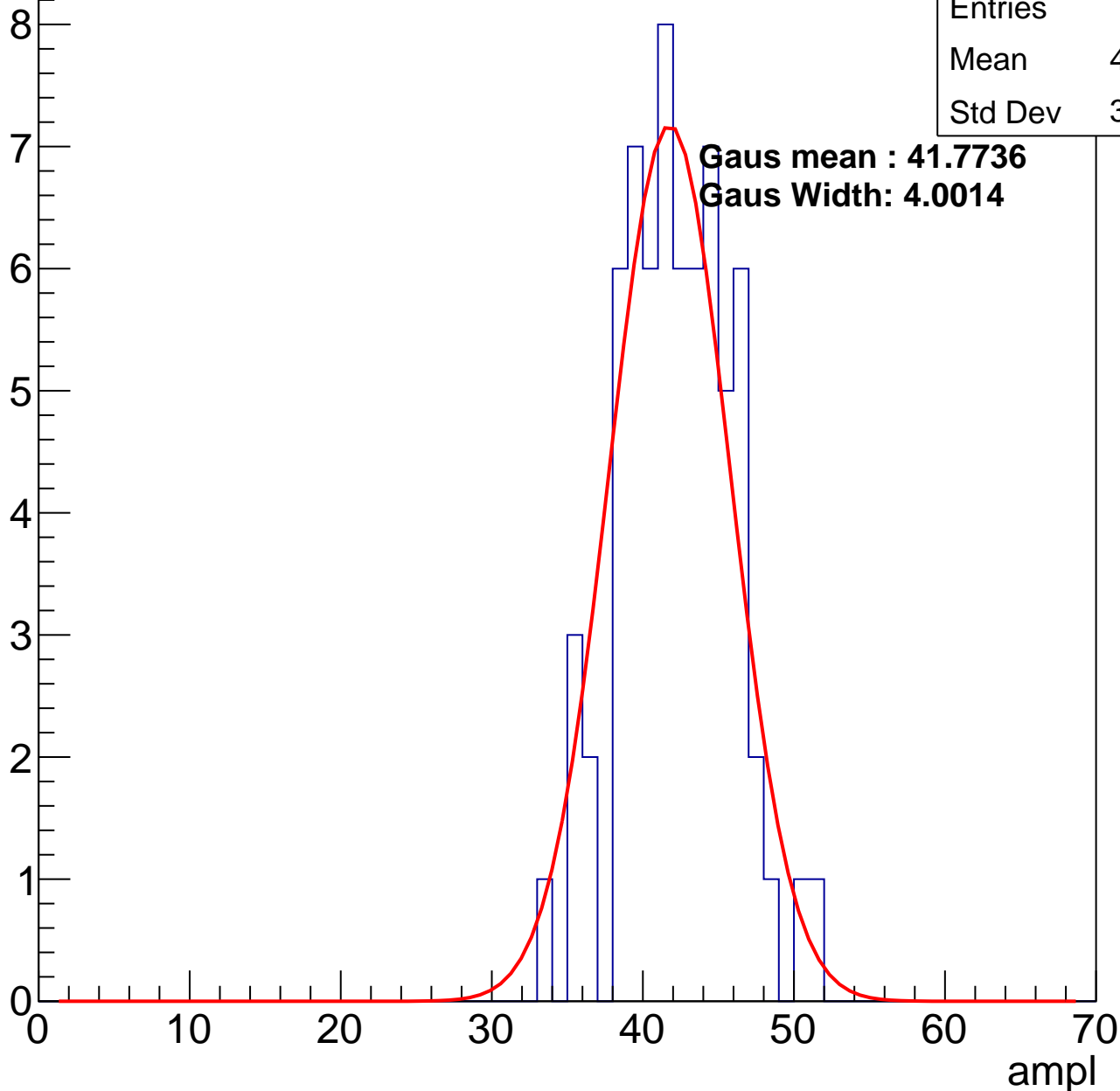
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	41.78
Std Dev	3.625

Gaus mean : 41.7736

Gaus Width: 4.0014

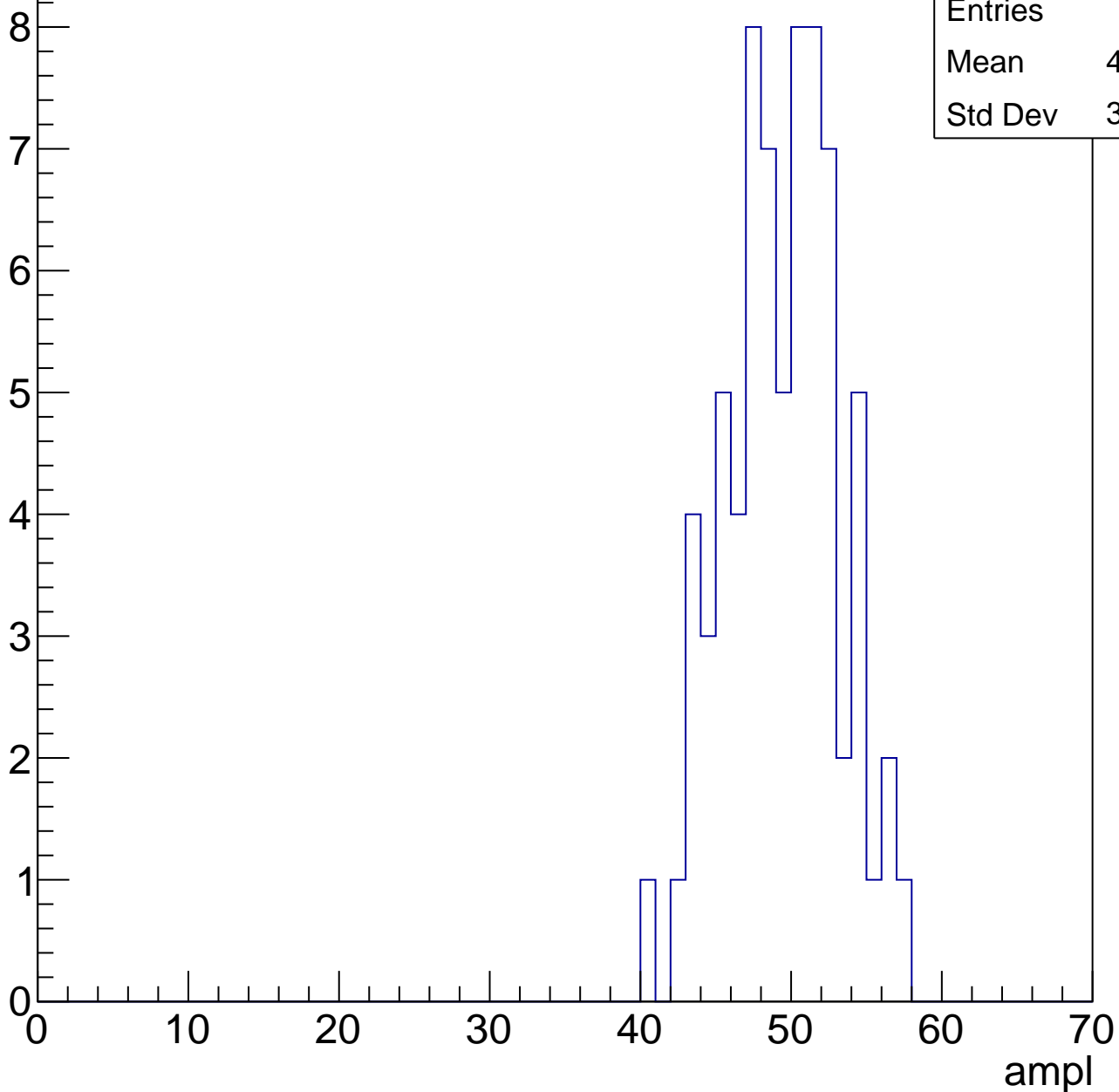


B1L103S, U14-ch42, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	48.94
Std Dev	3.655

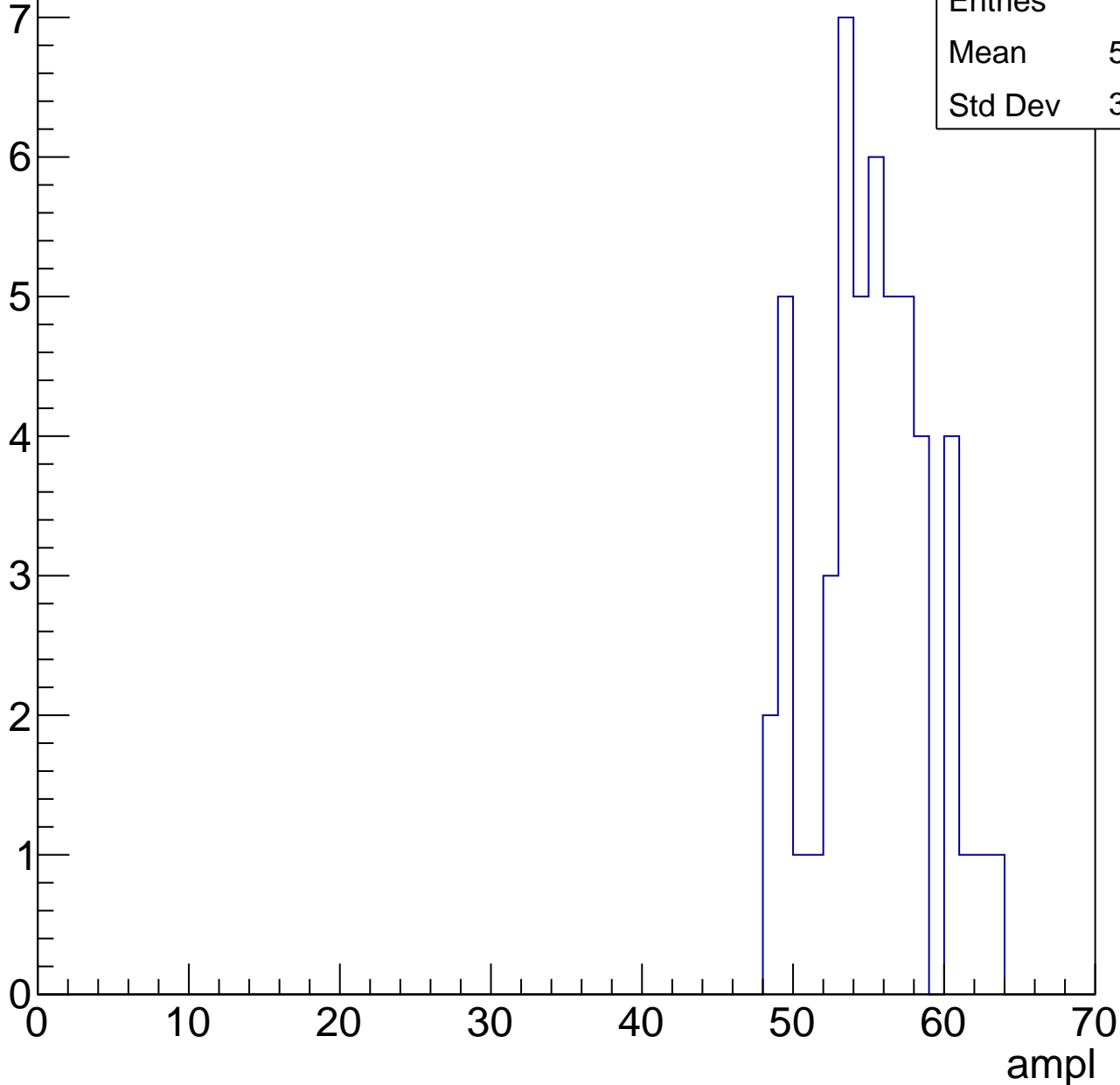


B1L103S, U14-ch42, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	54.75
Std Dev	3.667



B1L103S, U14-ch42, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

Entries 58

Mean 58.62

Std Dev 8.141

8

6

4

2

0

0

10

20

30

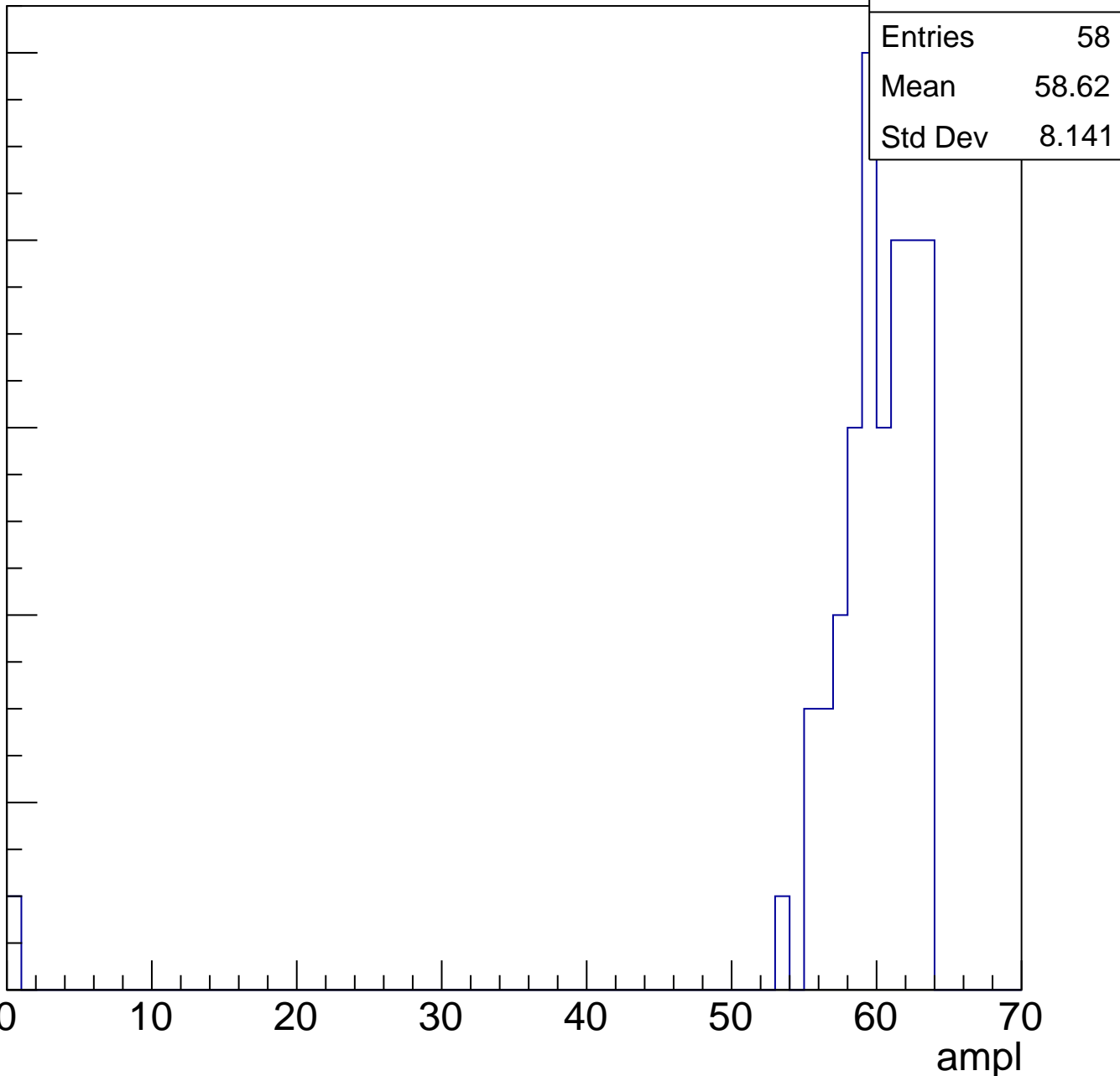
40

50

60

70

ampl



B1L103S, U14-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

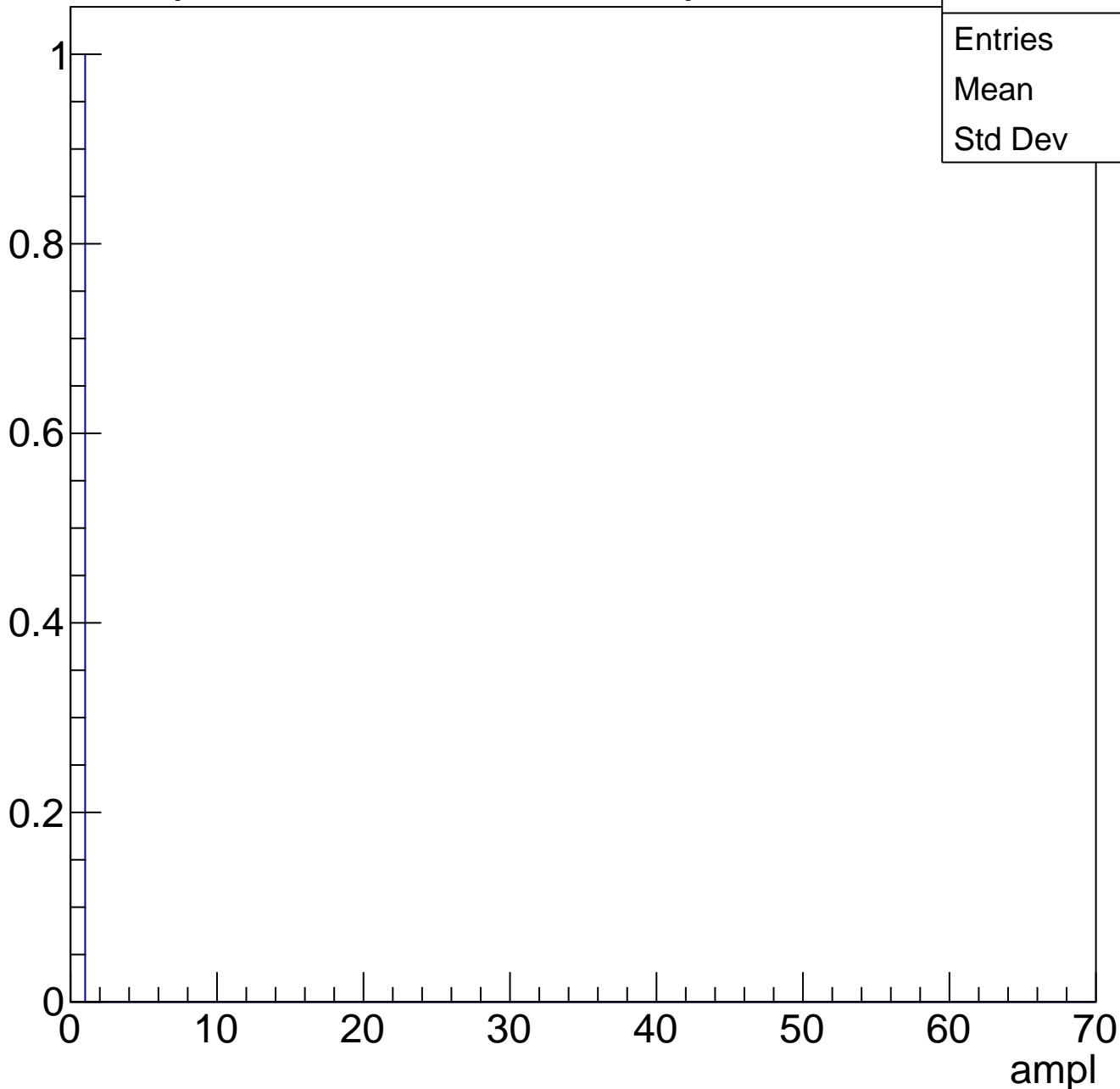
Entry



B1L103S, U14-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch43, adc0

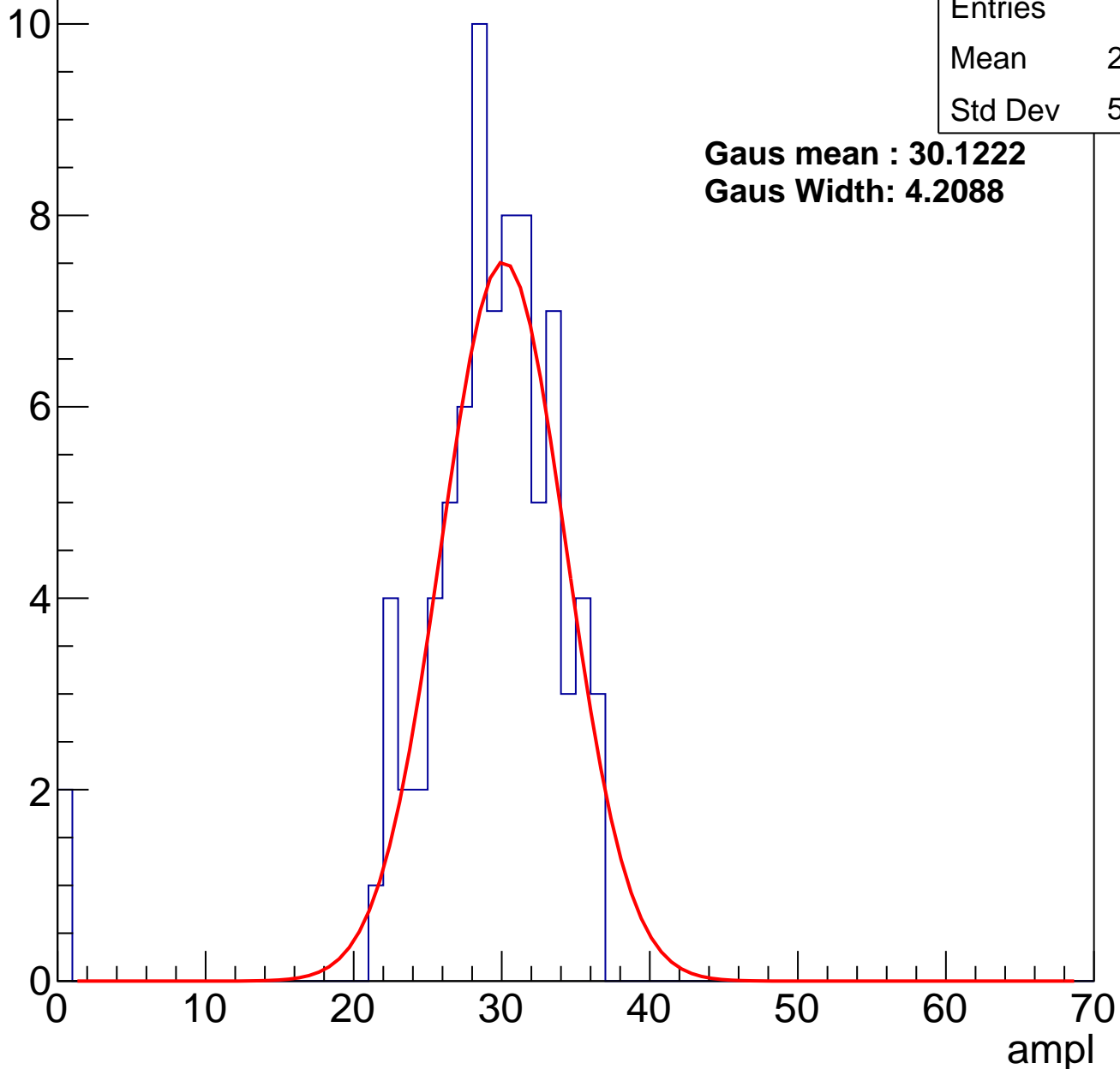
calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	28.48
Std Dev	5.825

Gaus mean : 30.1222

Gaus Width: 4.2088

Entry



B1L103S, U14-ch43, adc1

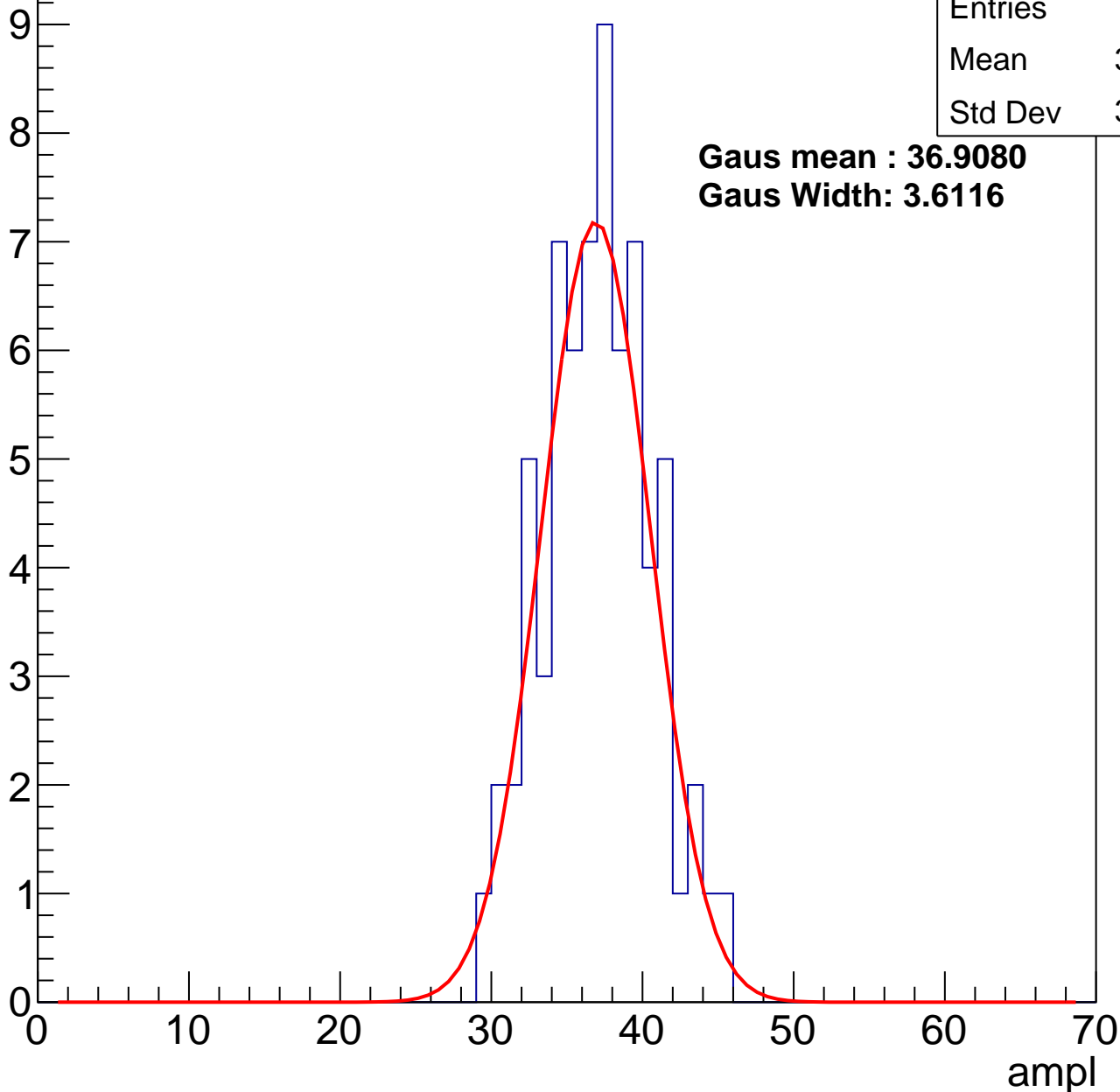
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.61
Std Dev	3.511

Gaus mean : 36.9080

Gaus Width: 3.6116



B1L103S, U14-ch43, adc2

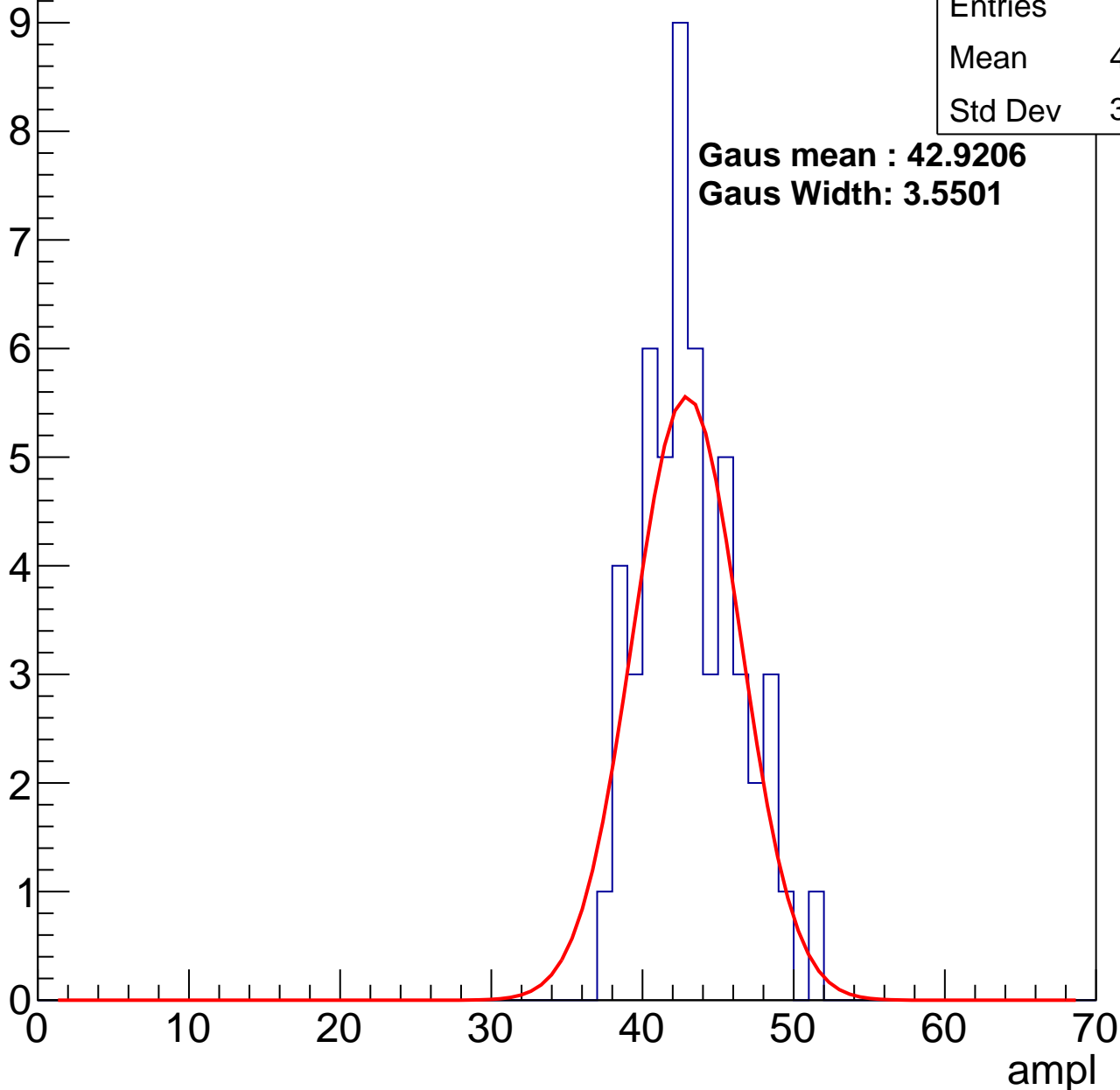
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	42.69
Std Dev	3.159

Gaus mean : 42.9206

Gaus Width: 3.5501

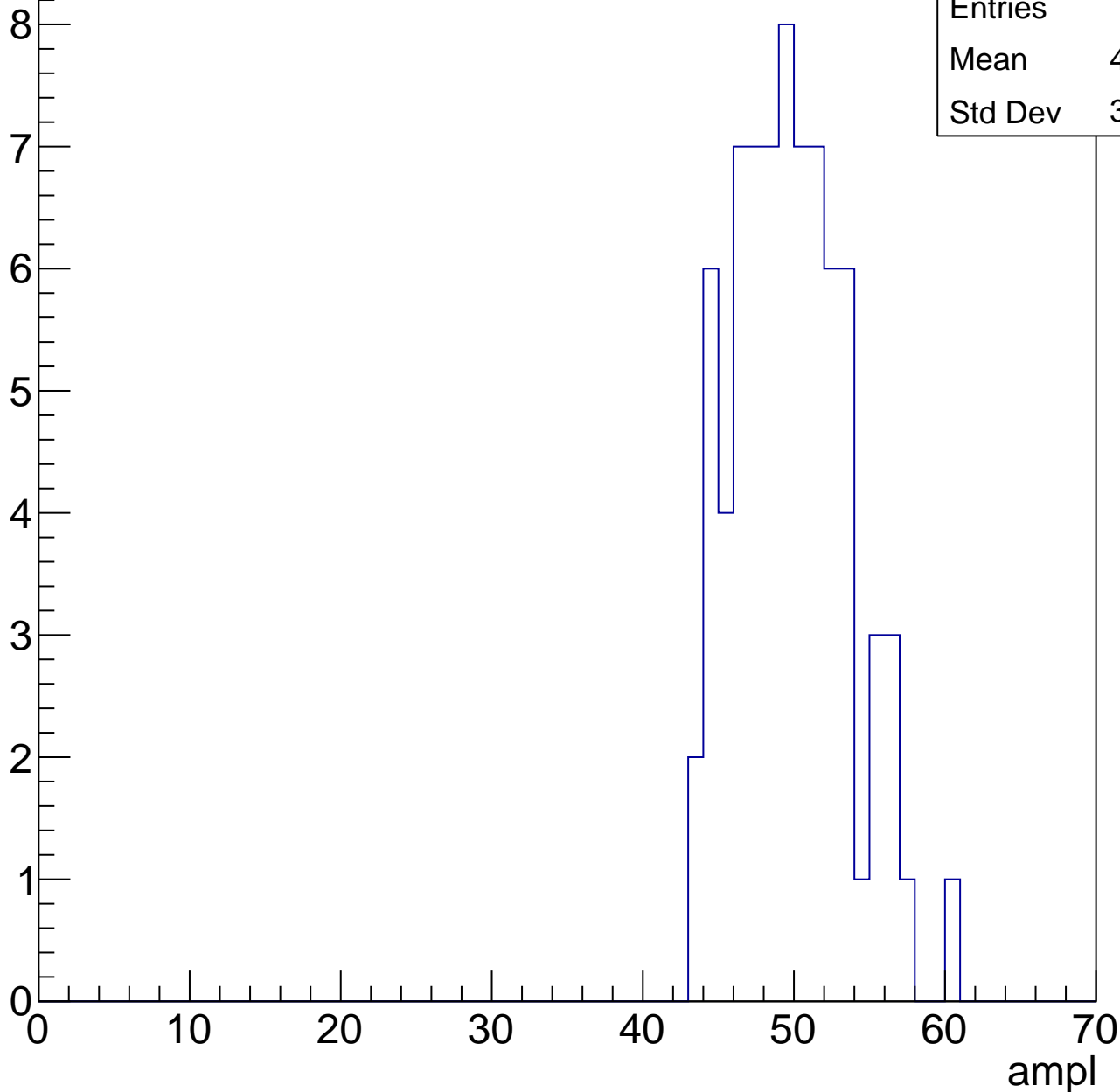


B1L103S, U14-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	49.34
Std Dev	3.673

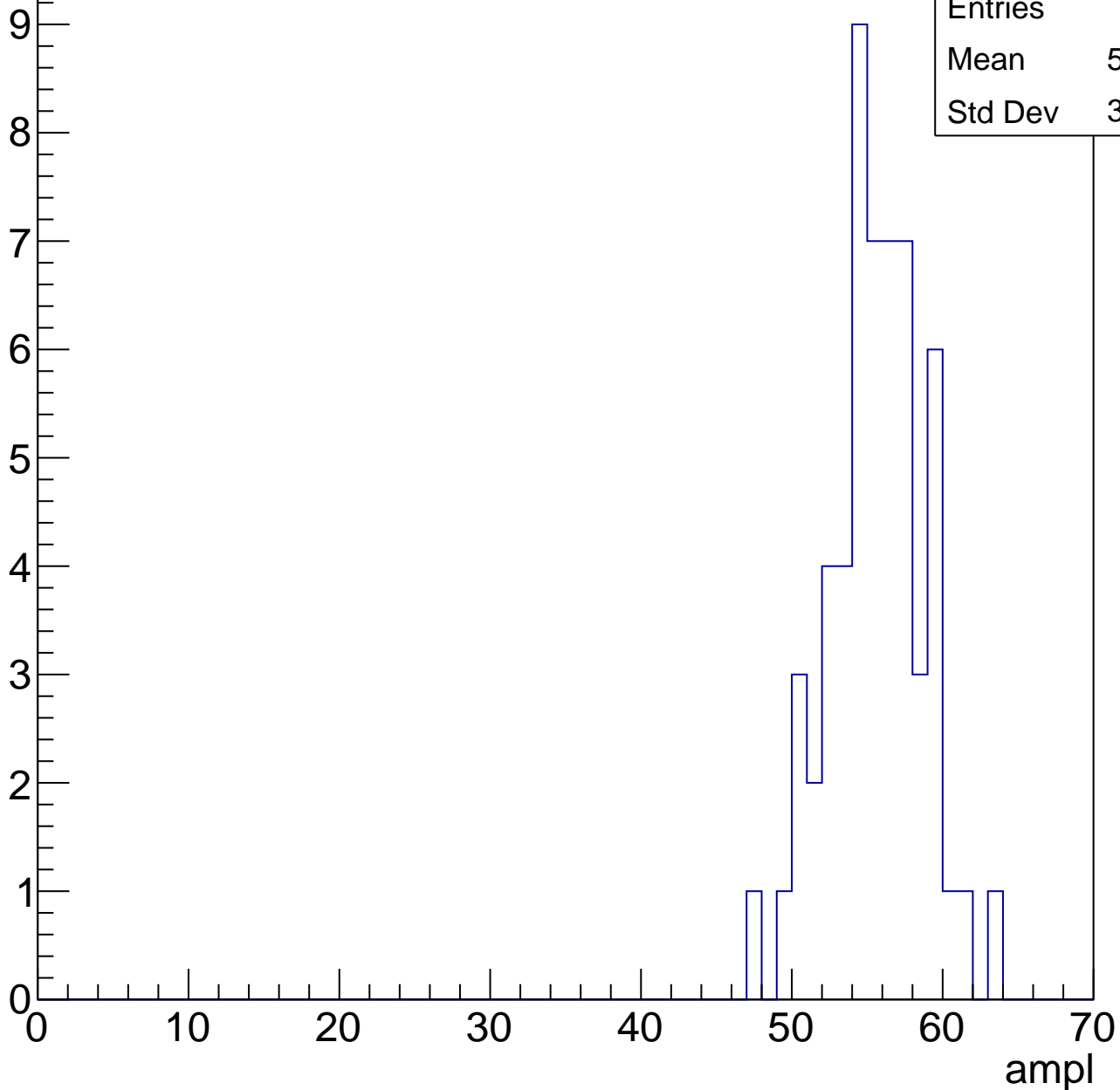


B1L103S, U14-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.12
Std Dev	3.107



B1L103S, U14-ch43, adc5

calib_packv5_042523_0143.root, FC#7, port C2

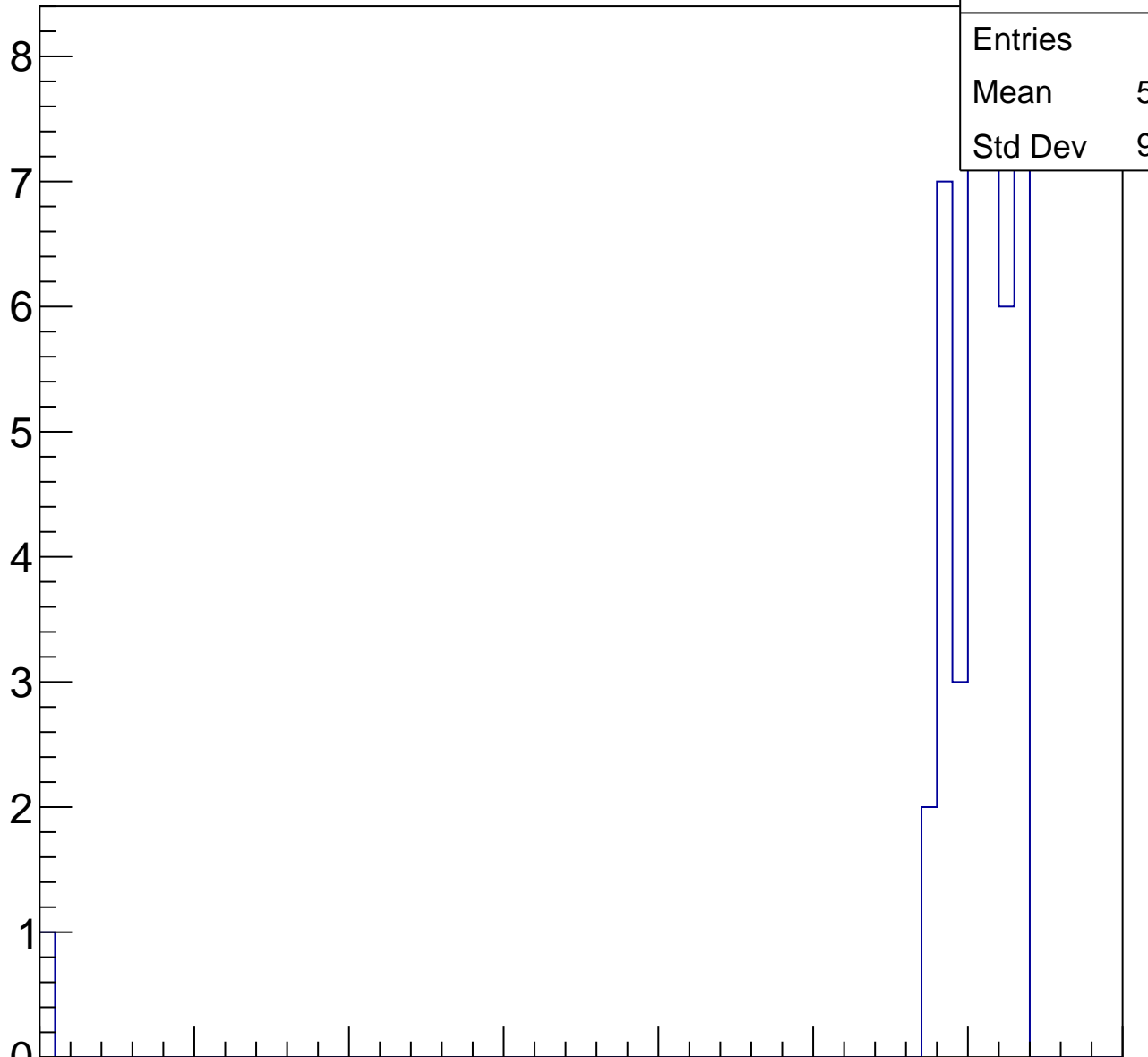
Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.09
Std Dev	9.298

ampl

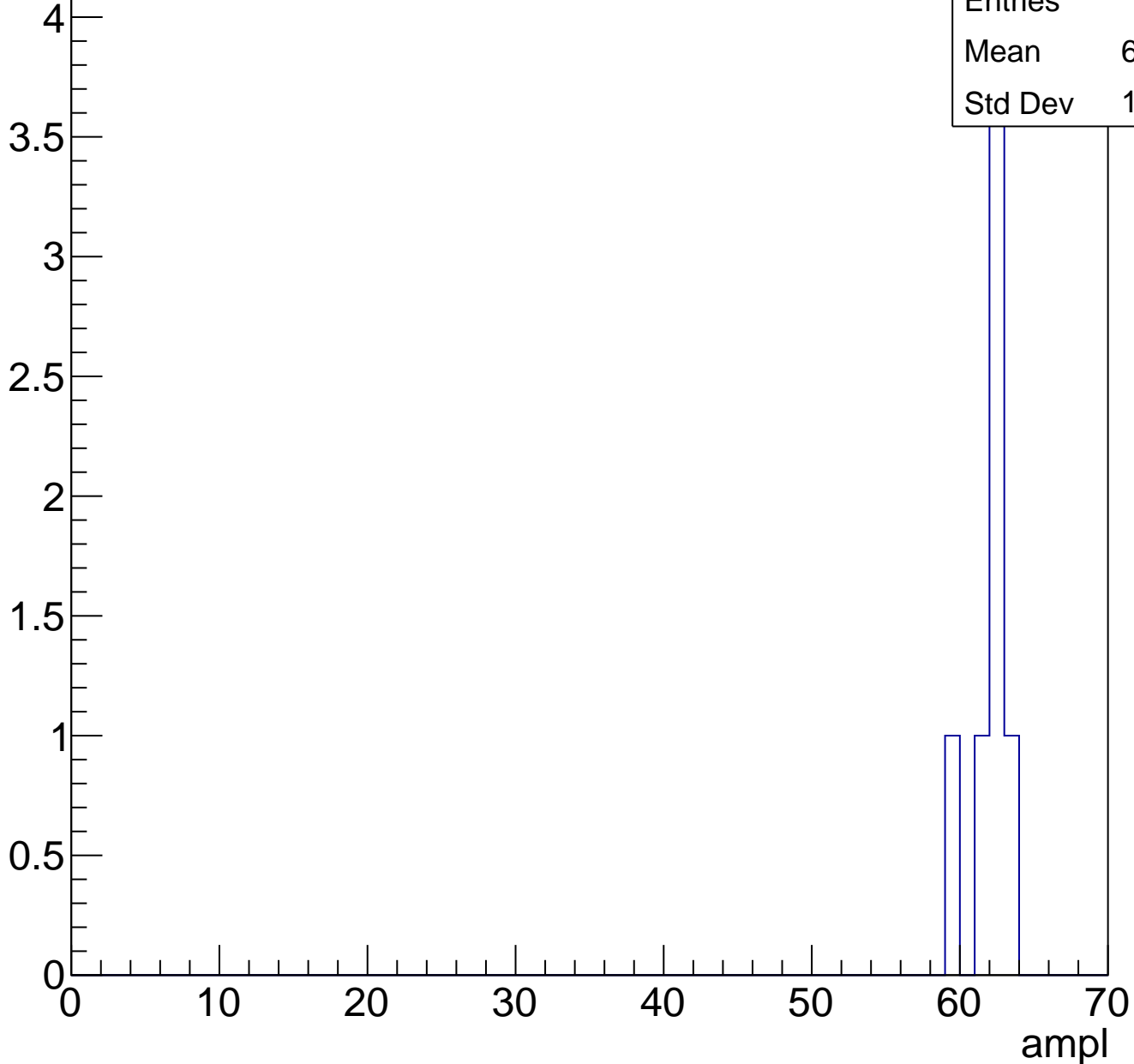
0 10 20 30 40 50 60 70



B1L103S, U14-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U14-ch44, adc0

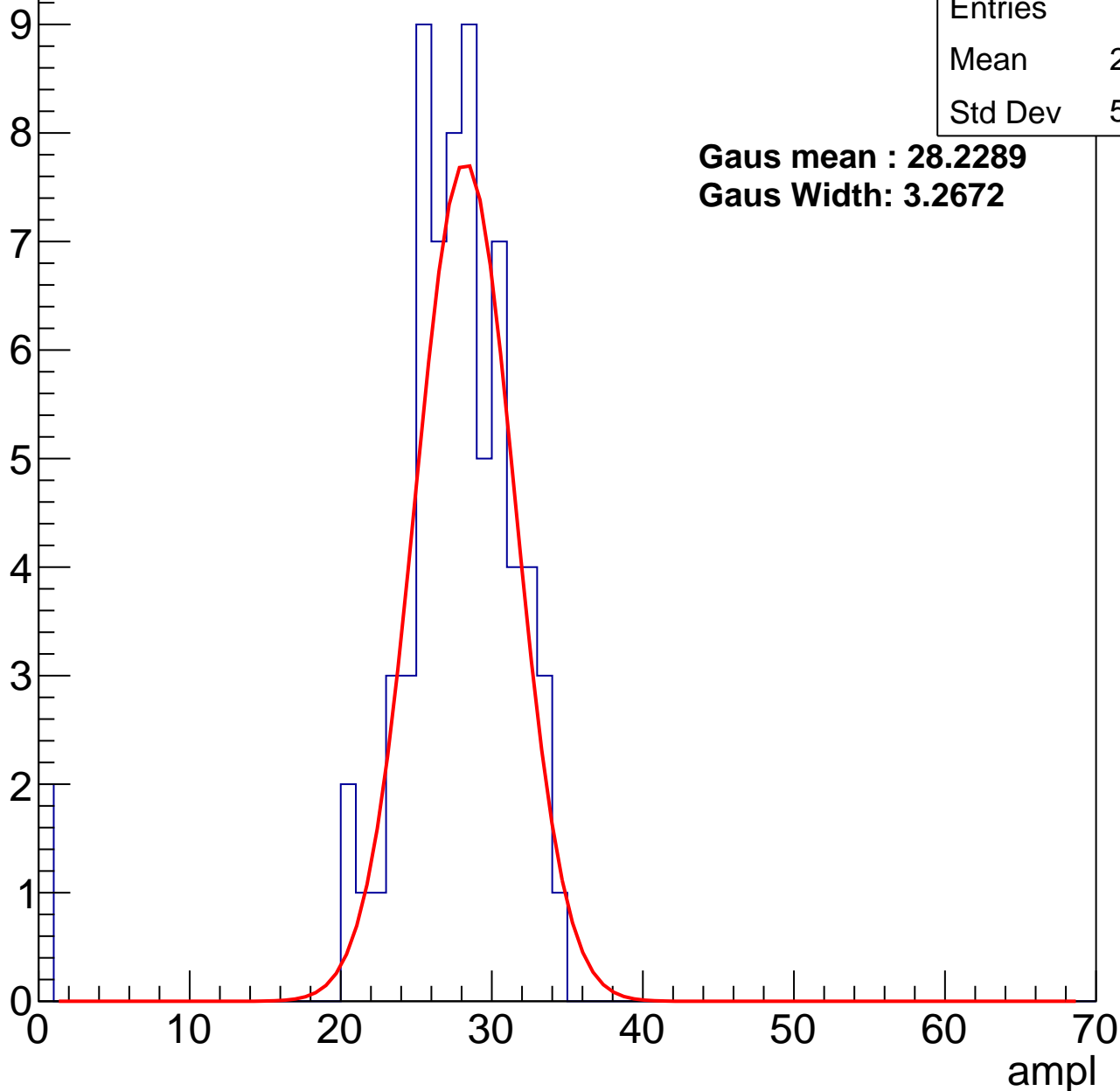
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	26.65
Std Dev	5.569

Gaus mean : 28.2289

Gaus Width: 3.2672



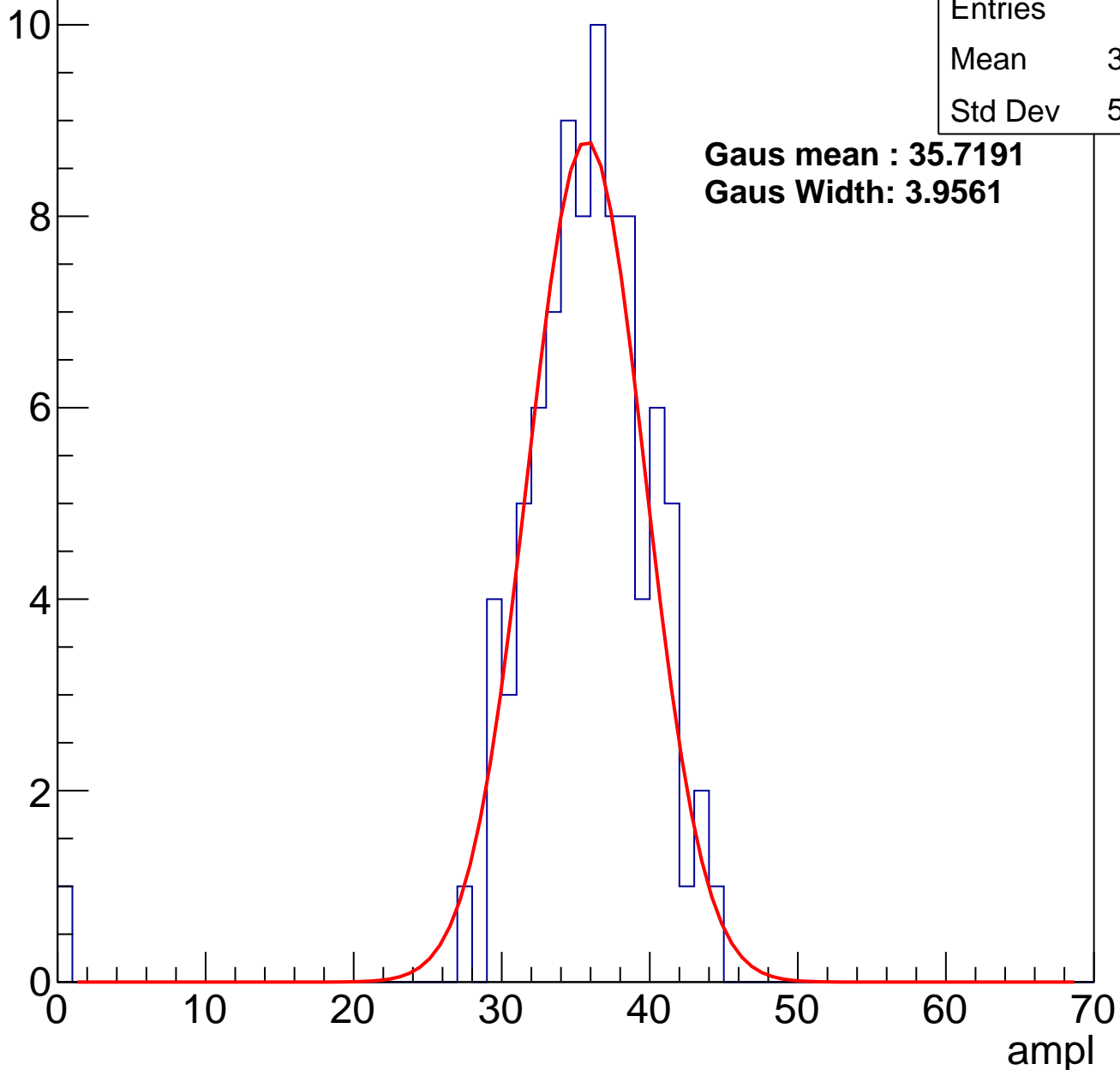
B1L103S, U14-ch44, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	89
Mean	35.17
Std Dev	5.239

Gaus mean : 35.7191
Gaus Width: 3.9561

Entry



B1L103S, U14-ch44, adc2

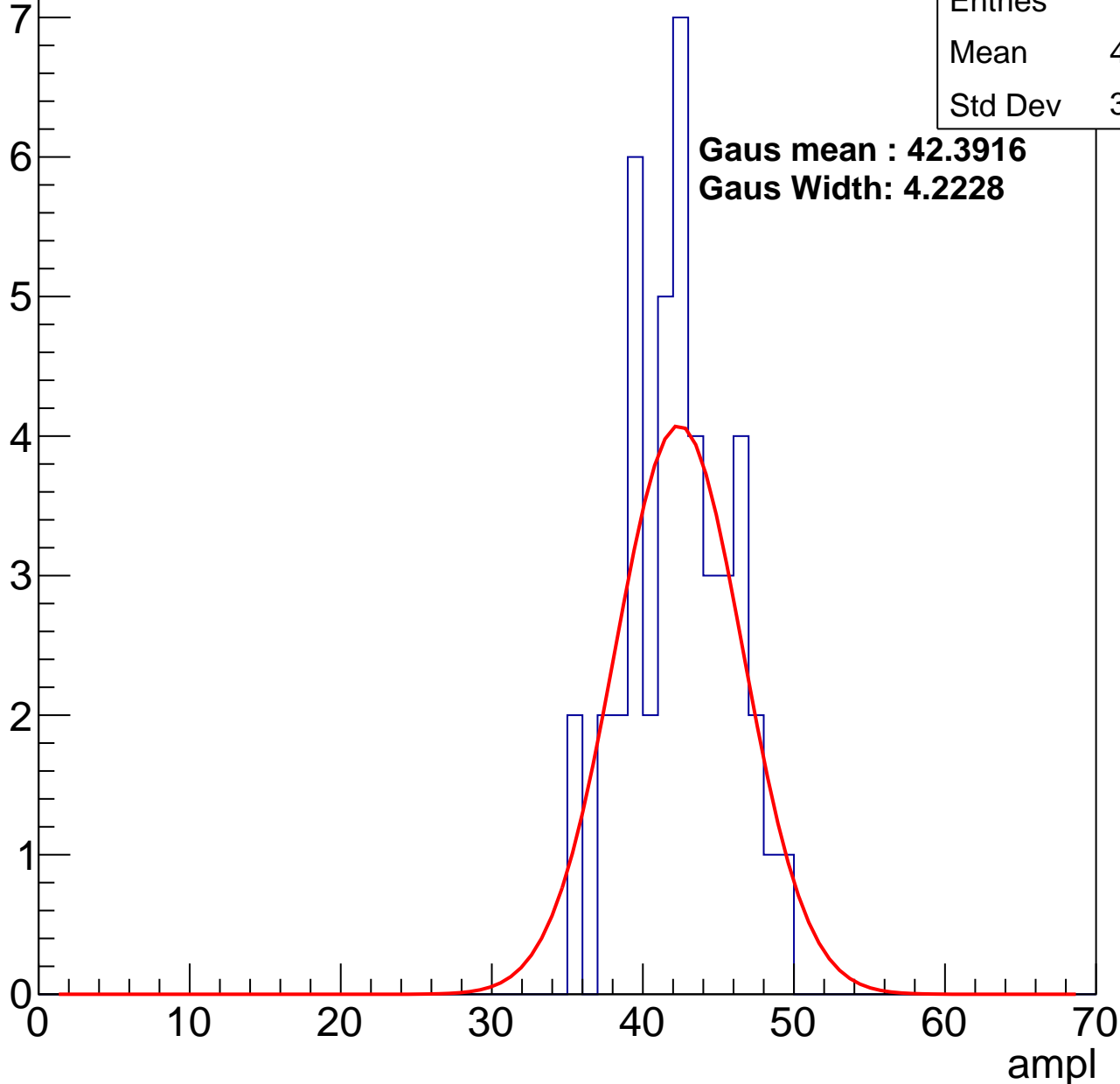
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	41.98
Std Dev	3.334

Gaus mean : 42.3916

Gaus Width: 4.2228

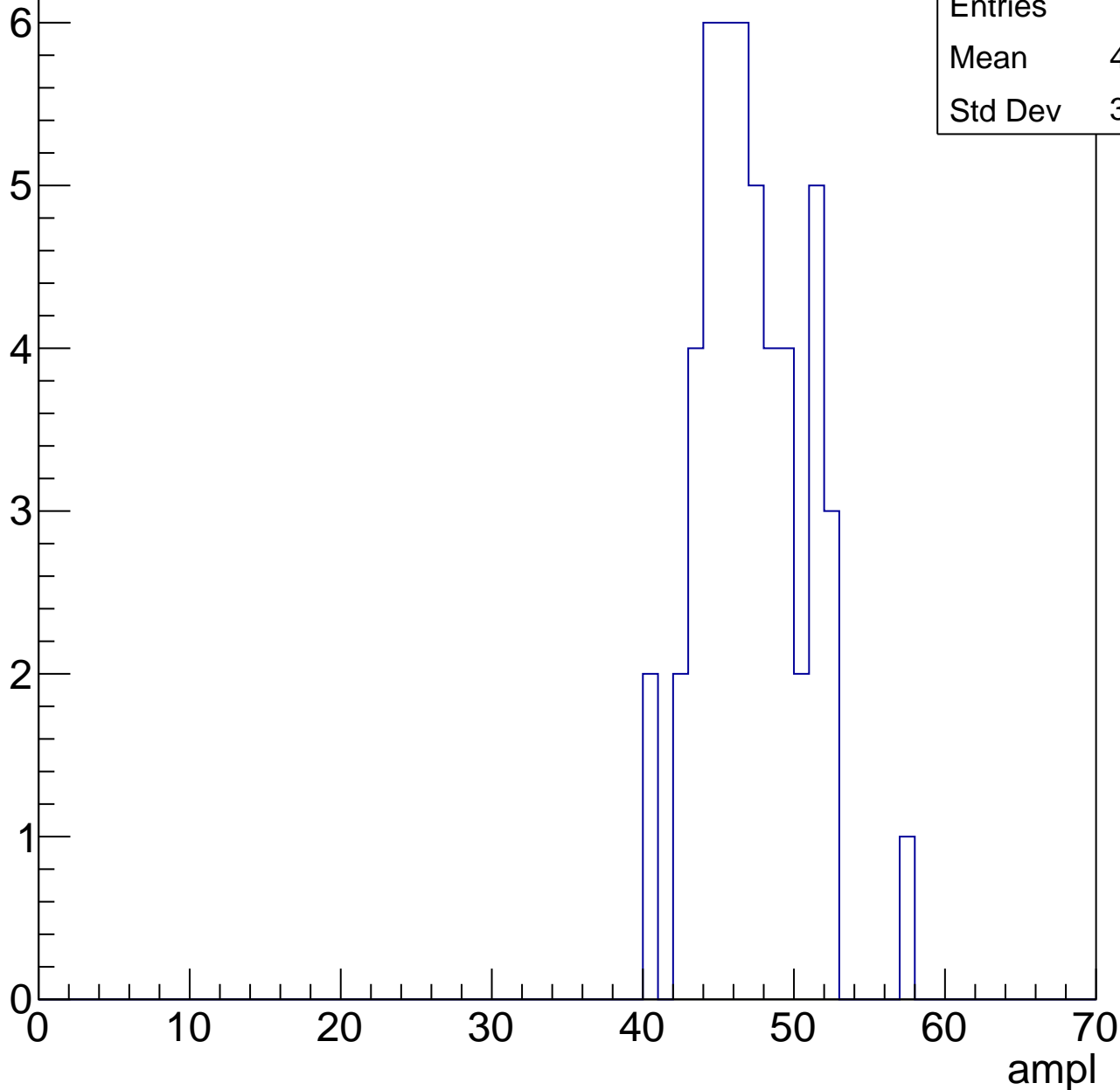


B1L103S, U14-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	46.74
Std Dev	3.422

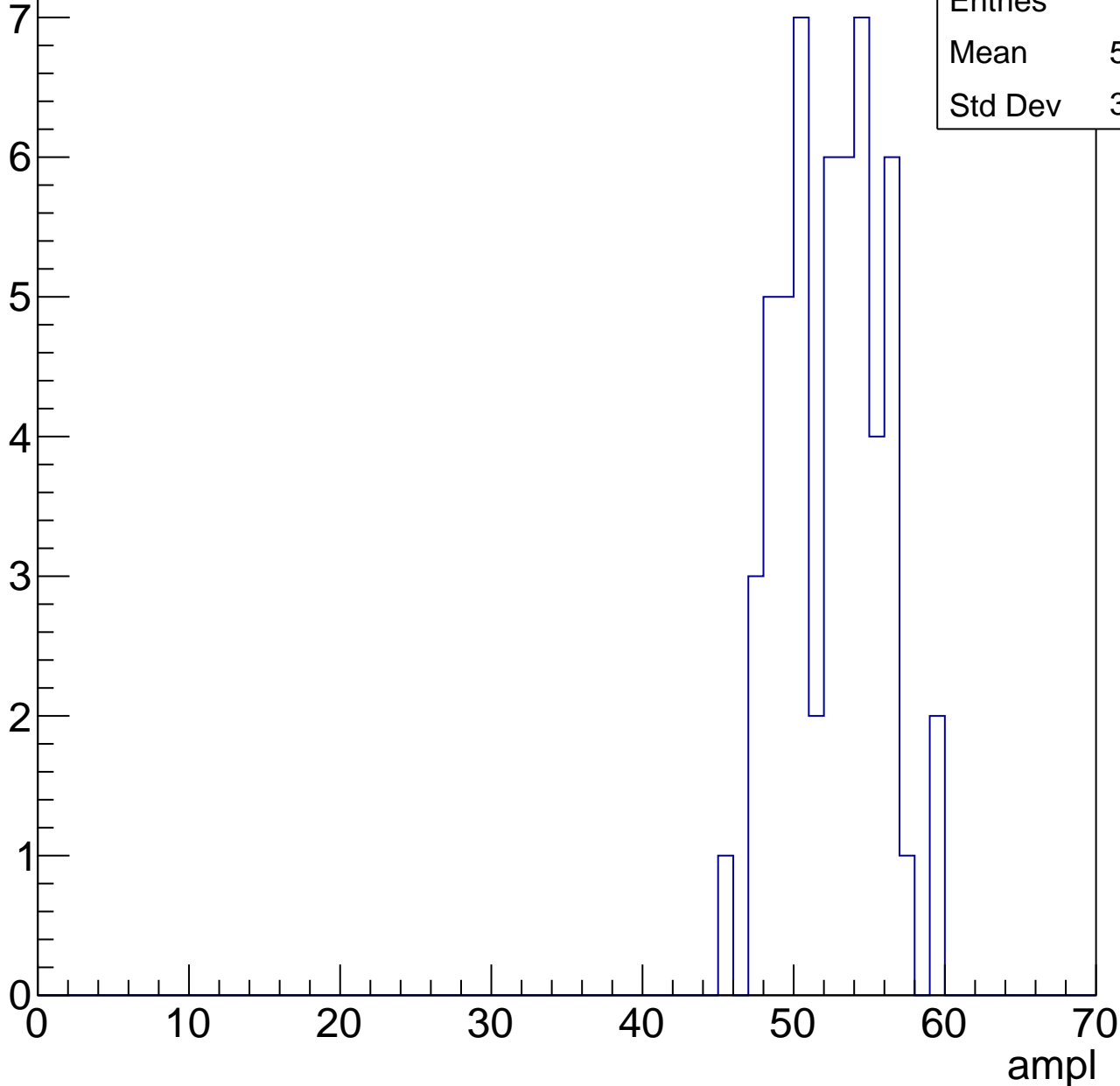


B1L103S, U14-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

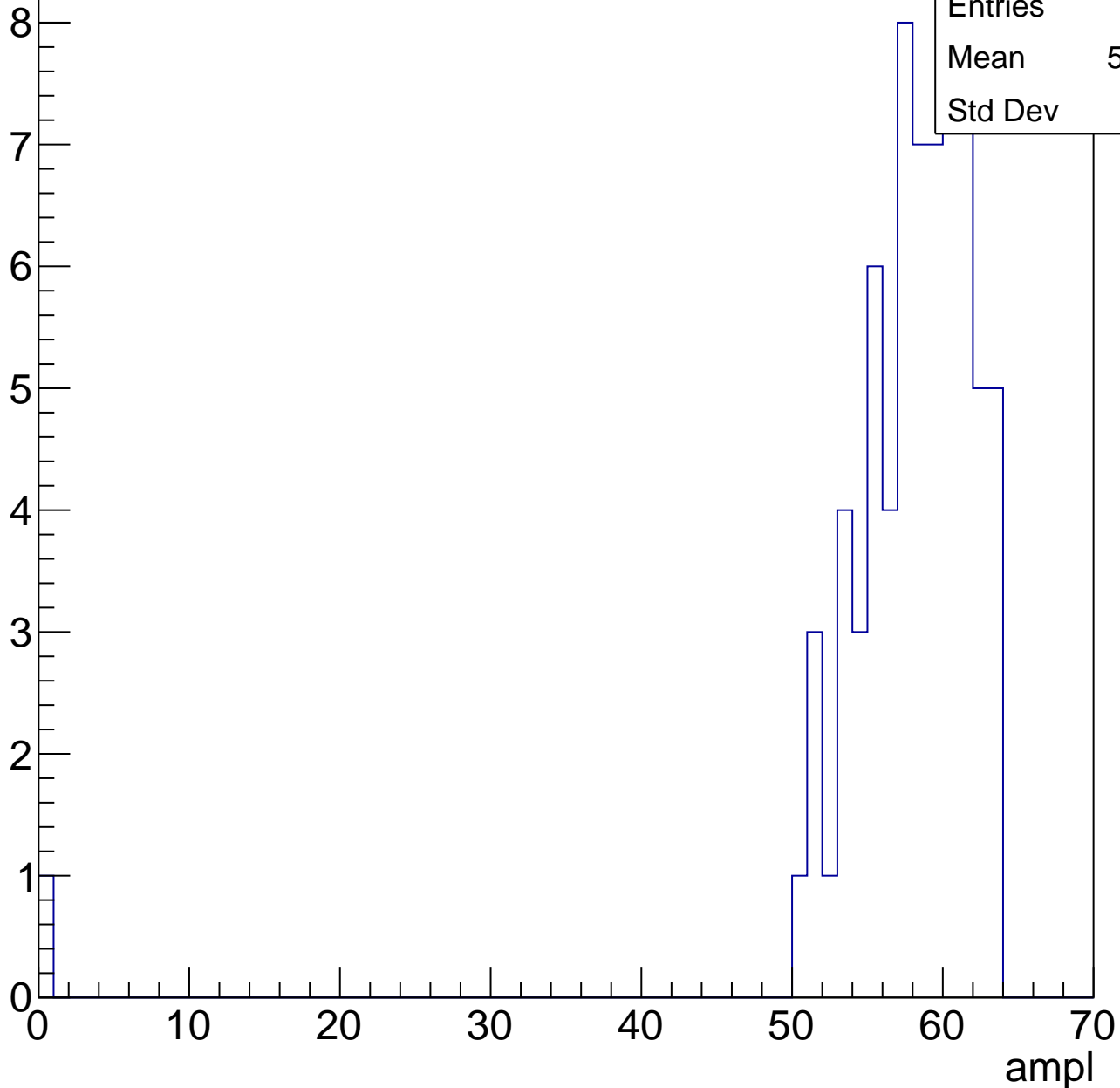
Entries	55
Mean	52.04
Std Dev	3.213



B1L103S, U14-ch44, adc5

calib_packv5_042523_0143.root, FC#7, port C2

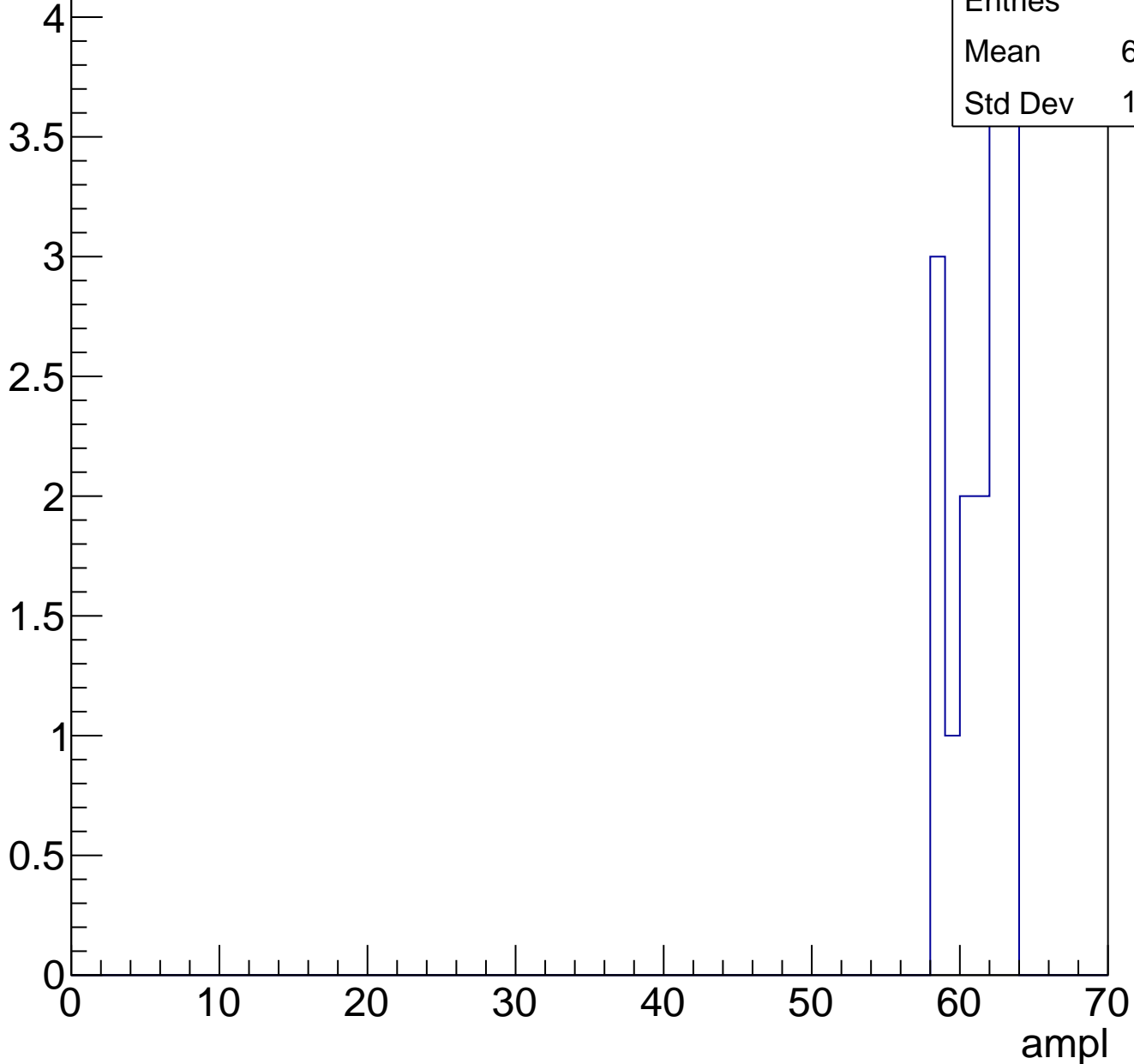
Entry



B1L103S, U14-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

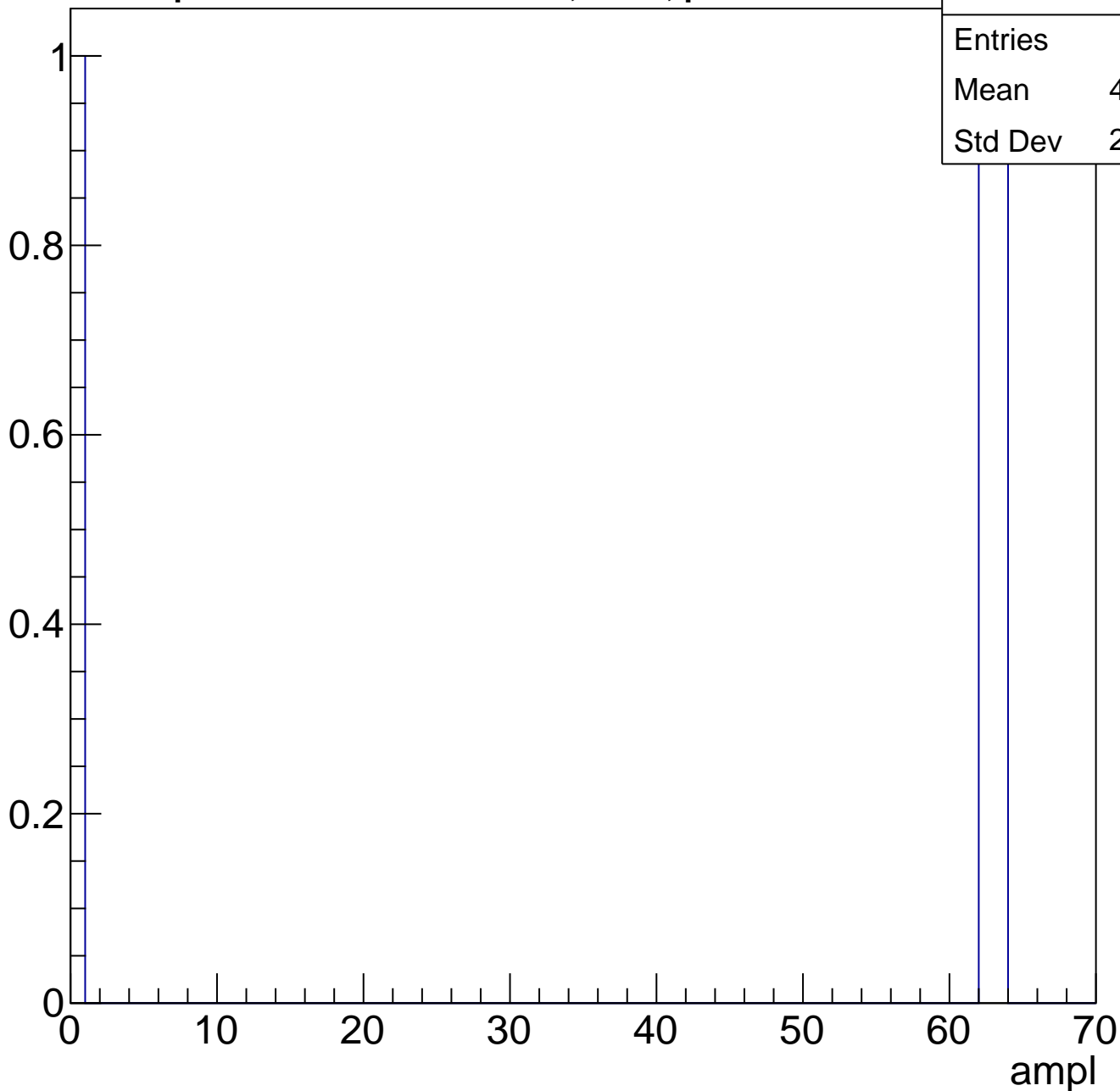
Entry



B1L103S, U14-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch45, adc0

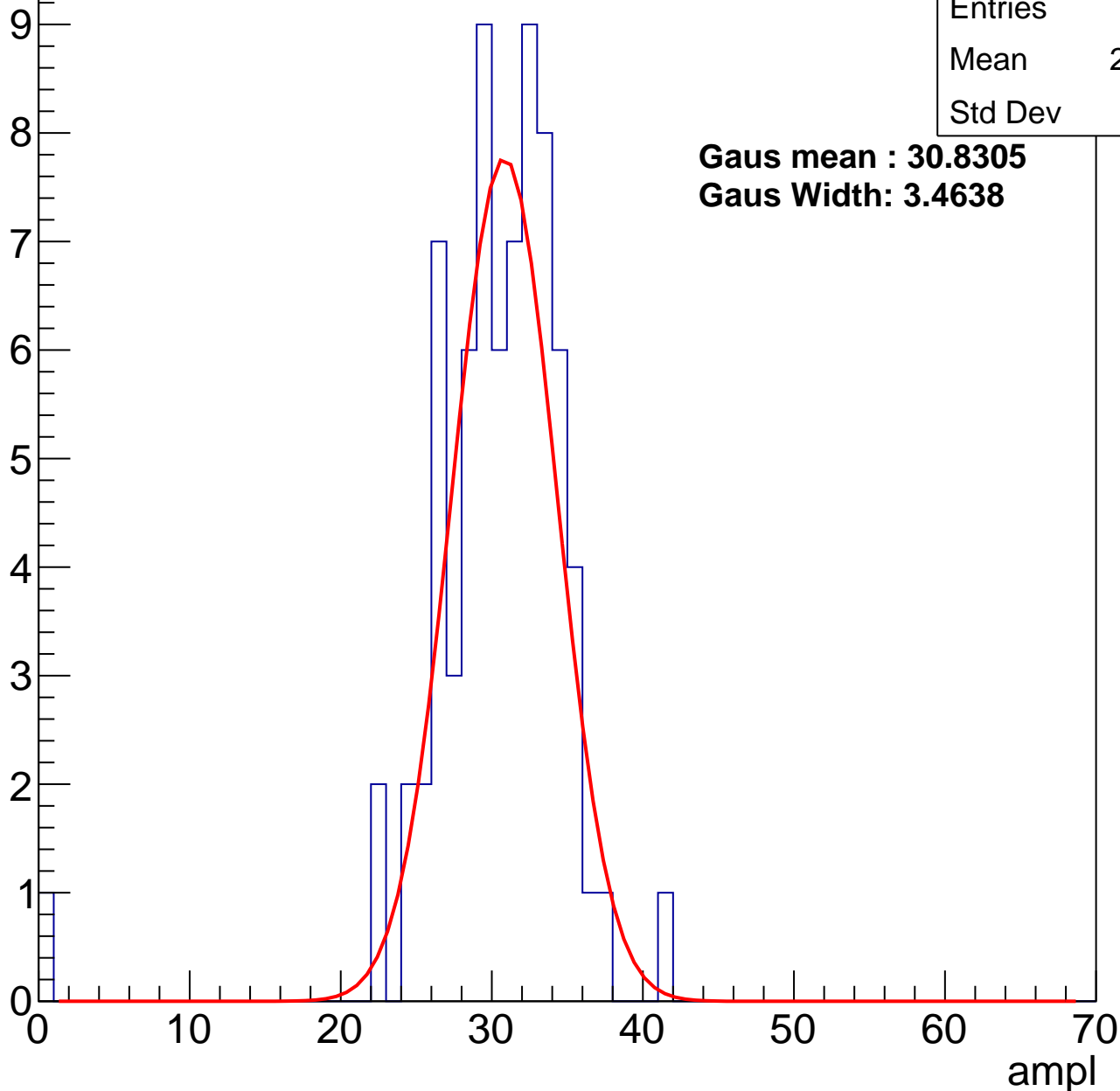
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	29.88
Std Dev	4.95

Gaus mean : 30.8305

Gaus Width: 3.4638



B1L103S, U14-ch45, adc1

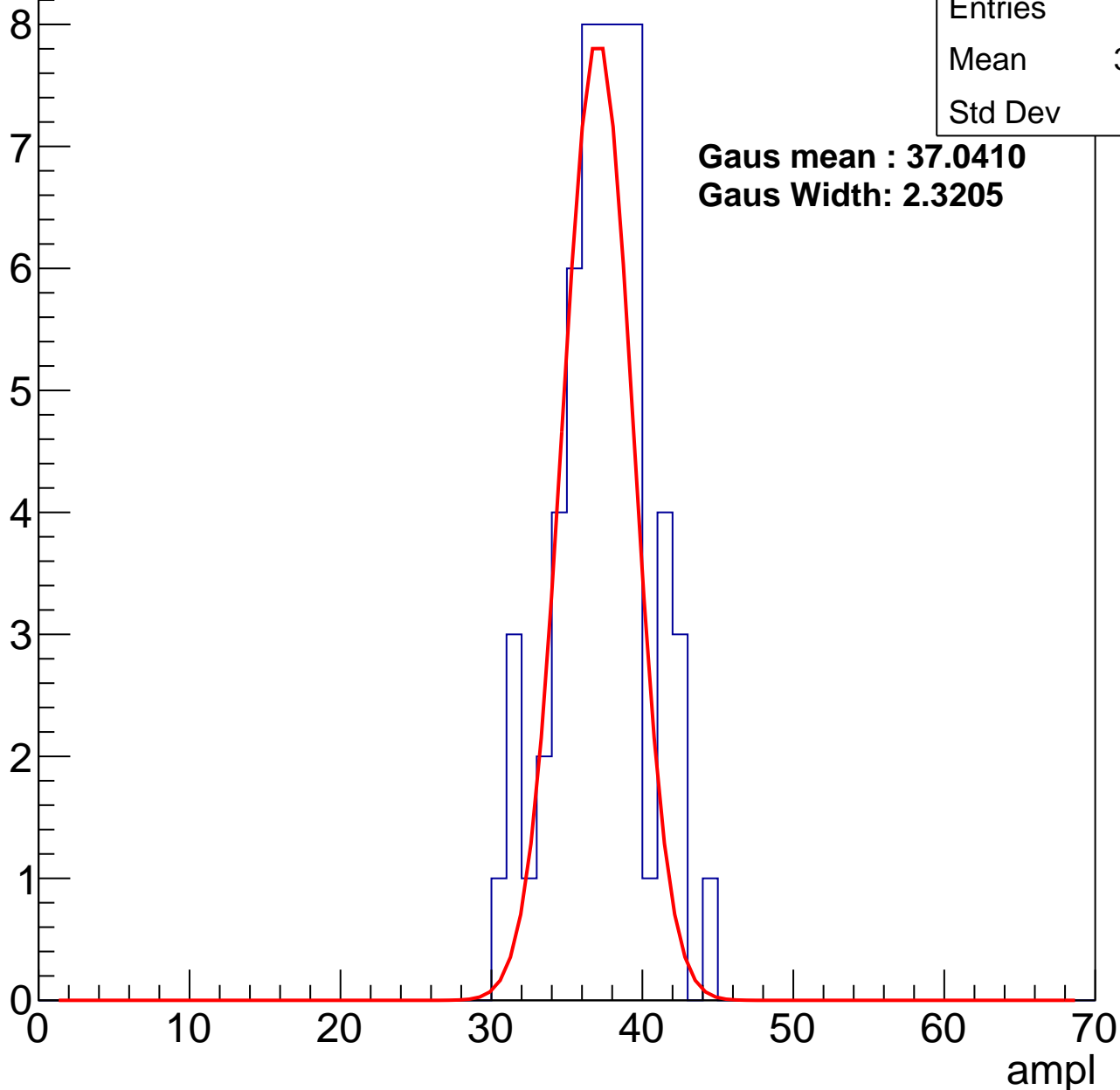
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	36.91
Std Dev	2.99

Gaus mean : 37.0410

Gaus Width: 2.3205



B1L103S, U14-ch45, adc2

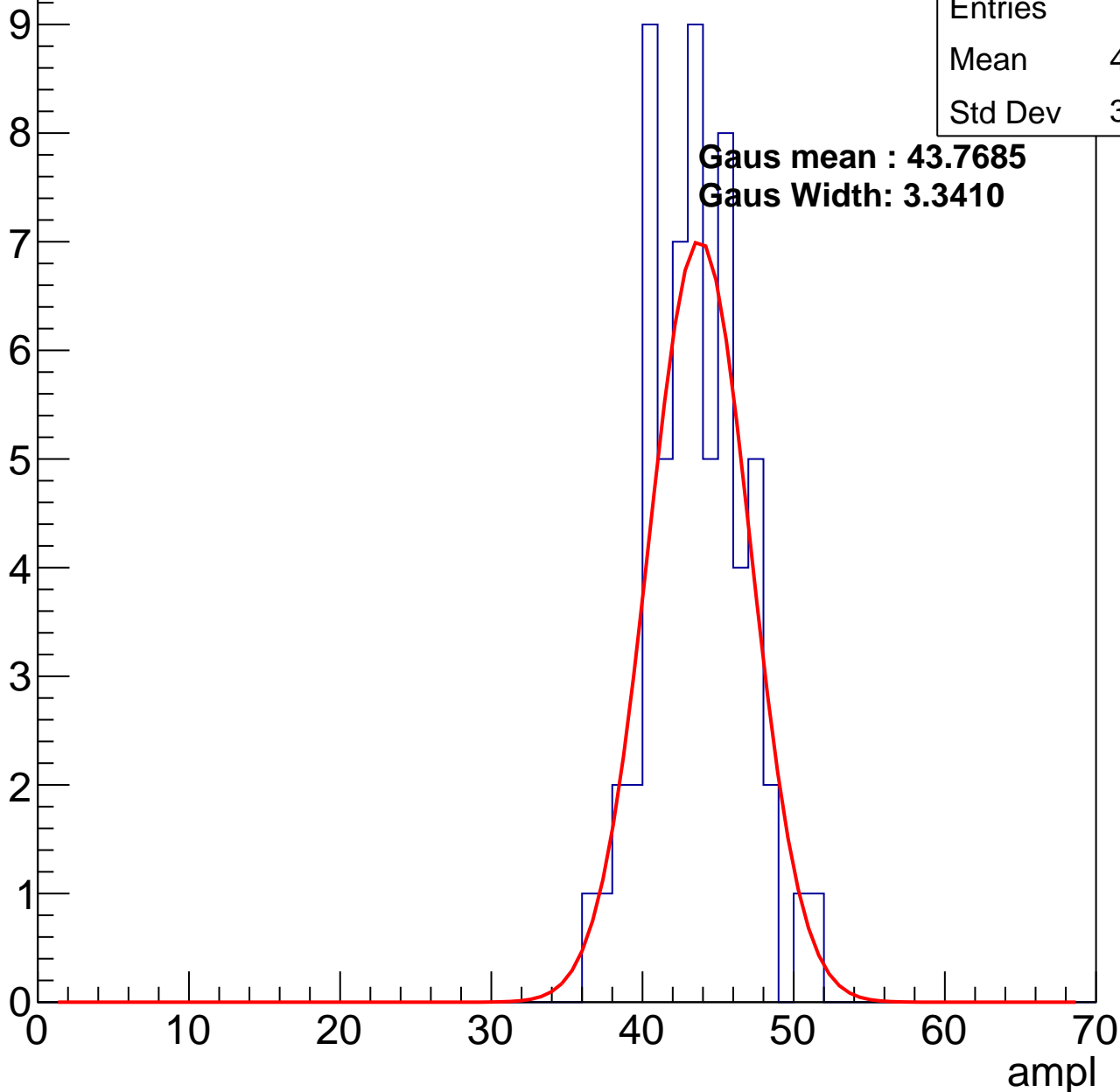
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.05
Std Dev	3.087

Gaus mean : 43.7685

Gaus Width: 3.3410

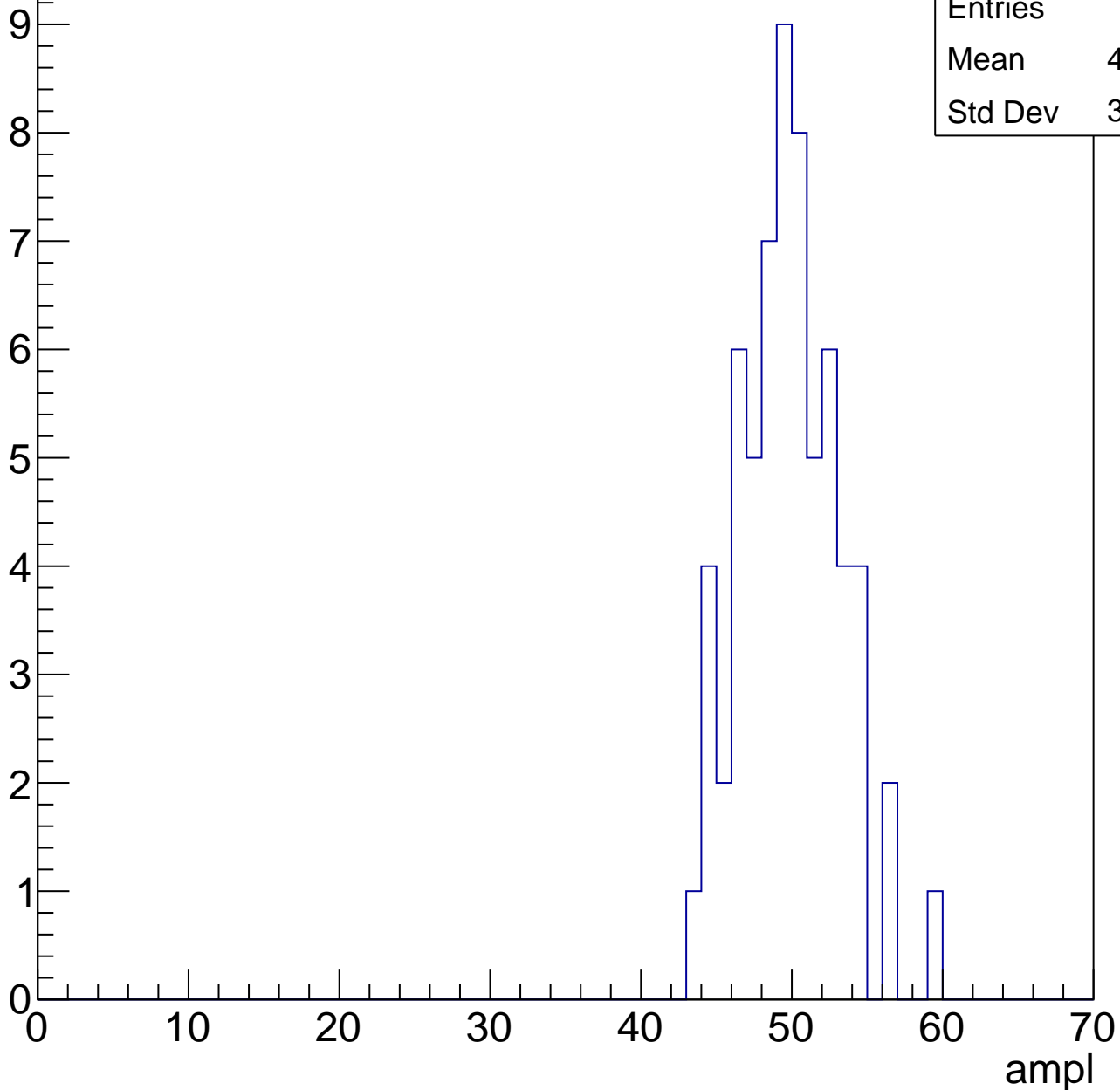


B1L103S, U14-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	49.42
Std Dev	3.263

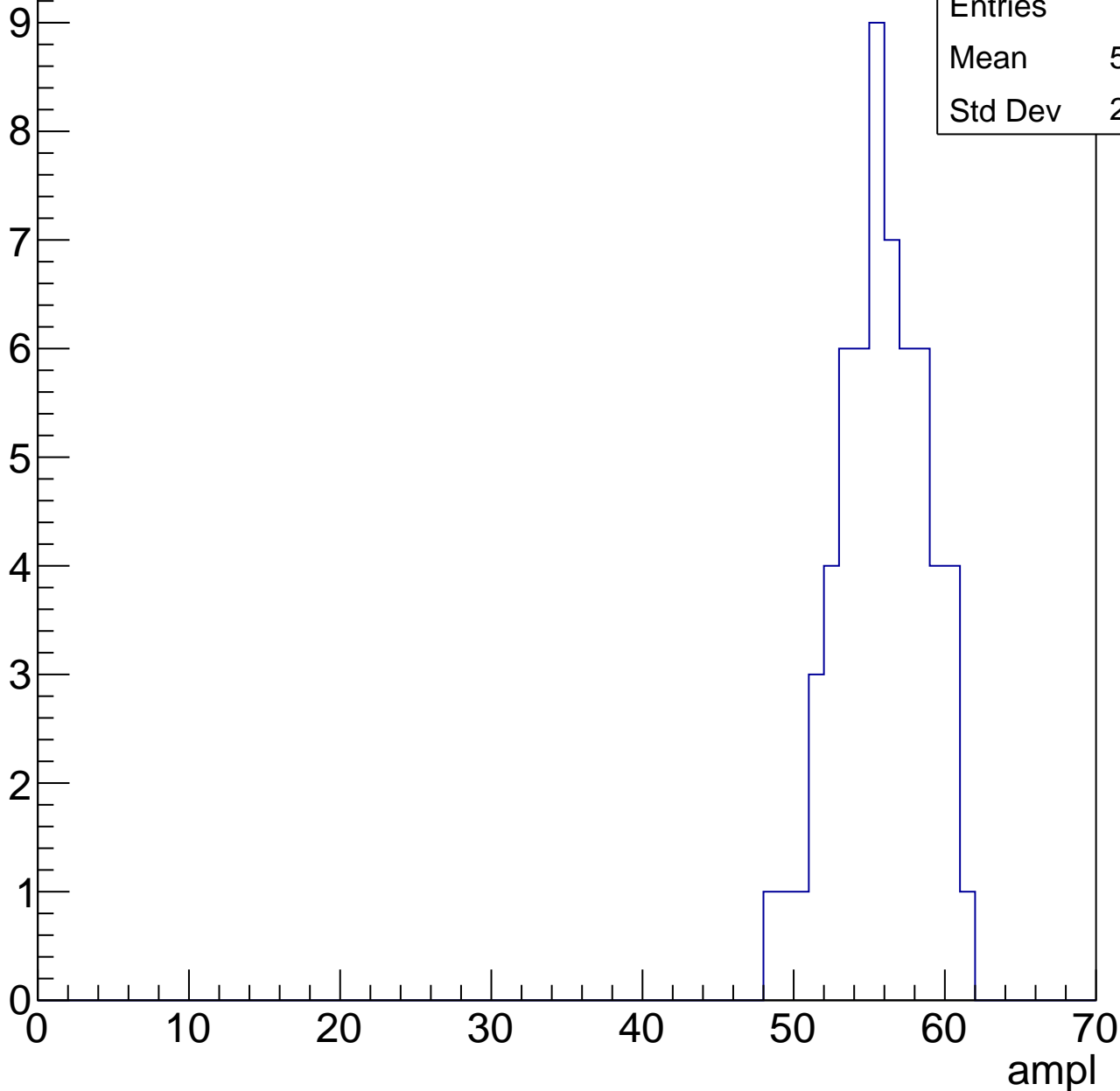


B1L103S, U14-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.32
Std Dev	2.919

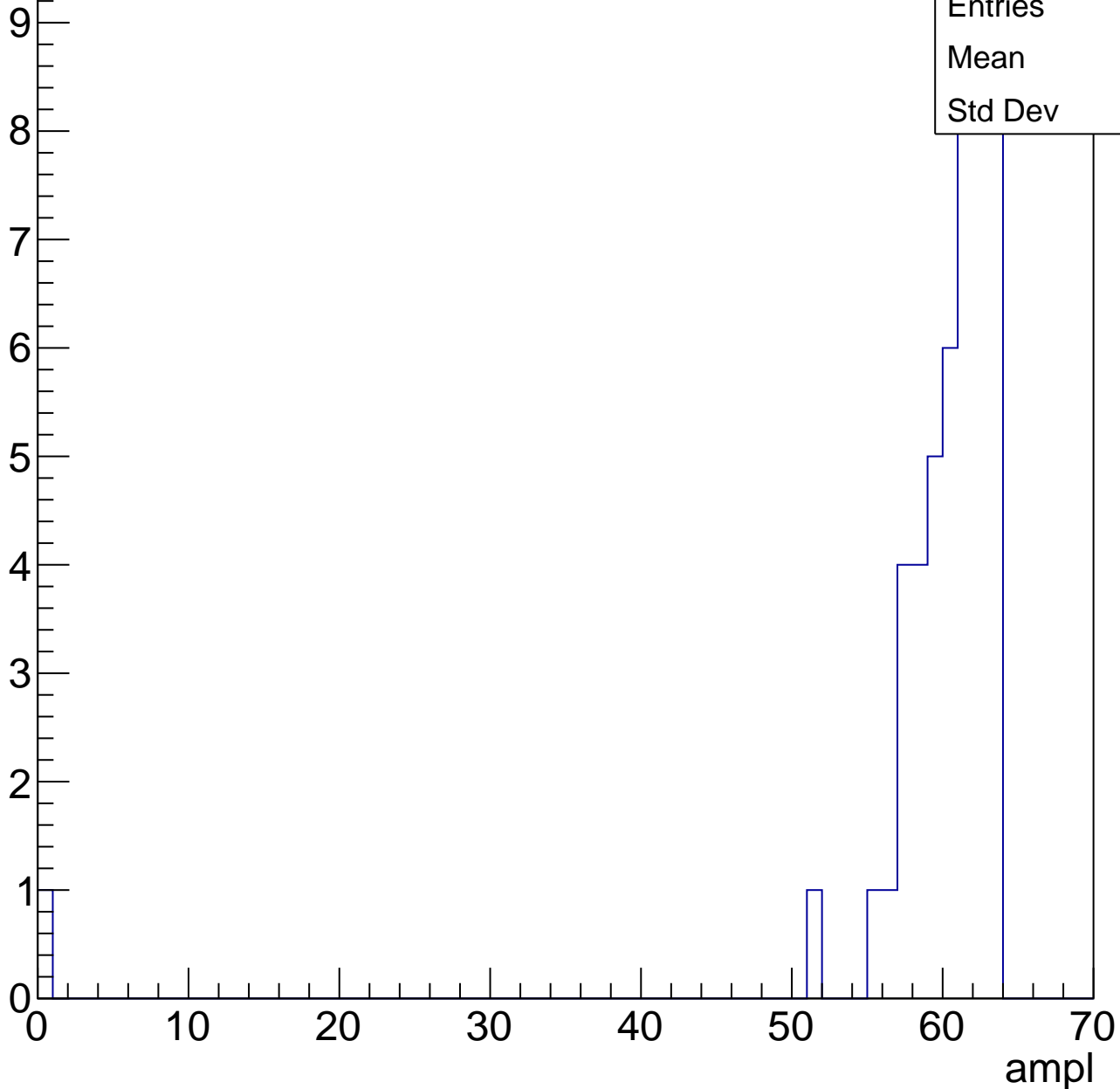


B1L103S, U14-ch45, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.9
Std Dev	8.94



B1L103S, U14-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch46, adc0

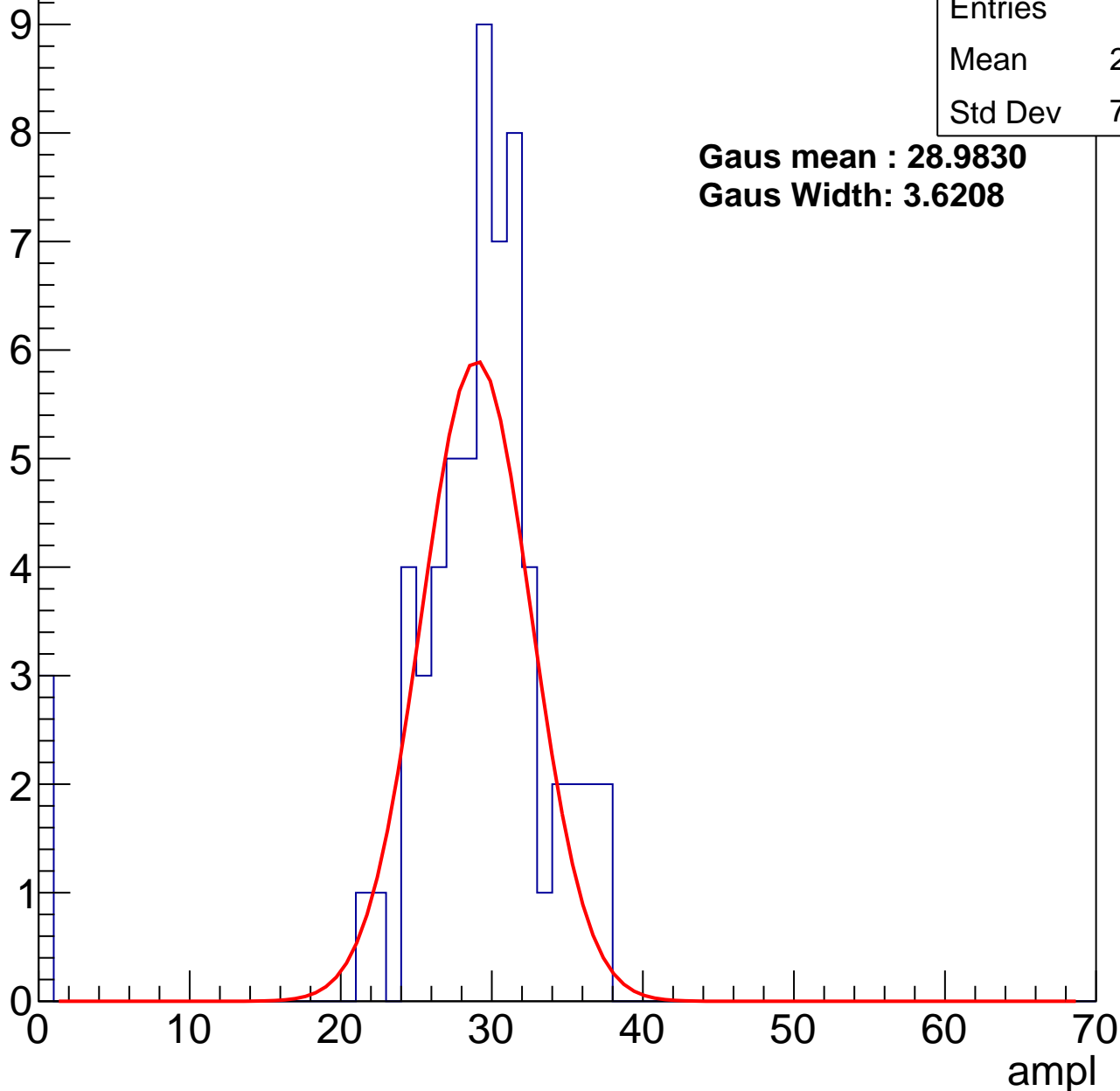
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	27.89
Std Dev	7.134

Gaus mean : 28.9830

Gaus Width: 3.6208



B1L103S, U14-ch46, adc1

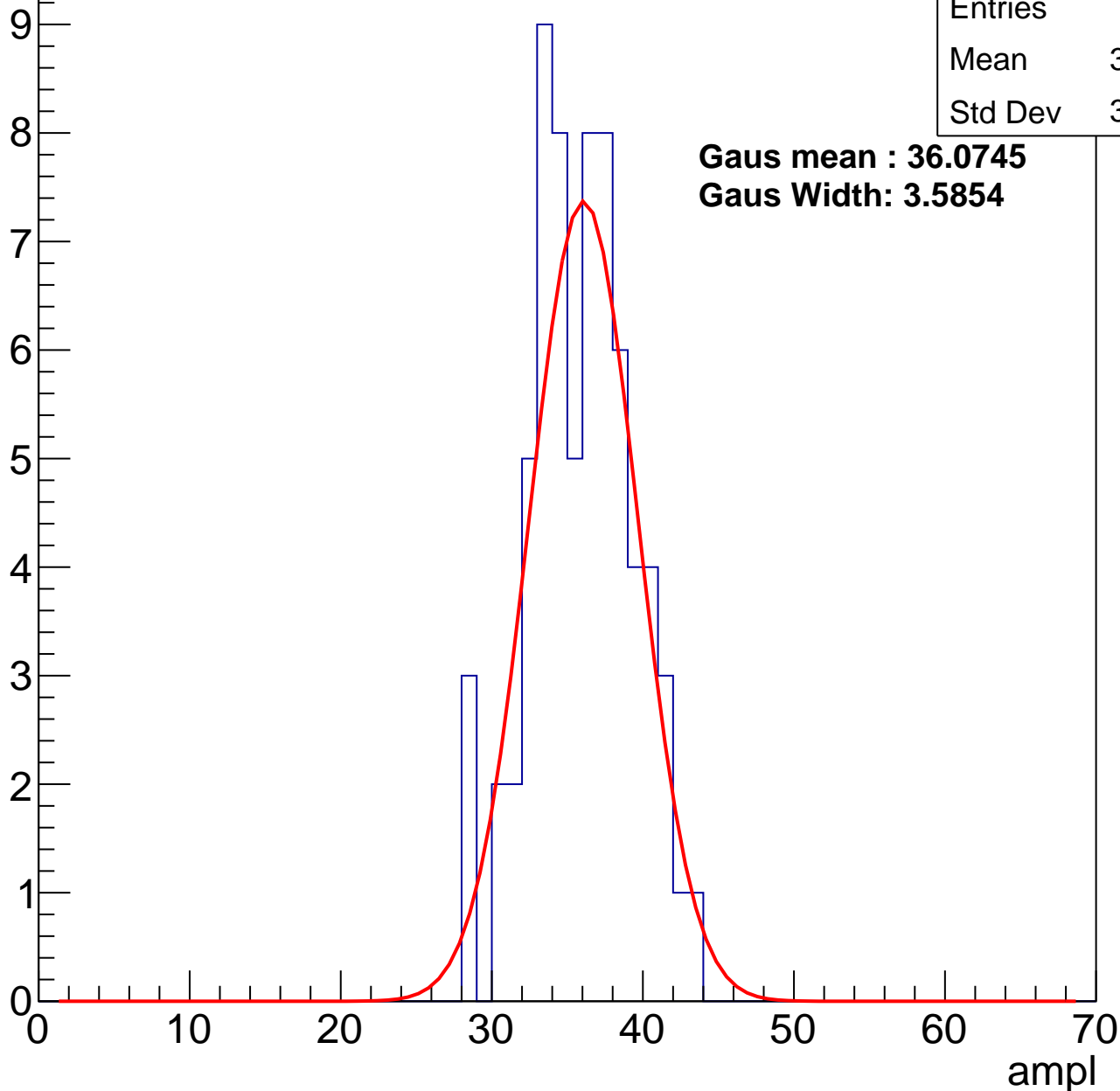
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.45
Std Dev	3.369

Gaus mean : 36.0745

Gaus Width: 3.5854



B1L103S, U14-ch46, adc2

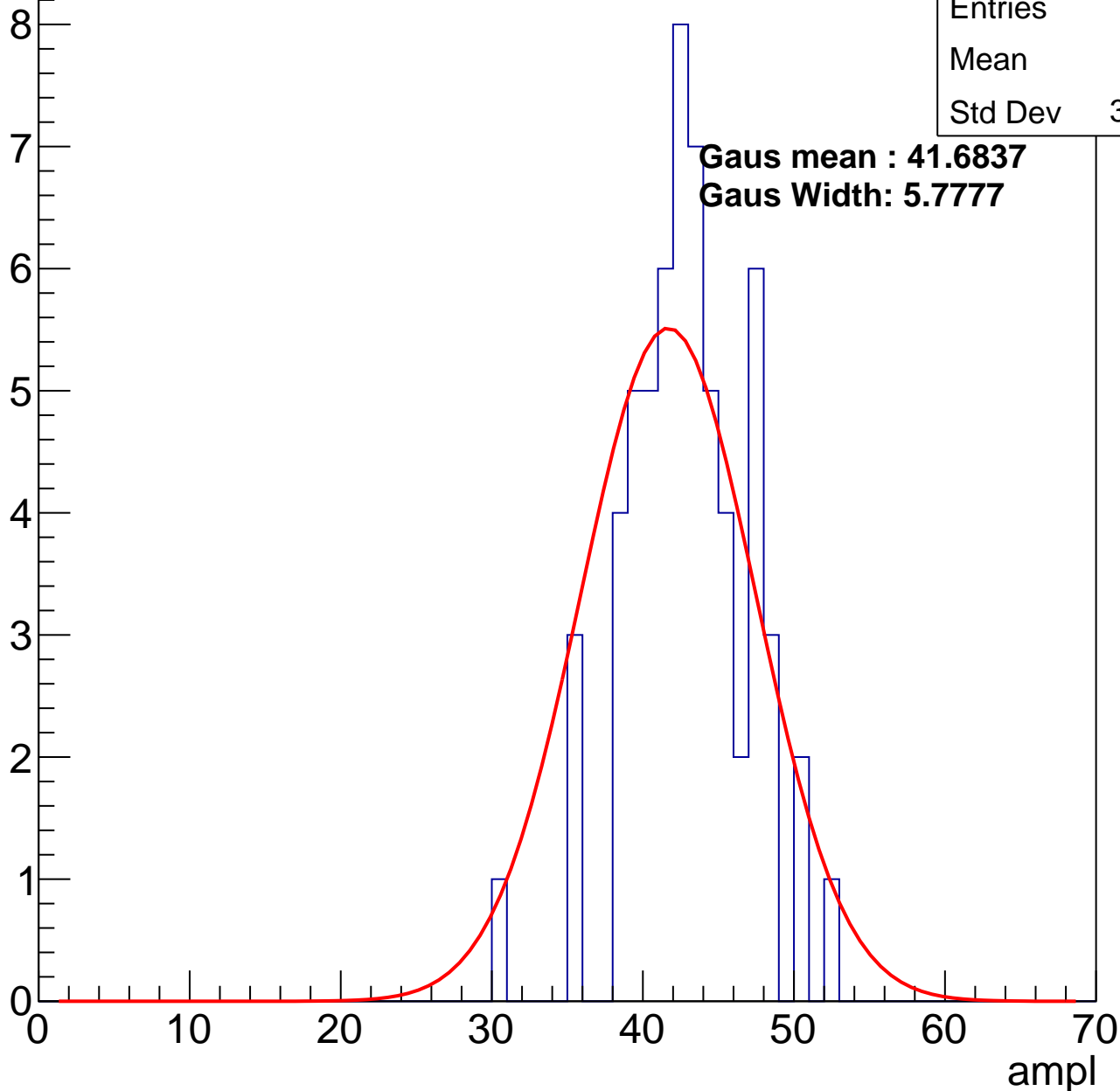
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.5
Std Dev	3.999

Gaus mean : 41.6837

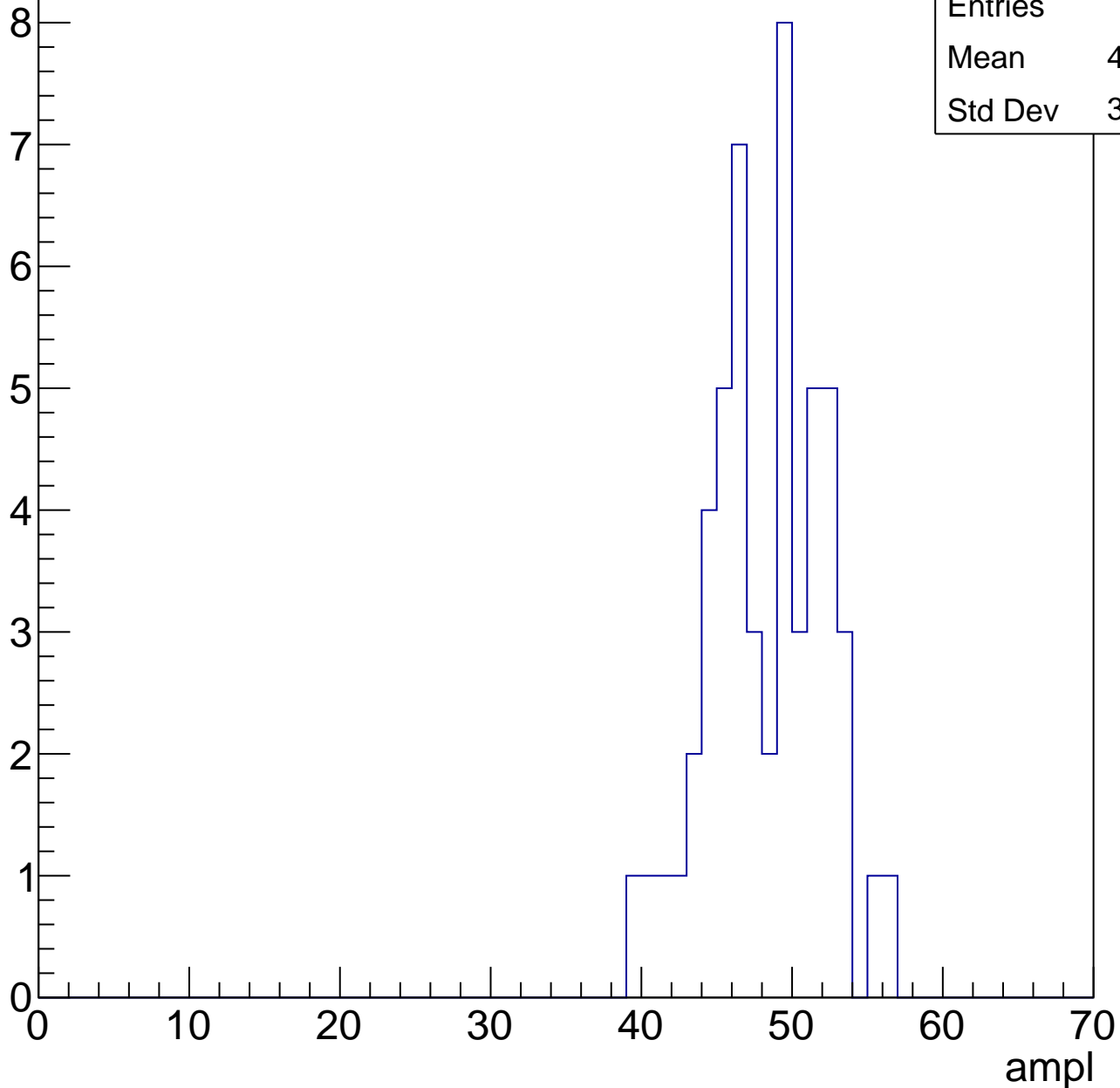
Gaus Width: 5.7777



B1L103S, U14-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

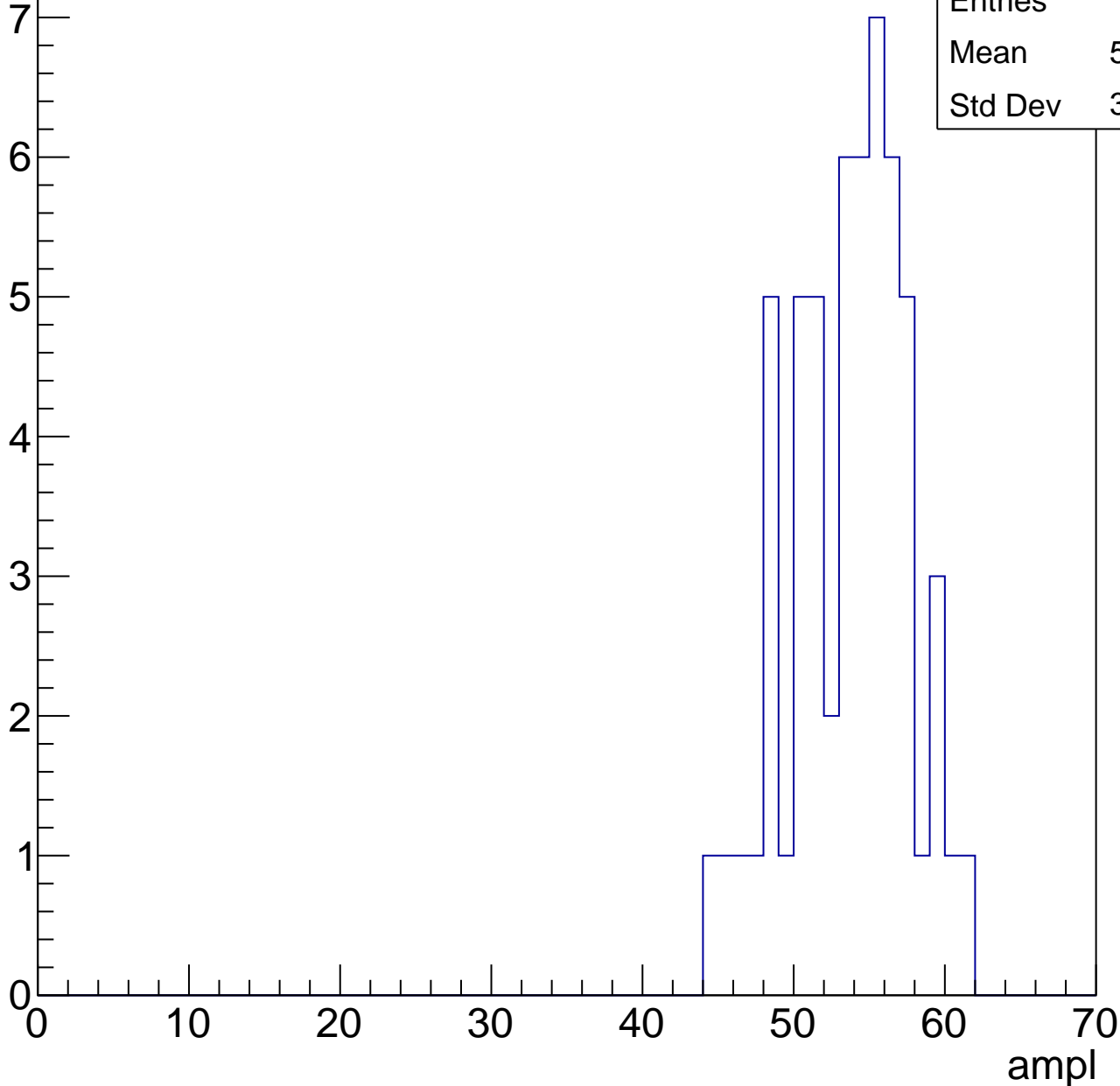


B1L103S, U14-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

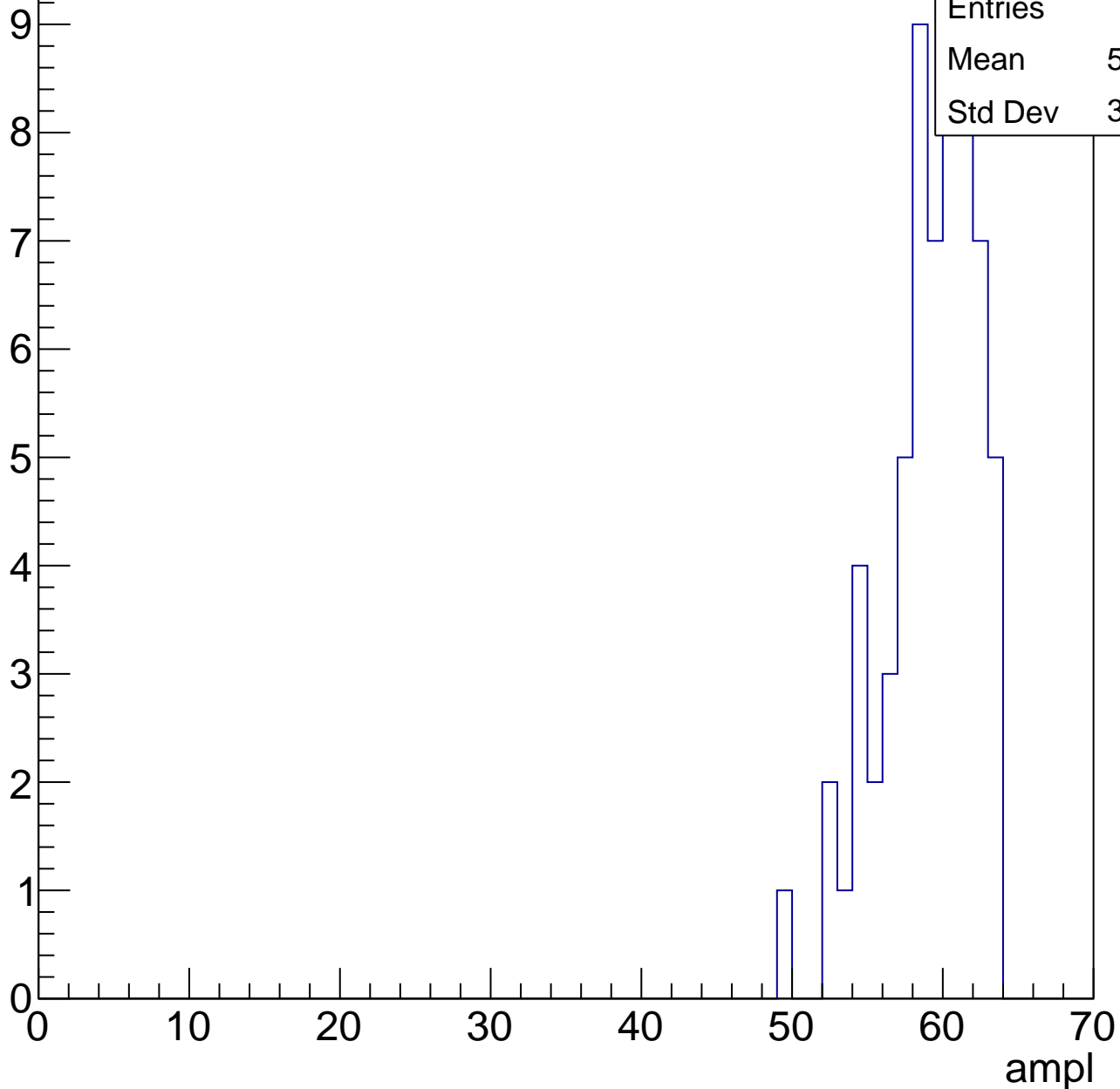
Entries	58
Mean	53.17
Std Dev	3.829



B1L103S, U14-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

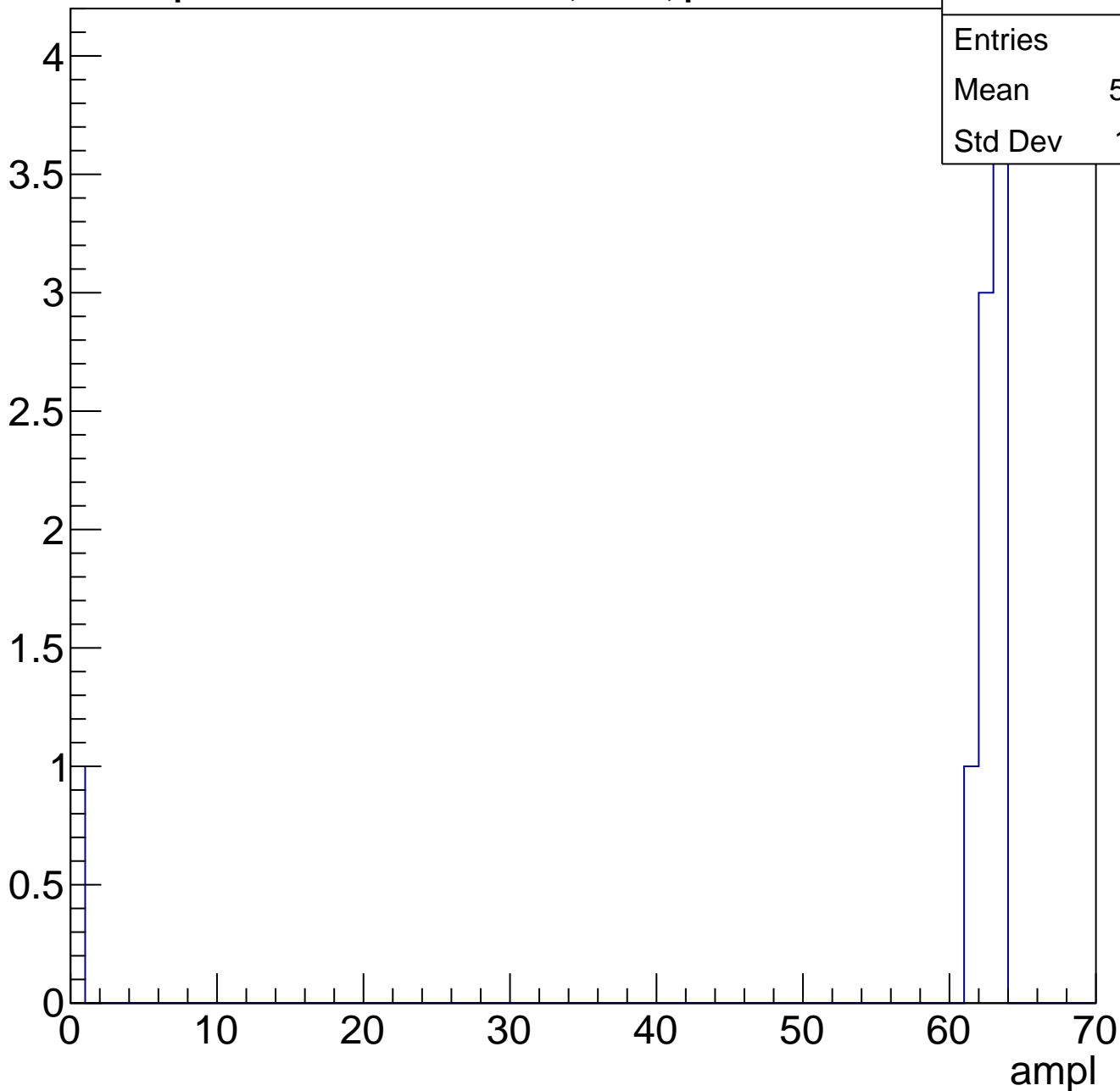
Entry



B1L103S, U14-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch47, adc0

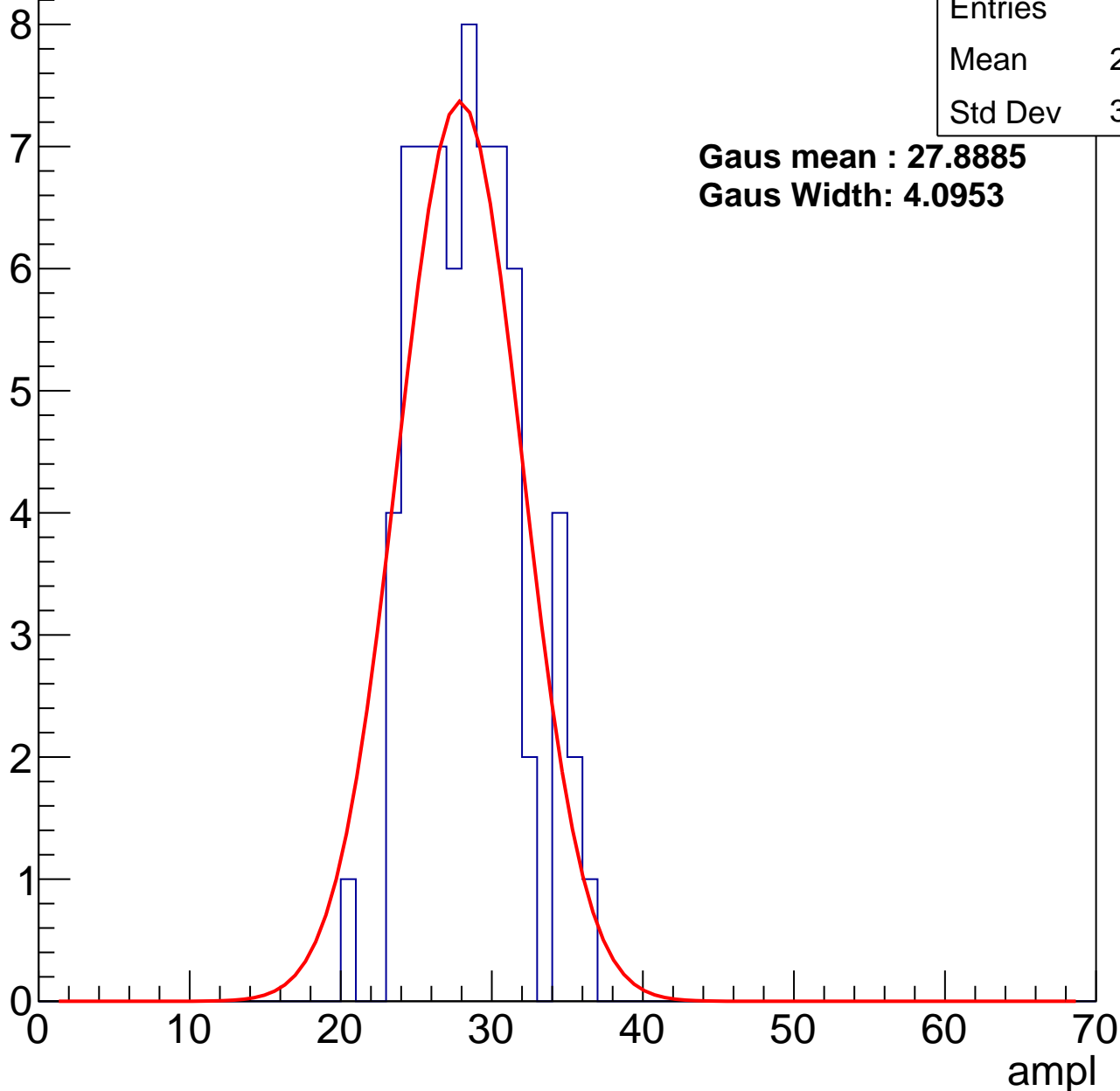
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	27.94
Std Dev	3.405

Gaus mean : 27.8885

Gaus Width: 4.0953



B1L103S, U14-ch47, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	35.42
Std Dev	3.232

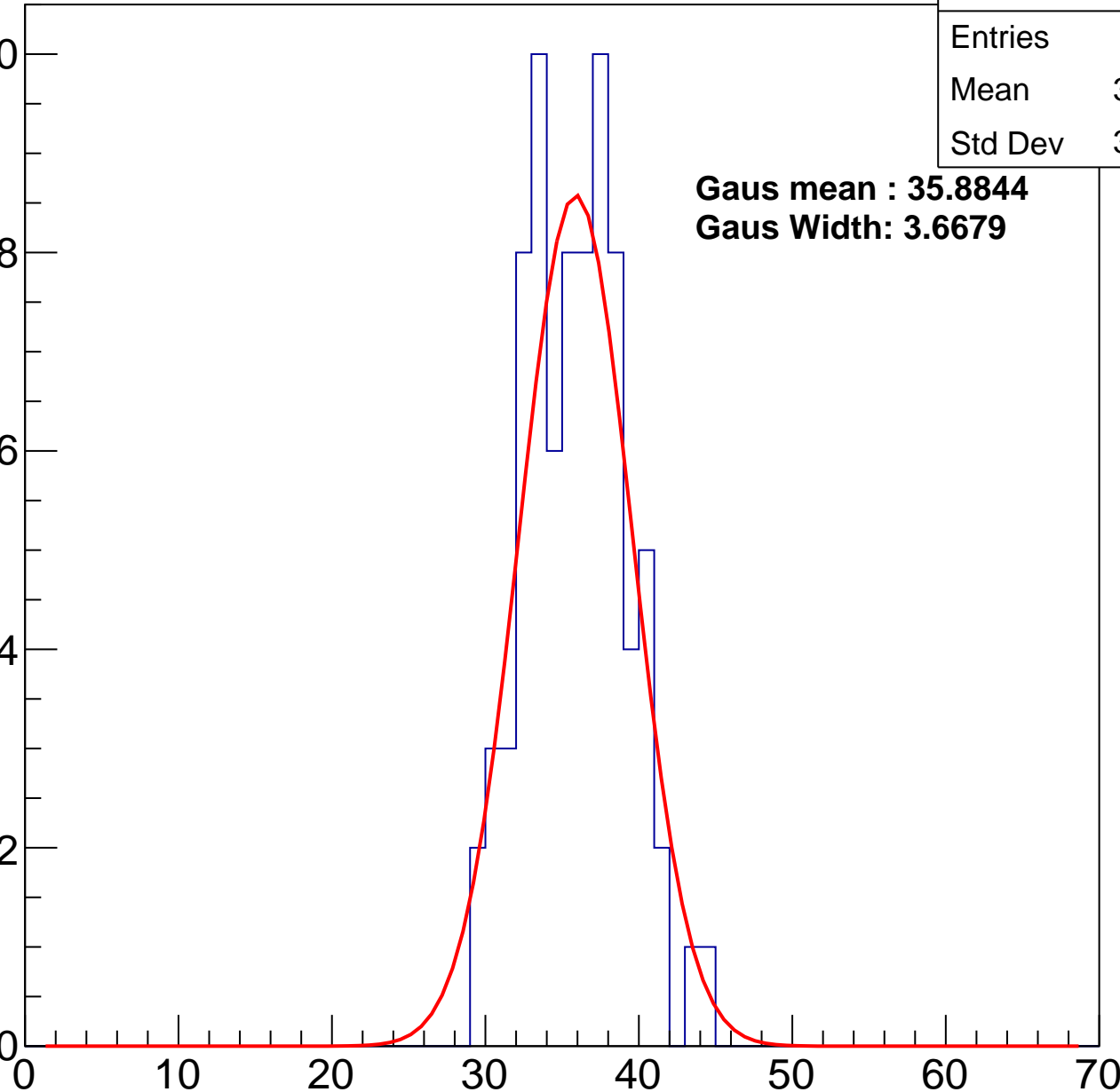
Gaus mean : 35.8844

Gaus Width: 3.6679

Entry

10
8
6
4
2
0

ampl



B1L103S, U14-ch47, adc2

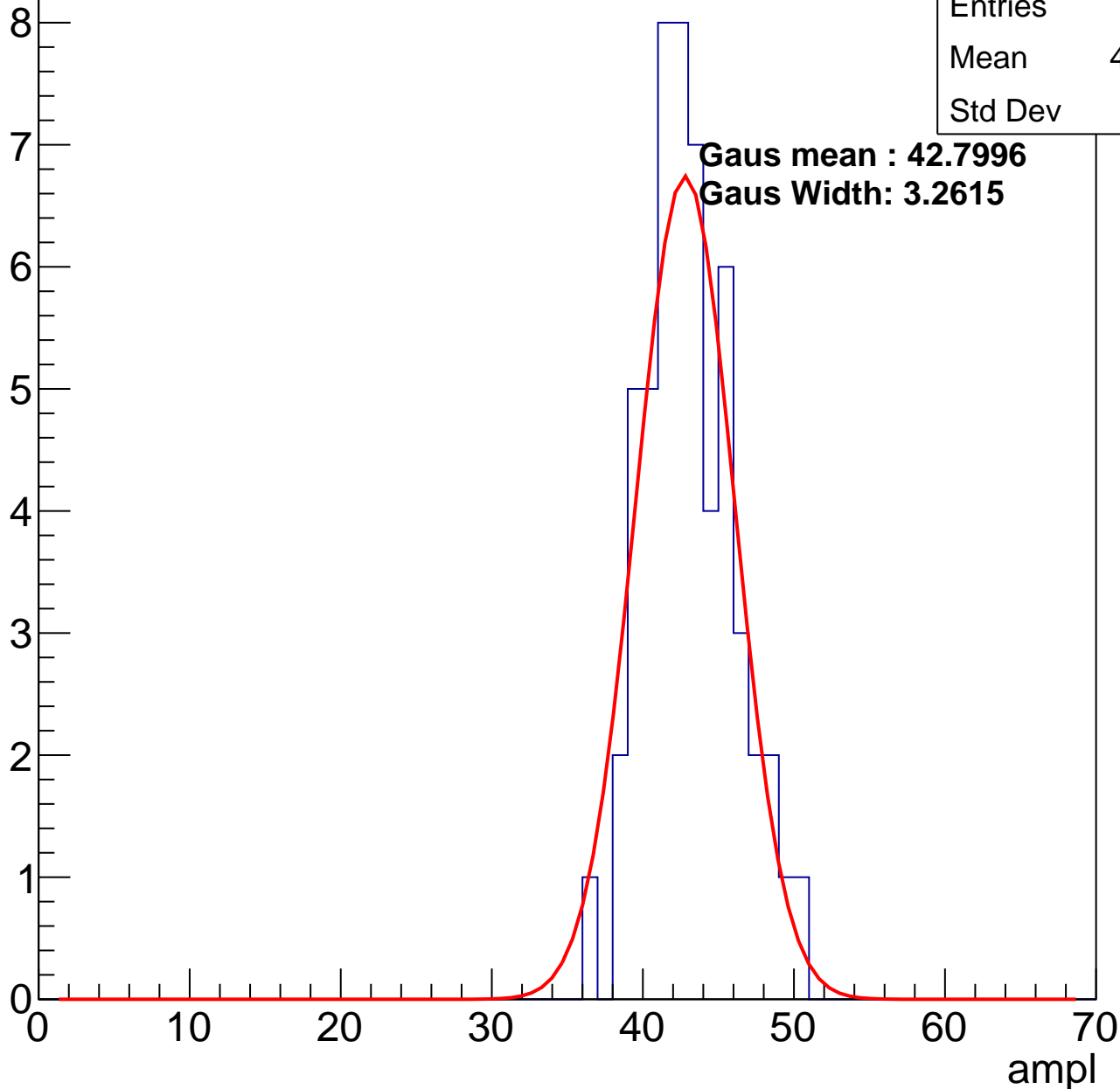
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.64
Std Dev	2.95

Gaus mean : 42.7996

Gaus Width: 3.2615

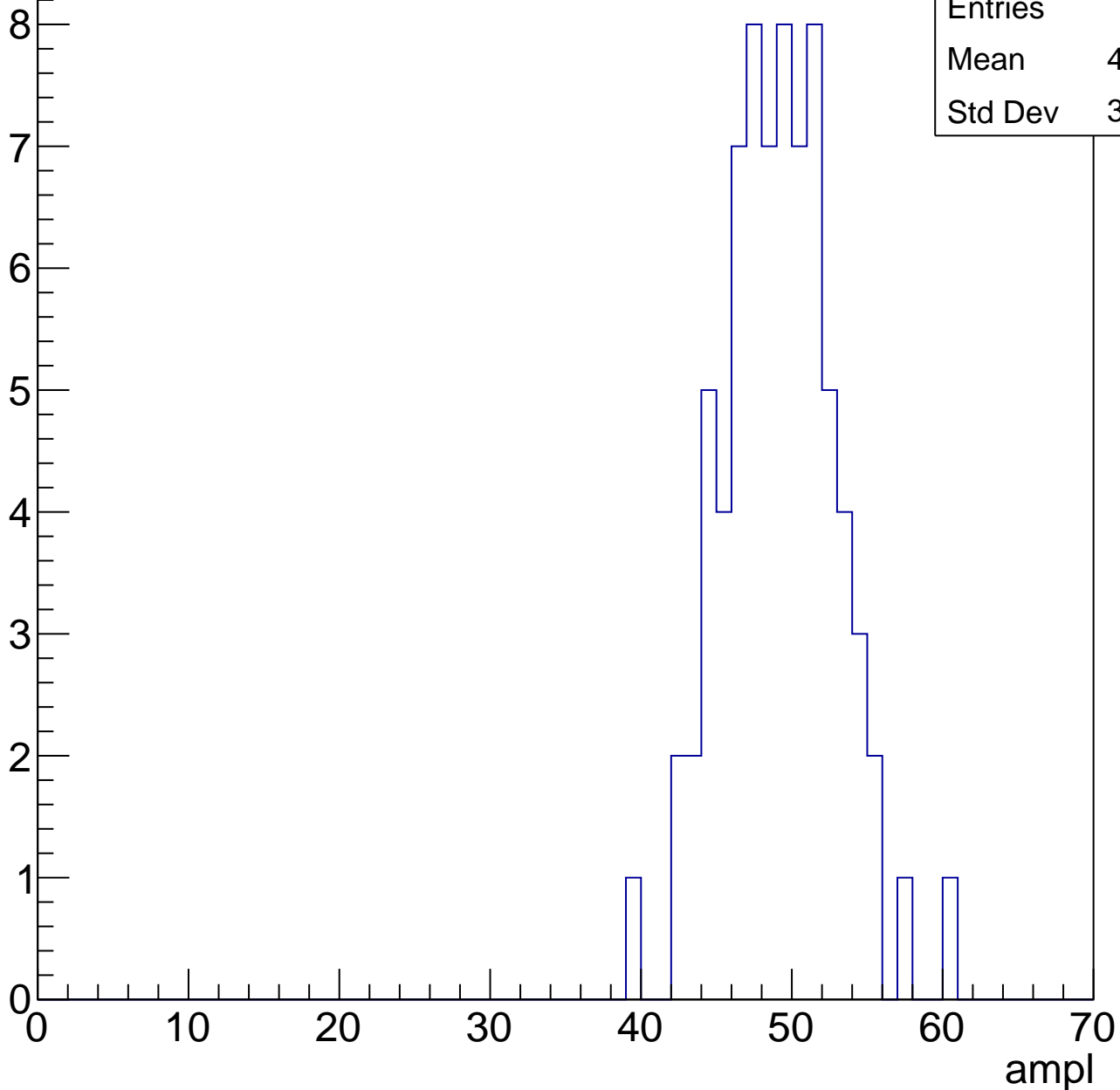


B1L103S, U14-ch47, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

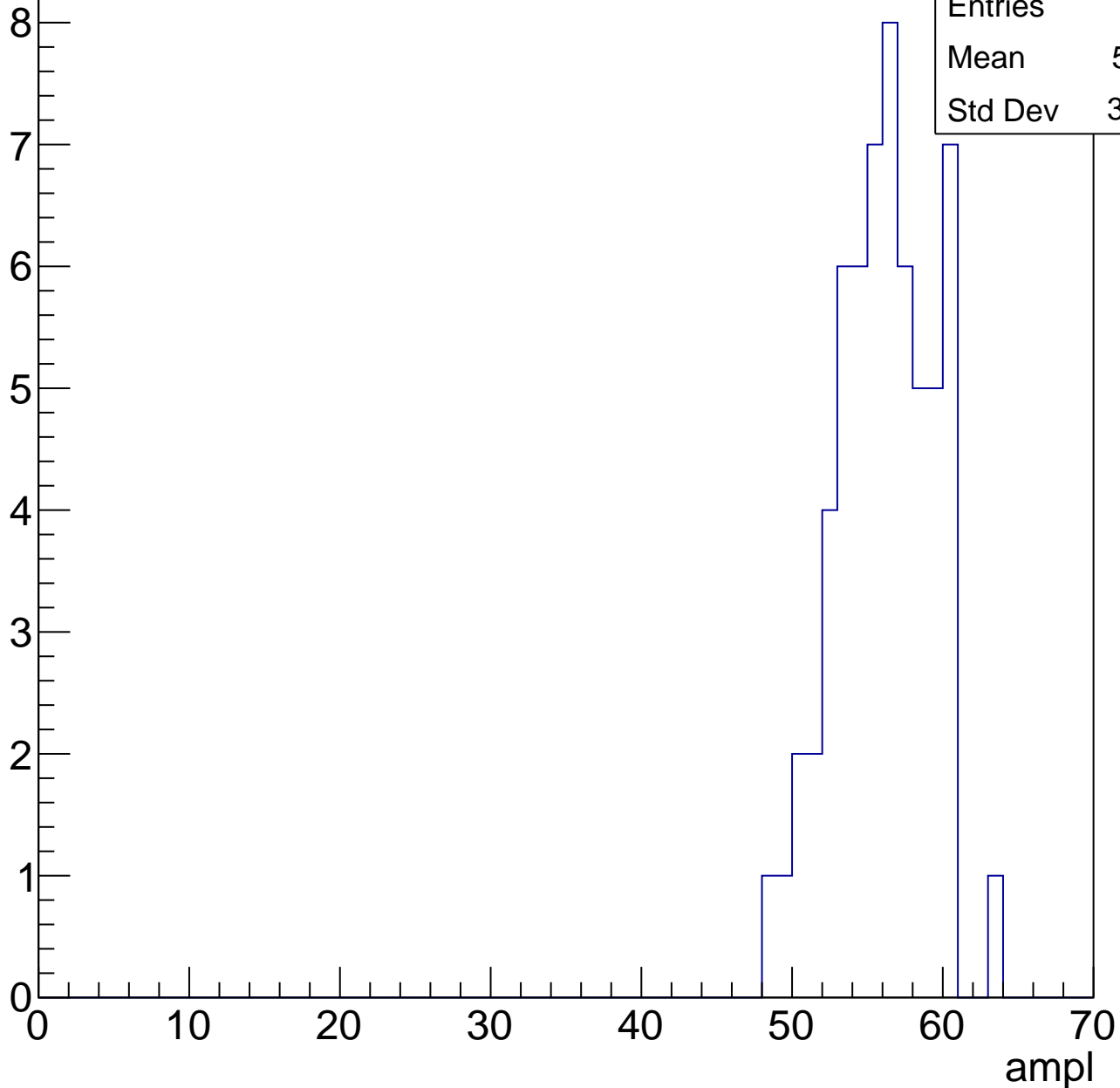
Entries	75
Mean	48.72
Std Dev	3.715



B1L103S, U14-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



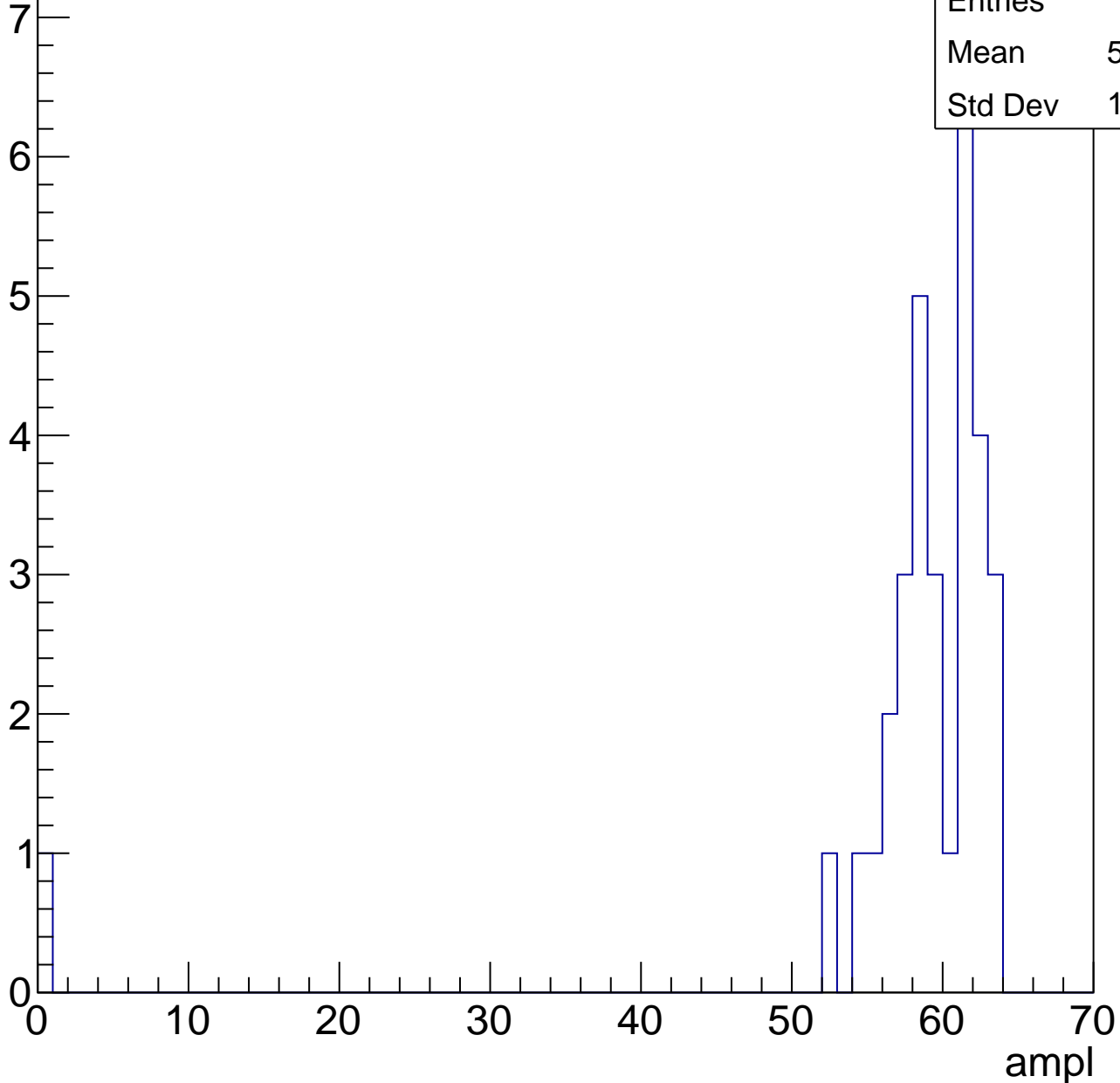
Entries	61
Mean	55.61
Std Dev	3.153

B1L103S, U14-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

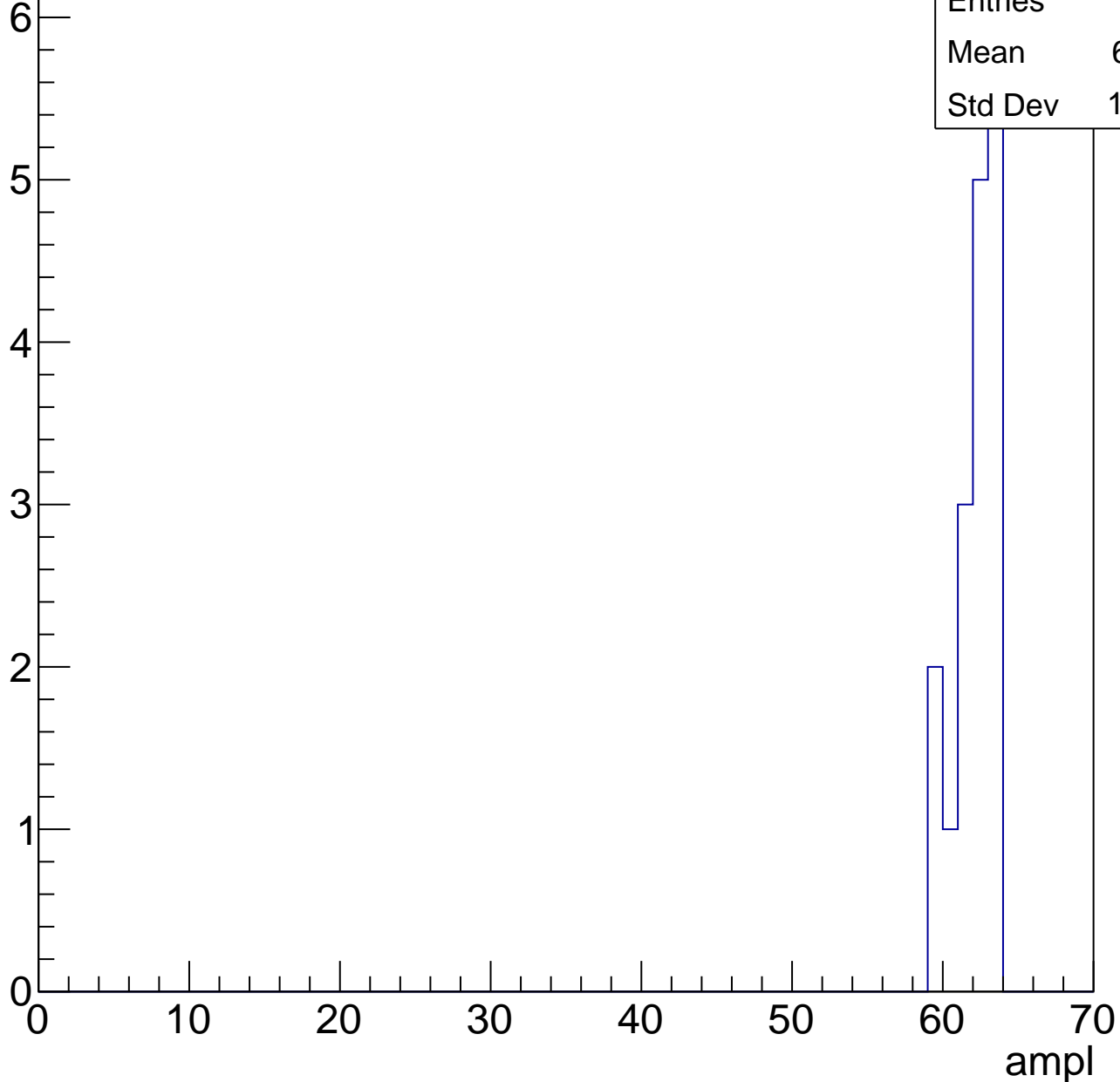
Entries	32
Mean	57.34
Std Dev	10.65



B1L103S, U14-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	17
Mean	61.71
Std Dev	1.318

B1L103S, U14-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch48, adc0

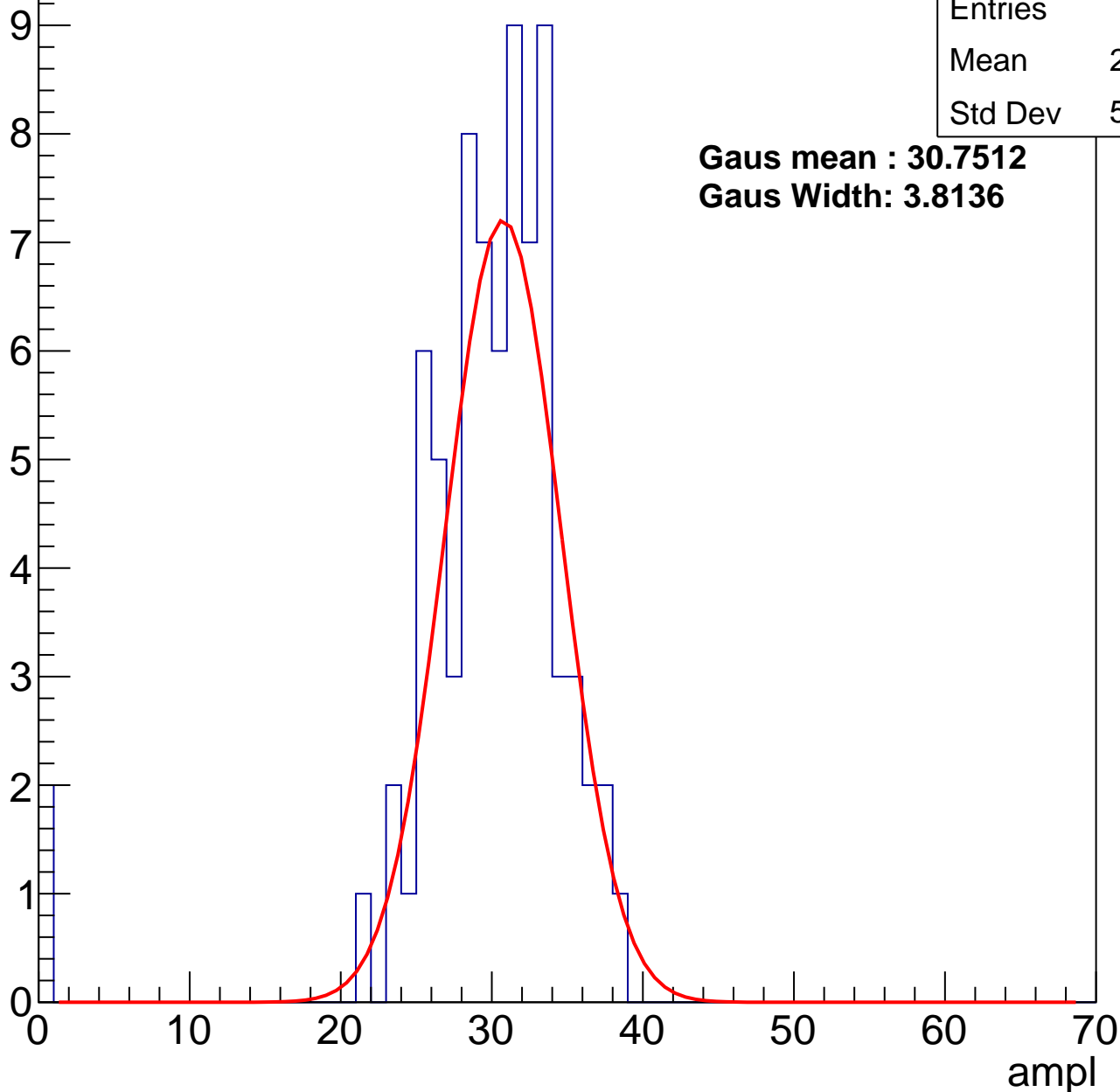
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	29.22
Std Dev	5.964

Gaus mean : 30.7512

Gaus Width: 3.8136



B1L103S, U14-ch48, adc1

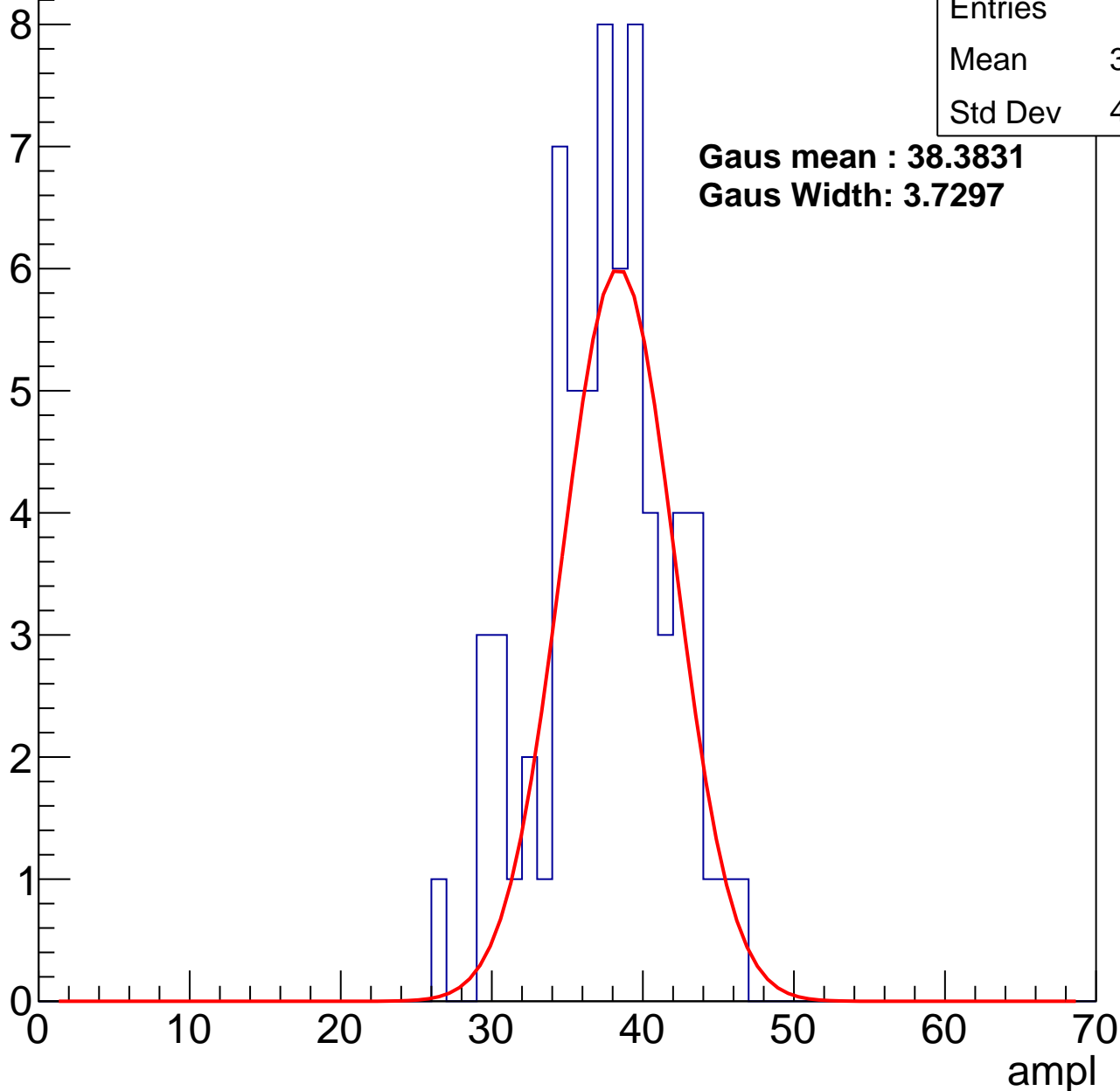
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.03
Std Dev	4.218

Gaus mean : 38.3831

Gaus Width: 3.7297



B1L103S, U14-ch48, adc2

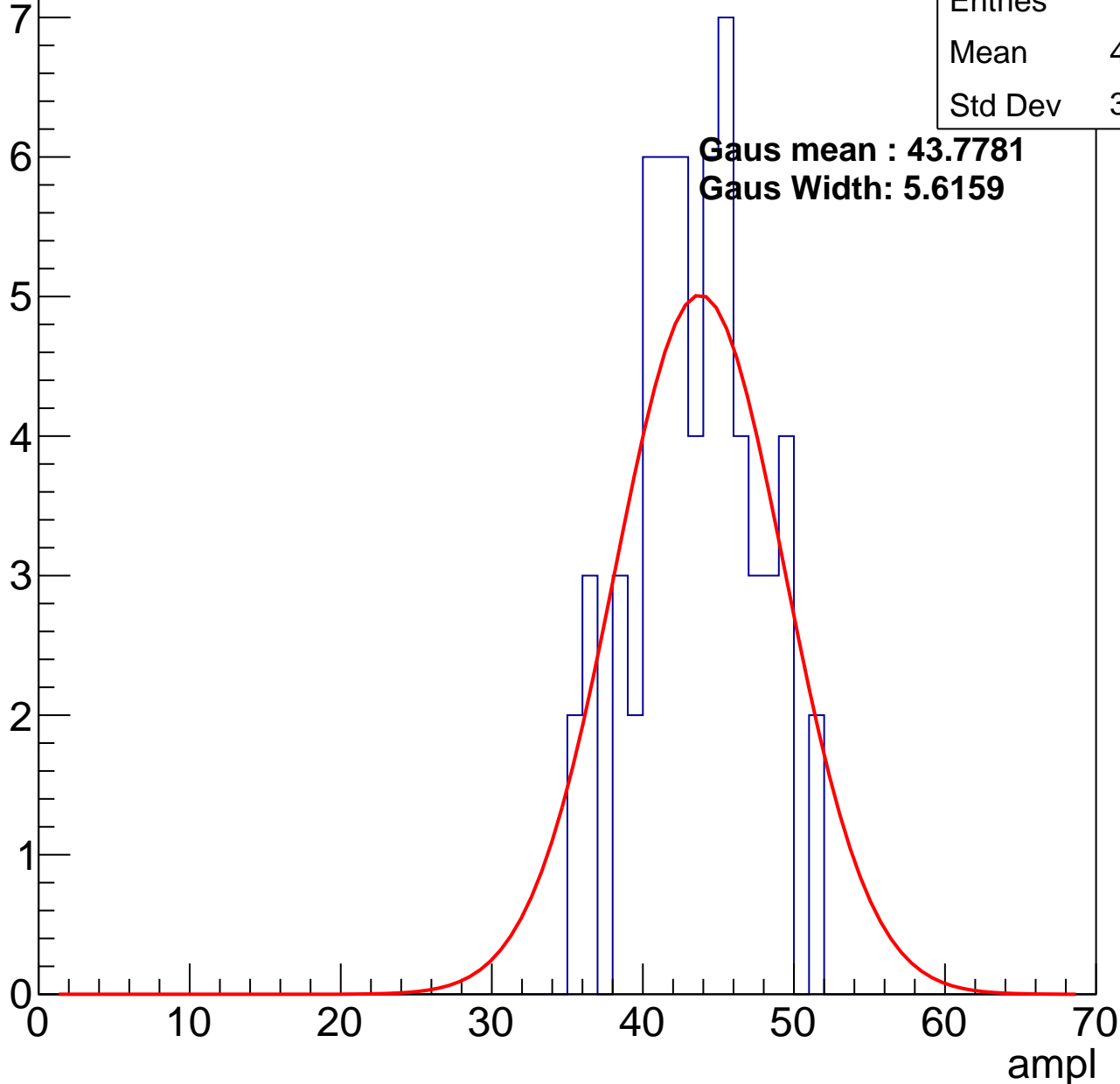
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	43.05
Std Dev	3.906

Gaus mean : 43.7781

Gaus Width: 5.6159

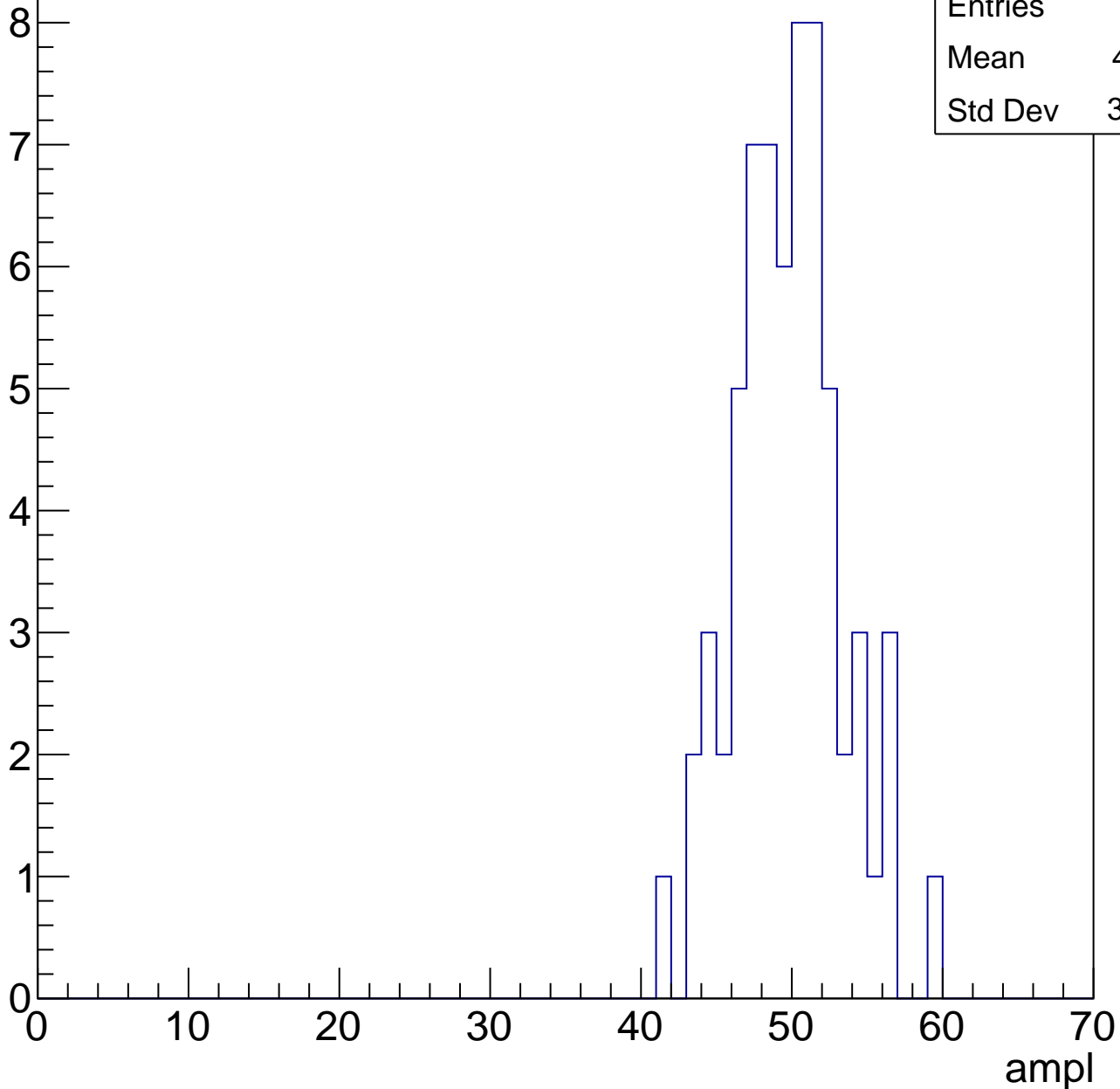


B1L103S, U14-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	49.31
Std Dev	3.517

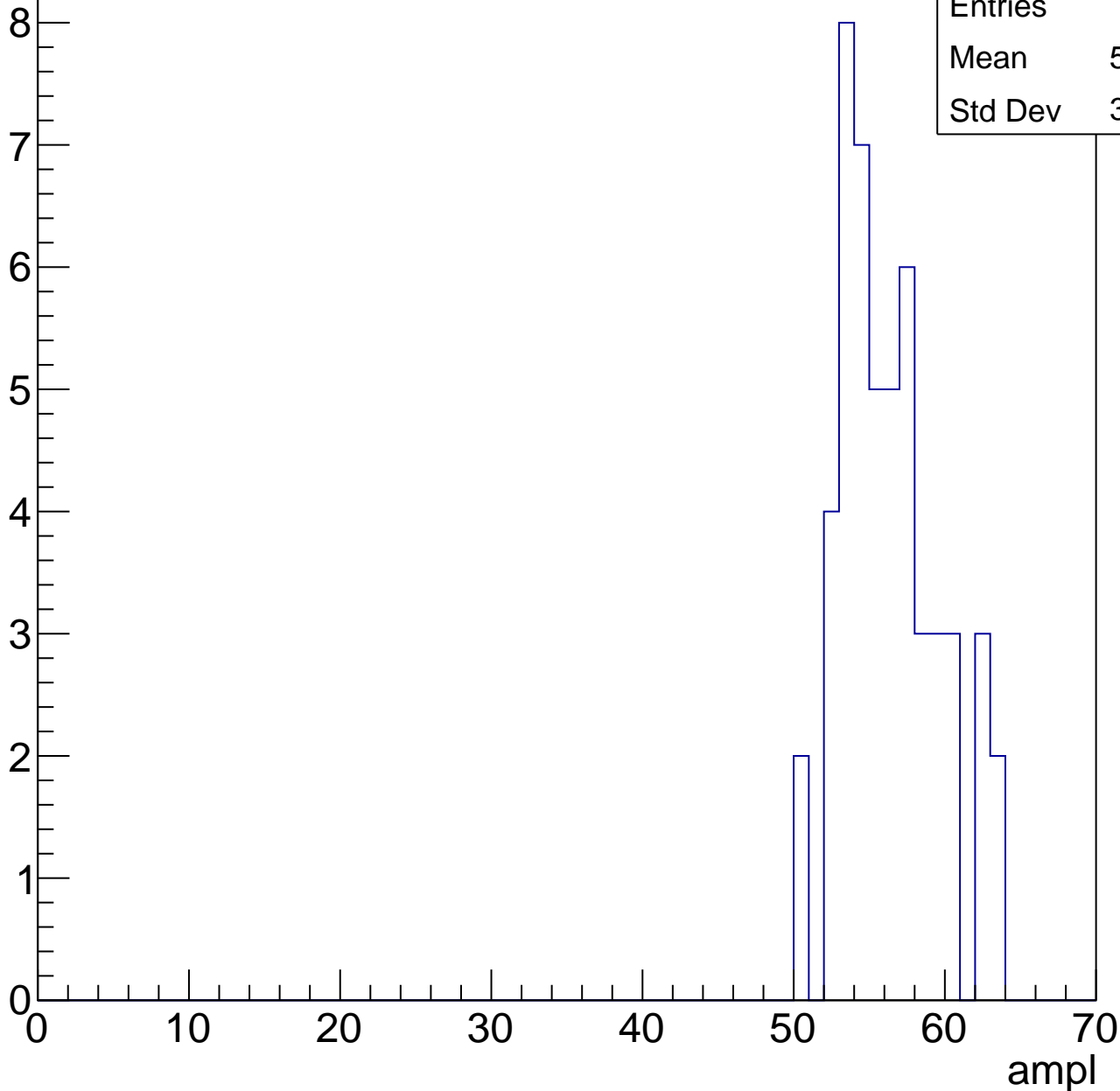


B1L103S, U14-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	55.88
Std Dev	3.252

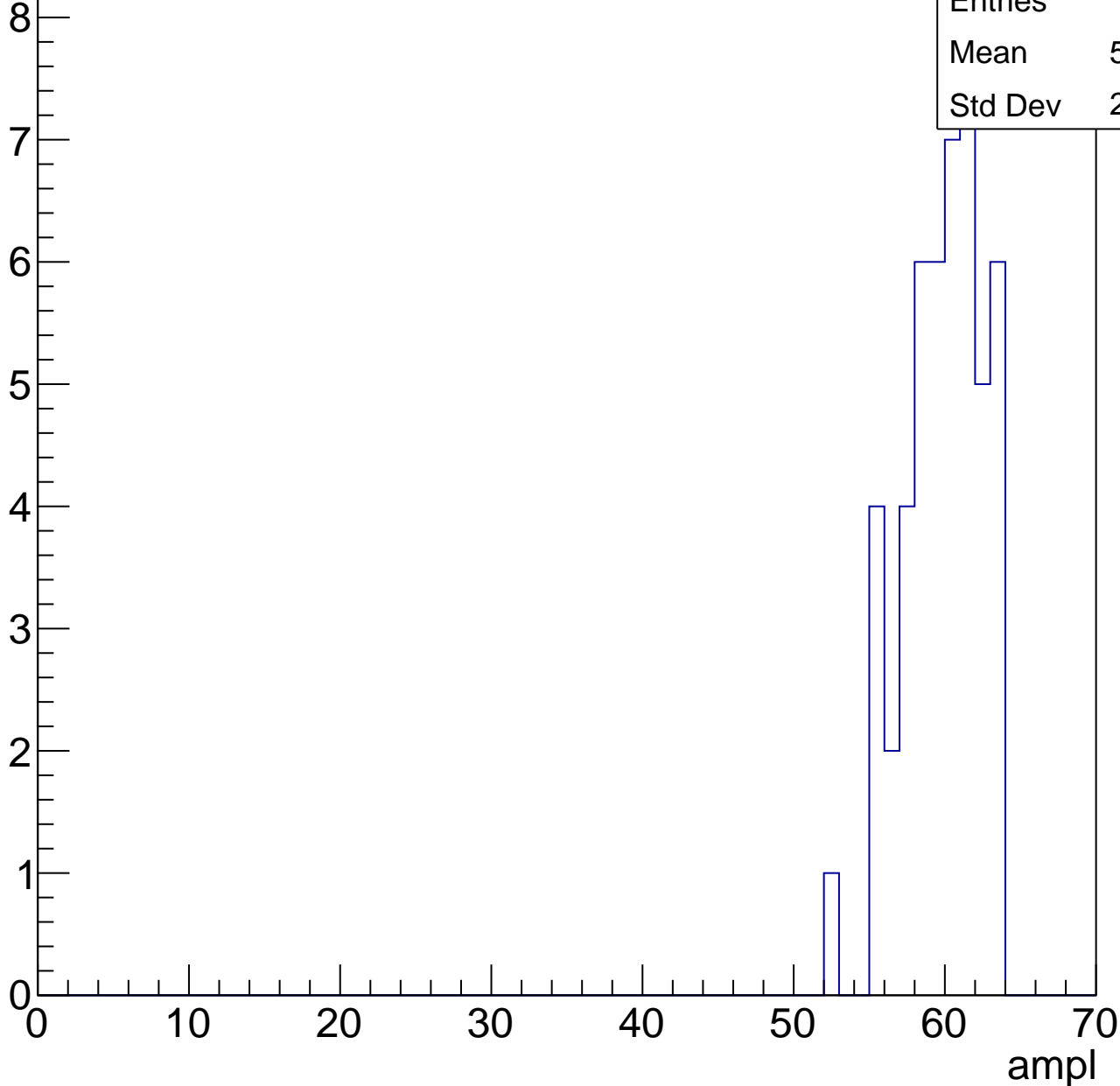


B1L103S, U14-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

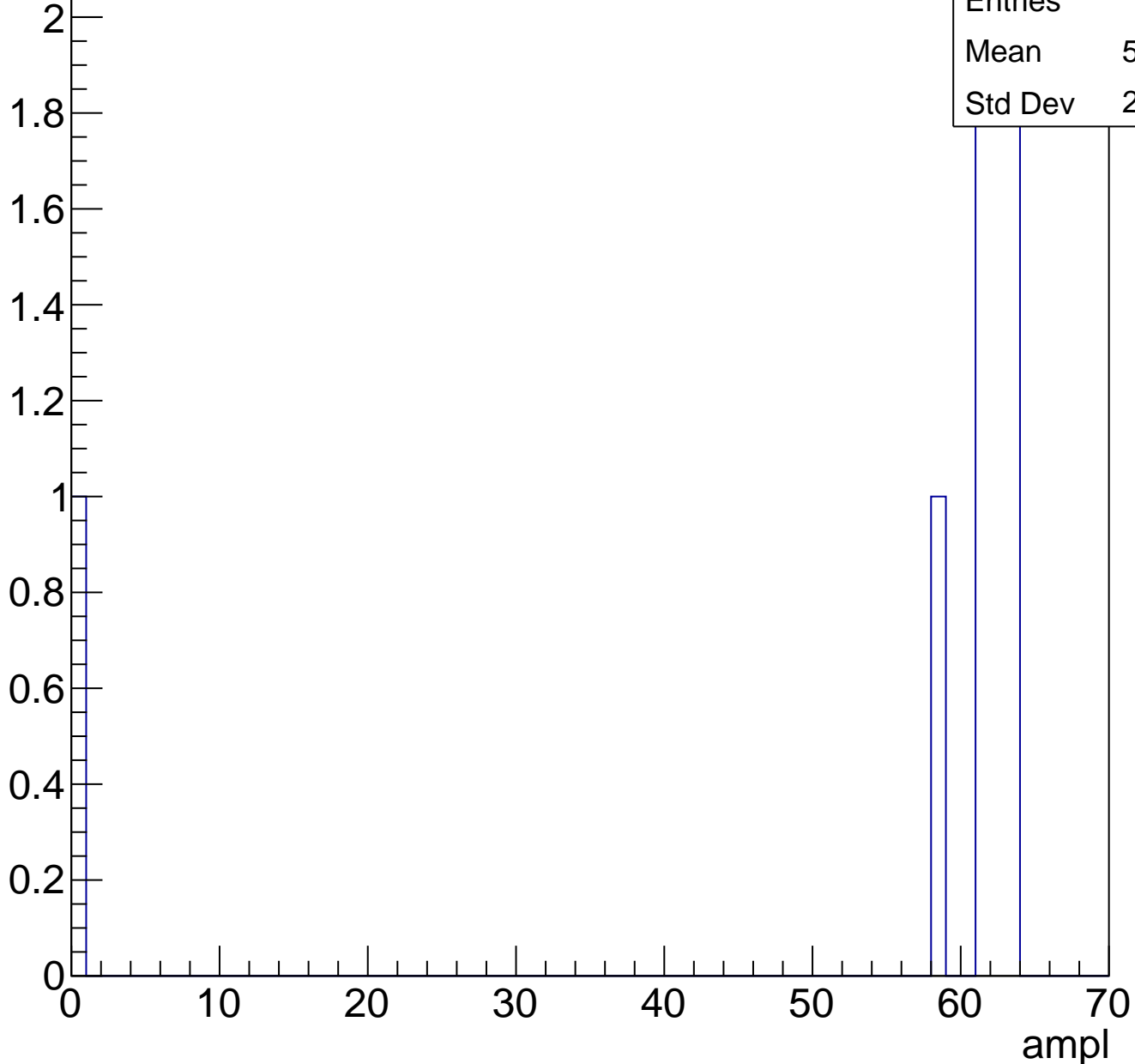
Entries	49
Mean	59.39
Std Dev	2.578



B1L103S, U14-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch49, adc0

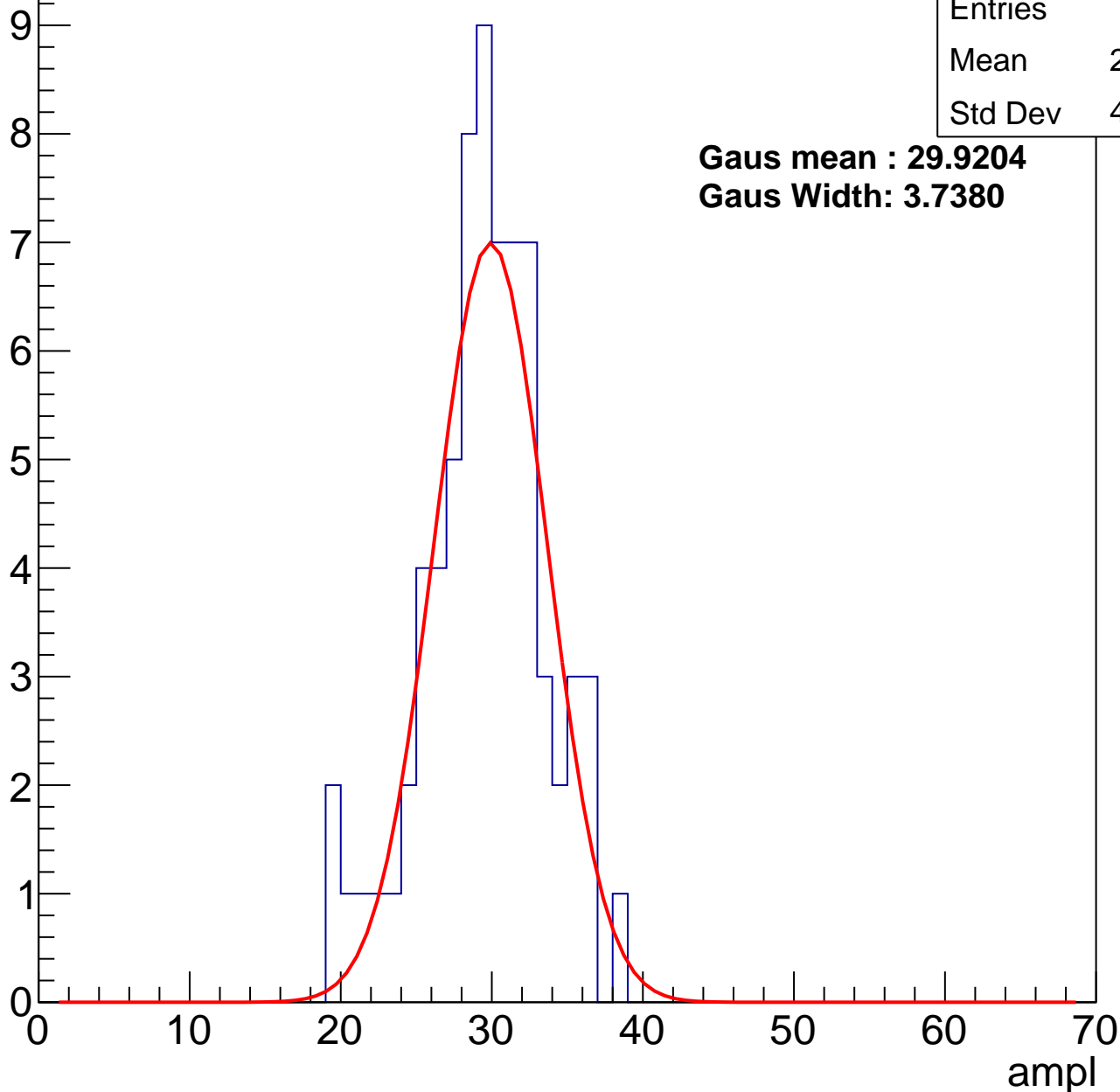
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	29.08
Std Dev	4.006

Gaus mean : 29.9204

Gaus Width: 3.7380



B1L103S, U14-ch49, adc1

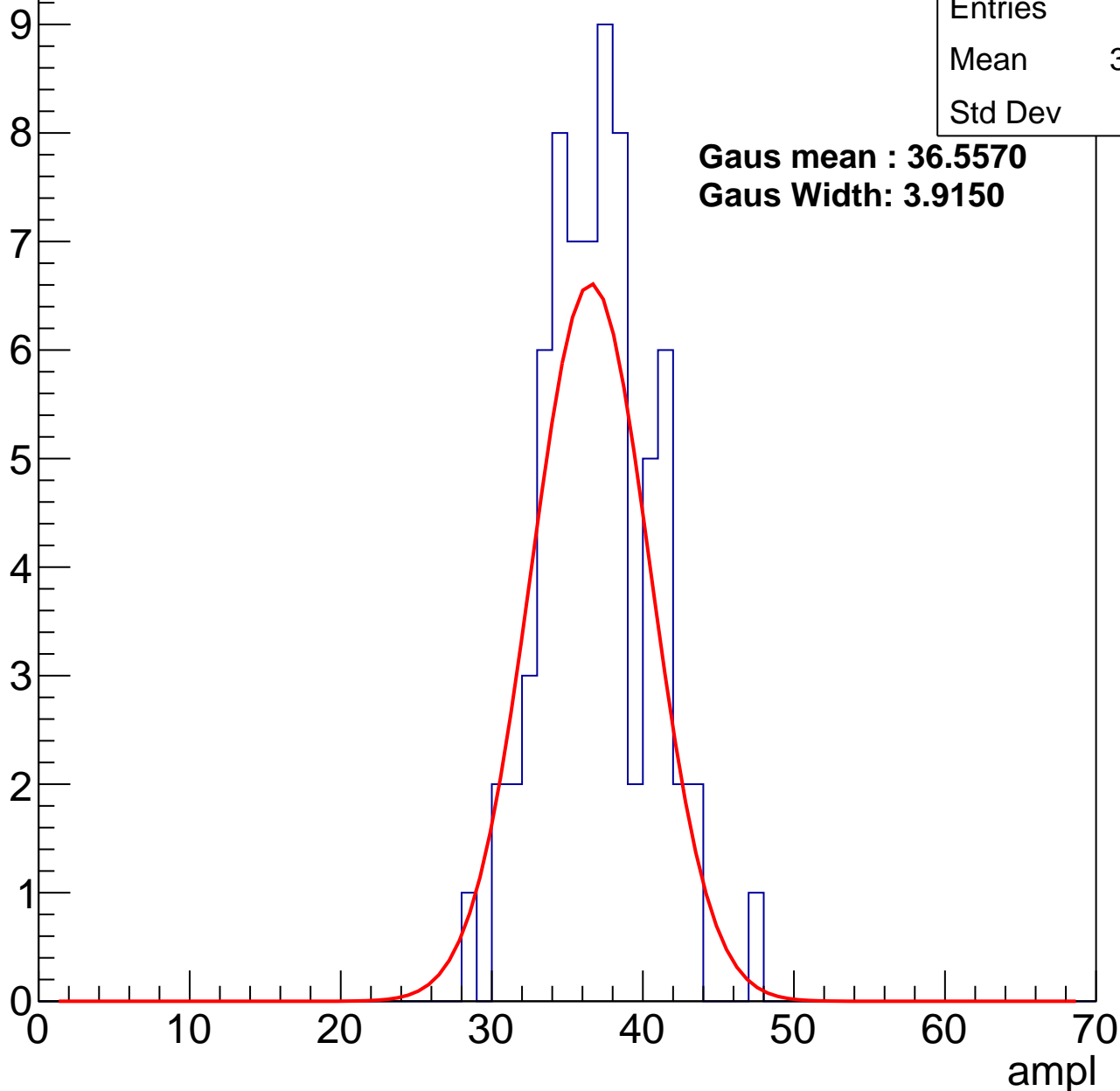
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.49
Std Dev	3.54

Gaus mean : 36.5570

Gaus Width: 3.9150



B1L103S, U14-ch49, adc2

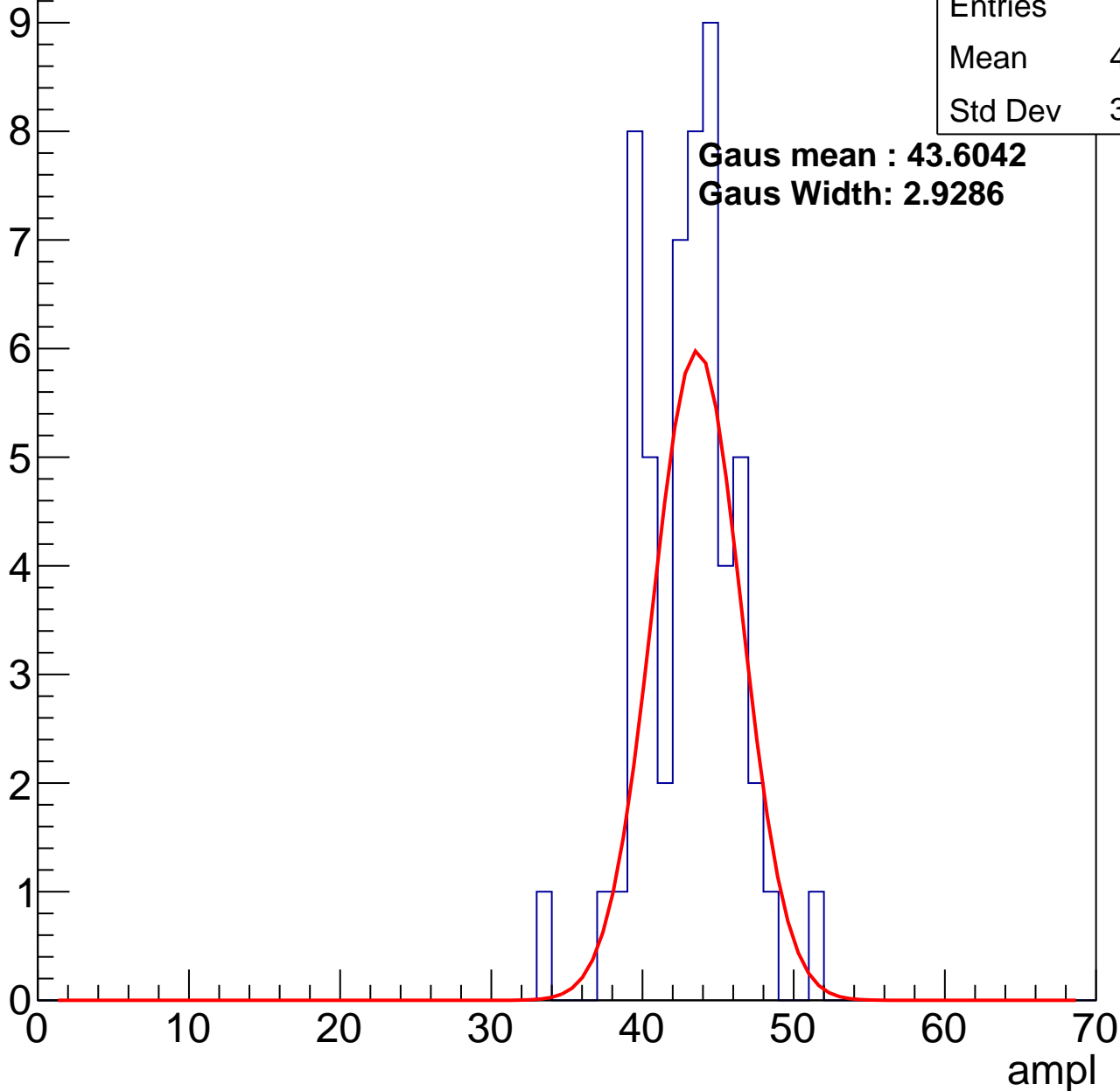
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.53
Std Dev	3.098

Gaus mean : 43.6042

Gaus Width: 2.9286



B1L103S, U14-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

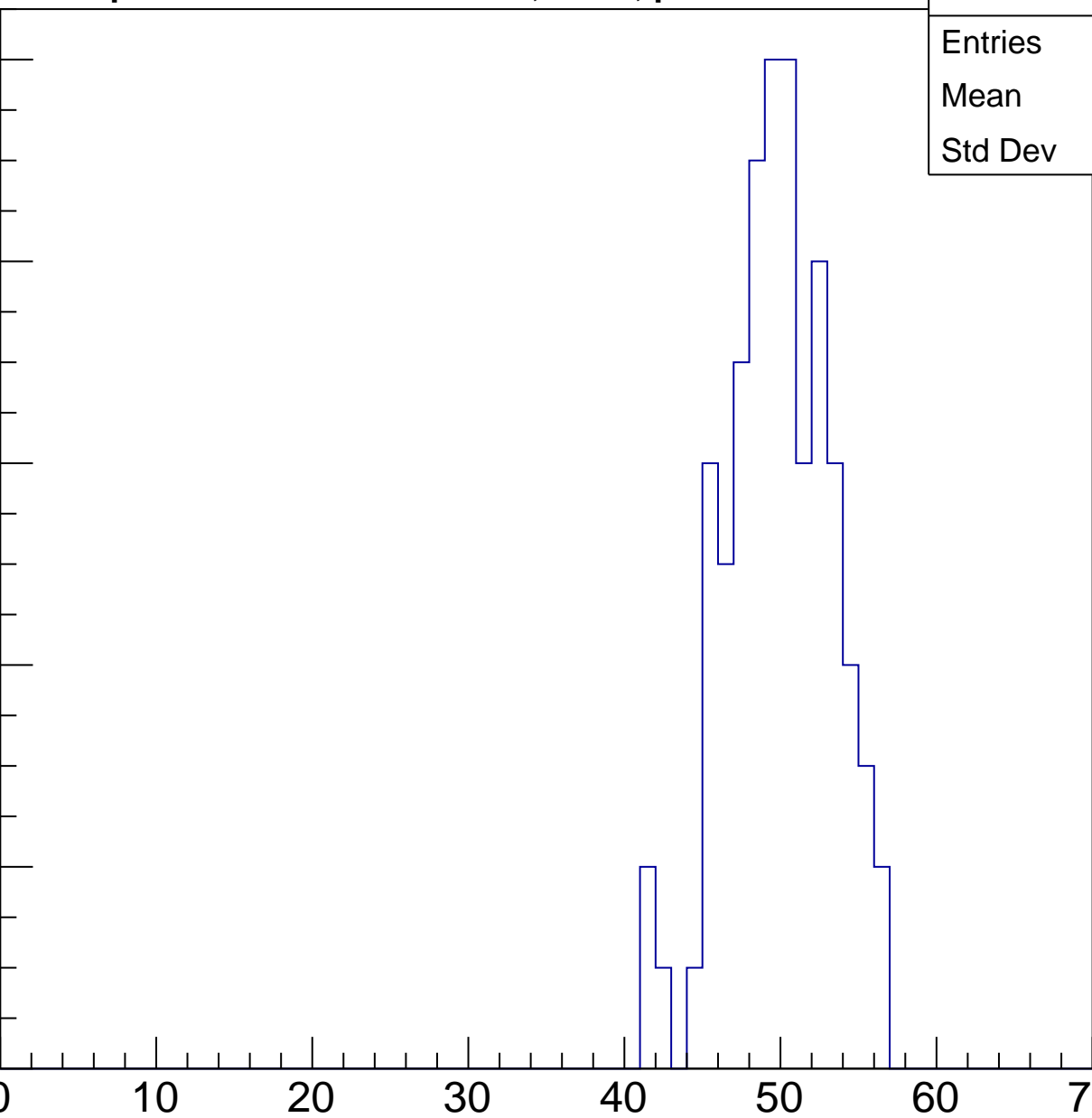
Entries	80
Mean	49.4
Std Dev	3.308

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

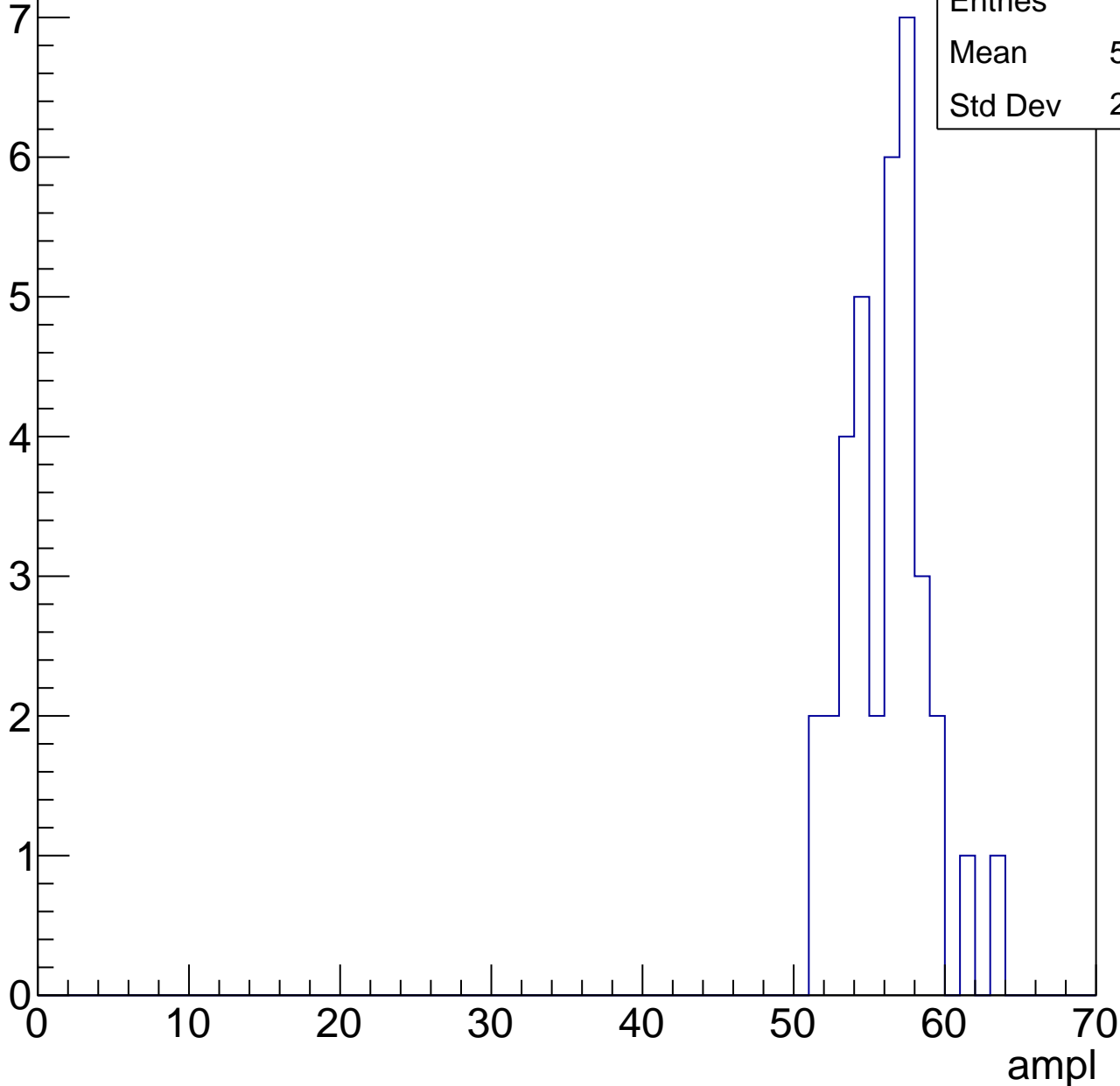


B1L103S, U14-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	55.69
Std Dev	2.659

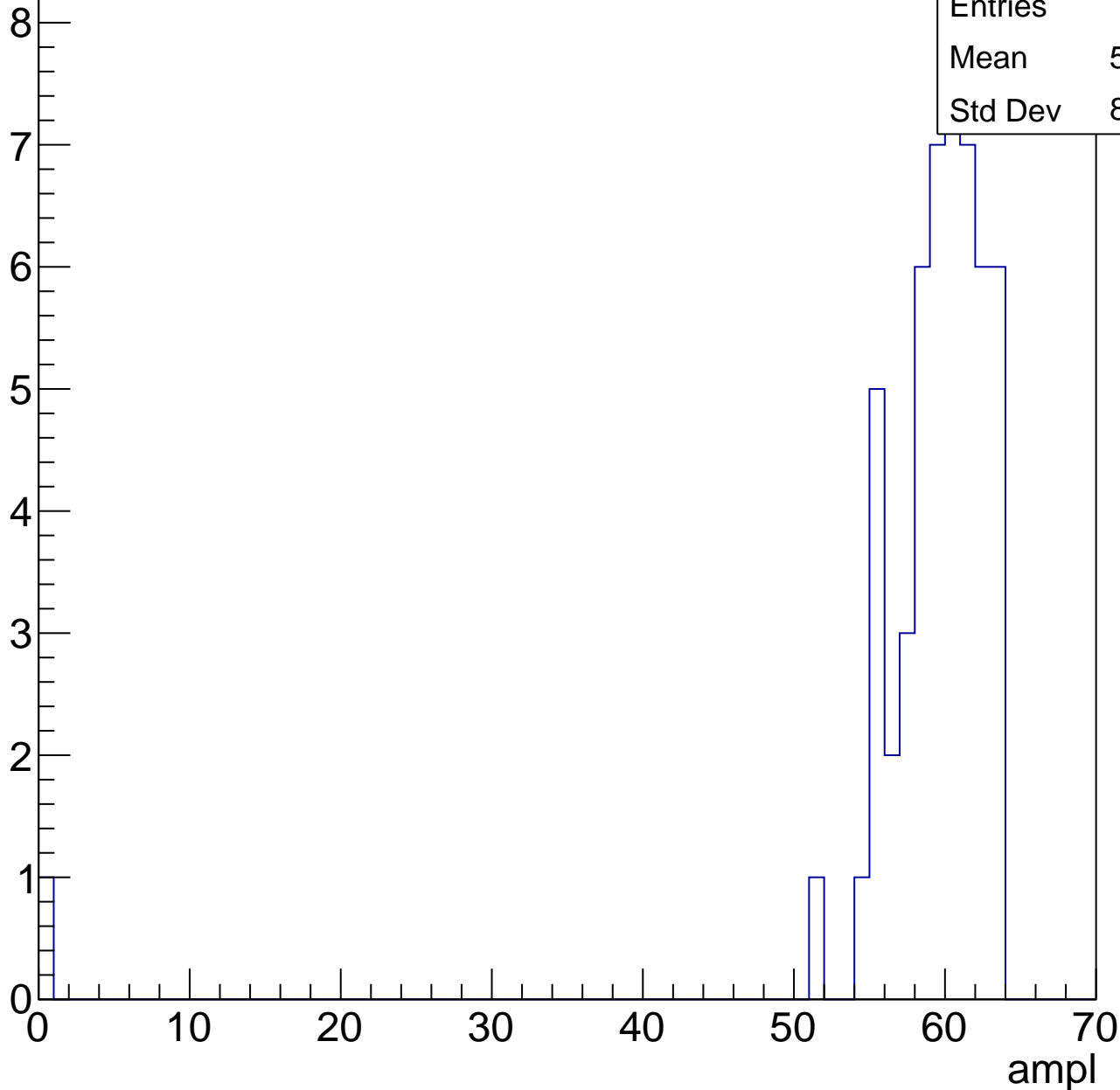


B1L103S, U14-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

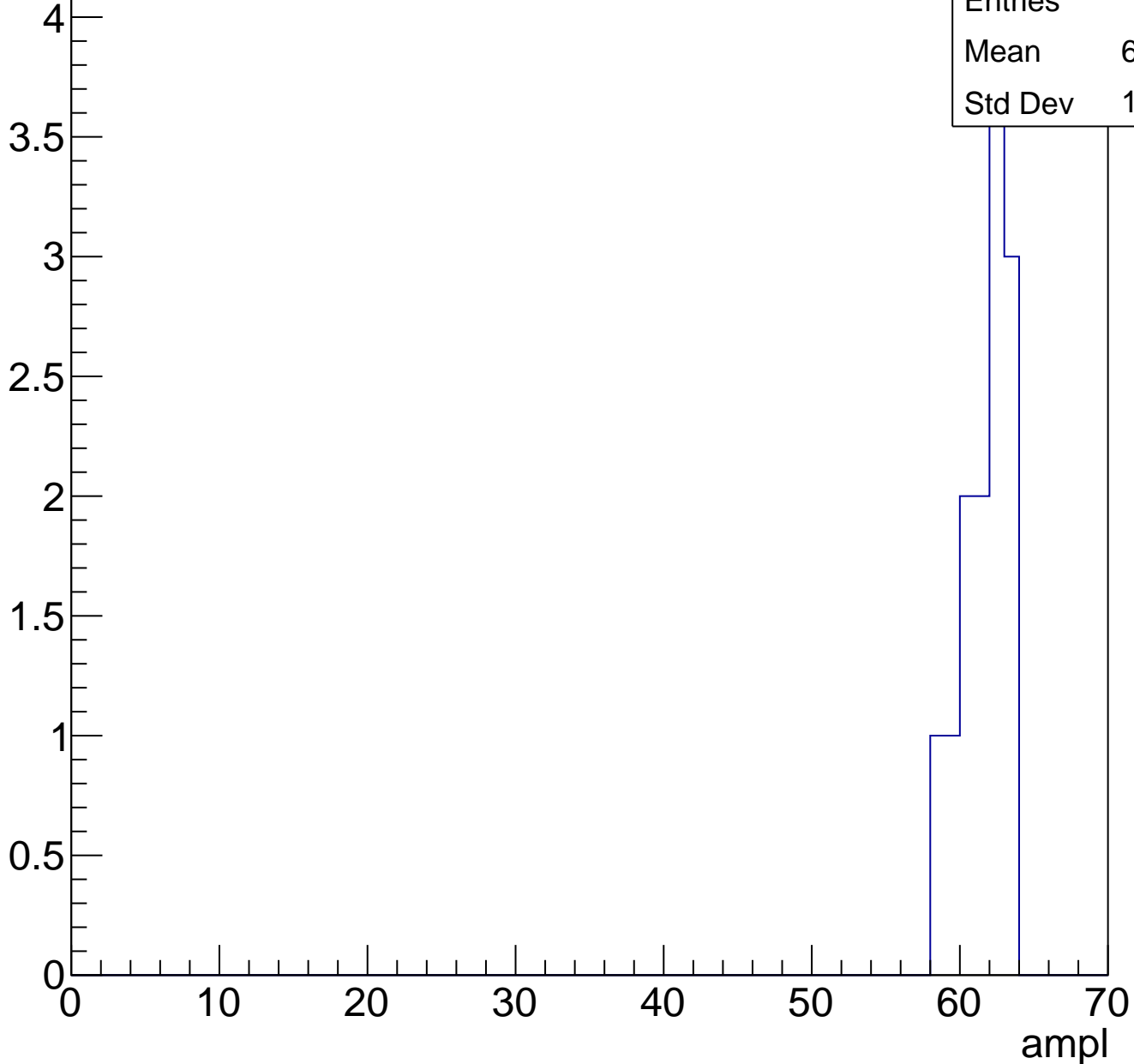
Entries	53
Mean	58.13
Std Dev	8.503



B1L103S, U14-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U14-ch50, adc0

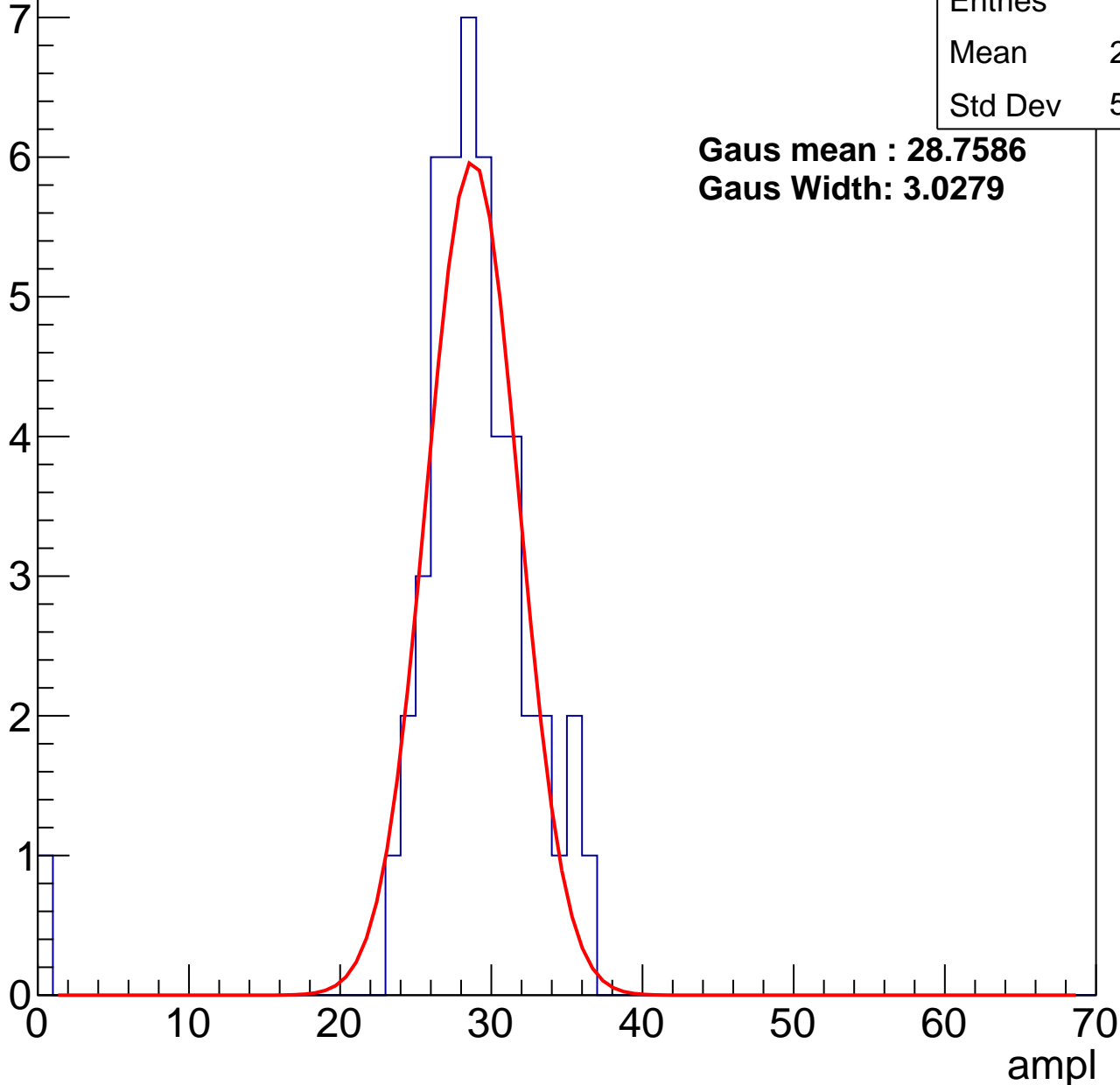
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	28.08
Std Dev	5.074

Gaus mean : 28.7586

Gaus Width: 3.0279



B1L103S, U14-ch50, adc1

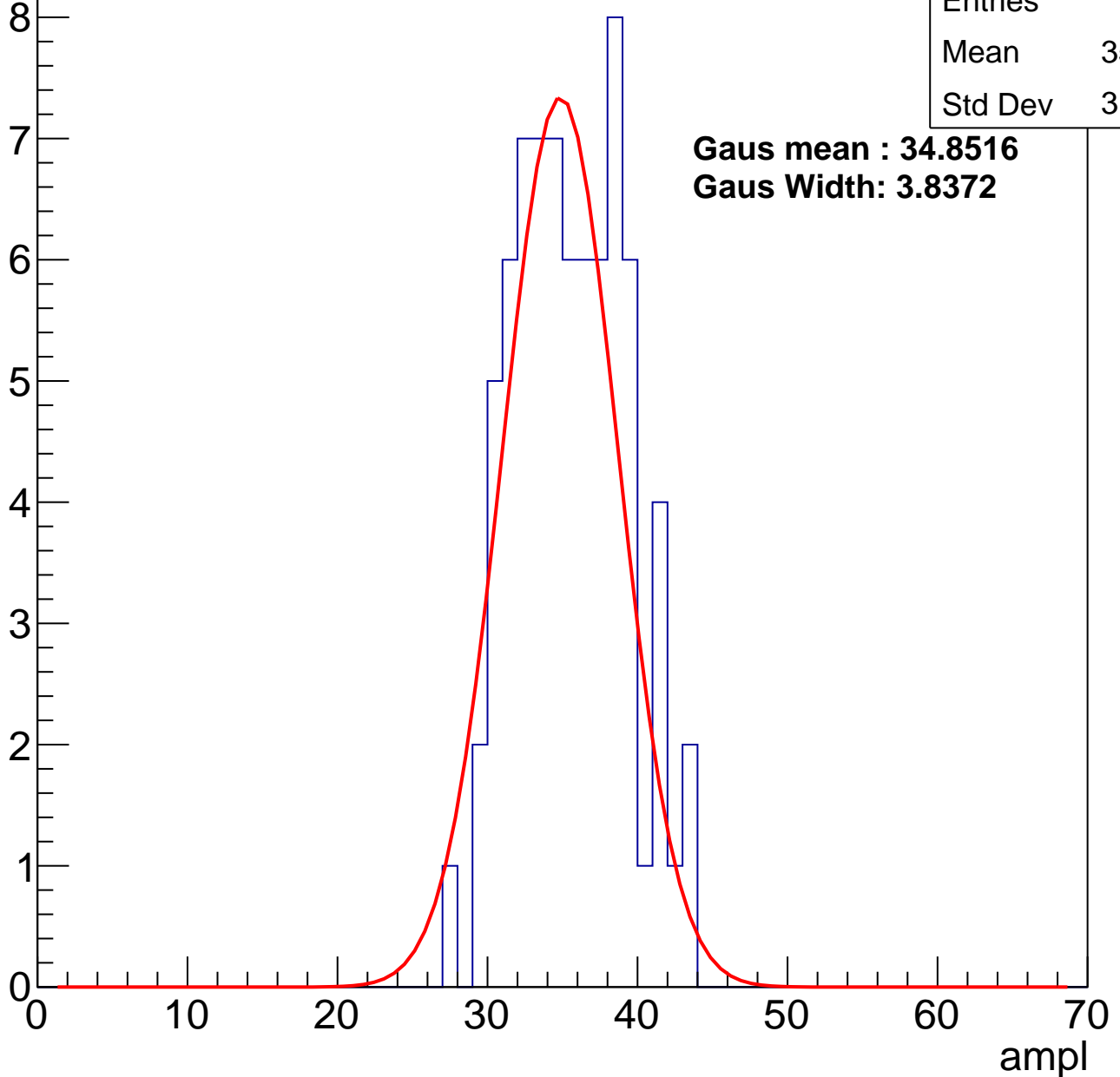
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	35.09
Std Dev	3.648

Gaus mean : 34.8516

Gaus Width: 3.8372



B1L103S, U14-ch50, adc2

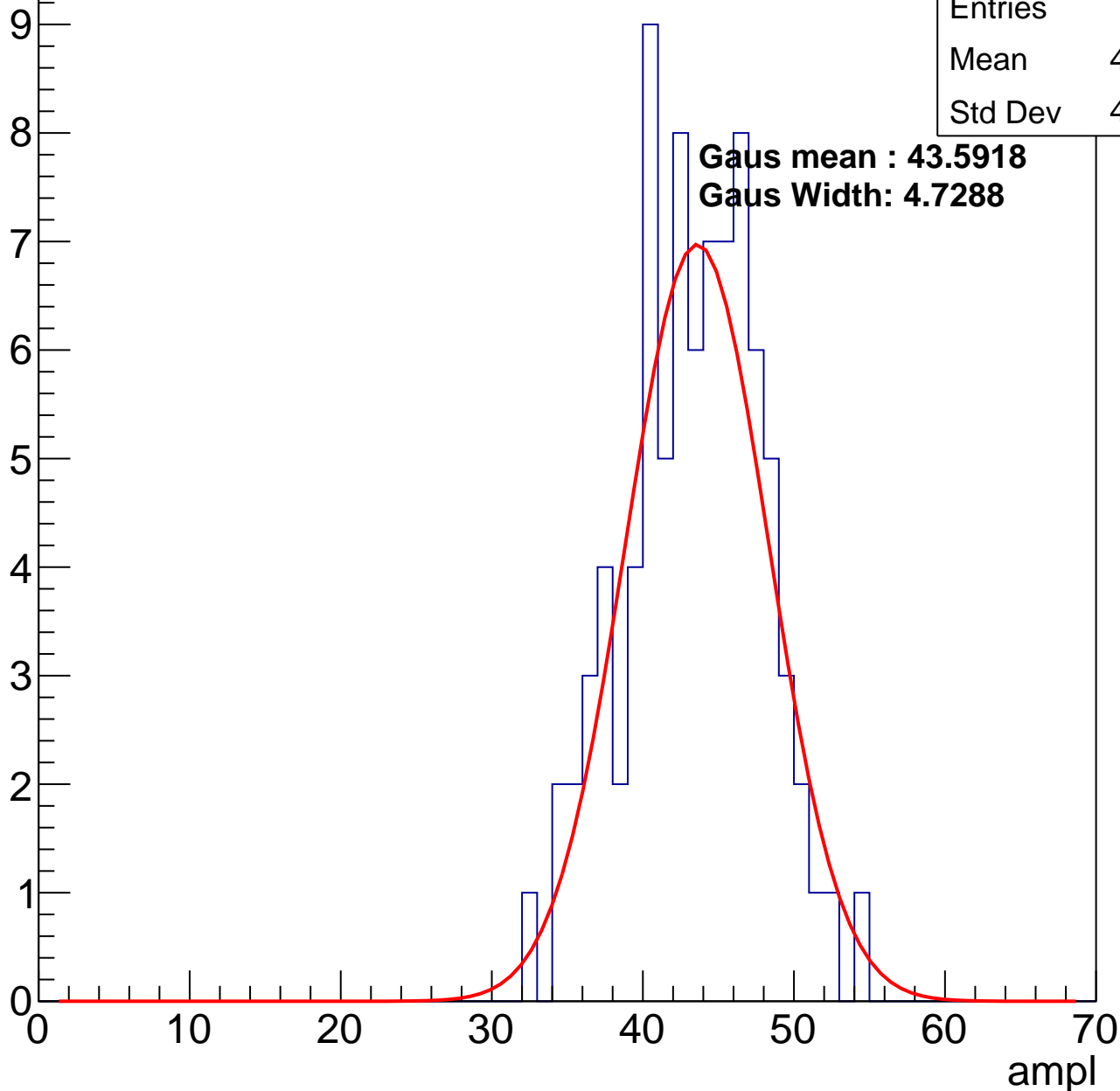
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	42.92
Std Dev	4.452

Gaus mean : 43.5918

Gaus Width: 4.7288

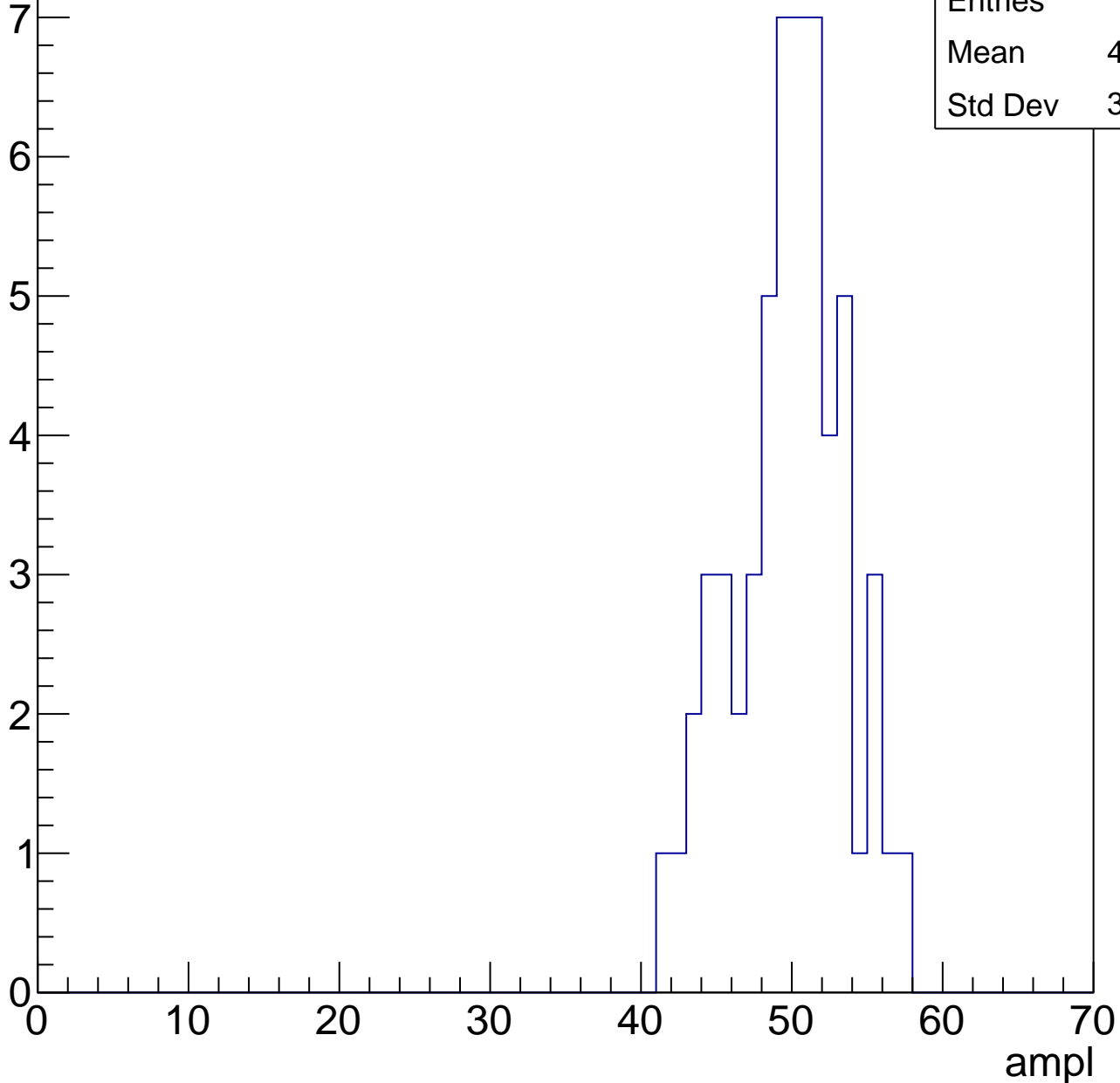


B1L103S, U14-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	49.36
Std Dev	3.618

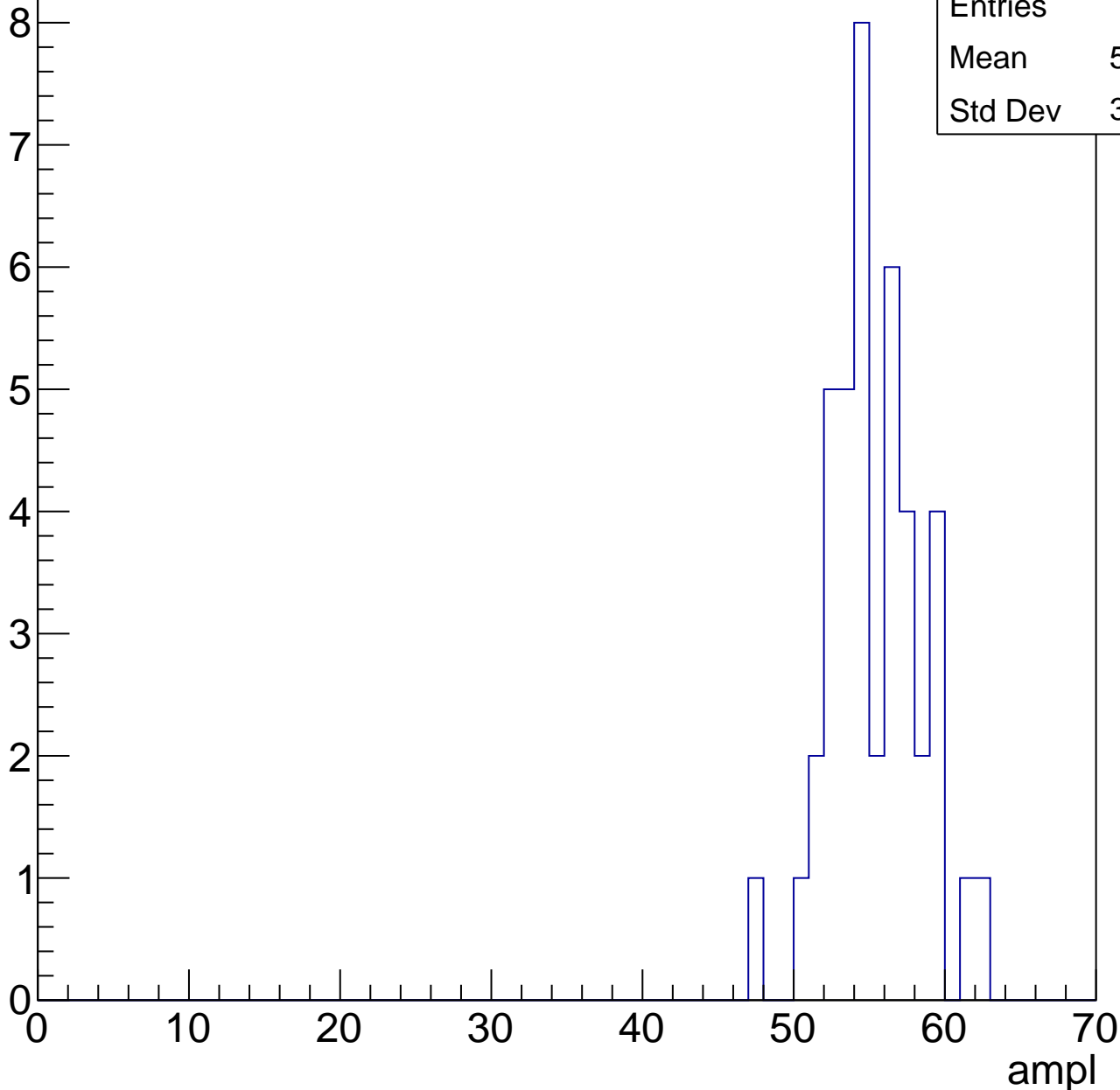


B1L103S, U14-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	54.88
Std Dev	3.017

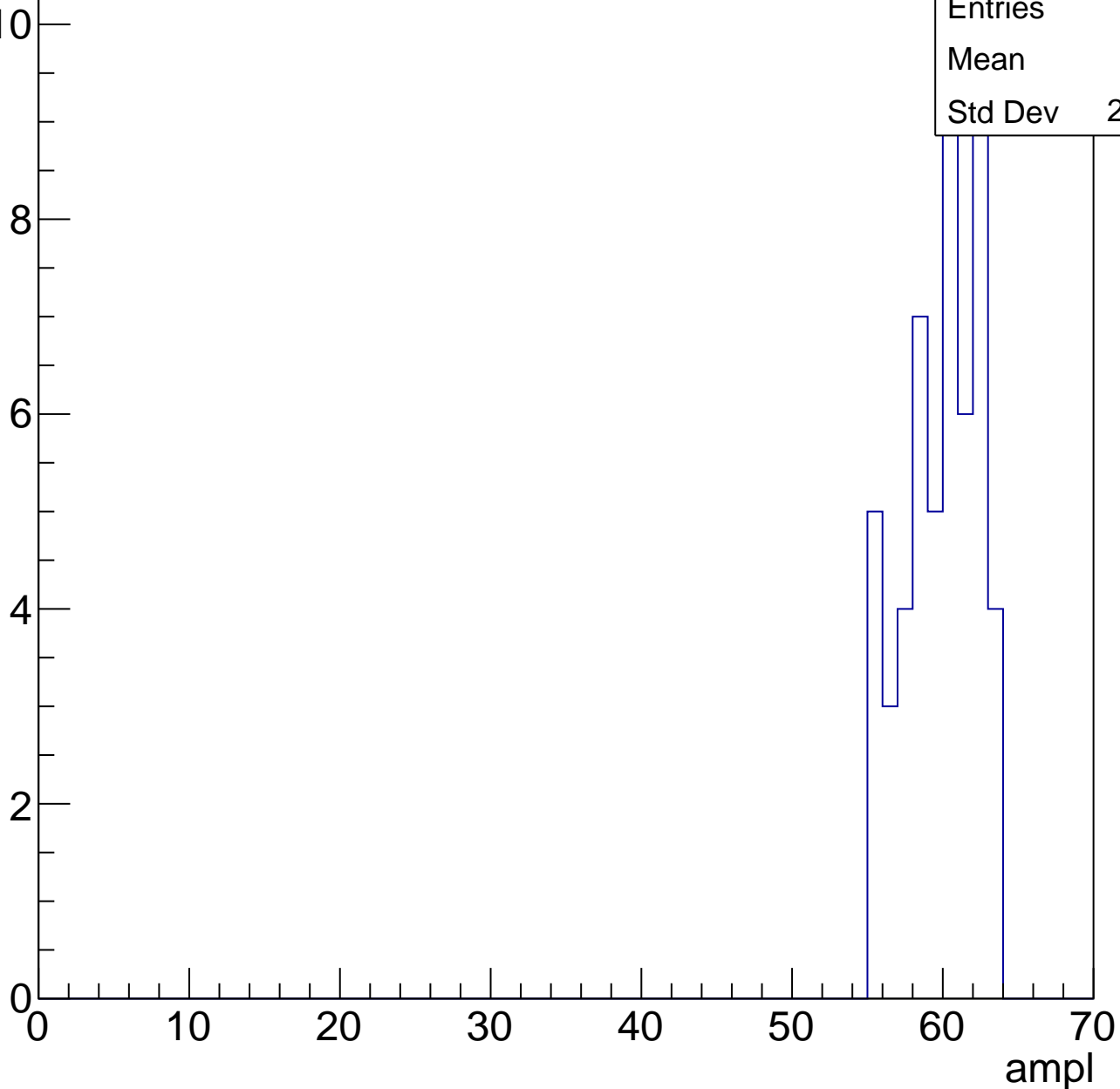


B1L103S, U14-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	59.4
Std Dev	2.382

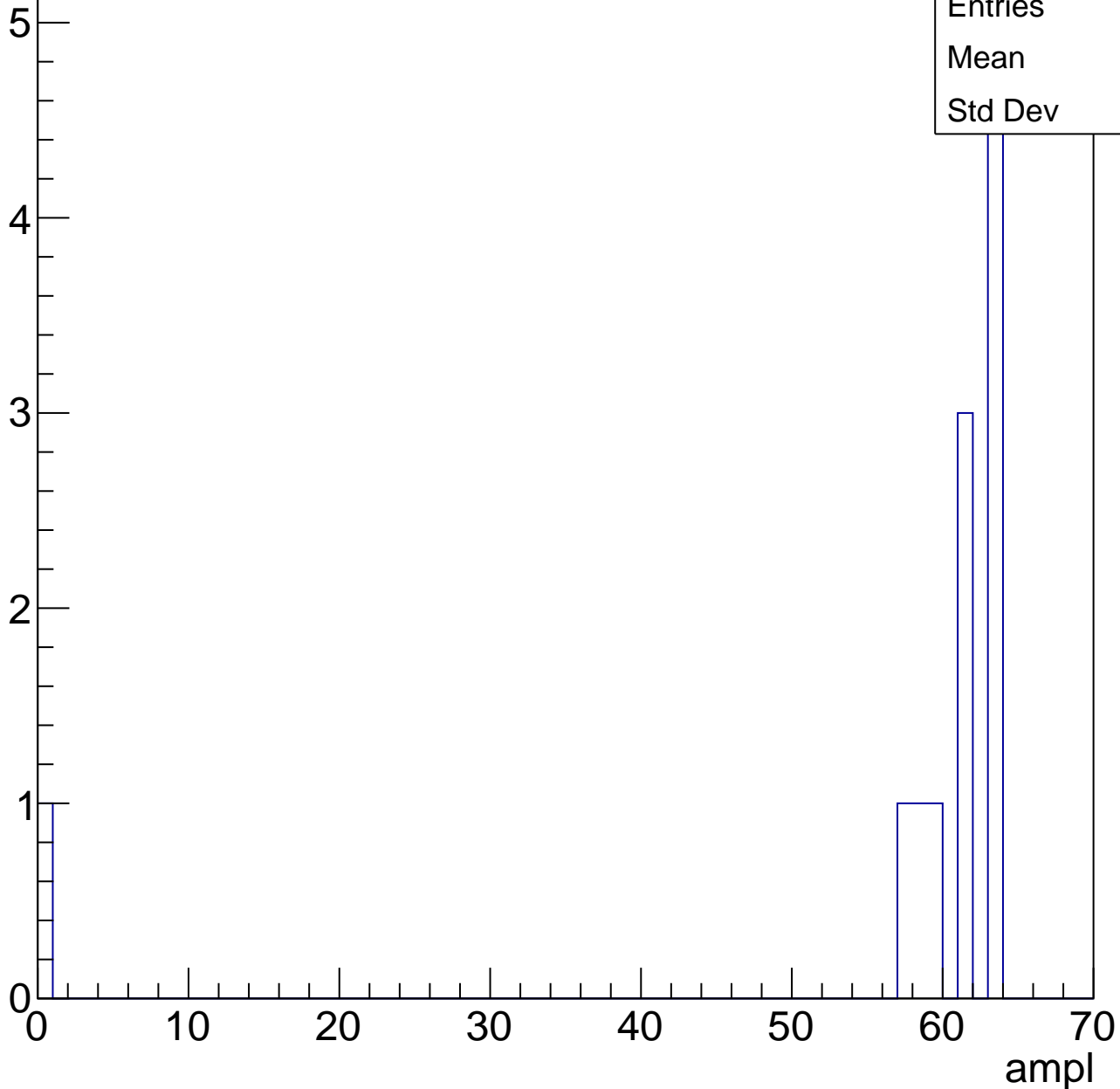


B1L103S, U14-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	56
Std Dev	17



B1L103S, U14-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch51, adc0

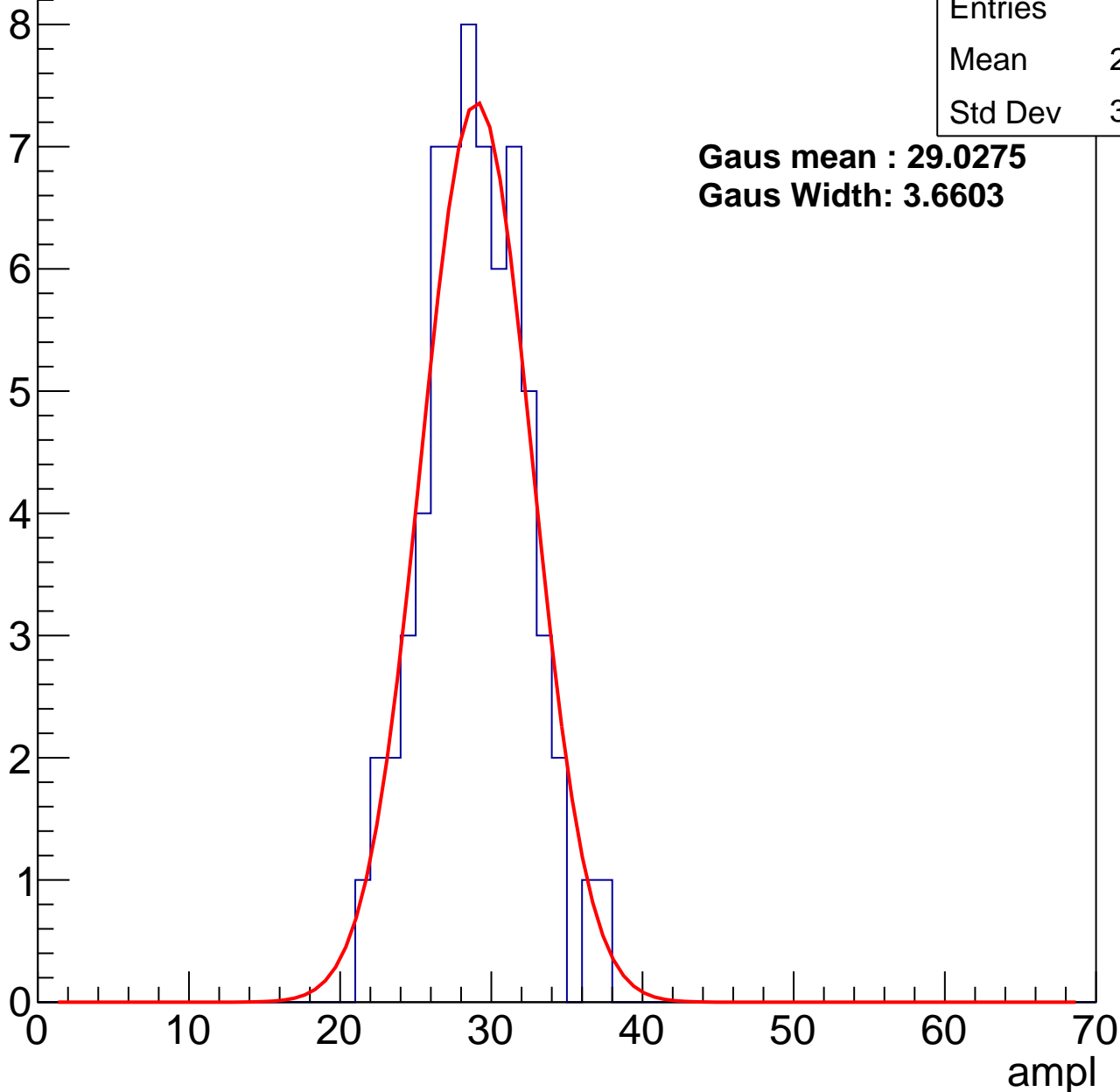
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.45
Std Dev	3.354

Gaus mean : 29.0275

Gaus Width: 3.6603



B1L103S, U14-ch51, adc1

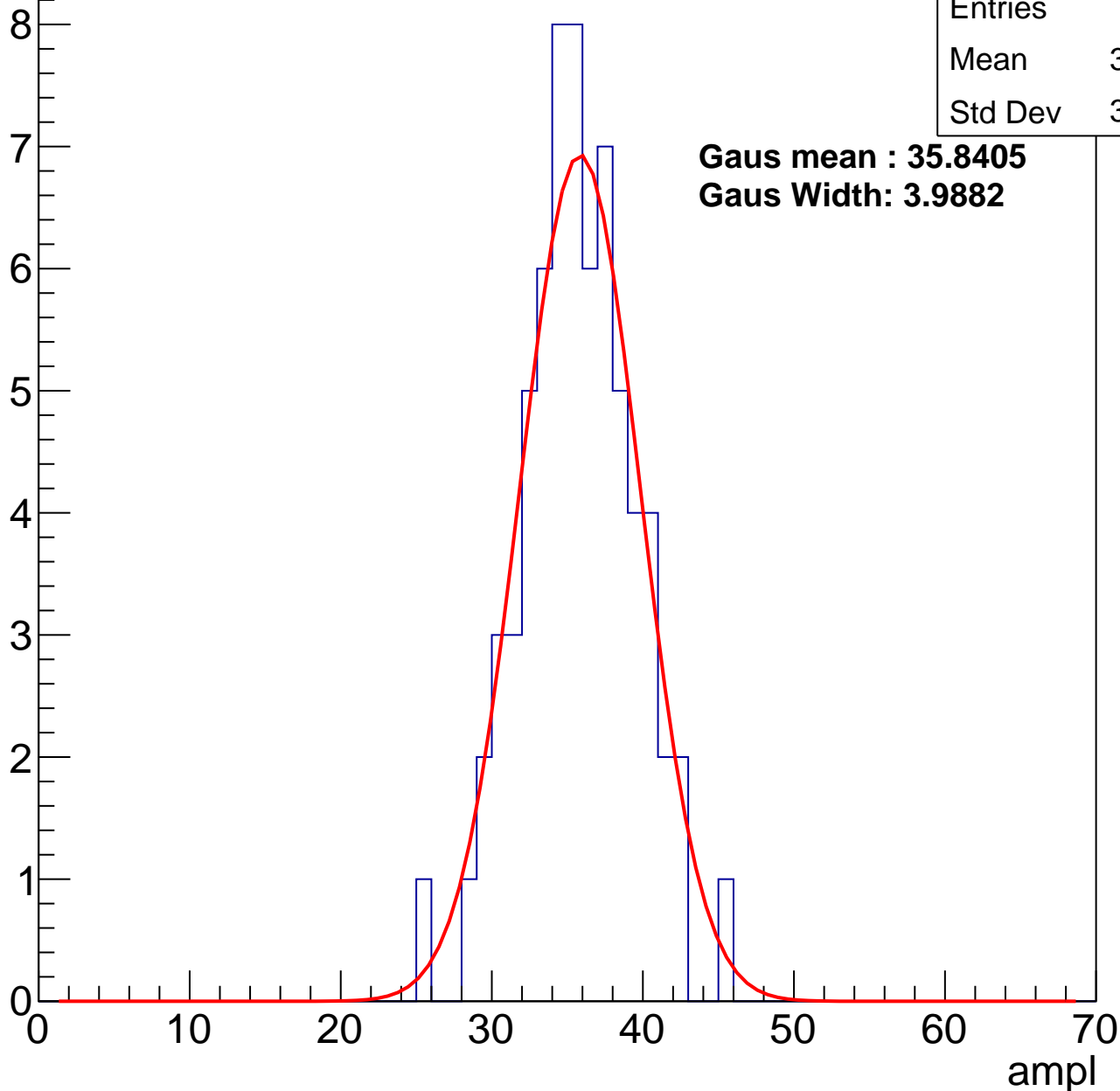
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.24
Std Dev	3.699

Gaus mean : 35.8405

Gaus Width: 3.9882



B1L103S, U14-ch51, adc2

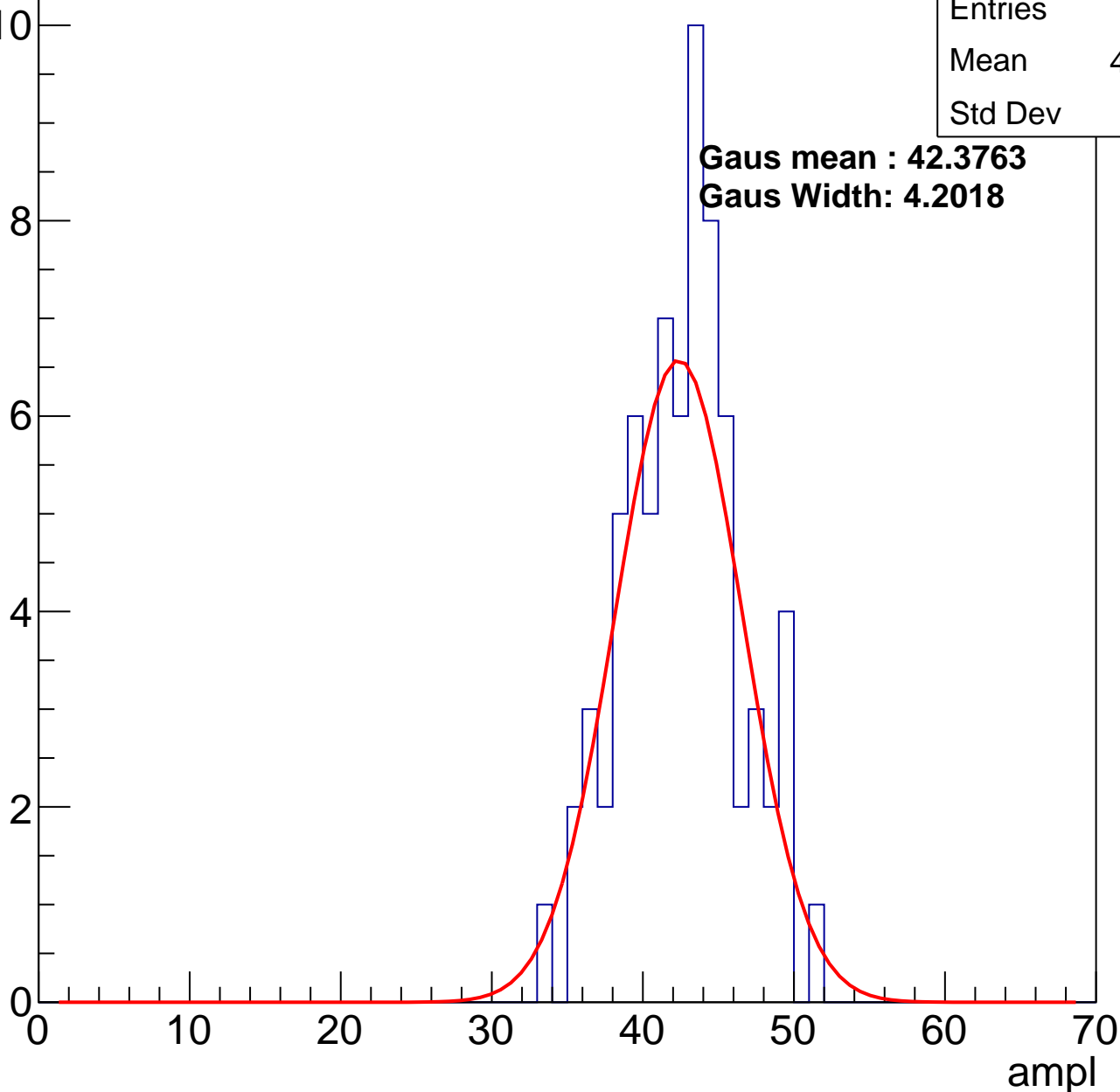
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	42.14
Std Dev	3.79

Gaus mean : 42.3763

Gaus Width: 4.2018

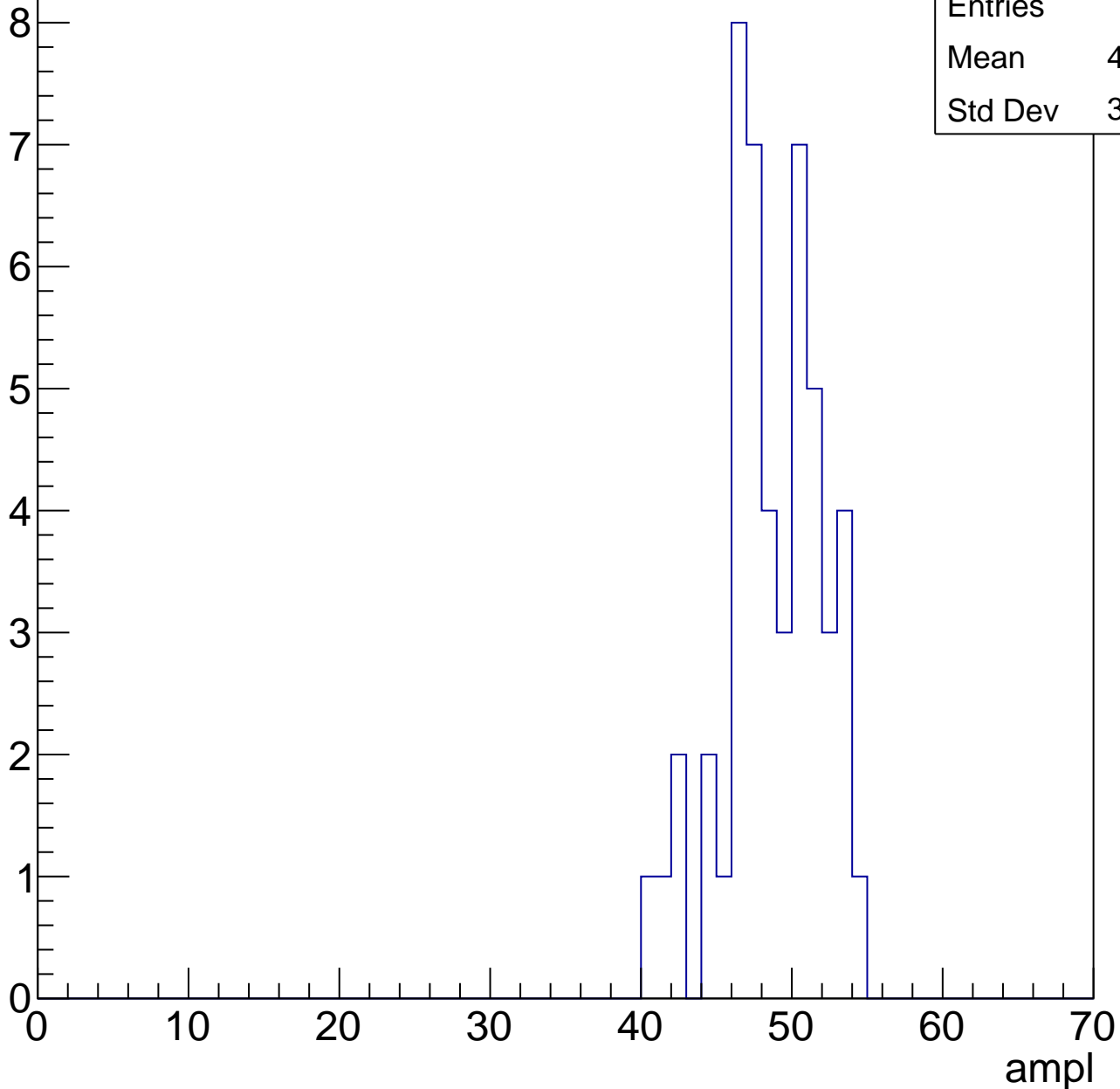


B1L103S, U14-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	48.18
Std Dev	3.268



B1L103S, U14-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	54.21
Std Dev	3.62

Entry

10

8

6

4

2

0

ampl

0

10

20

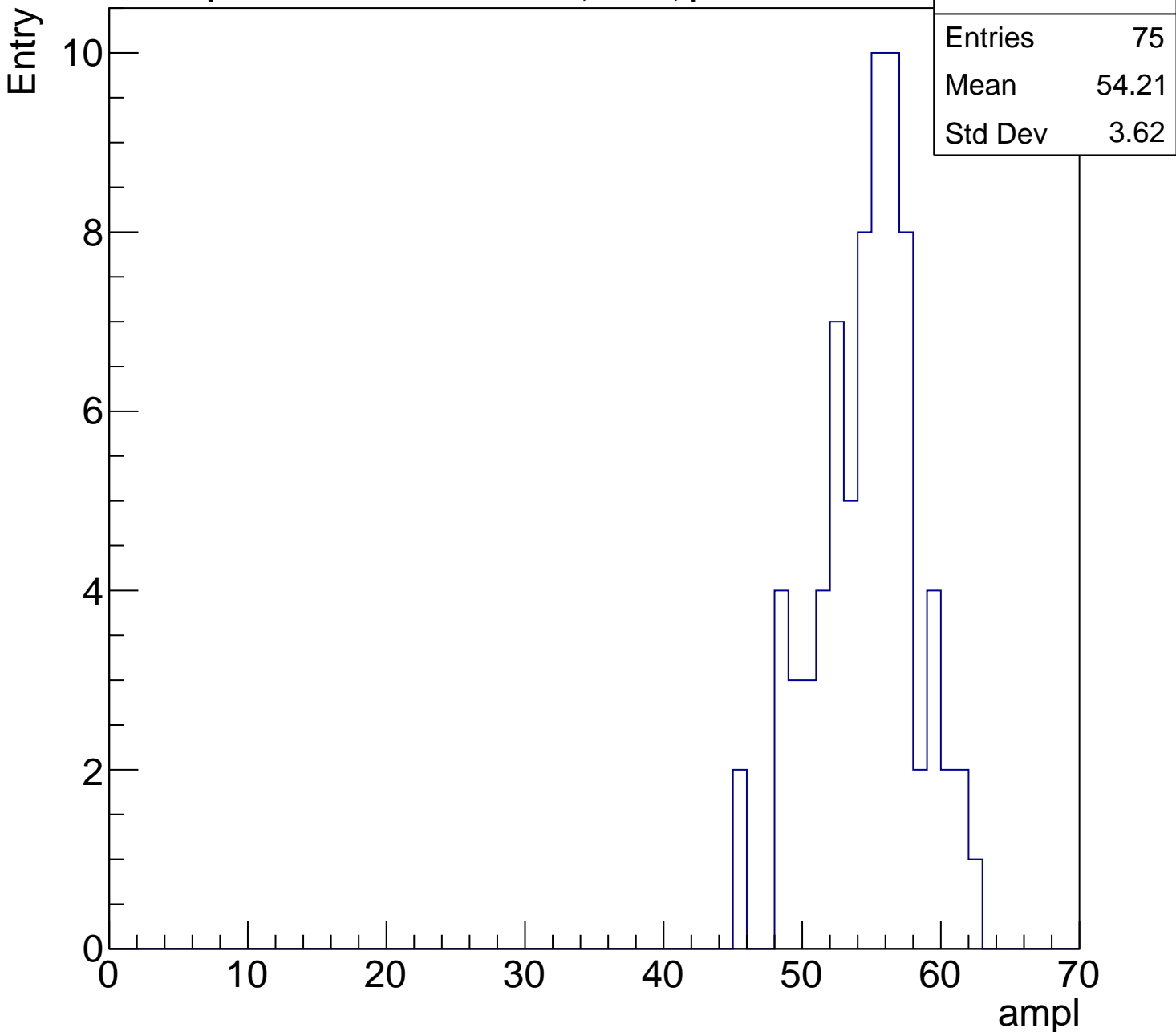
30

40

50

60

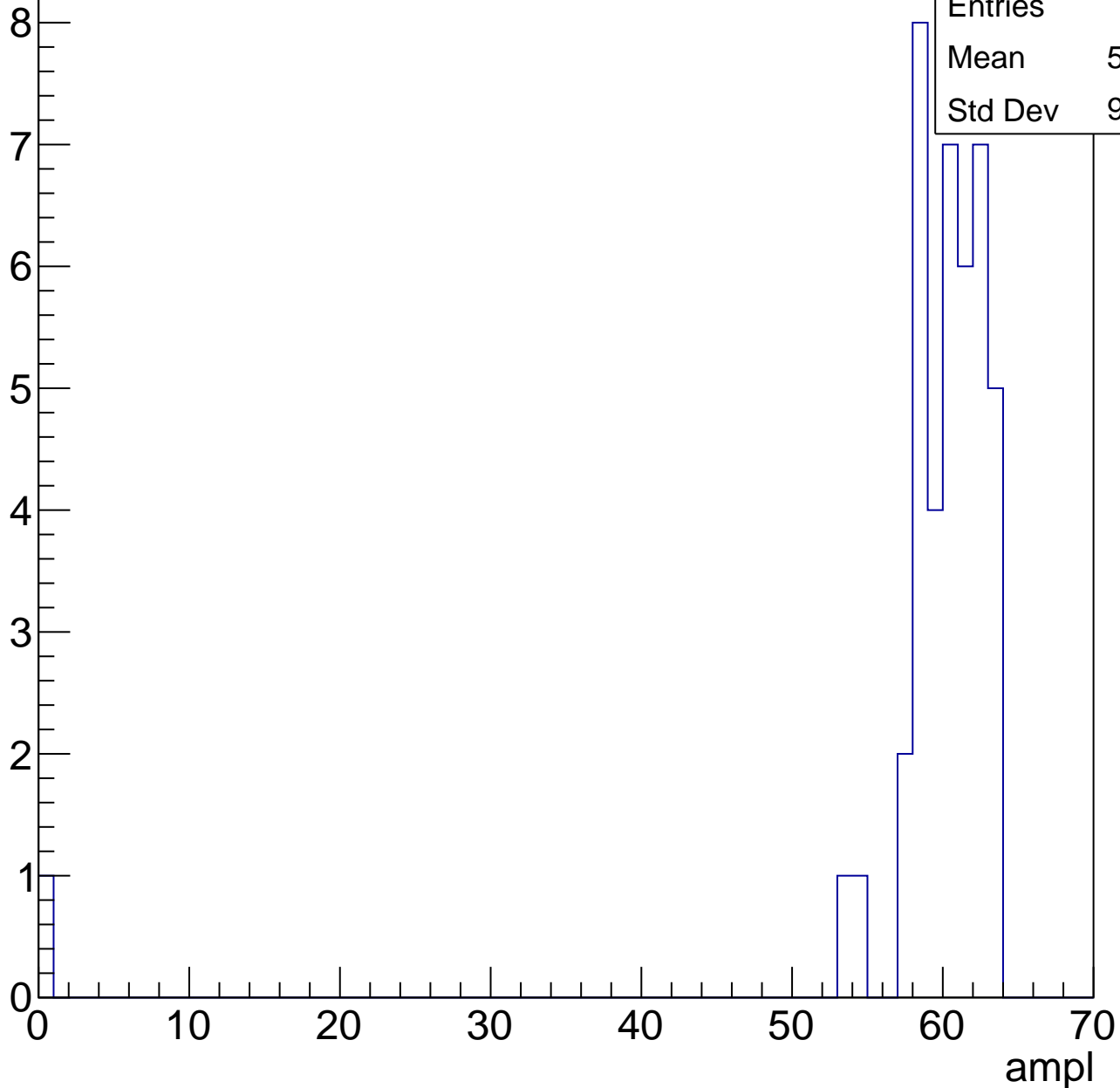
70



B1L103S, U14-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

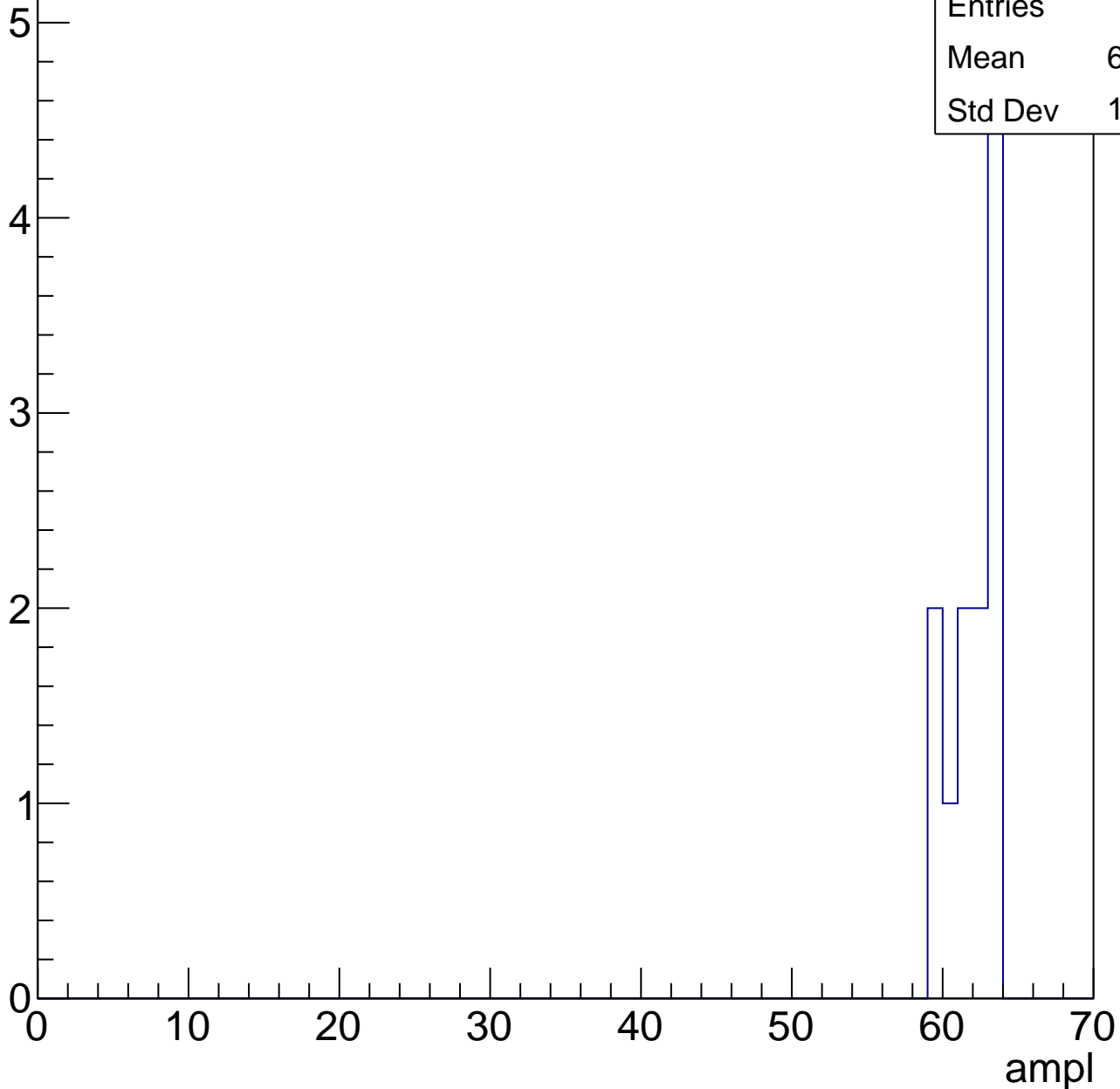


B1L103S, U14-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.498



B1L103S, U14-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch52, adc0

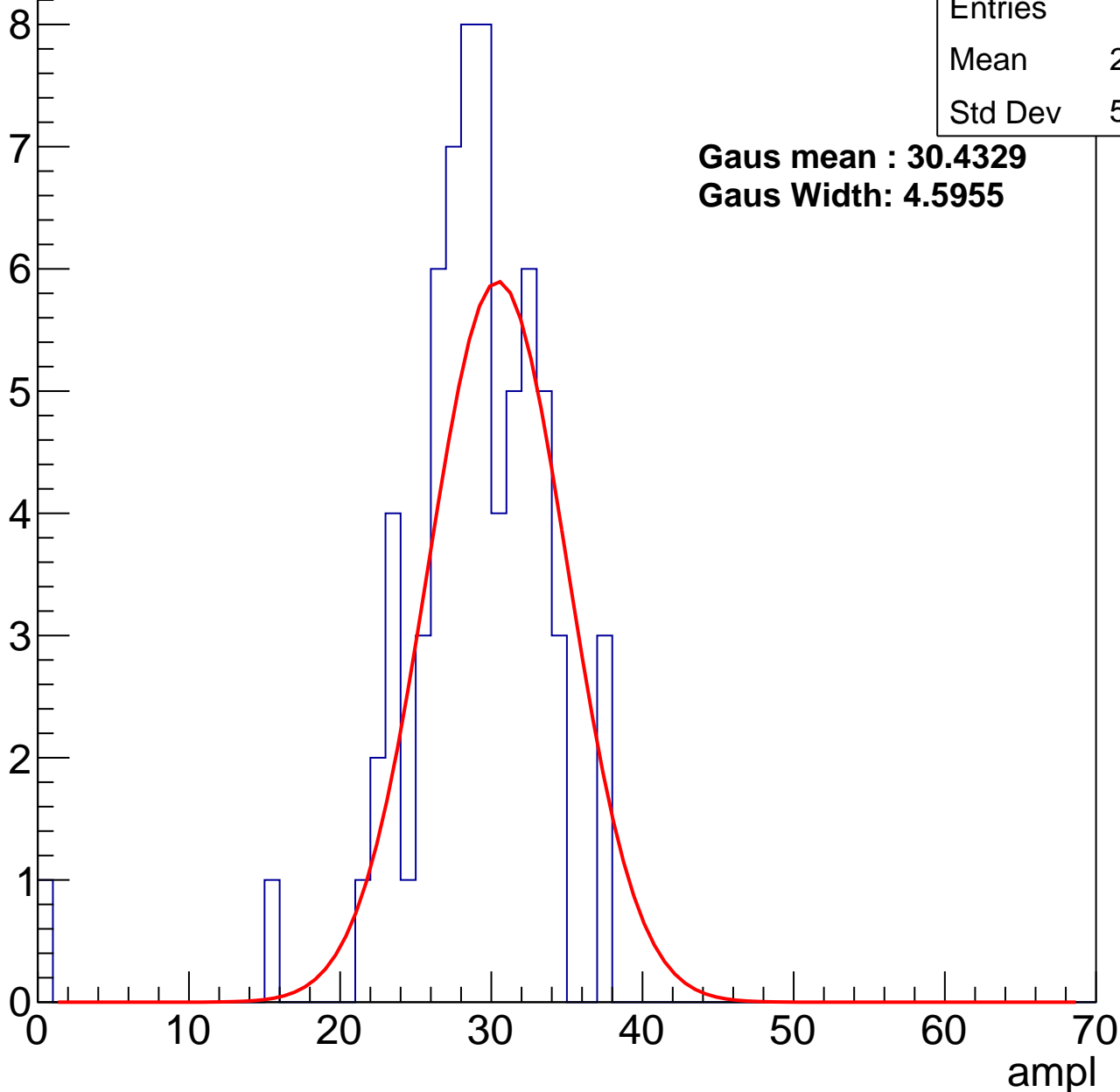
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.19
Std Dev	5.267

Gaus mean : 30.4329

Gaus Width: 4.5955



B1L103S, U14-ch52, adc1

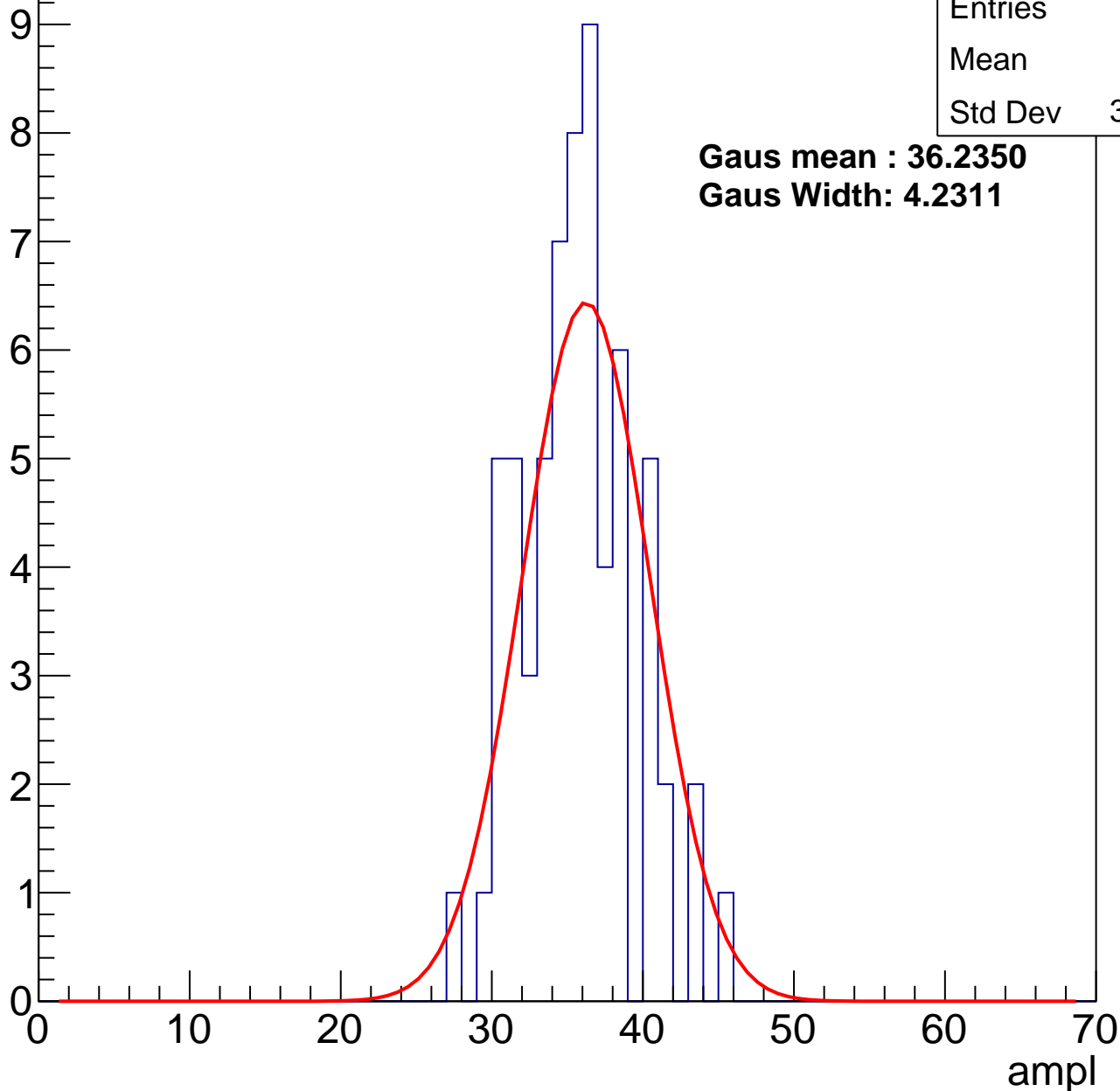
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.2
Std Dev	3.666

Gaus mean : 36.2350

Gaus Width: 4.2311



B1L103S, U14-ch52, adc2

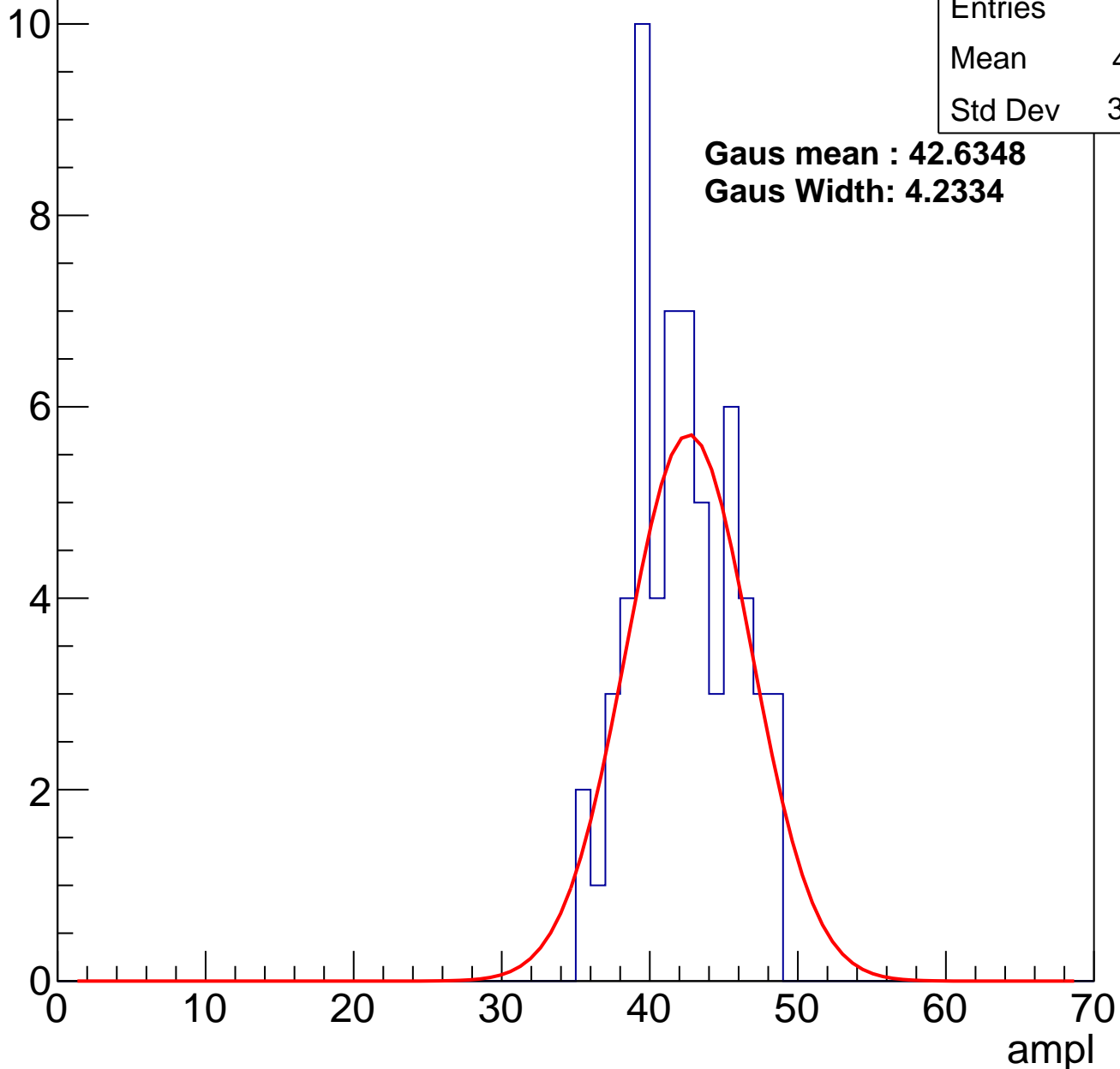
calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	41.71
Std Dev	3.357

Gaus mean : 42.6348

Gaus Width: 4.2334

Entry

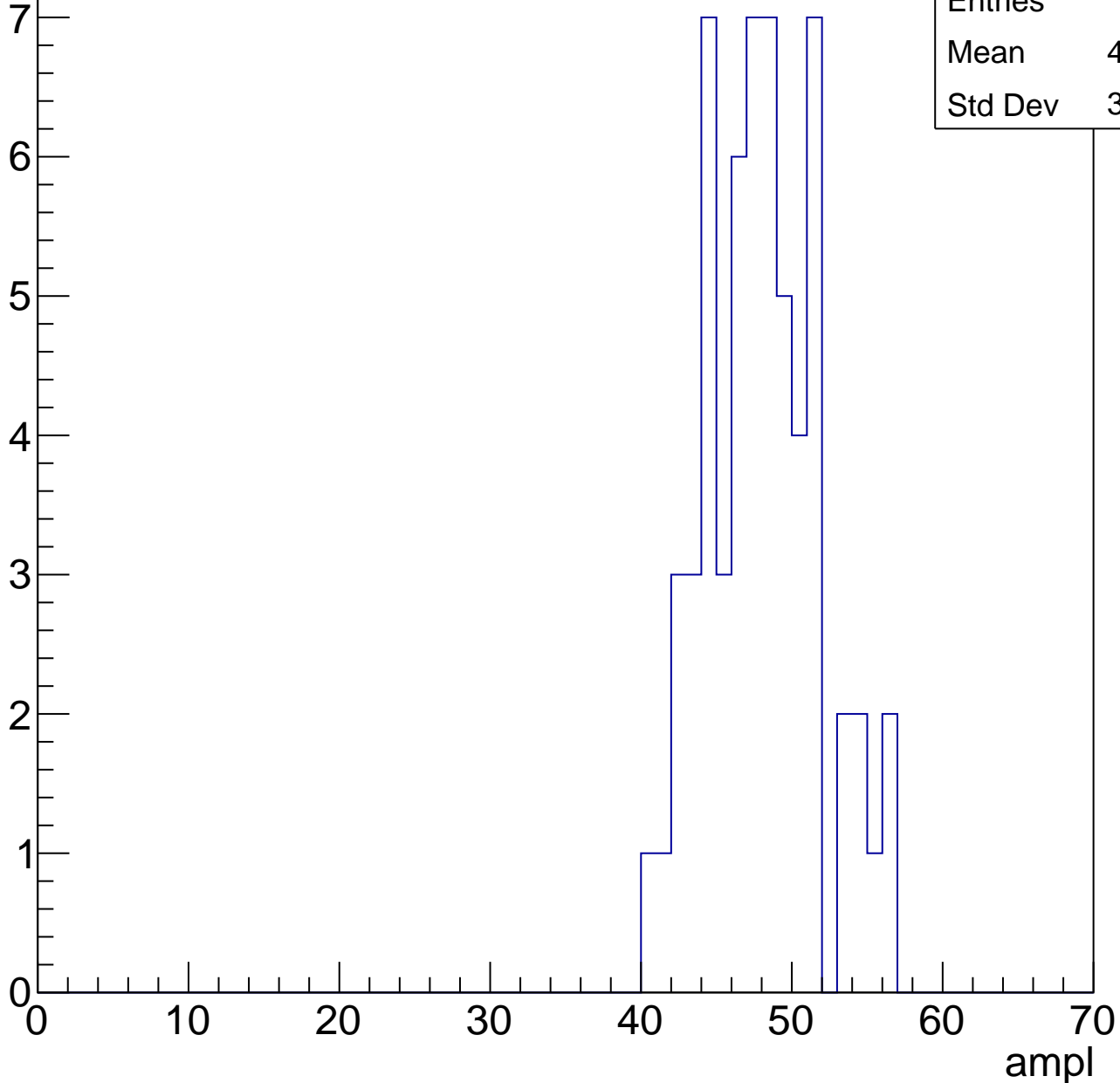


B1L103S, U14-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	47.59
Std Dev	3.708

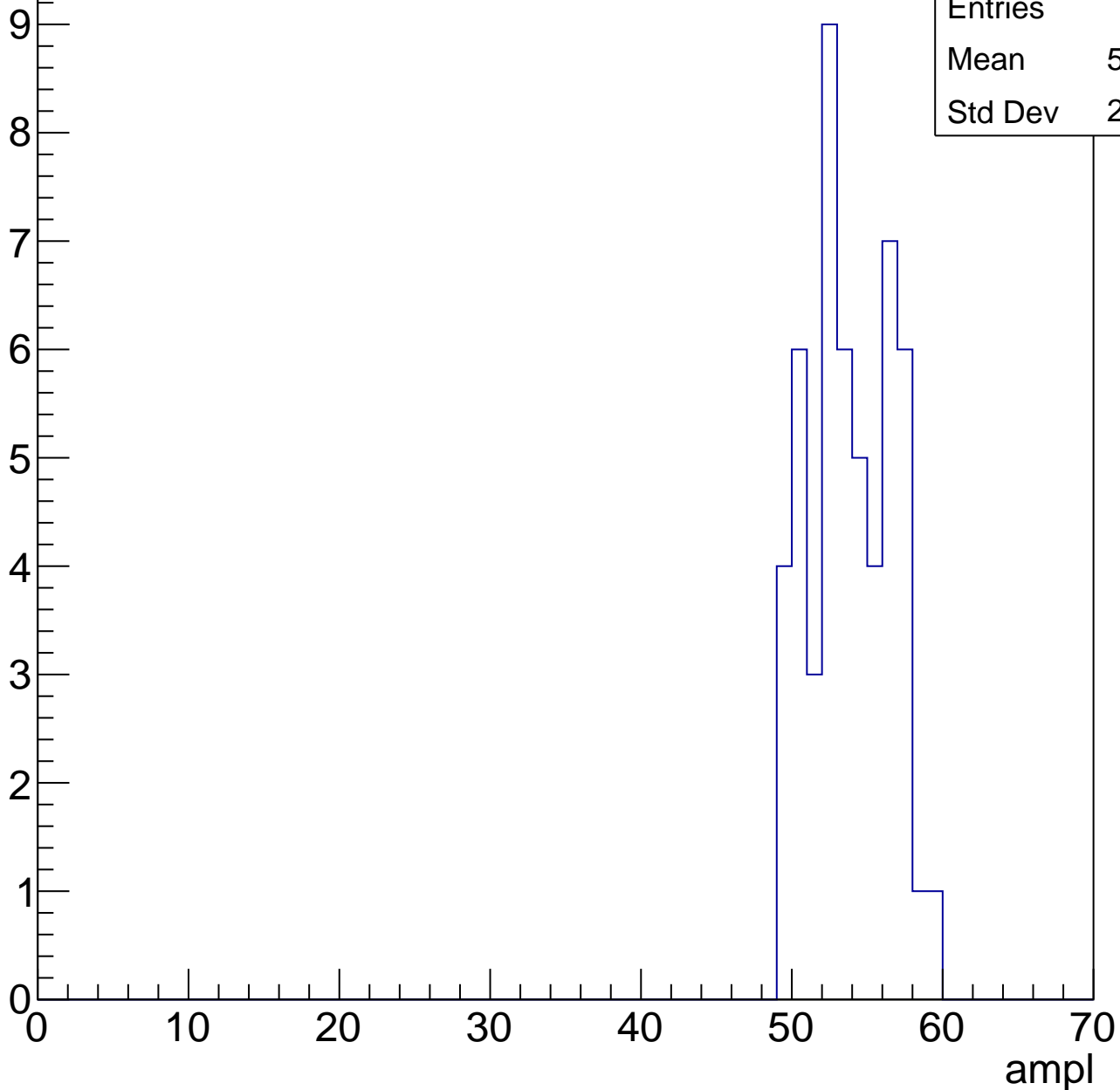


B1L103S, U14-ch52, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

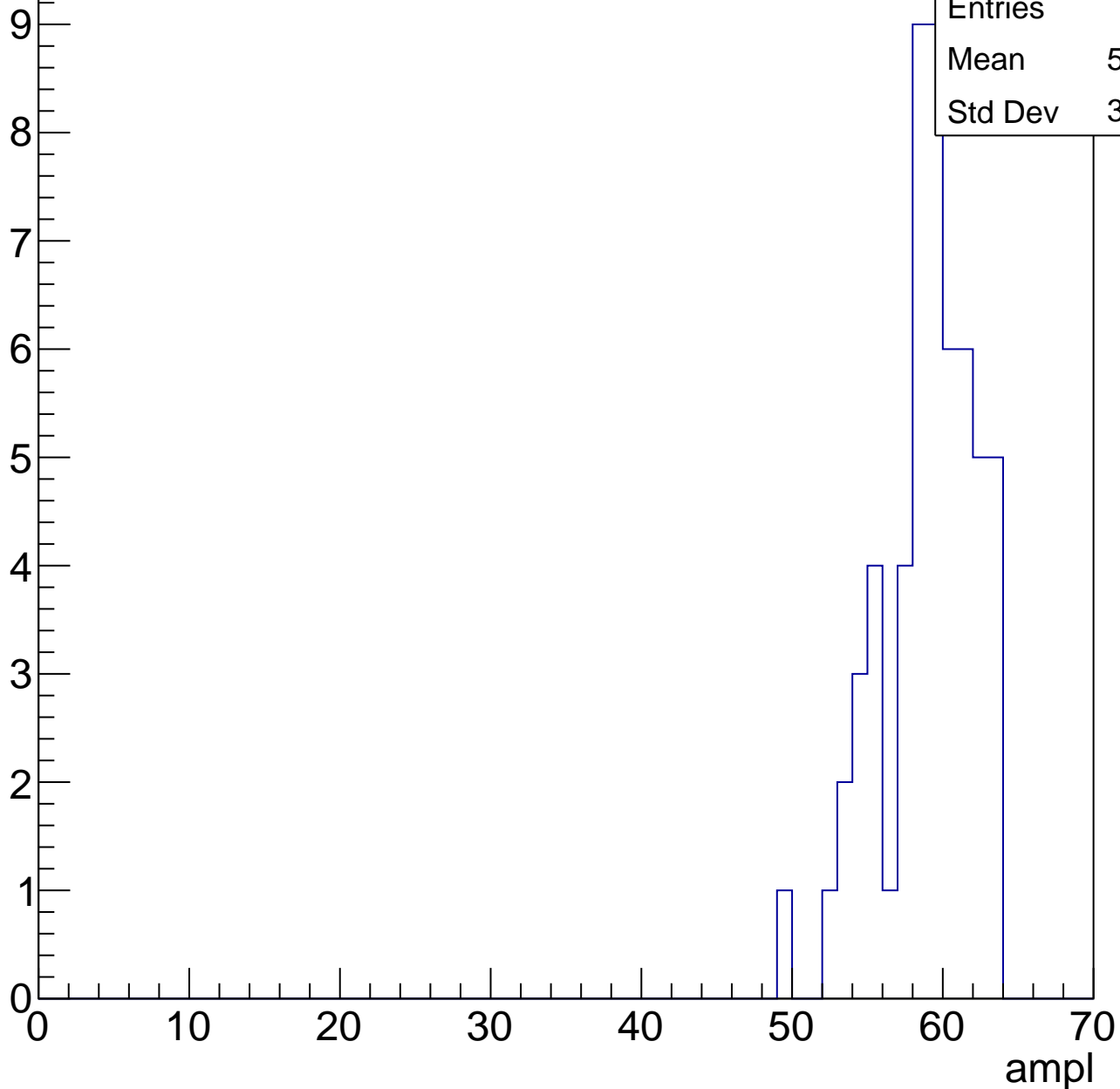
Entries	52
Mean	53.38
Std Dev	2.676



B1L103S, U14-ch52, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

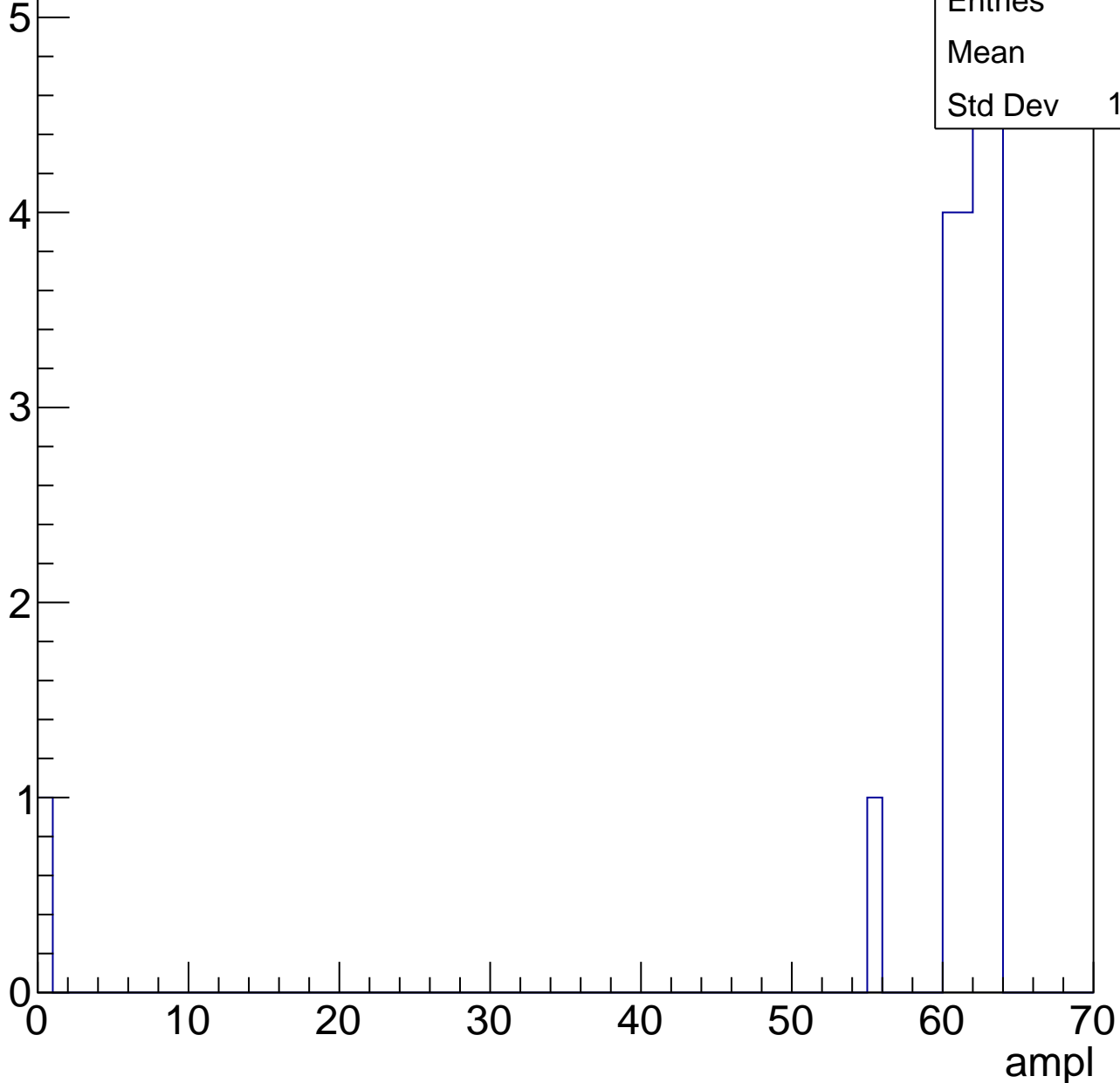


B1L103S, U14-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	20
Mean	58.2
Std Dev	13.47



B1L103S, U14-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch53, adc0

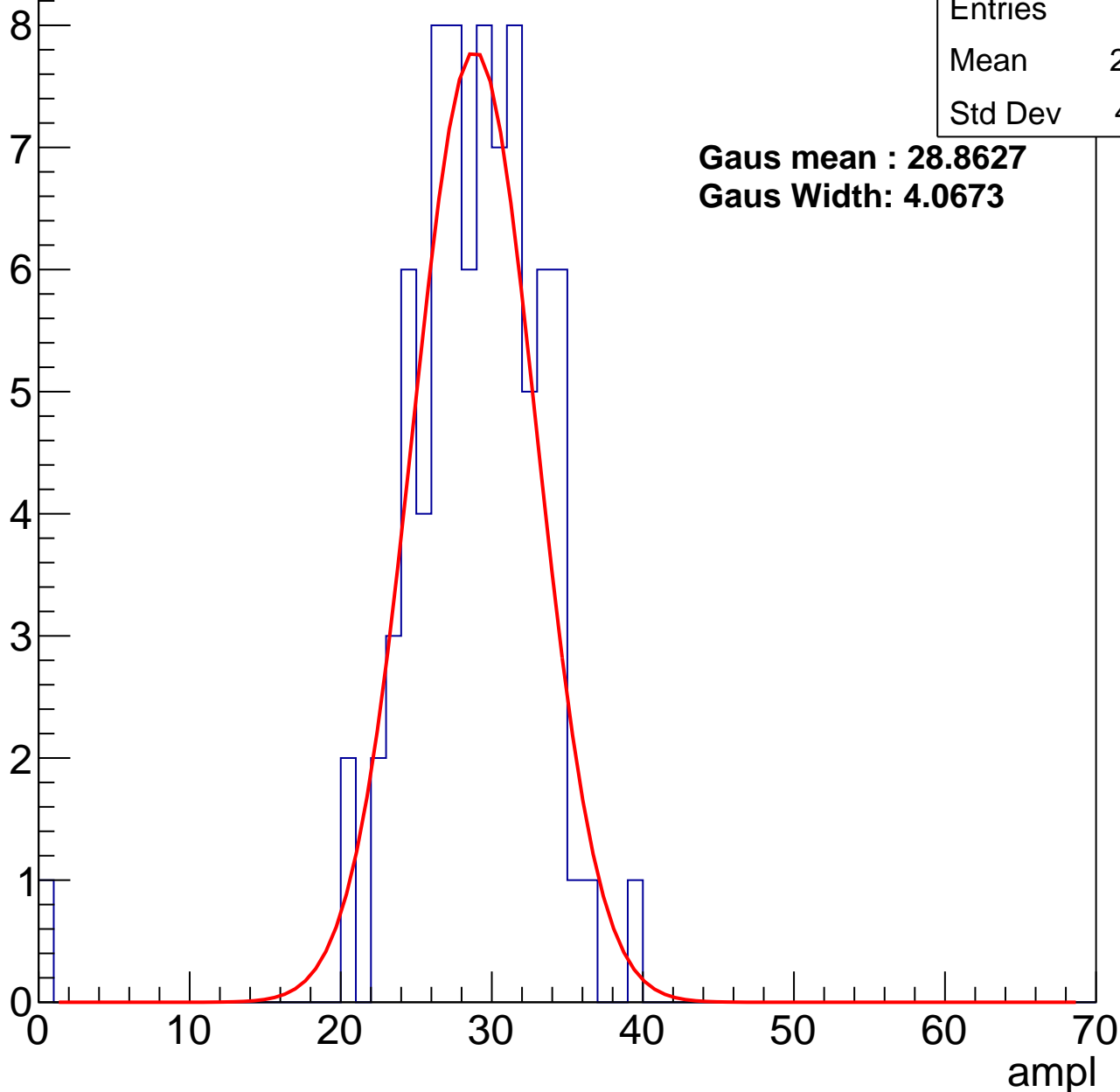
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	28.33
Std Dev	4.921

Gaus mean : 28.8627

Gaus Width: 4.0673



B1L103S, U14-ch53, adc1

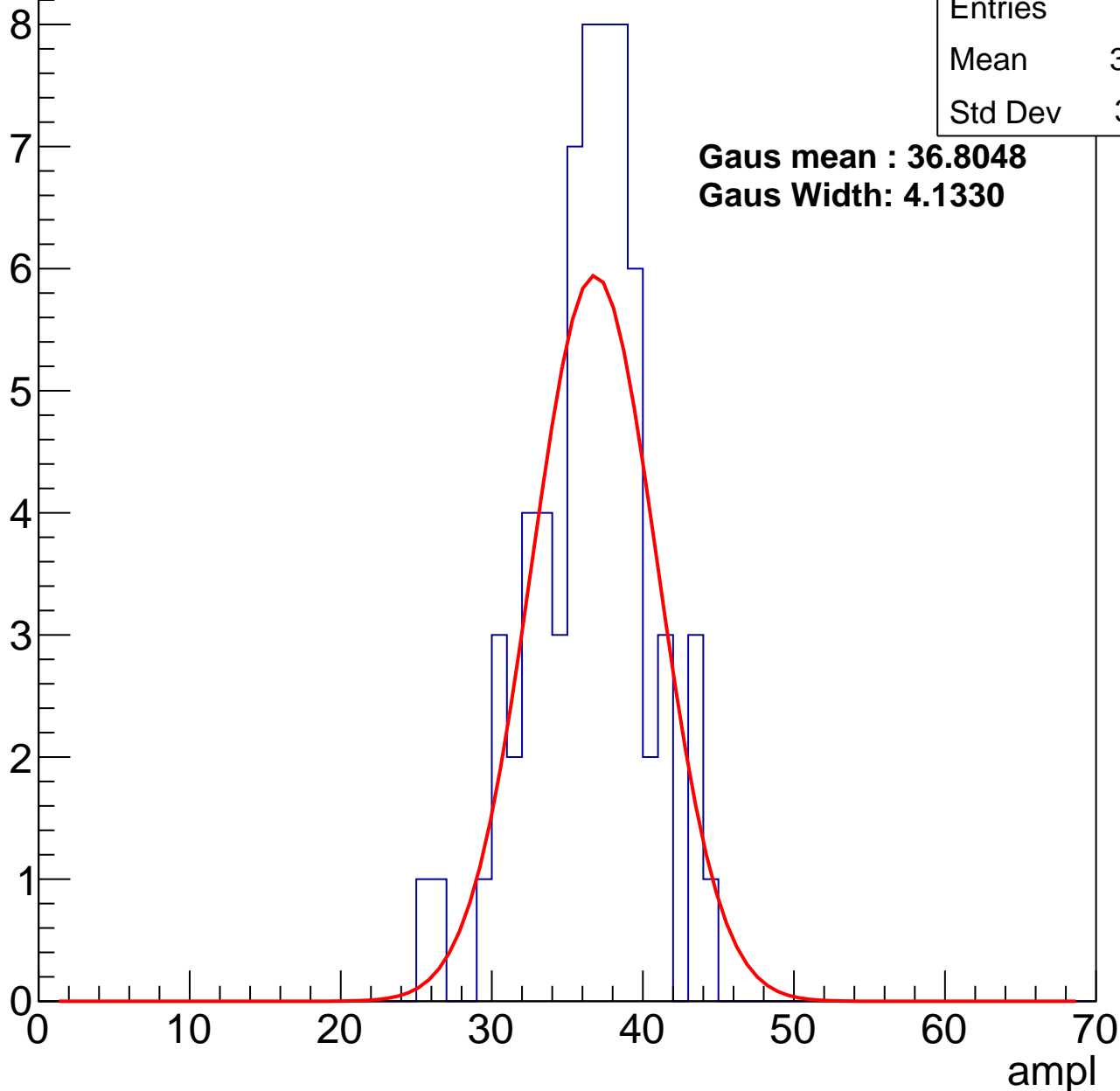
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.95
Std Dev	3.841

Gaus mean : 36.8048

Gaus Width: 4.1330



B1L103S, U14-ch53, adc2

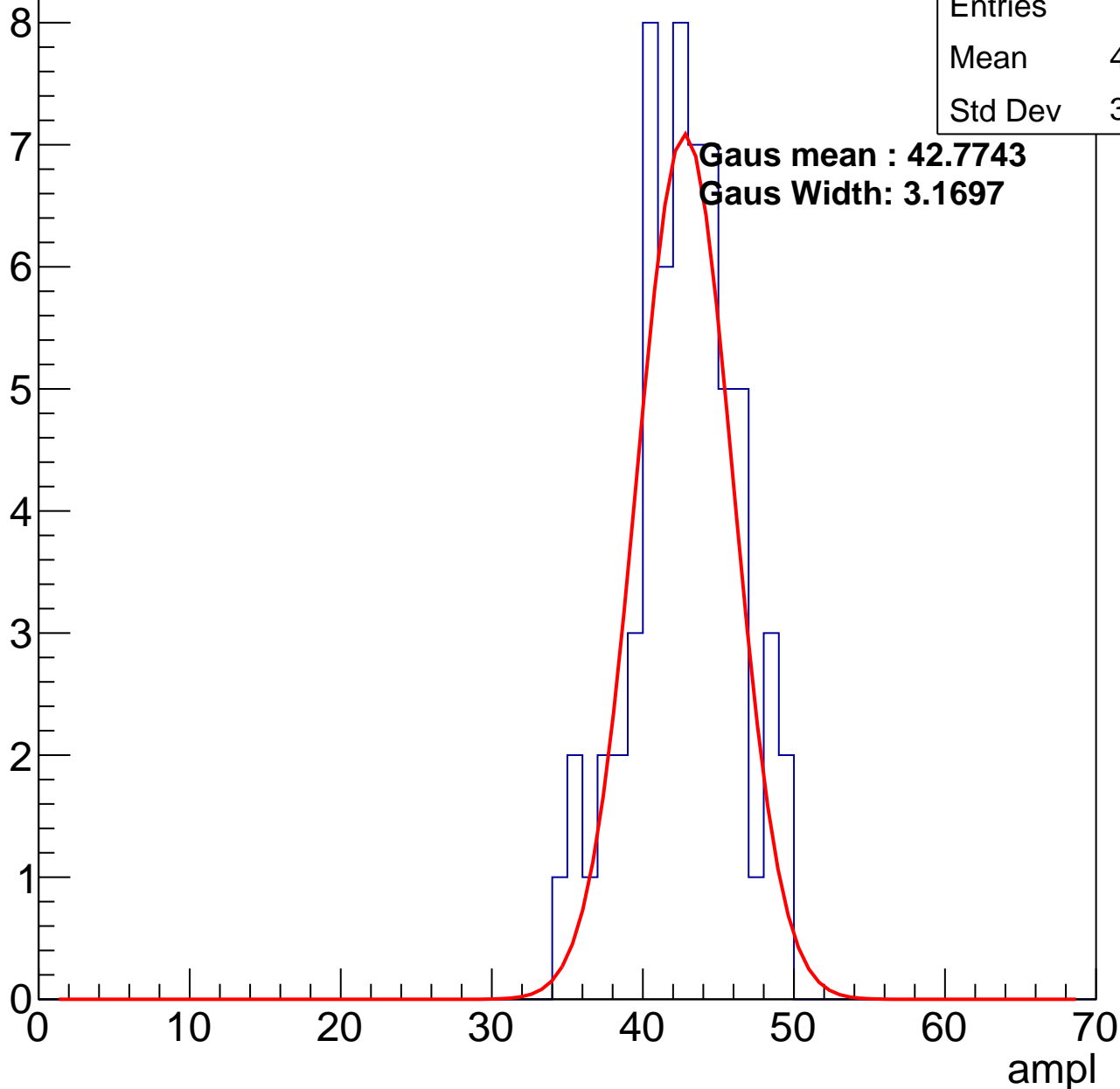
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.25
Std Dev	3.409

Gaus mean : 42.7743

Gaus Width: 3.1697

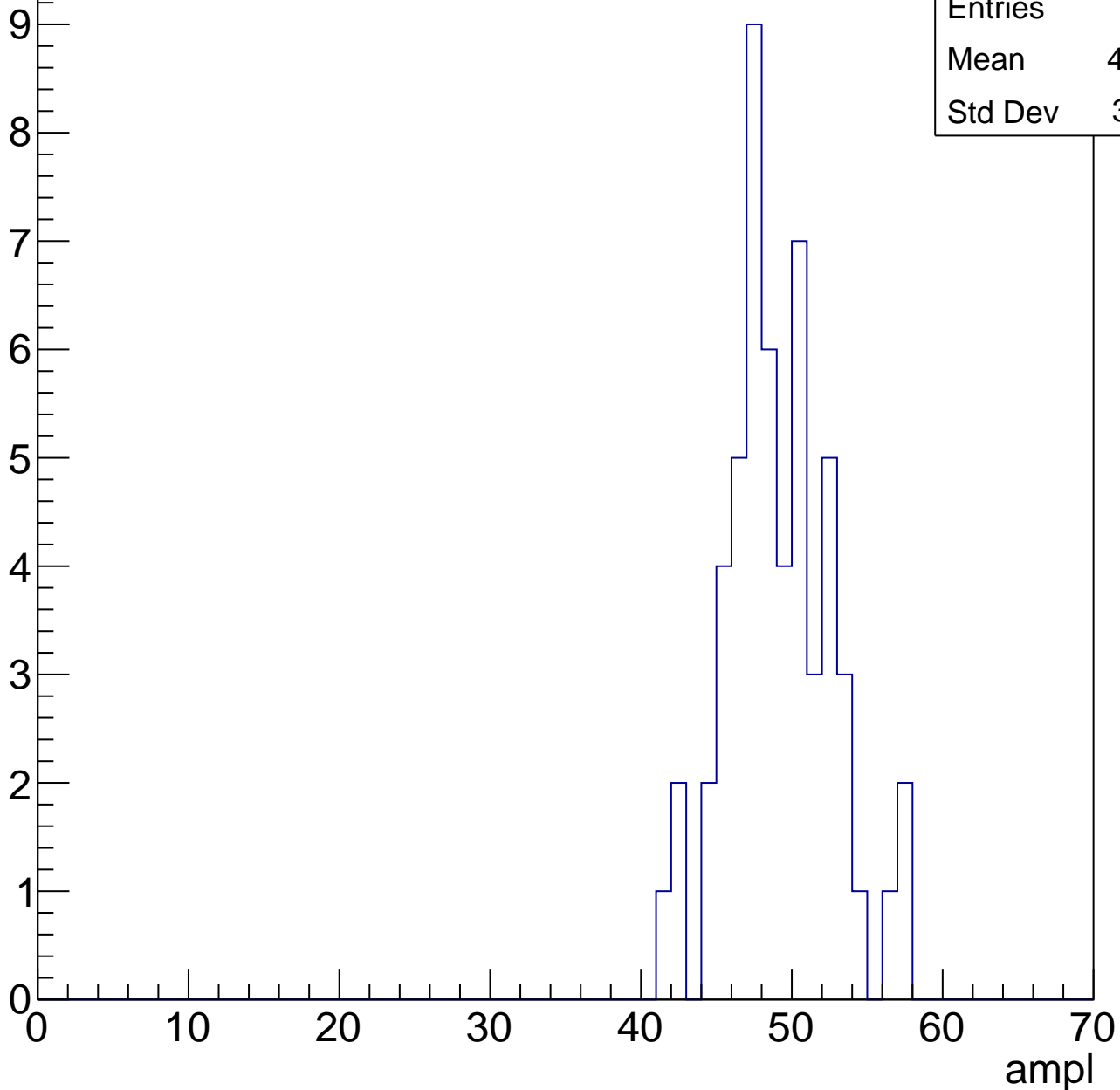


B1L103S, U14-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	48.65
Std Dev	3.491

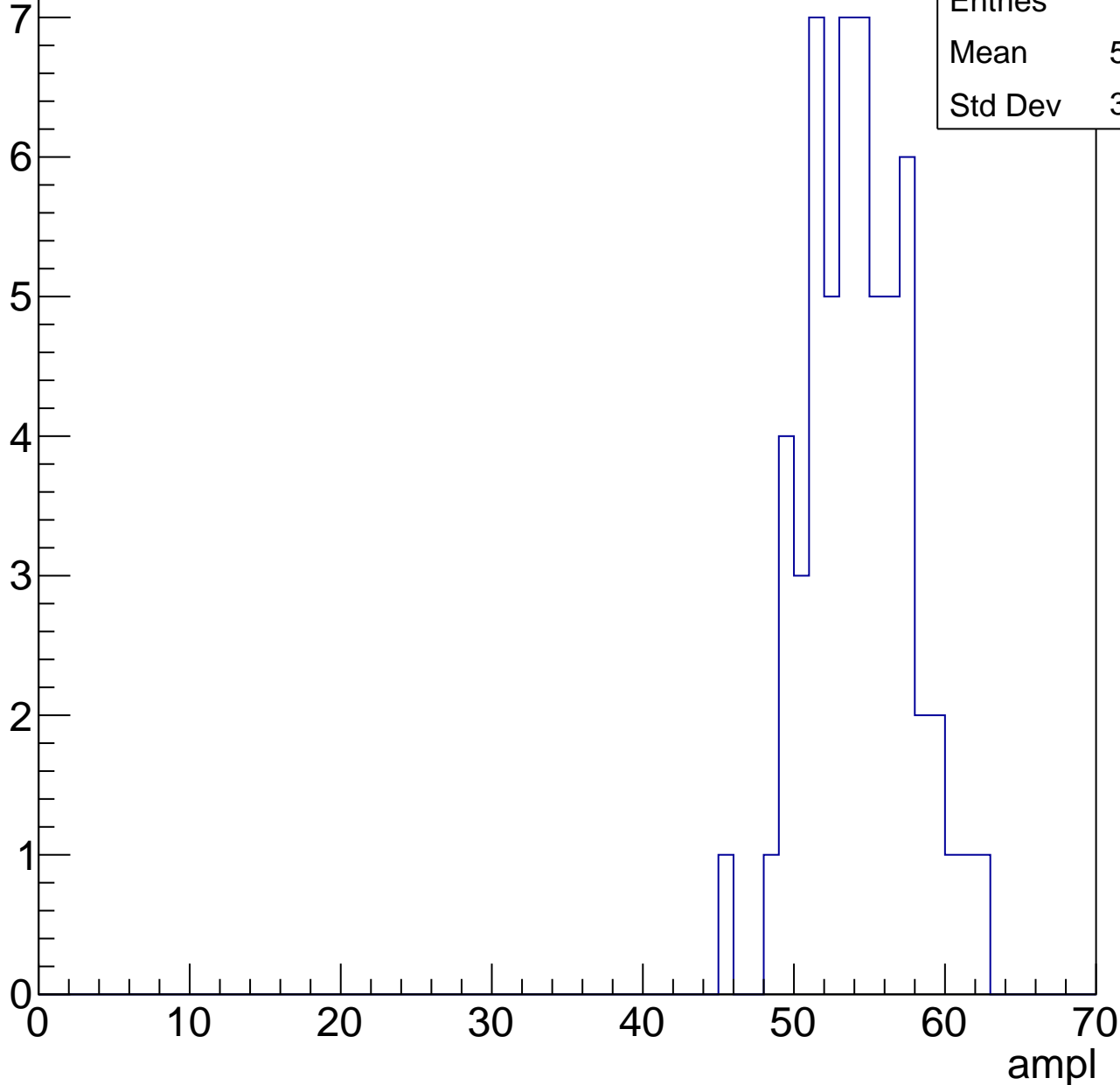


B1L103S, U14-ch53, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

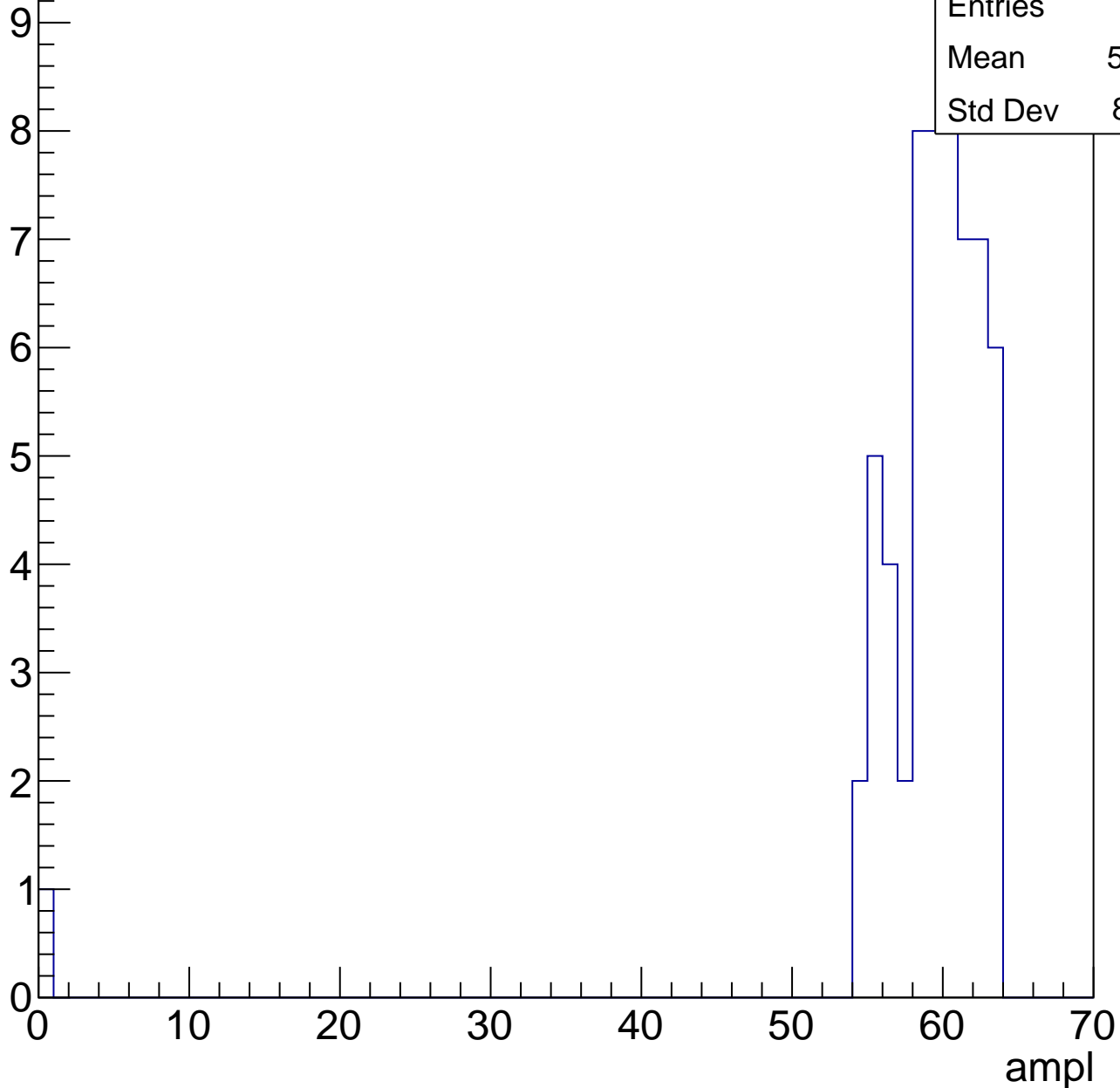
Entries	58
Mean	53.78
Std Dev	3.379



B1L103S, U14-ch53, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	8
Mean	62.25
Std Dev	0.8292

B1L103S, U14-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U14-ch54, adc0

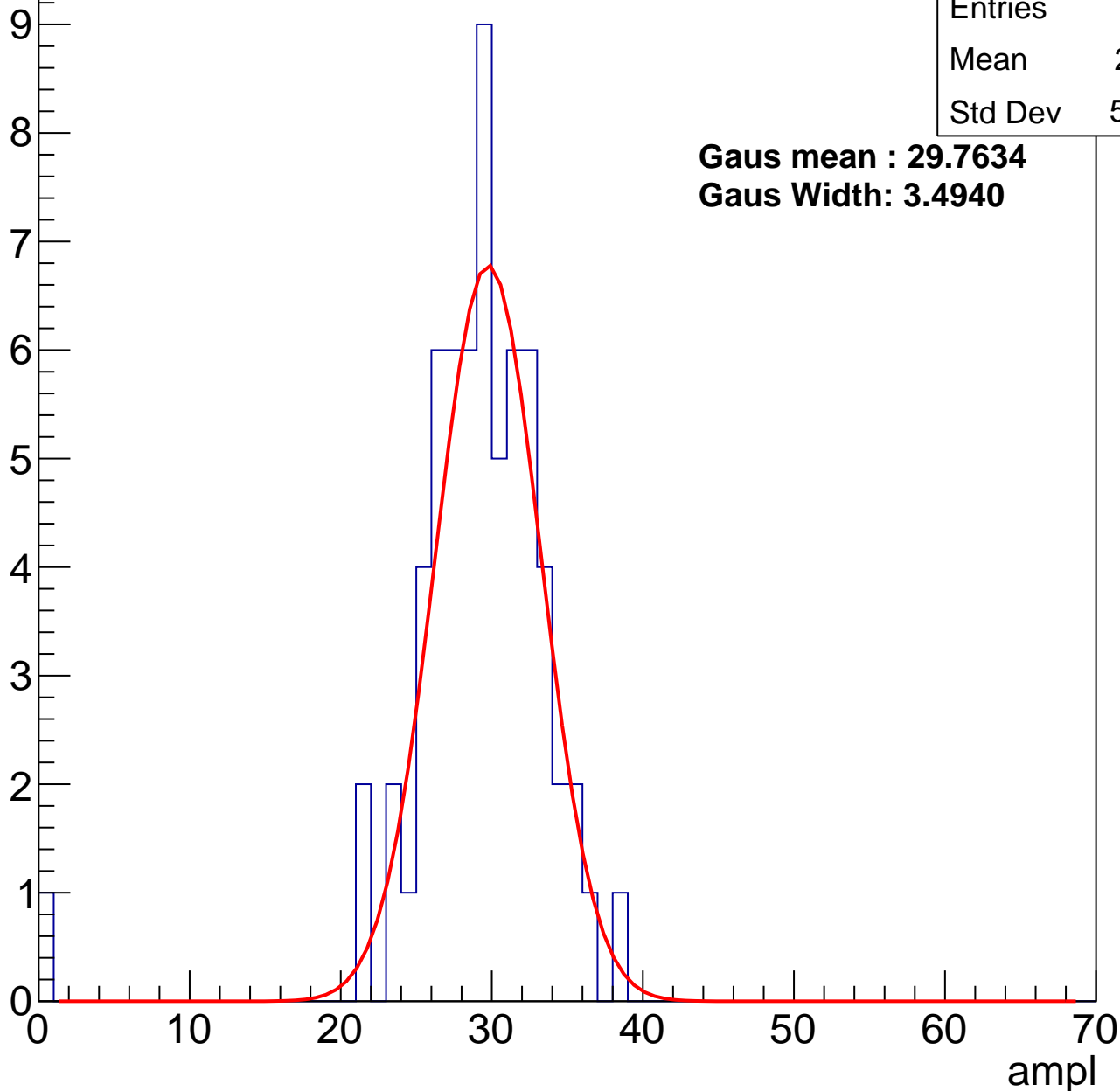
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.61
Std Dev	5.005

Gaus mean : 29.7634

Gaus Width: 3.4940



B1L103S, U14-ch54, adc1

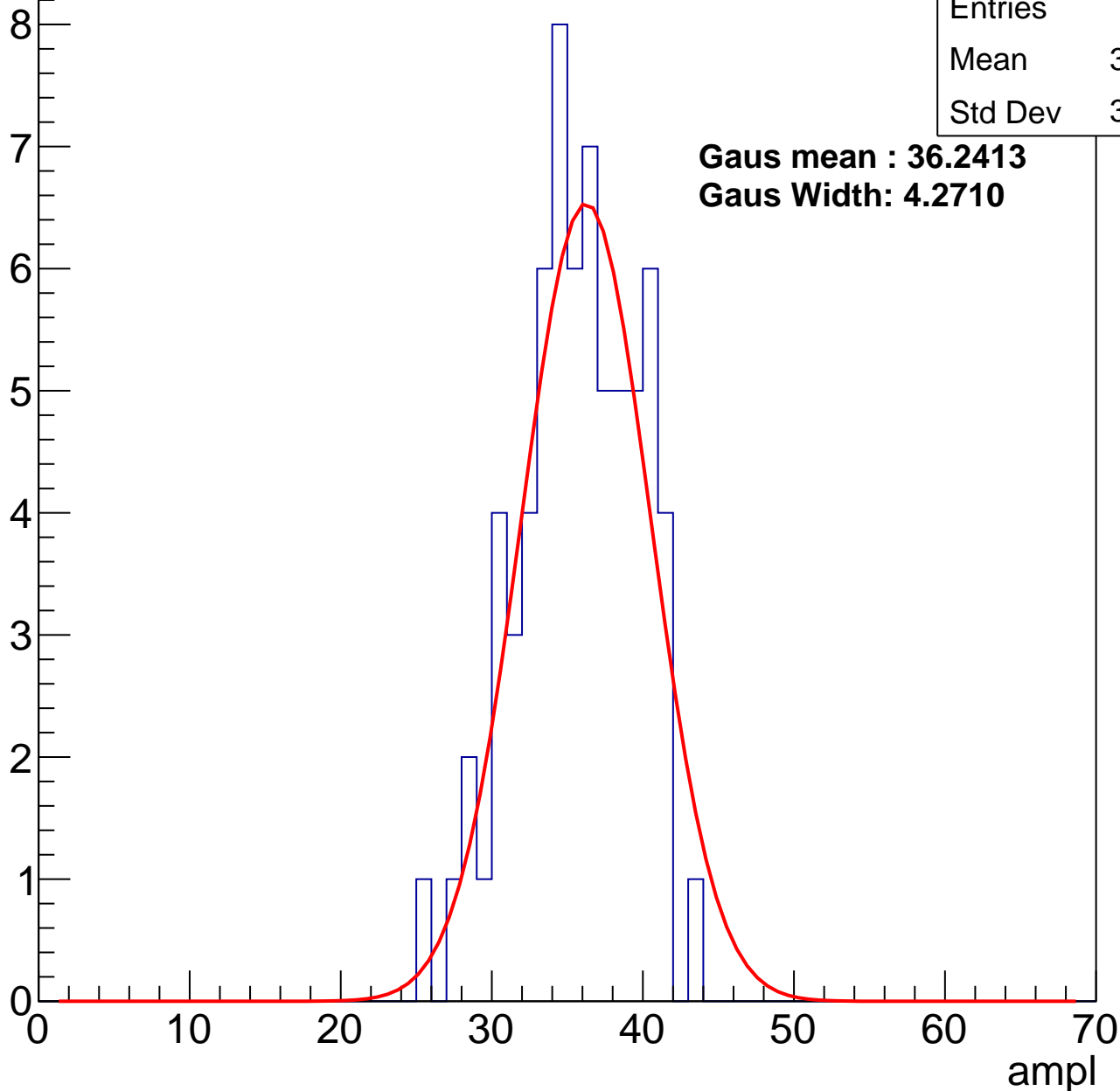
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.17
Std Dev	3.848

Gaus mean : 36.2413

Gaus Width: 4.2710



B1L103S, U14-ch54, adc2

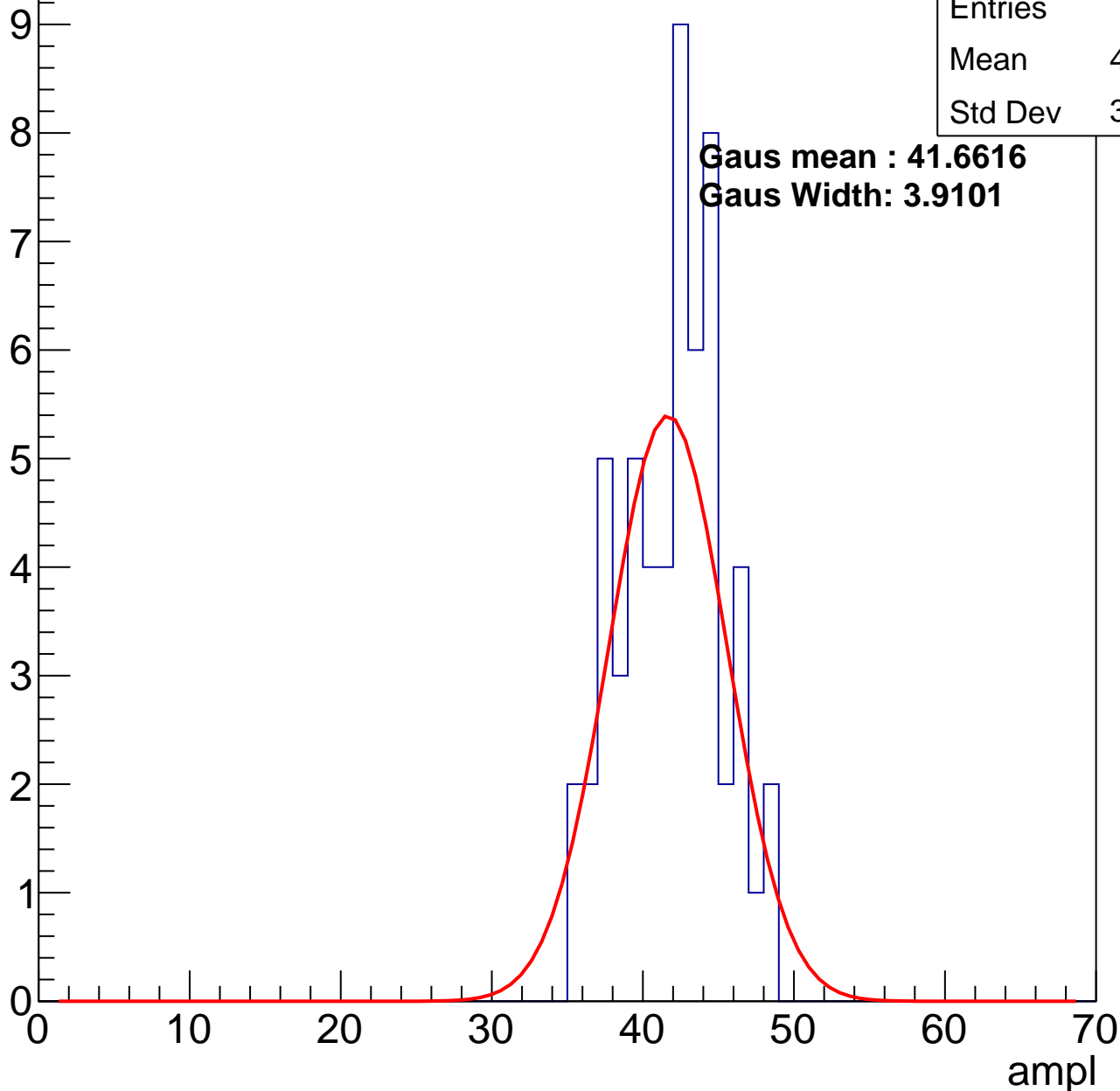
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41.49
Std Dev	3.272

Gaus mean : 41.6616

Gaus Width: 3.9101

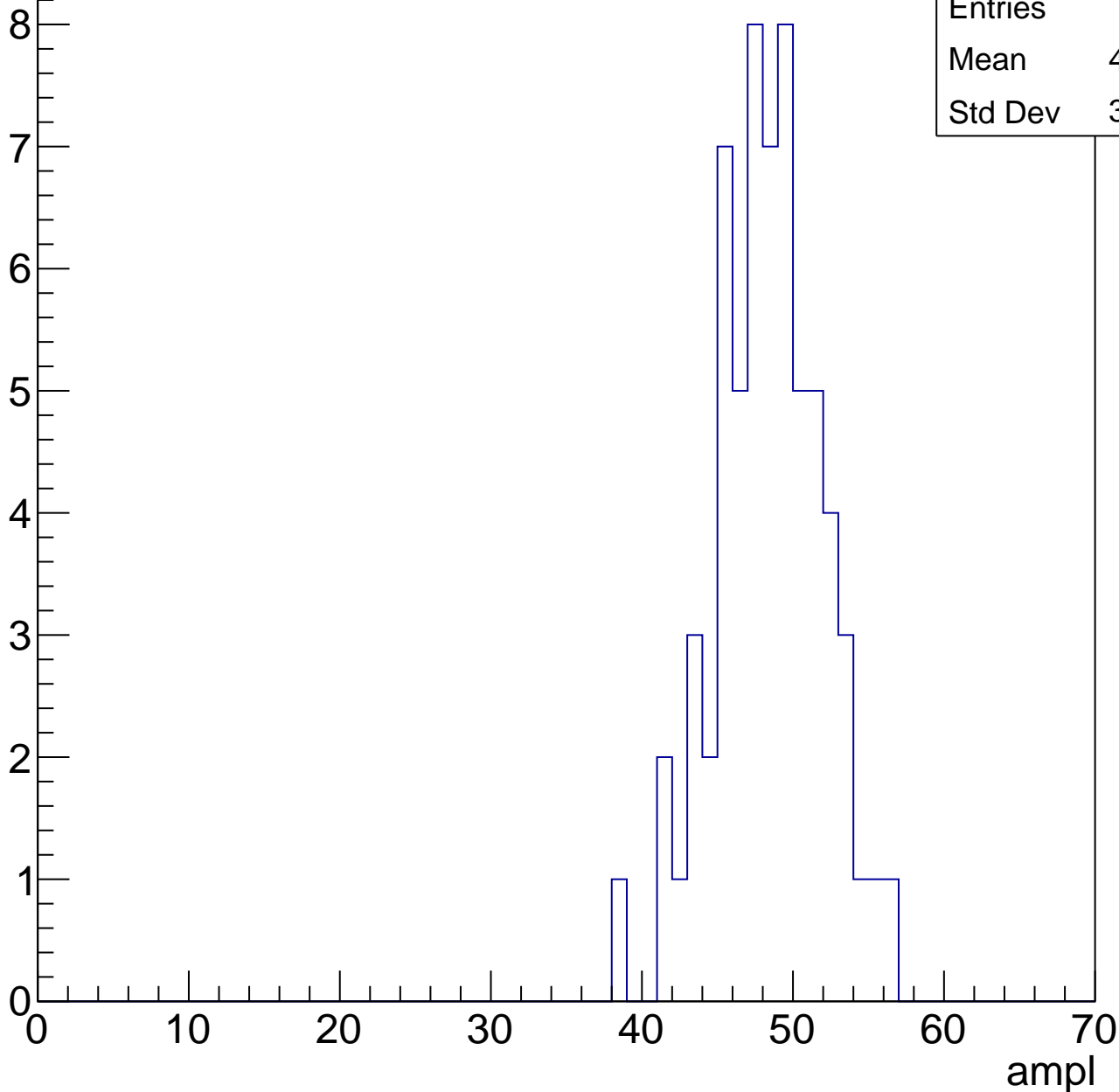


B1L103S, U14-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	47.89
Std Dev	3.518

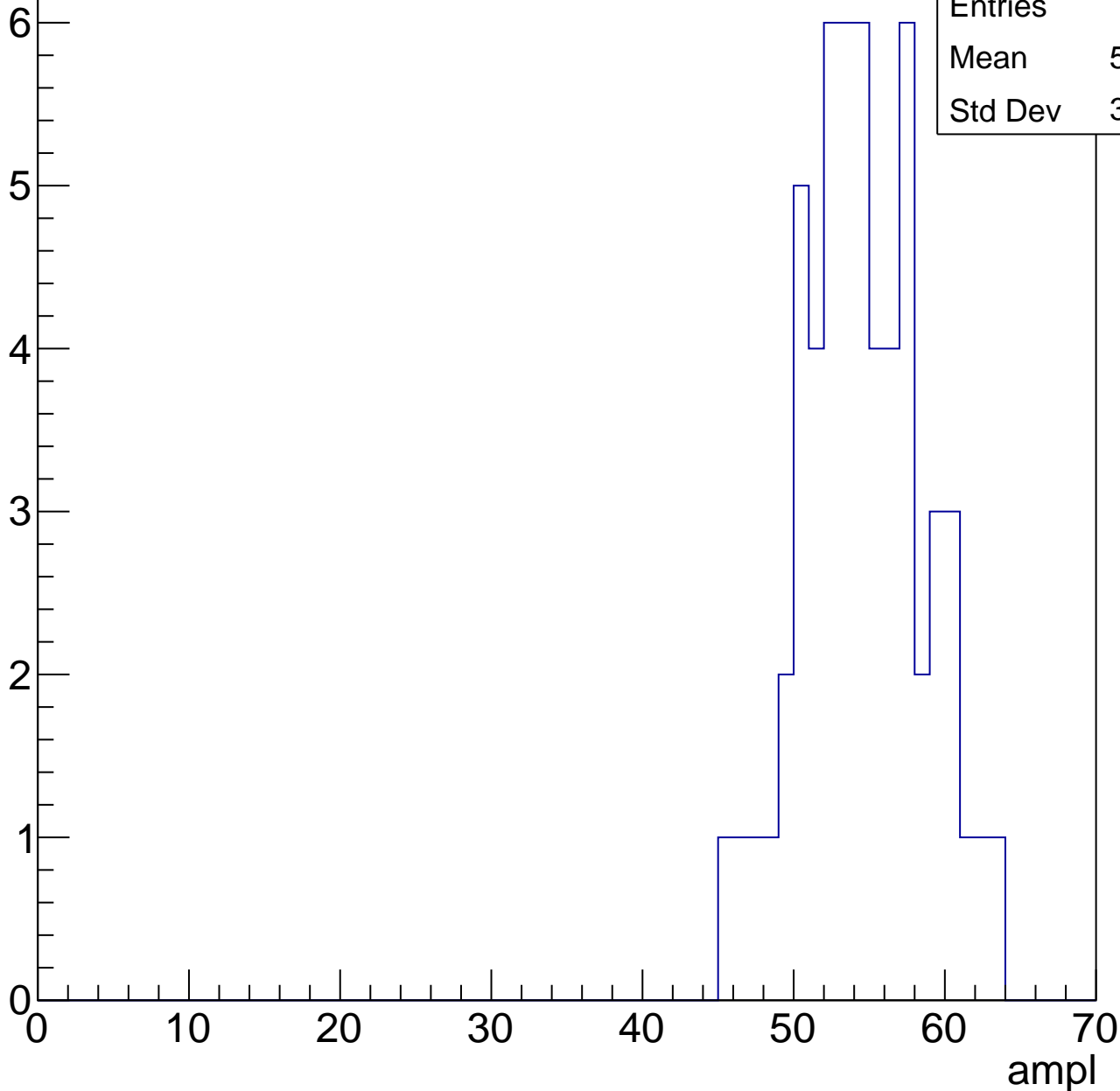


B1L103S, U14-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.09
Std Dev	3.958



B1L103S, U14-ch54, adc5

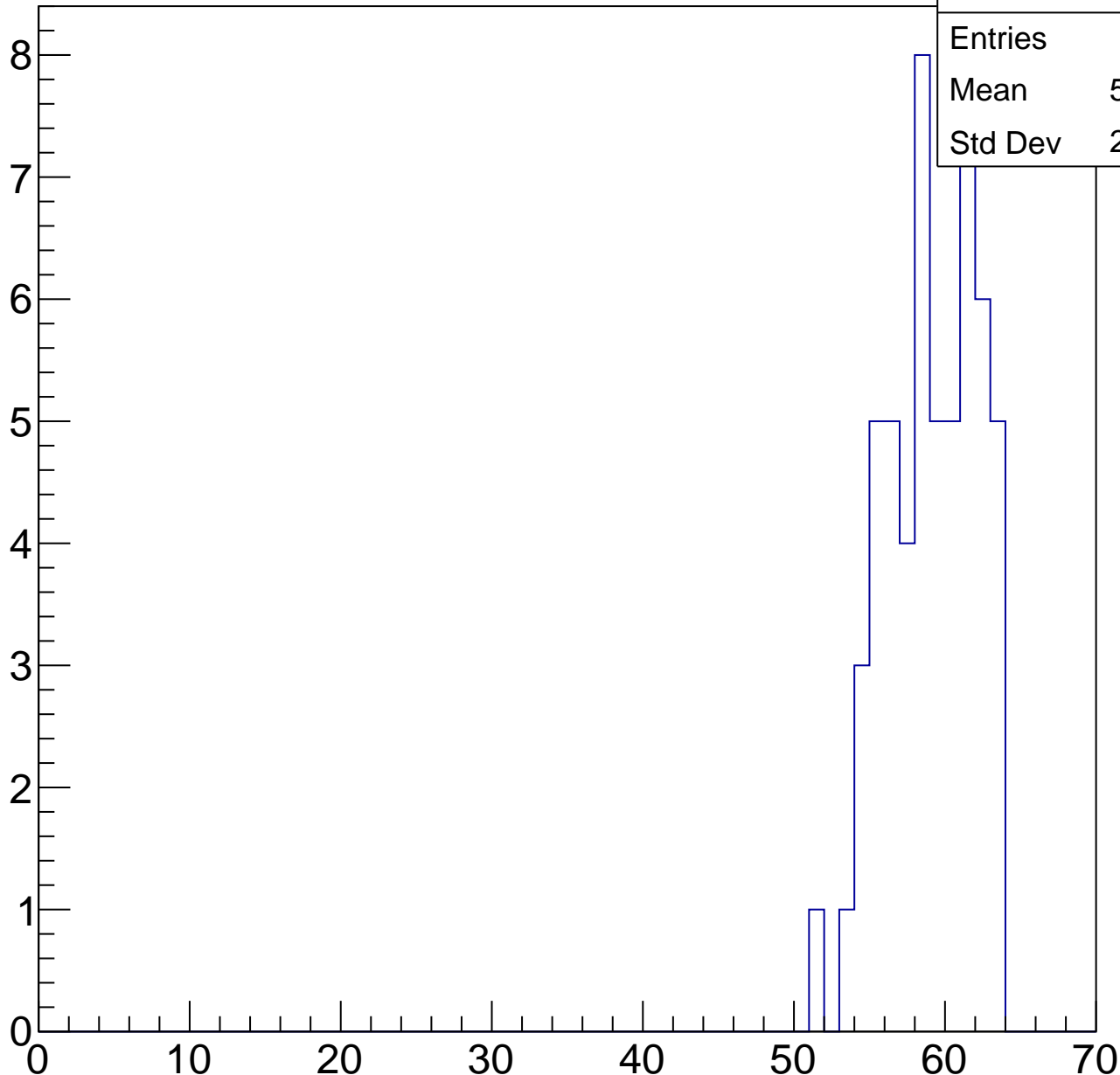
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.62
Std Dev	2.949

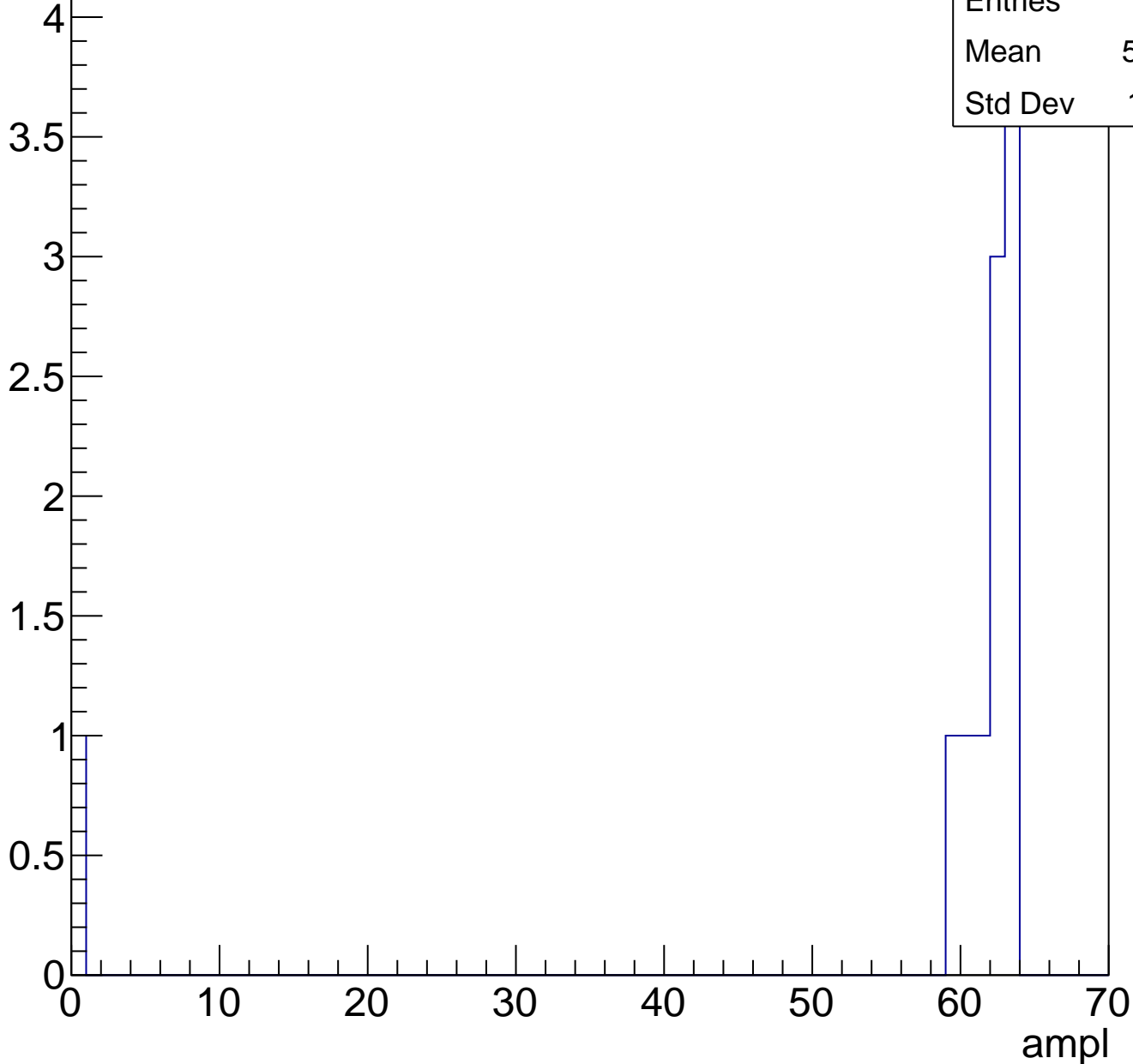
ampl



B1L103S, U14-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

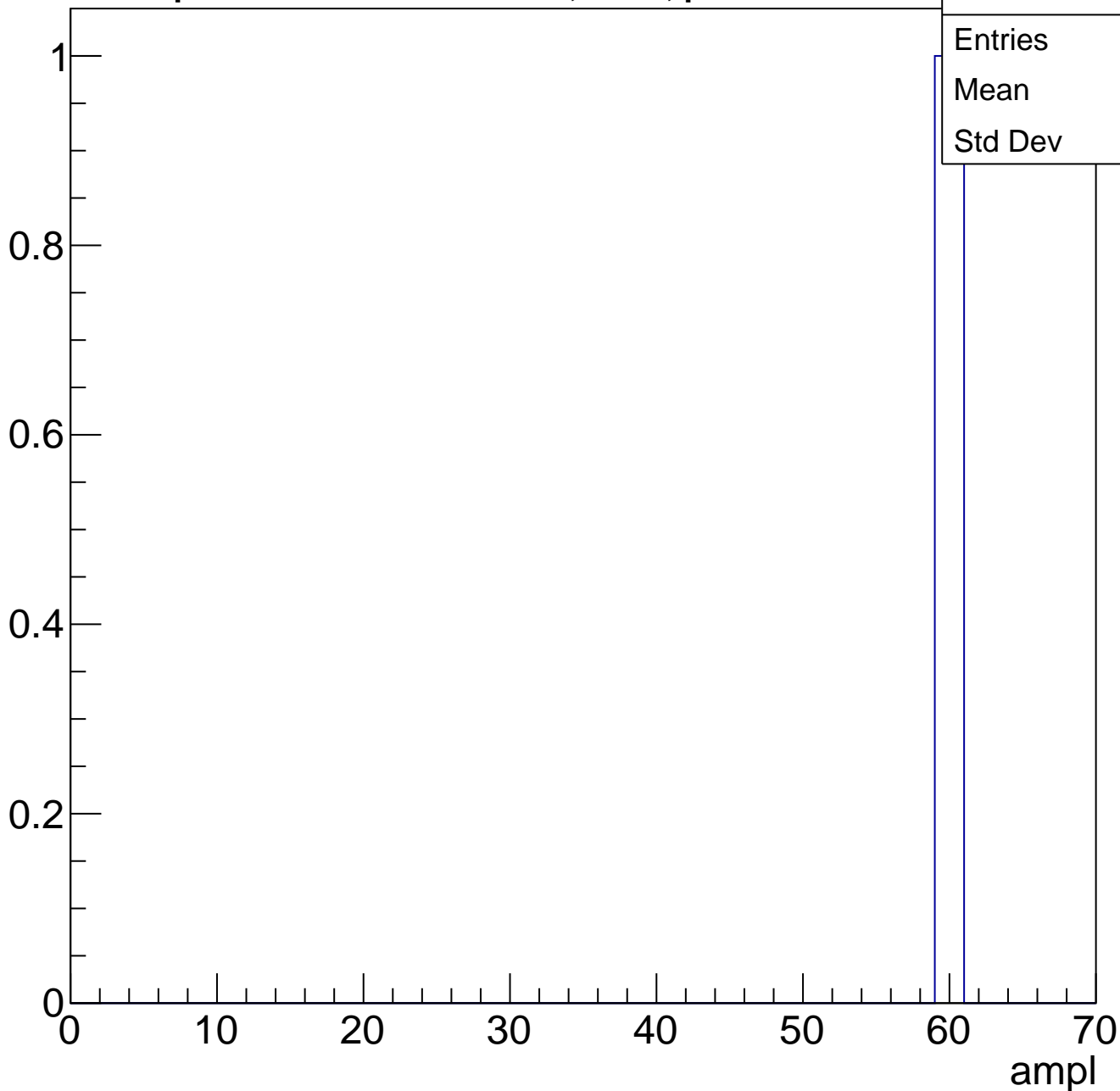
Entry



B1L103S, U14-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch55, adc0

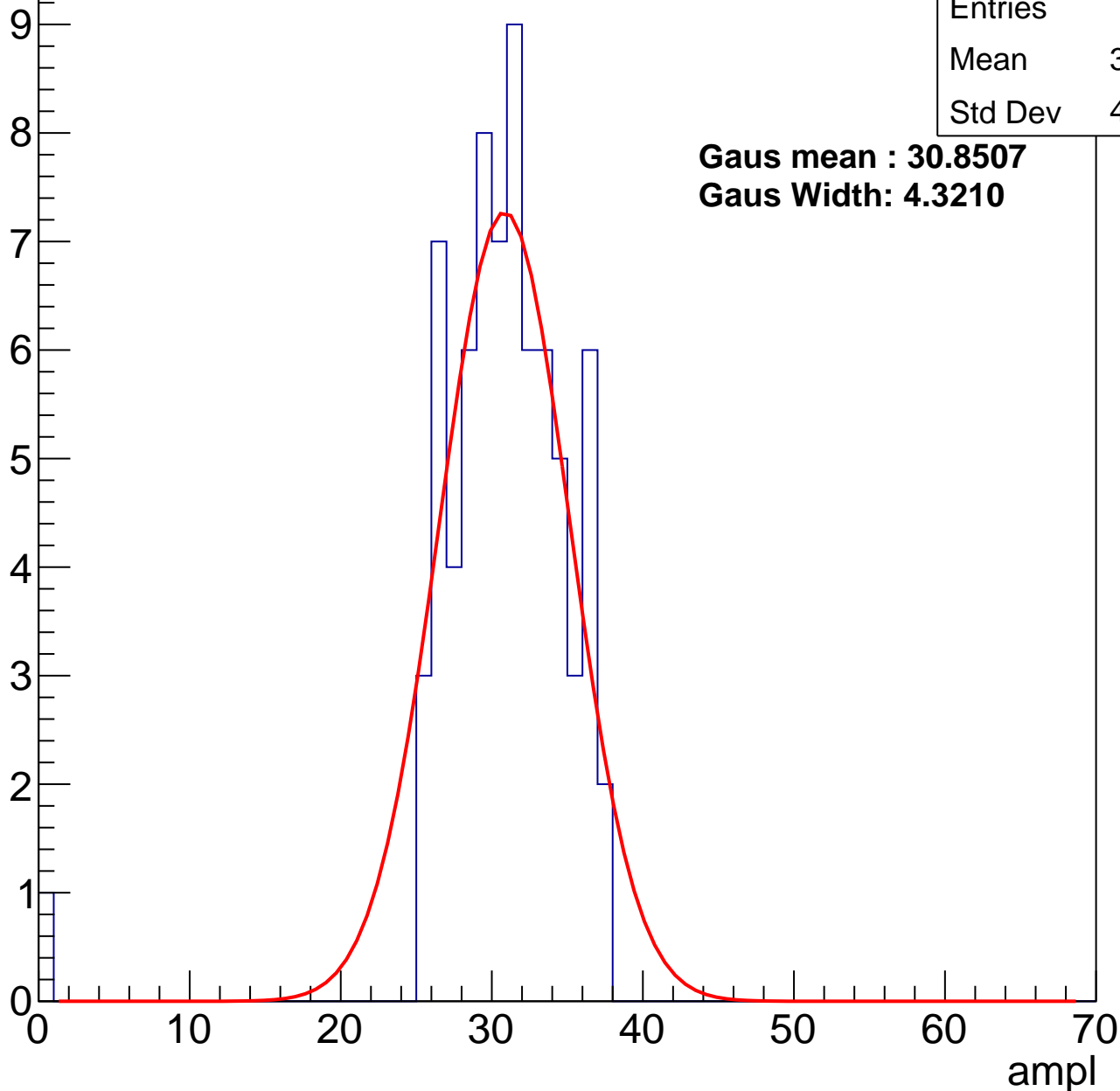
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	30.26
Std Dev	4.834

Gaus mean : 30.8507

Gaus Width: 4.3210



B1L103S, U14-ch55, adc1

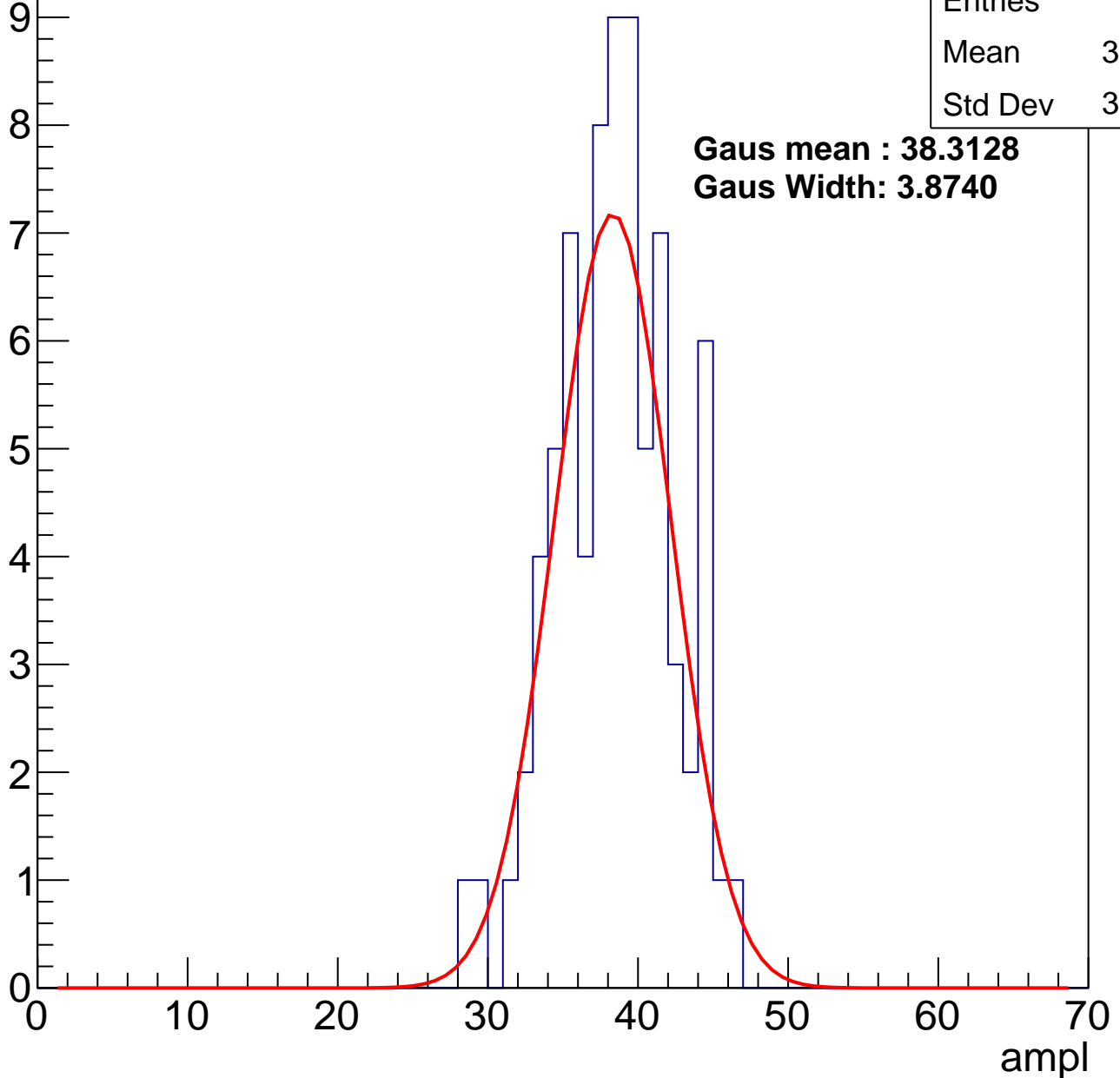
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	37.97
Std Dev	3.784

Gaus mean : 38.3128

Gaus Width: 3.8740



B1L103S, U14-ch55, adc2

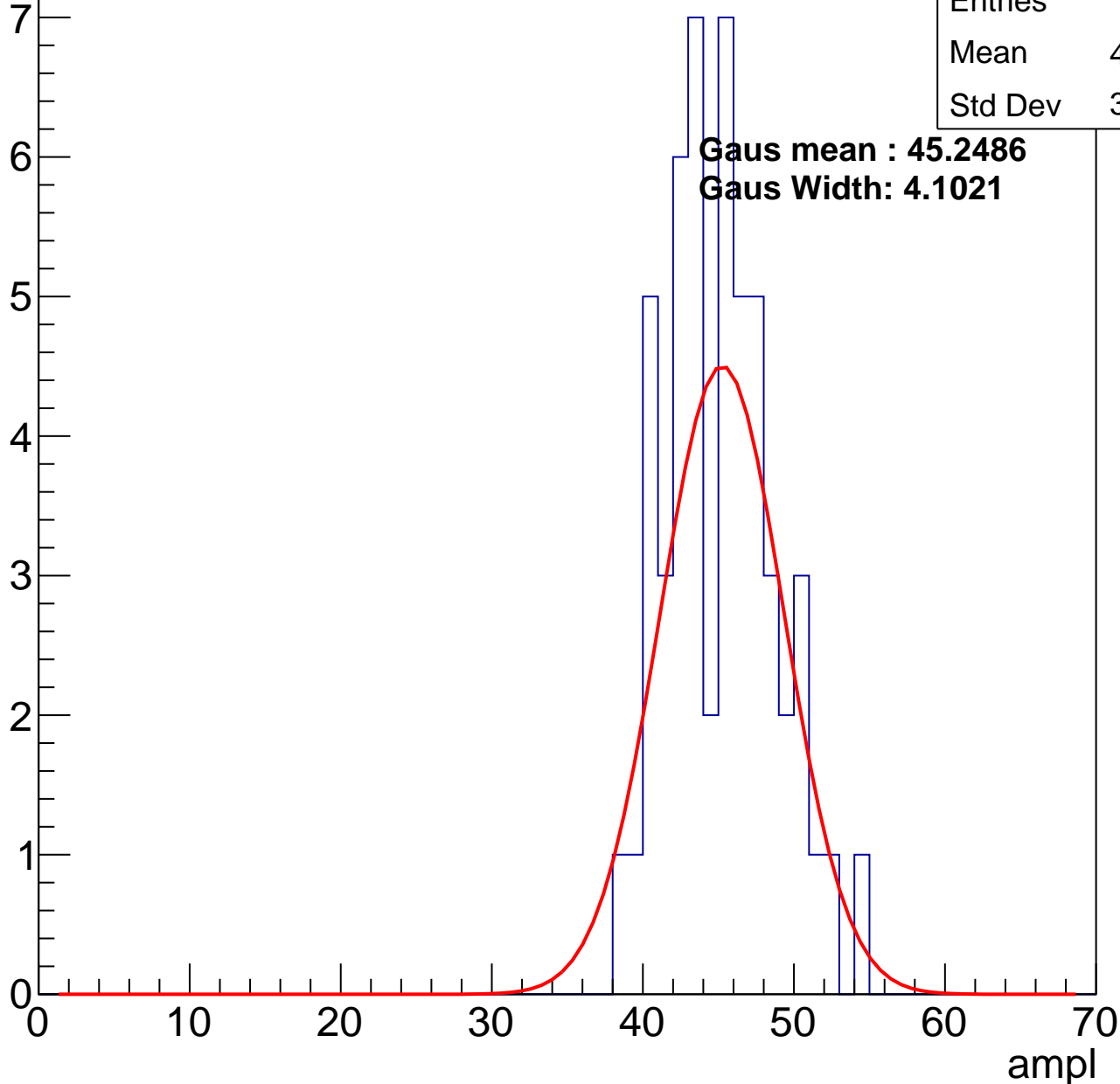
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	44.72
Std Dev	3.536

Gaus mean : 45.2486

Gaus Width: 4.1021

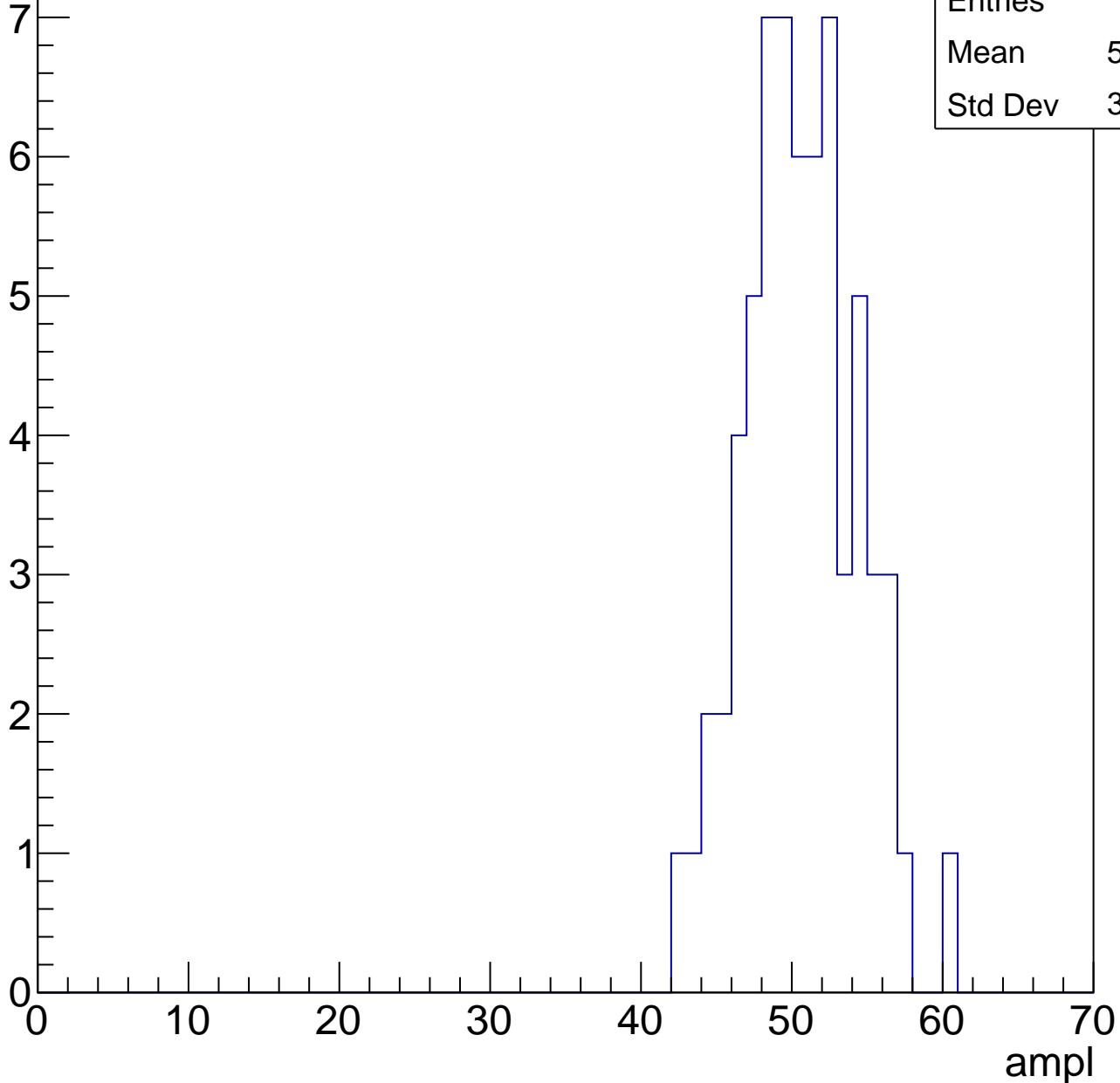


B1L103S, U14-ch55, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	50.16
Std Dev	3.645

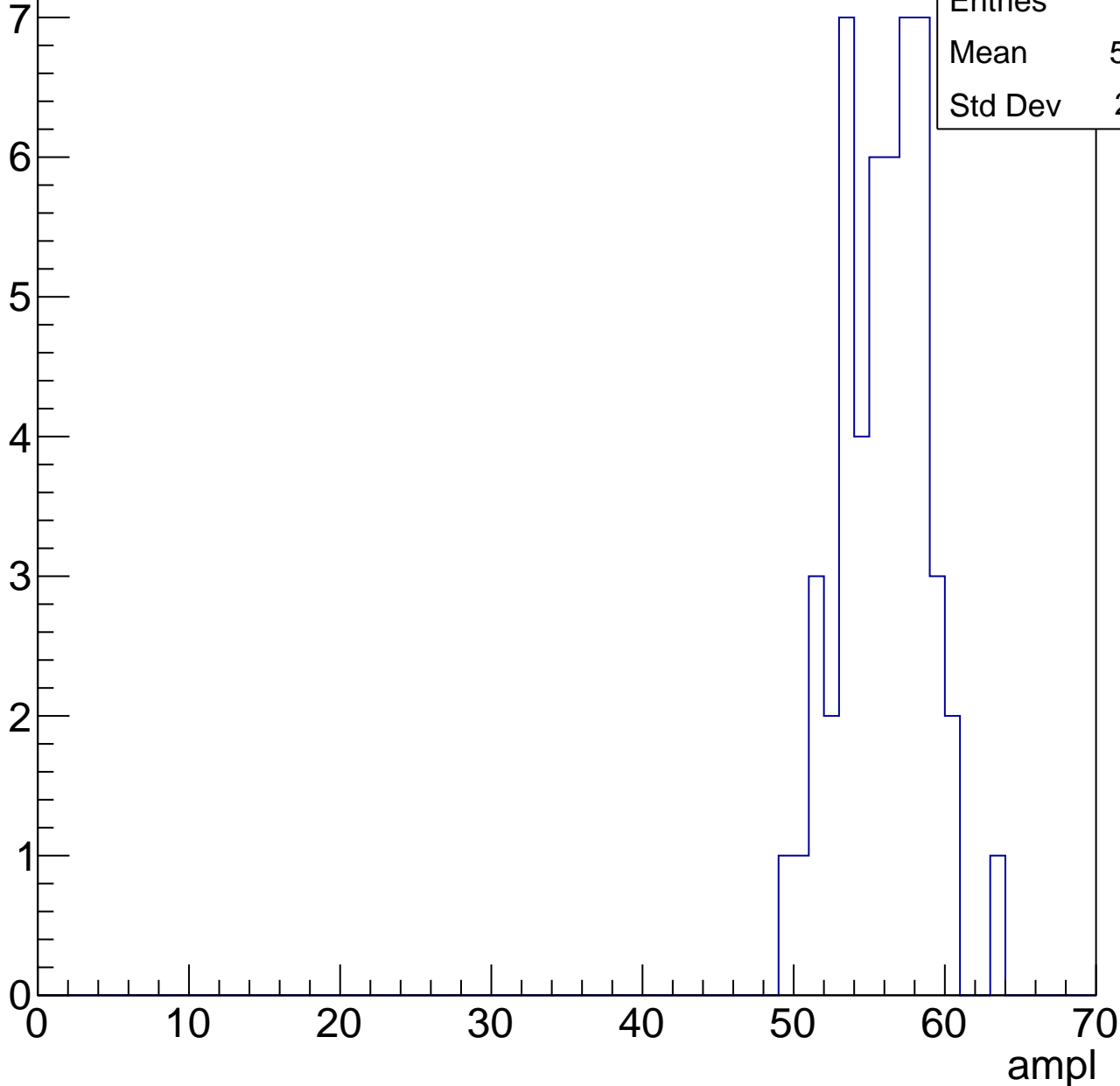


B1L103S, U14-ch55, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	55.48
Std Dev	2.851



B1L103S, U14-ch55, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries

37

Mean

60.19

Std Dev

2.064

ampl

0

10

20

30

40

50

60

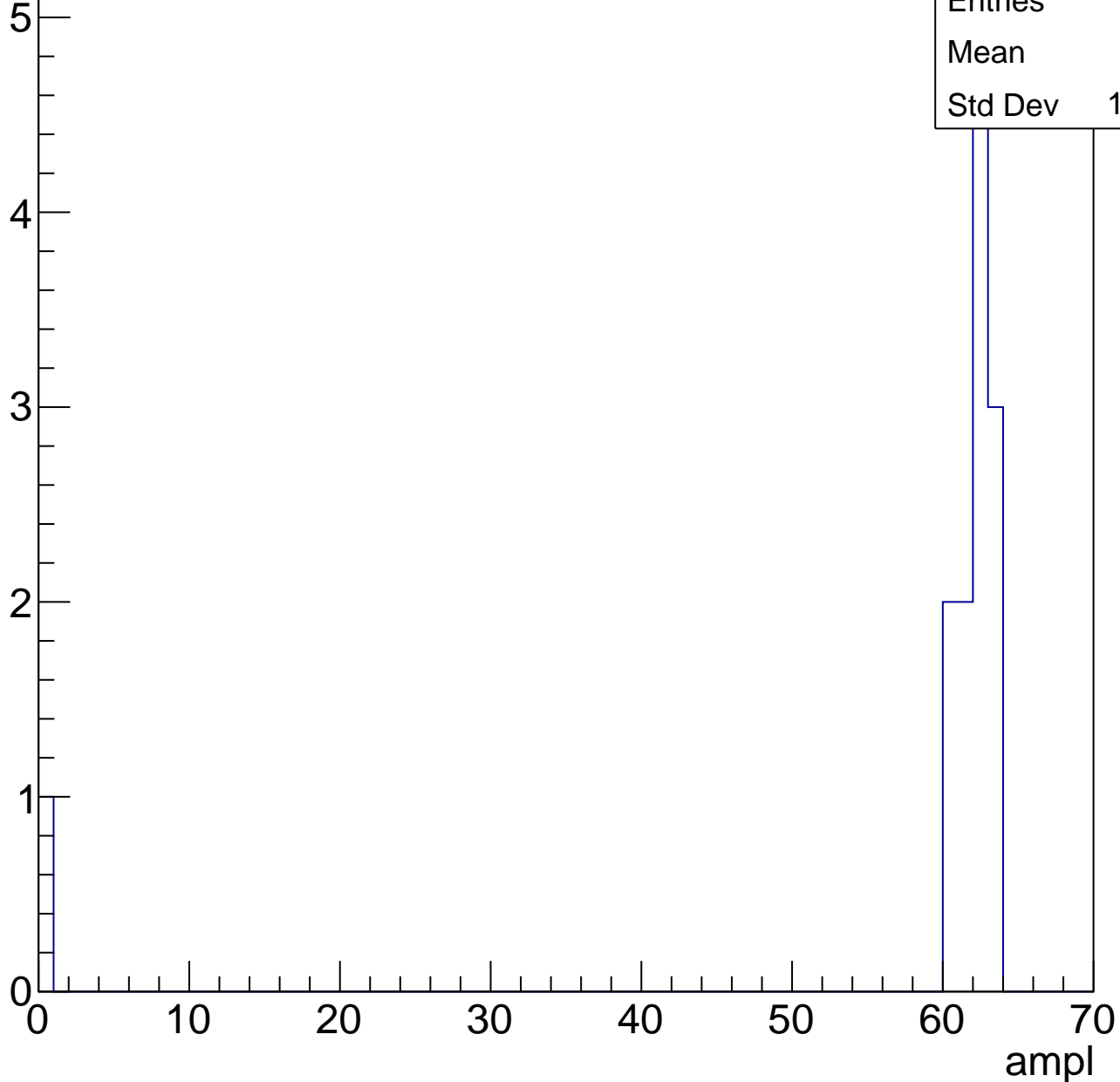
70

B1L103S, U14-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

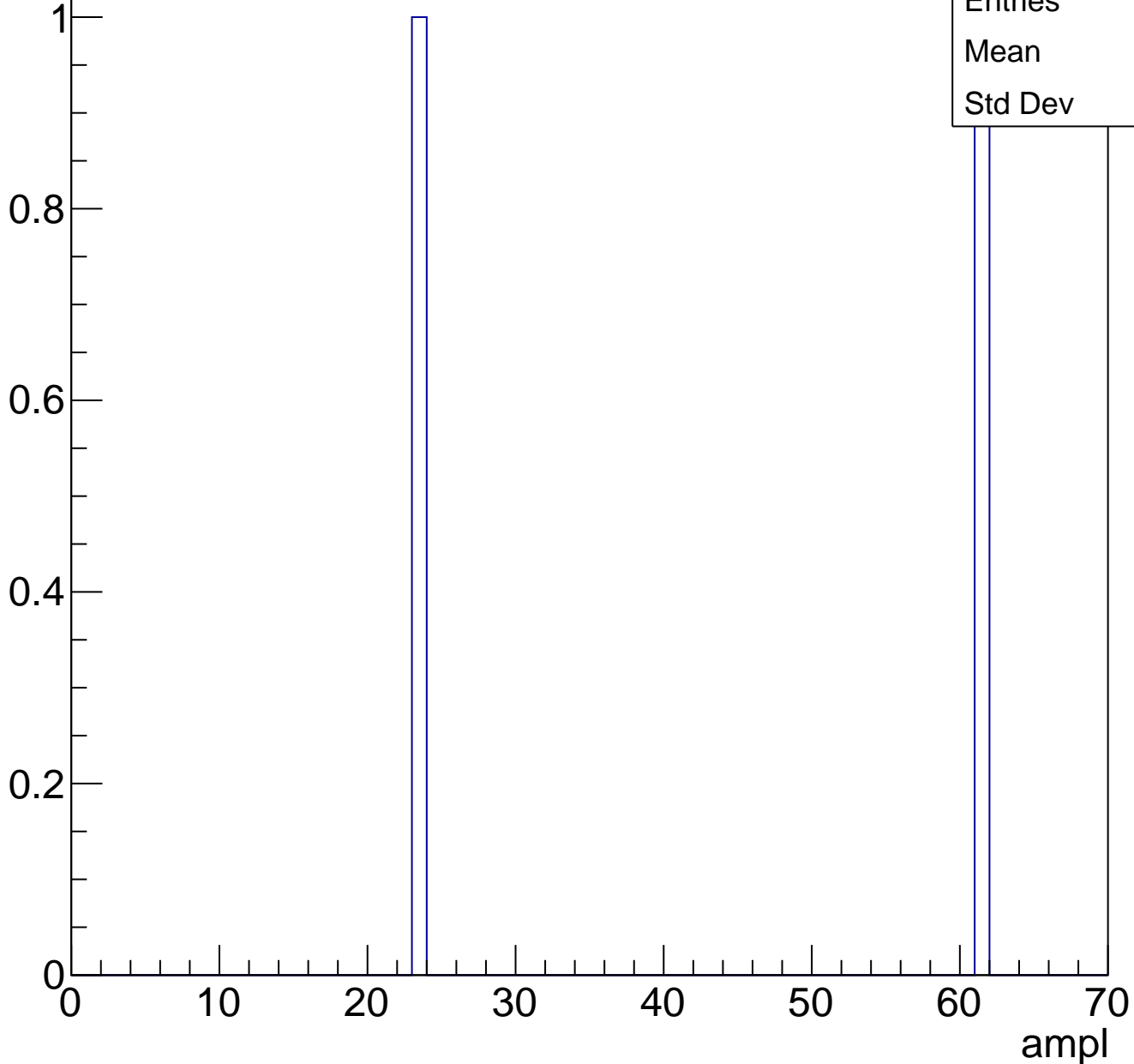
Entries	13
Mean	57
Std Dev	16.48



B1L103S, U14-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	42
Std Dev	19

B1L103S, U14-ch56, adc0

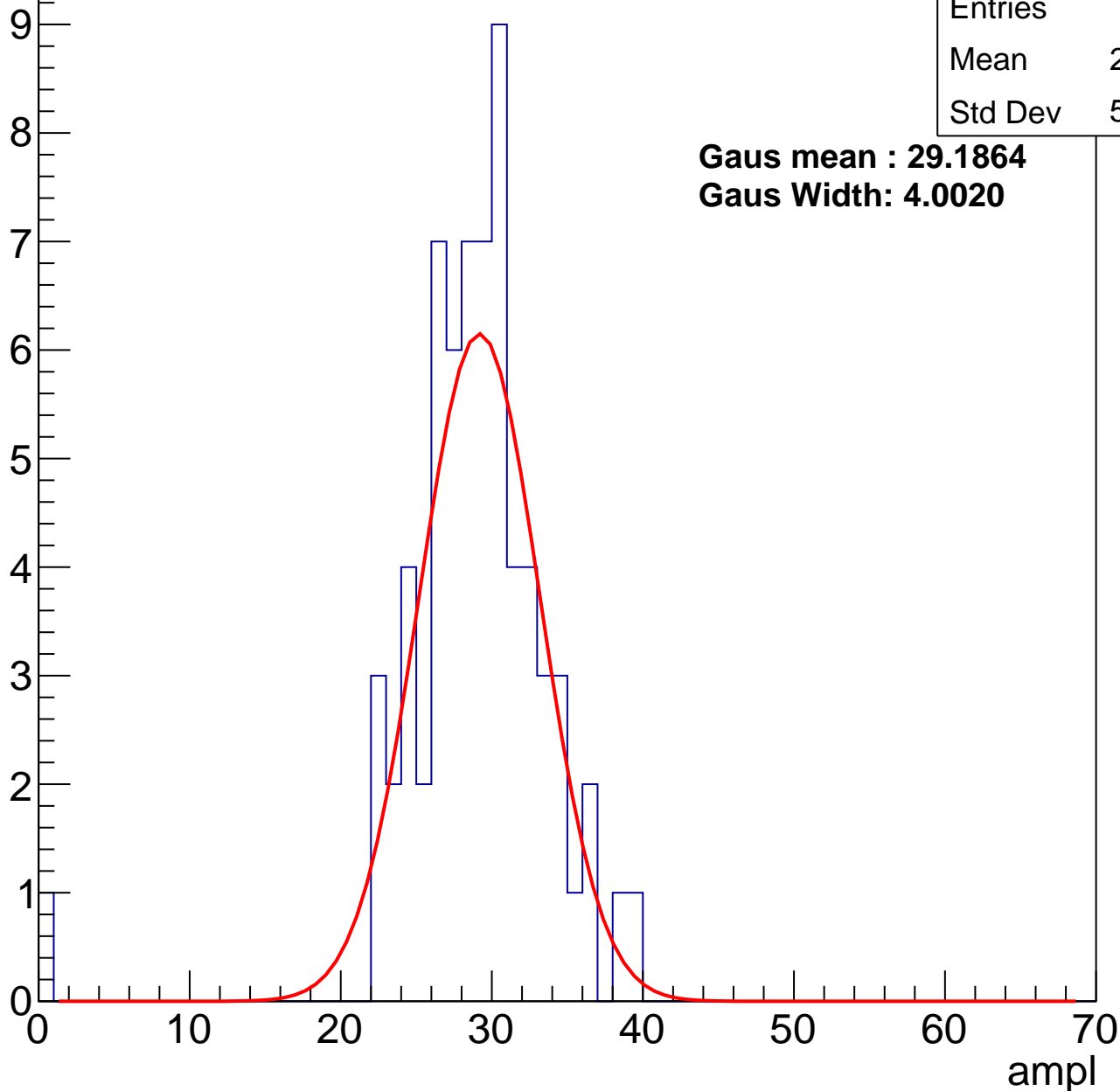
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.48
Std Dev	5.135

Gaus mean : 29.1864

Gaus Width: 4.0020



B1L103S, U14-ch56, adc1

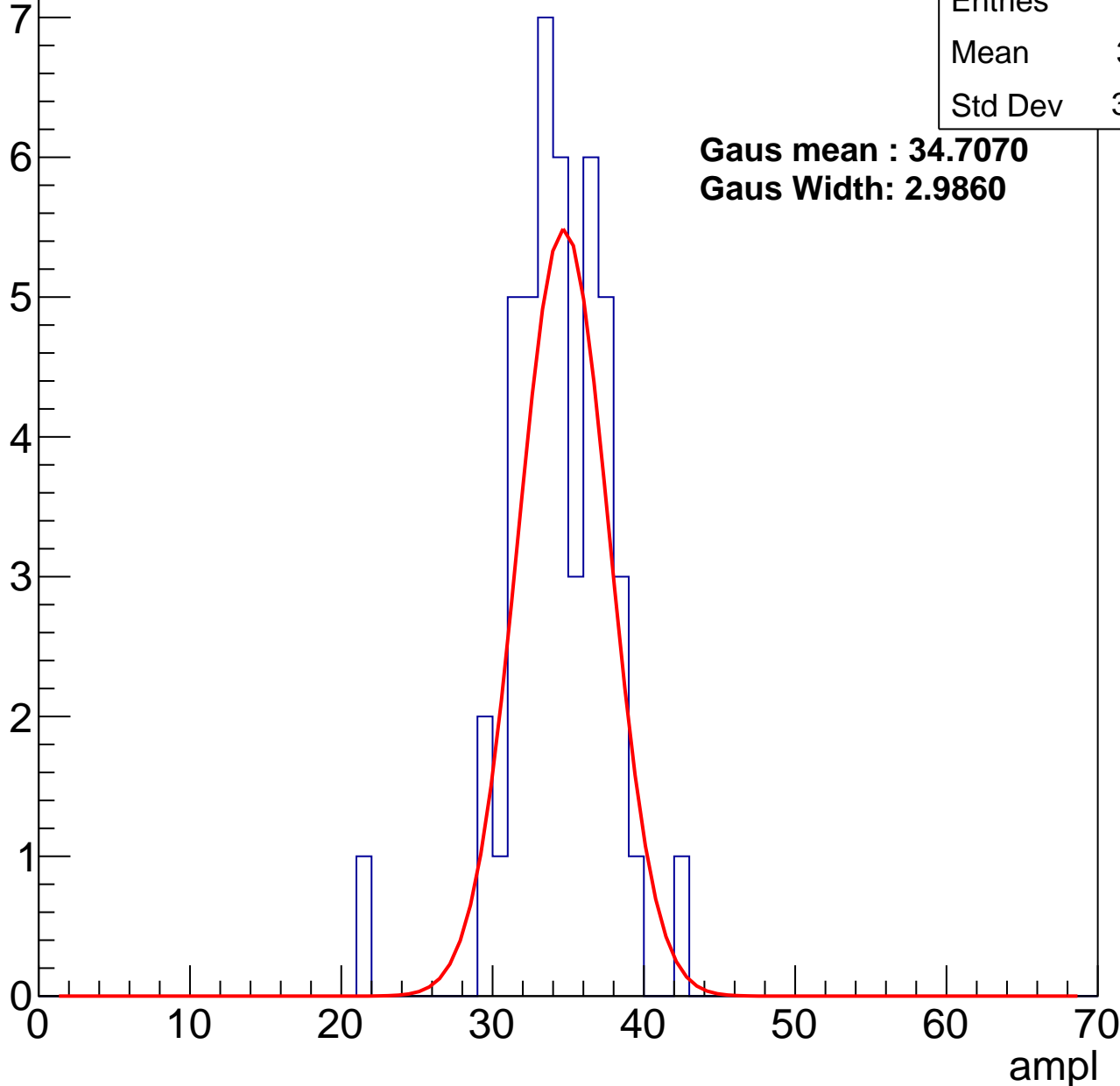
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	33.91
Std Dev	3.348

Gaus mean : 34.7070

Gaus Width: 2.9860



B1L103S, U14-ch56, adc2

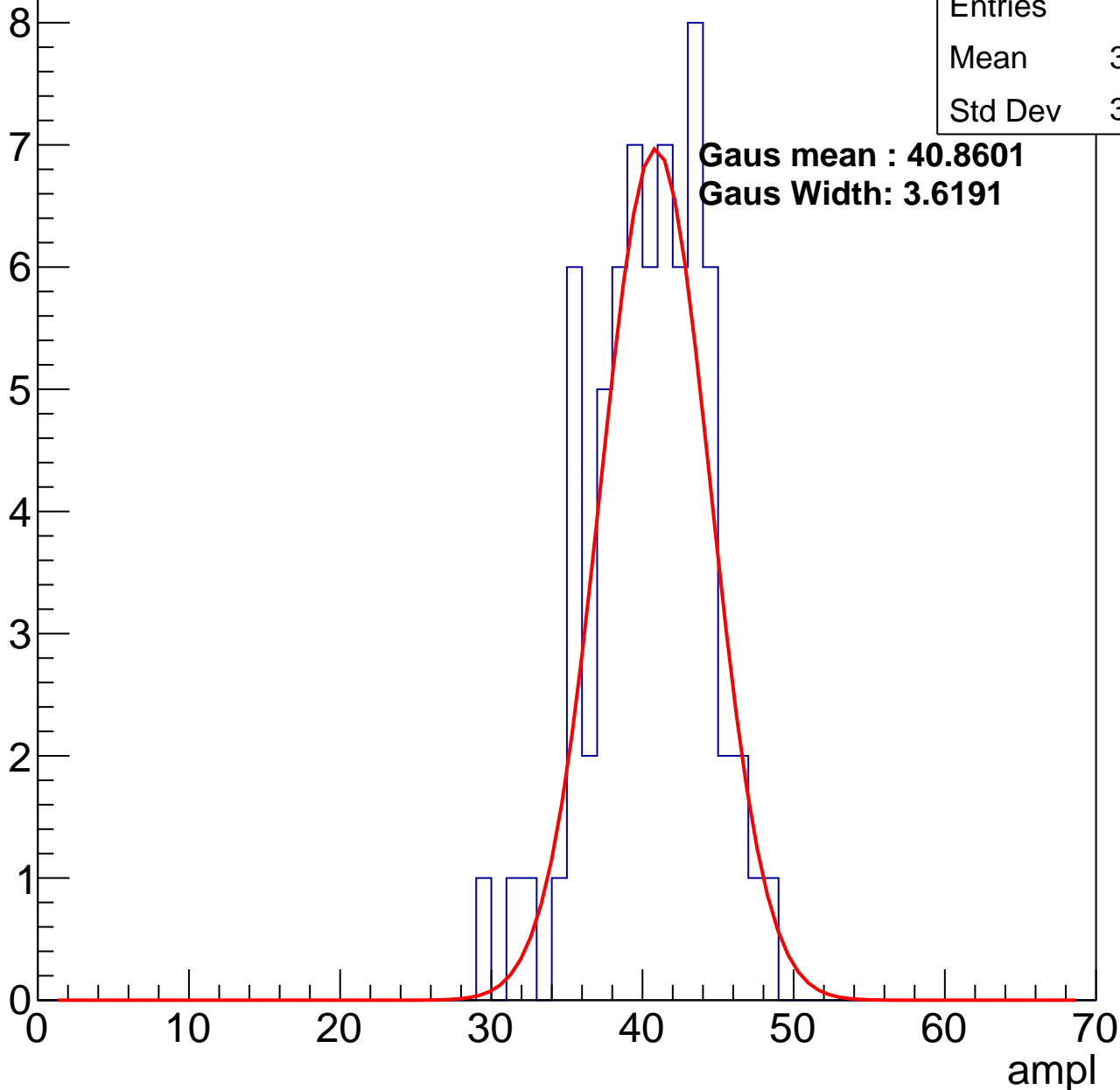
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	39.97
Std Dev	3.795

Gaus mean : 40.8601

Gaus Width: 3.6191

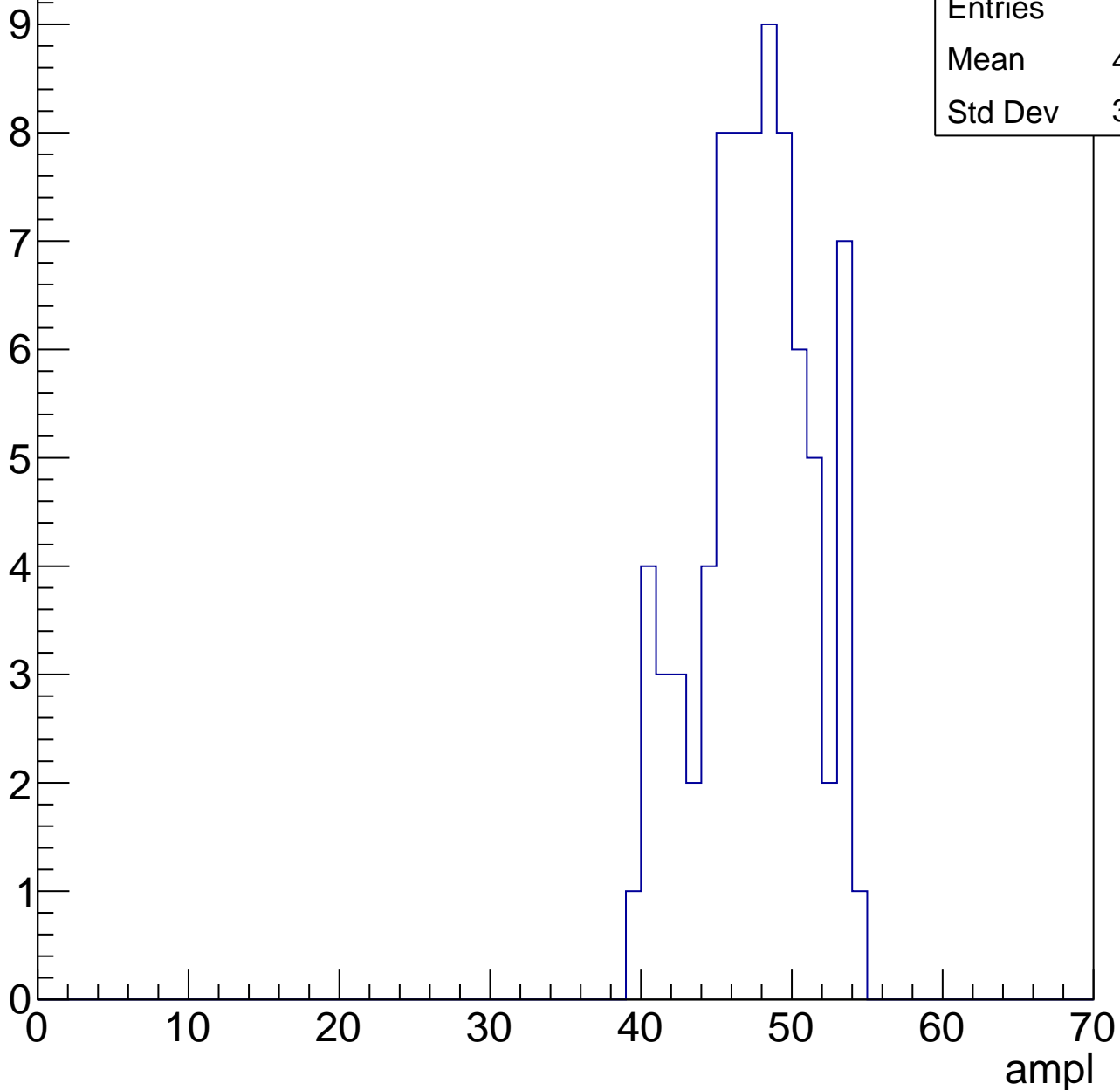


B1L103S, U14-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	47.11
Std Dev	3.691

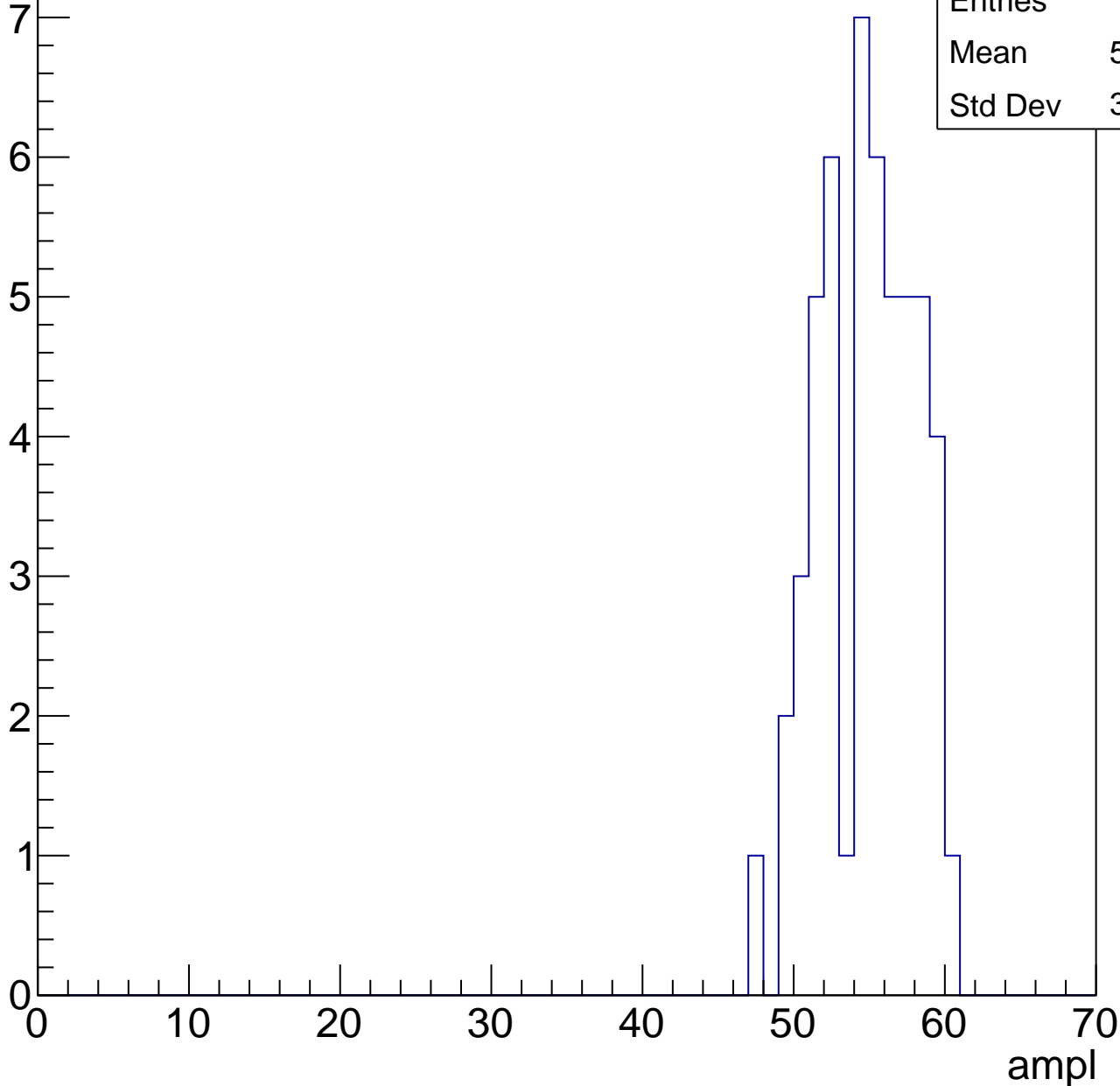


B1L103S, U14-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	54.39
Std Dev	3.119

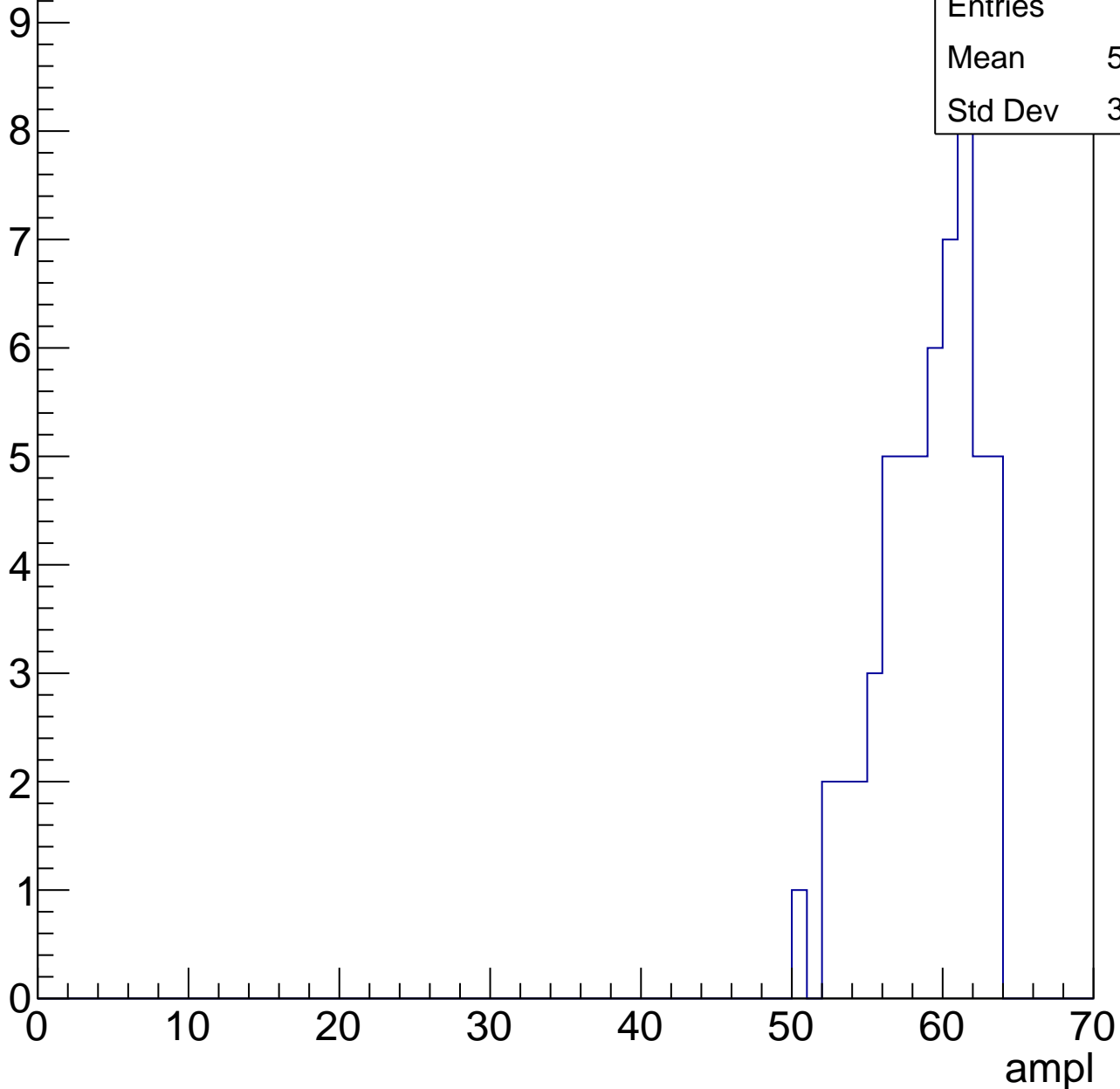


B1L103S, U14-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	58.53
Std Dev	3.174

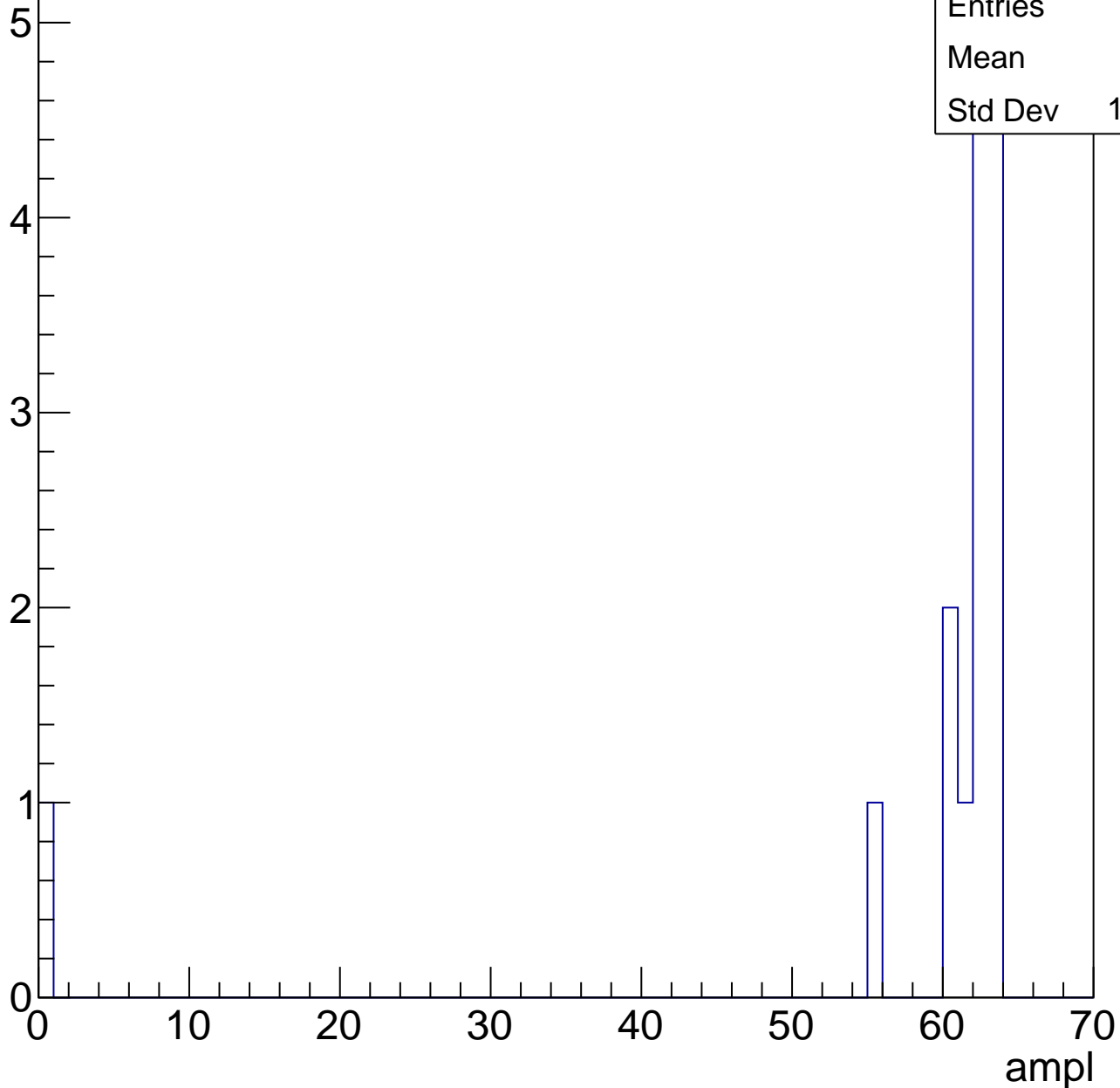


B1L103S, U14-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	57.4
Std Dev	15.47



B1L103S, U14-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U14-ch57, adc0

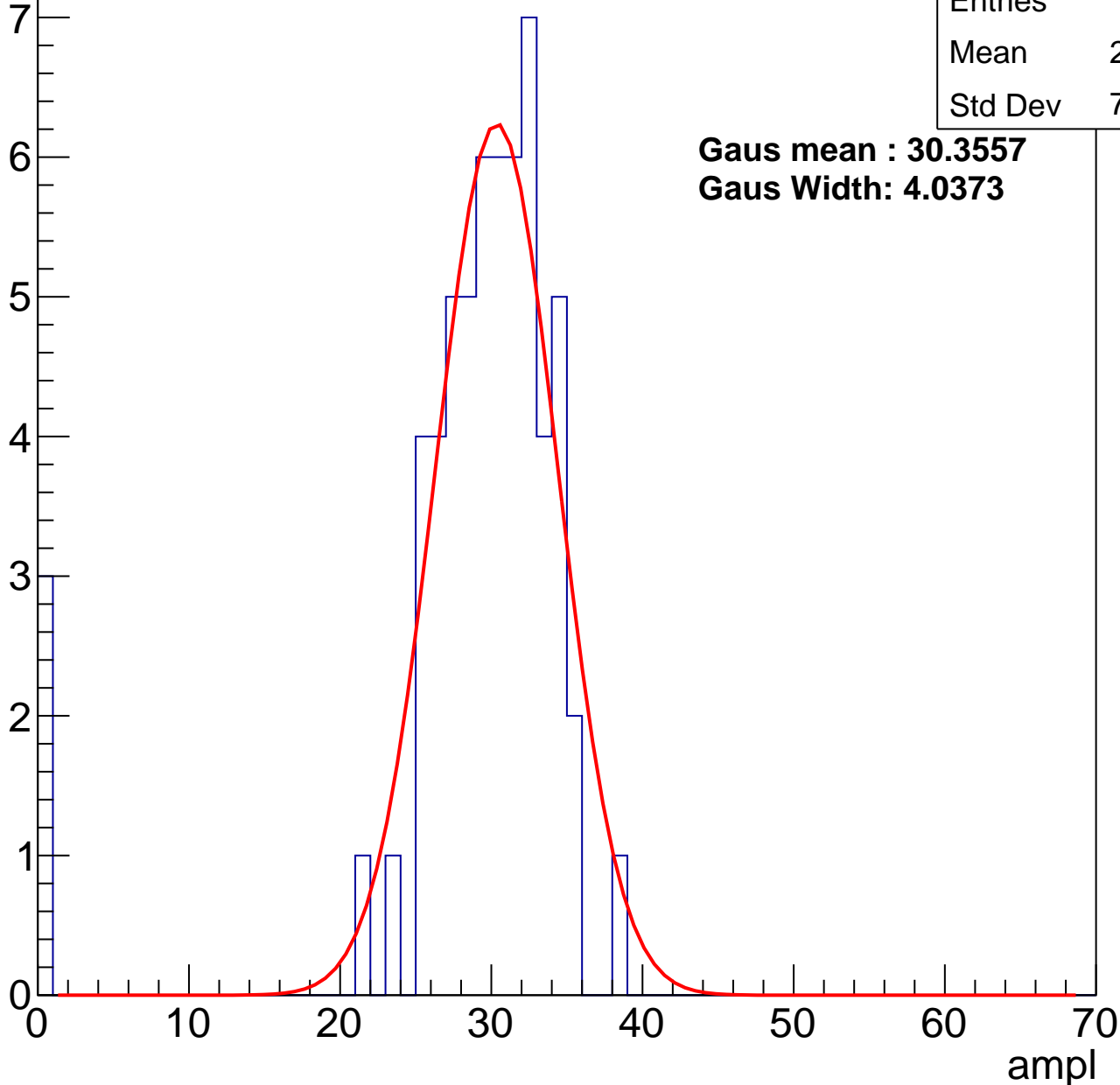
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	28.28
Std Dev	7.248

Gaus mean : 30.3557

Gaus Width: 4.0373



B1L103S, U14-ch57, adc1

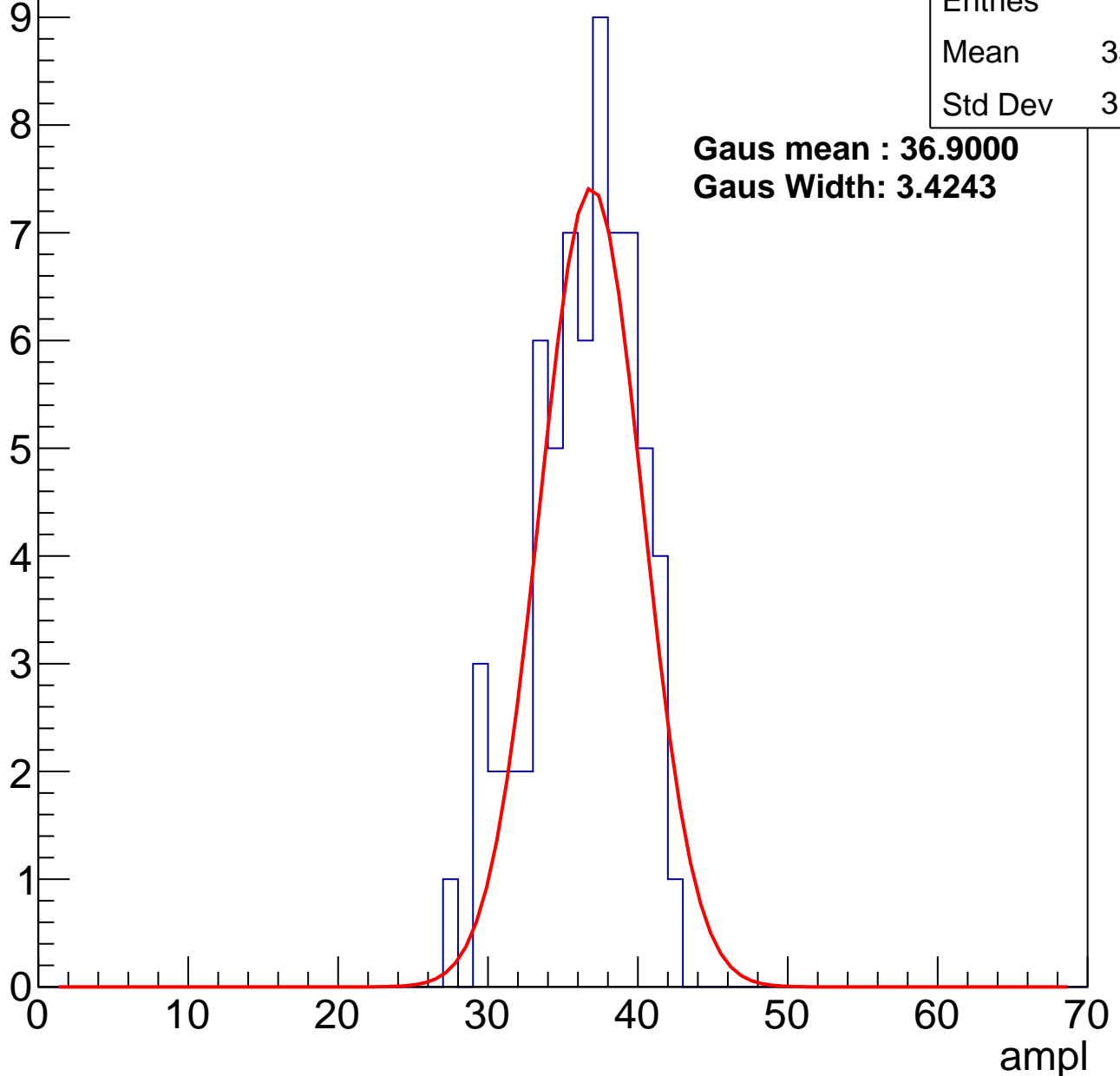
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.93
Std Dev	3.409

Gaus mean : 36.9000

Gaus Width: 3.4243



B1L103S, U14-ch57, adc2

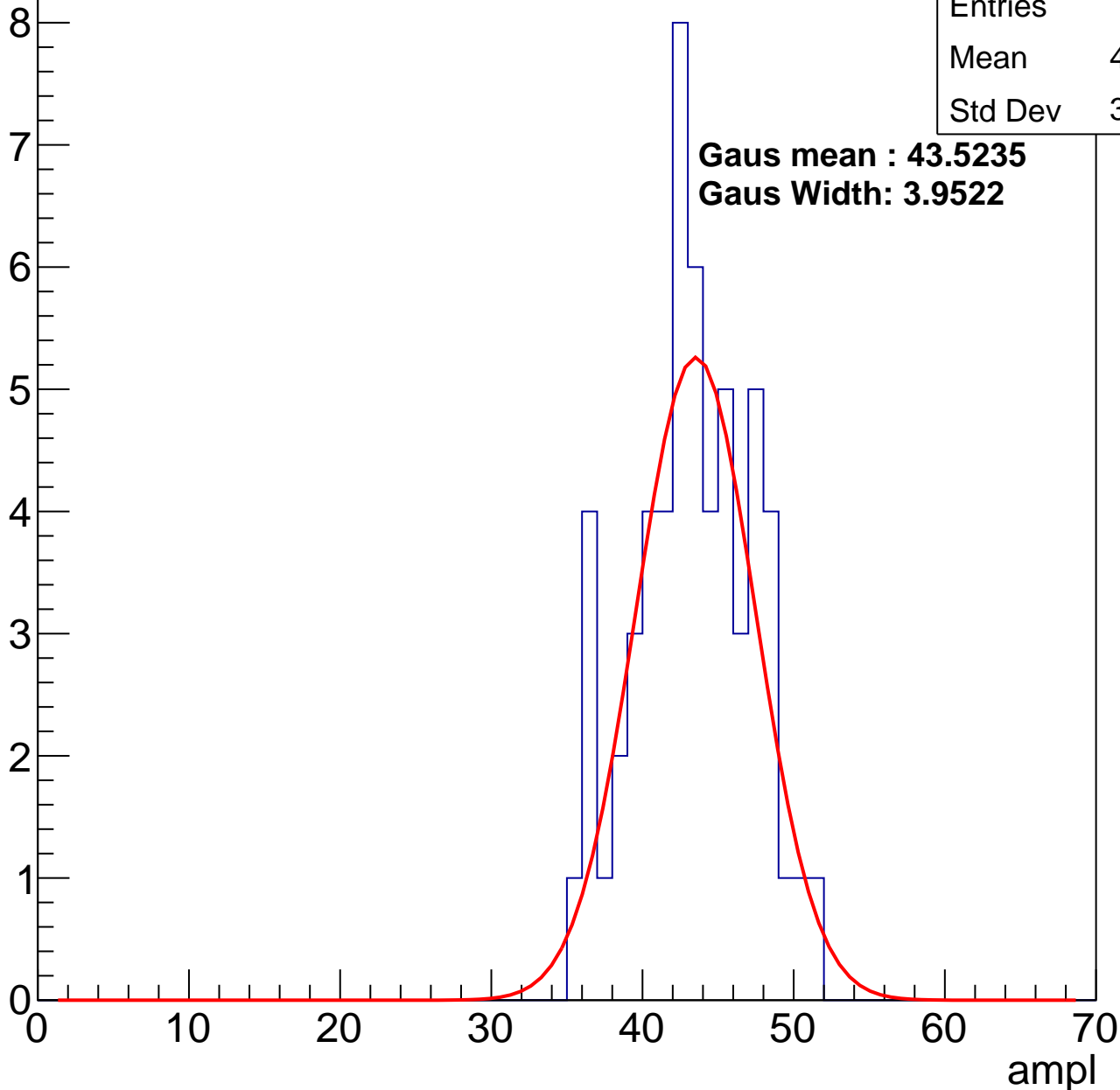
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	42.86
Std Dev	3.823

Gaus mean : 43.5235

Gaus Width: 3.9522

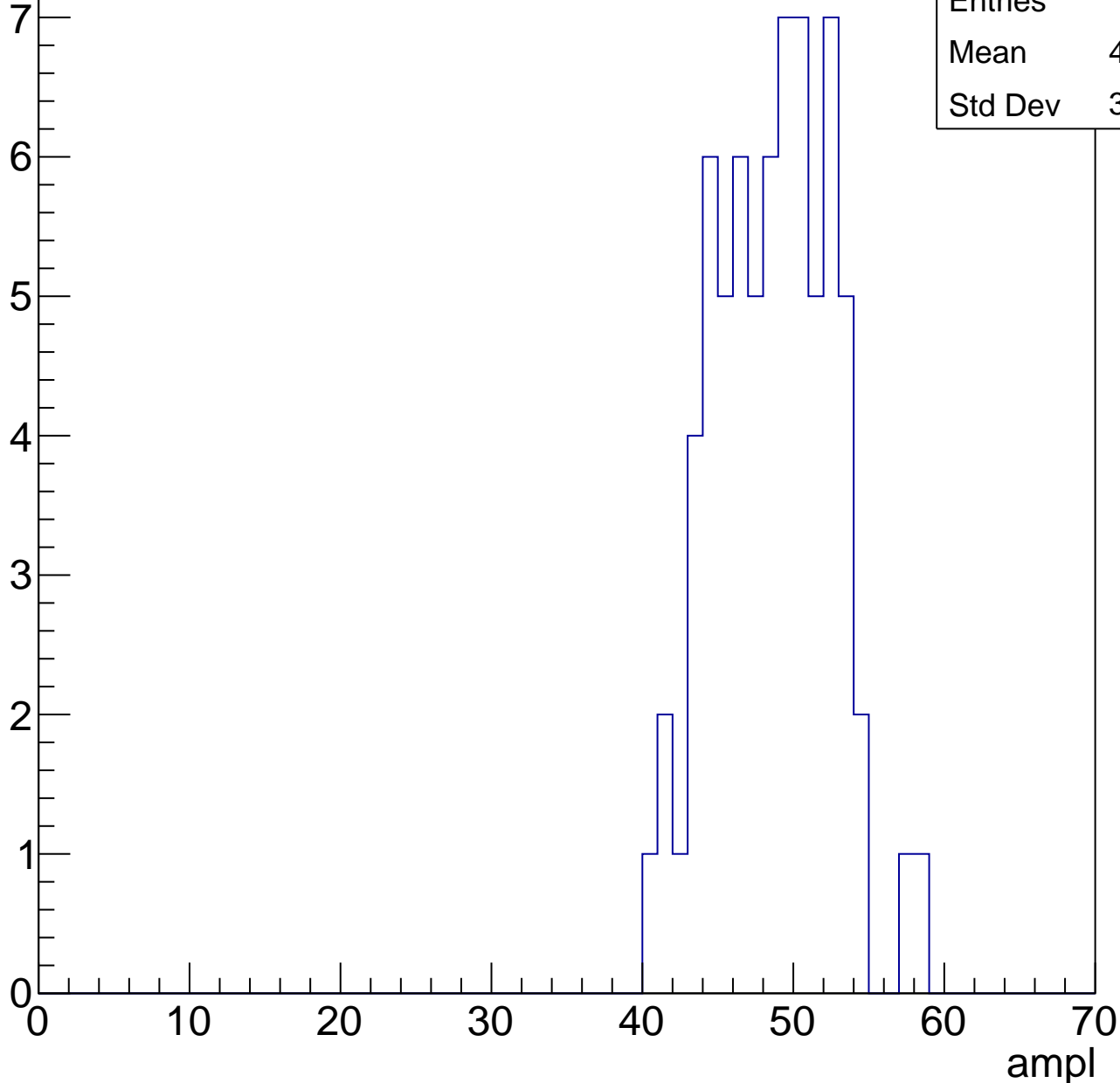


B1L103S, U14-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.23
Std Dev	3.817

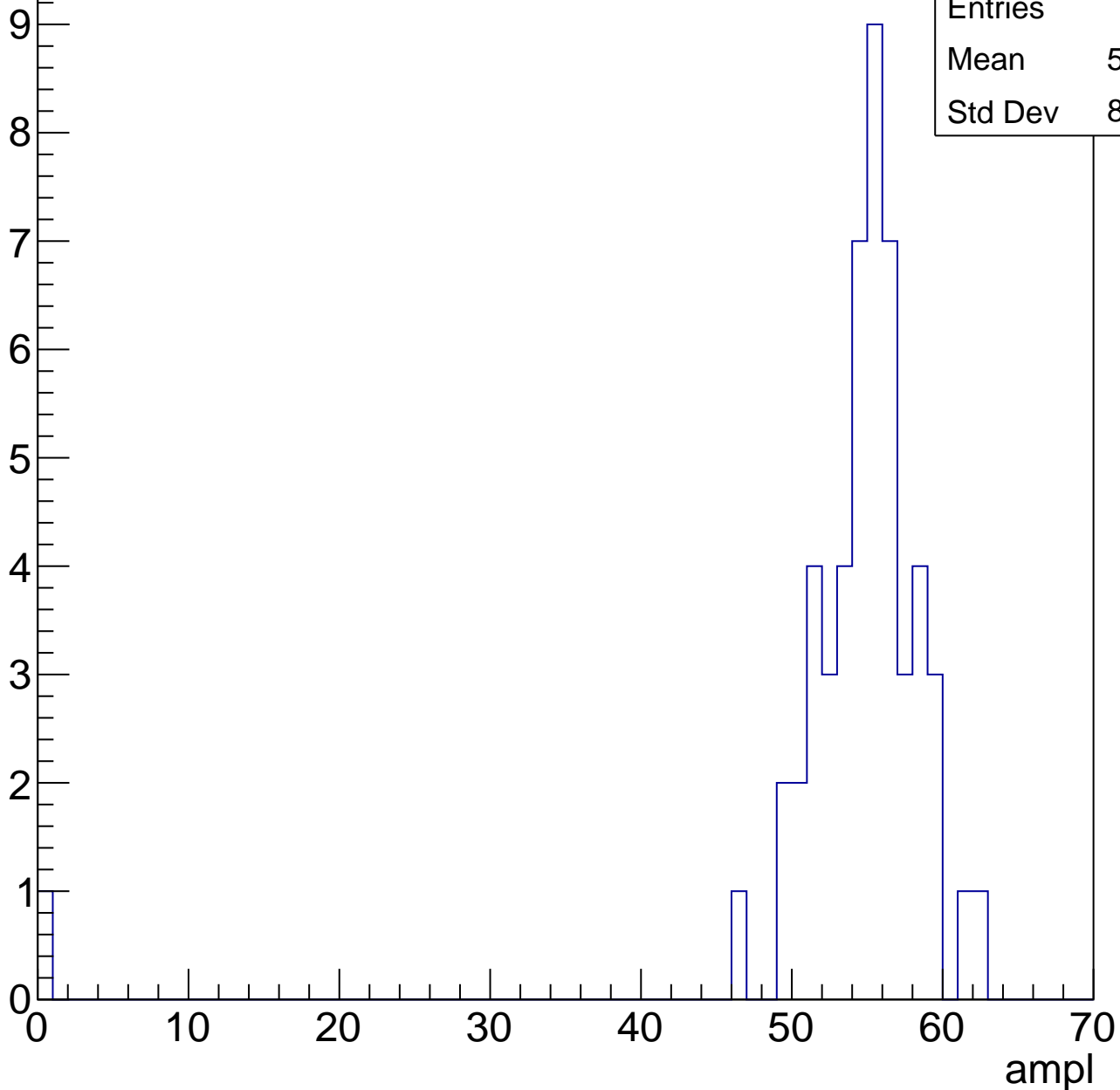


B1L103S, U14-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	53.54
Std Dev	8.109

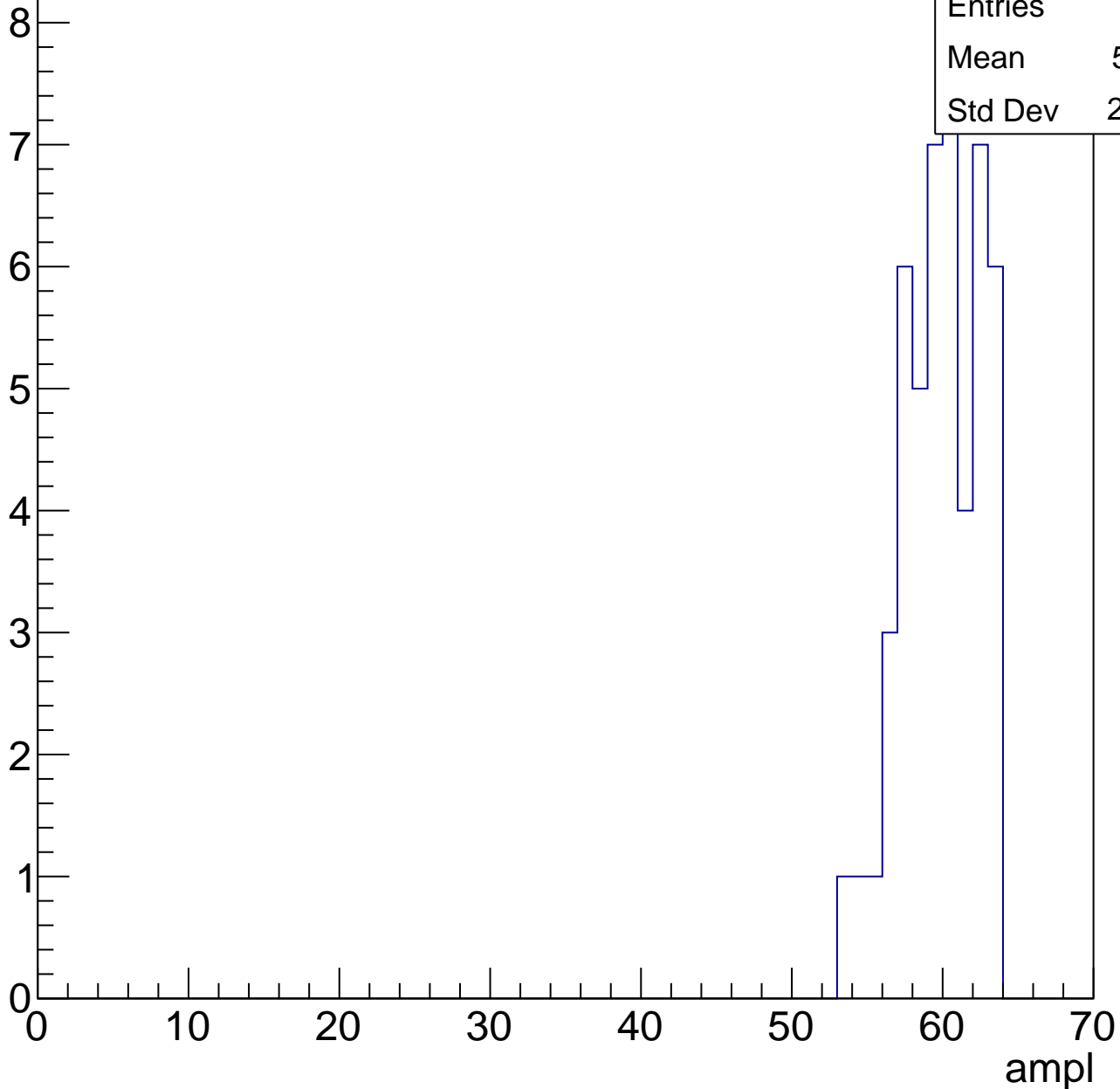


B1L103S, U14-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	59.41
Std Dev	2.506

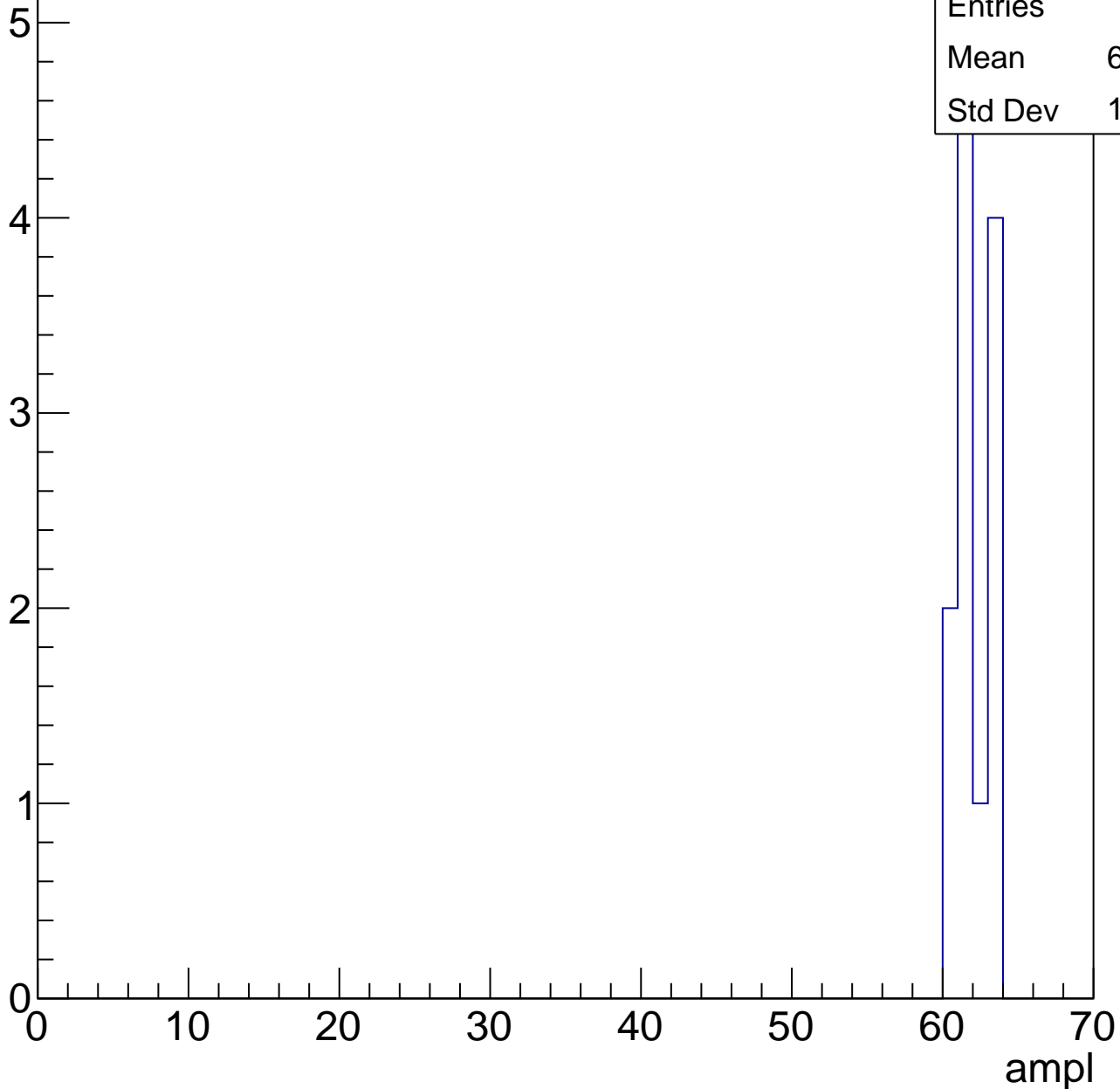


B1L103S, U14-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.115



B1L103S, U14-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch58, adc0

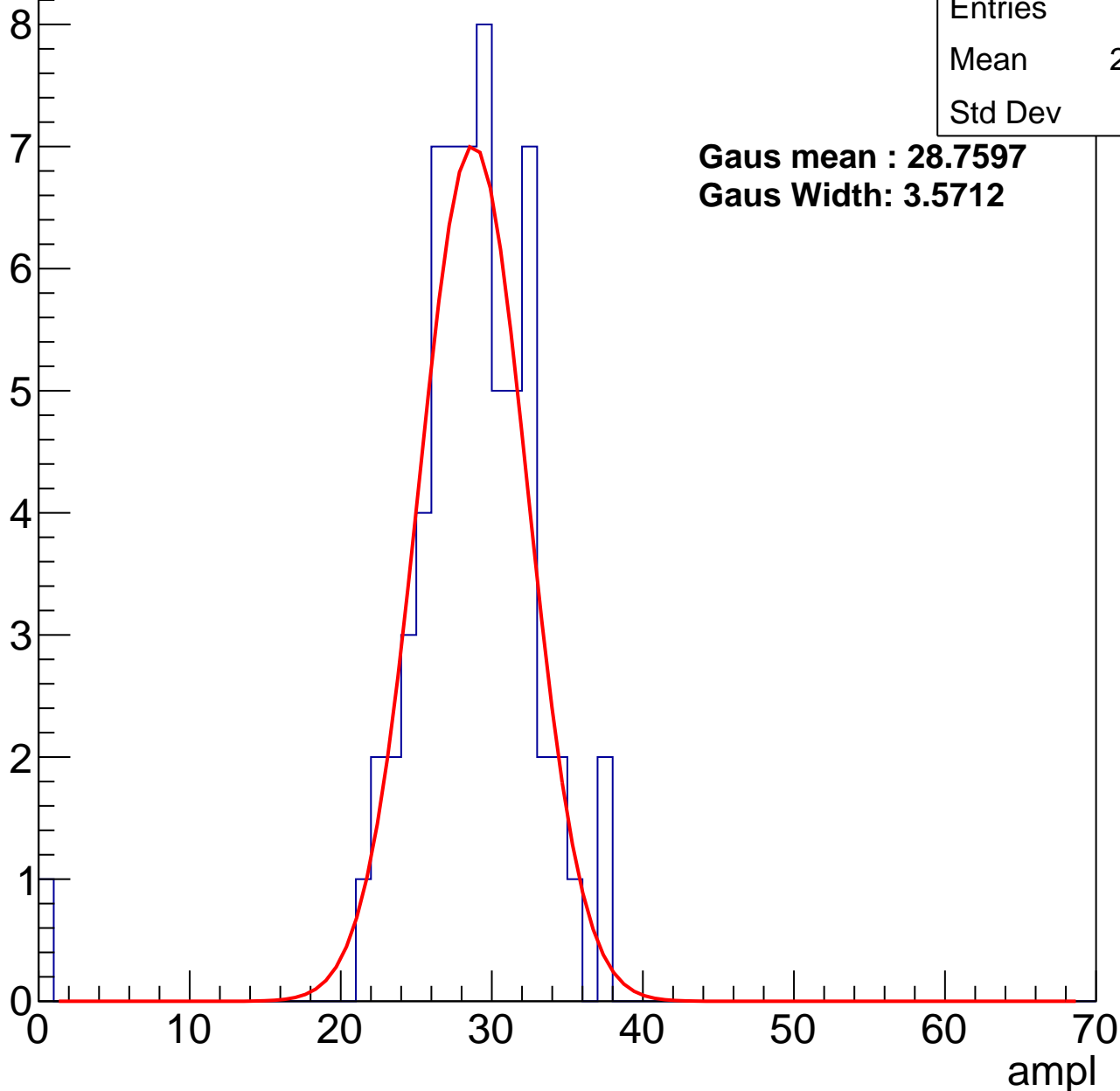
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.09
Std Dev	4.91

Gaus mean : 28.7597

Gaus Width: 3.5712



B1L103S, U14-ch58, adc1

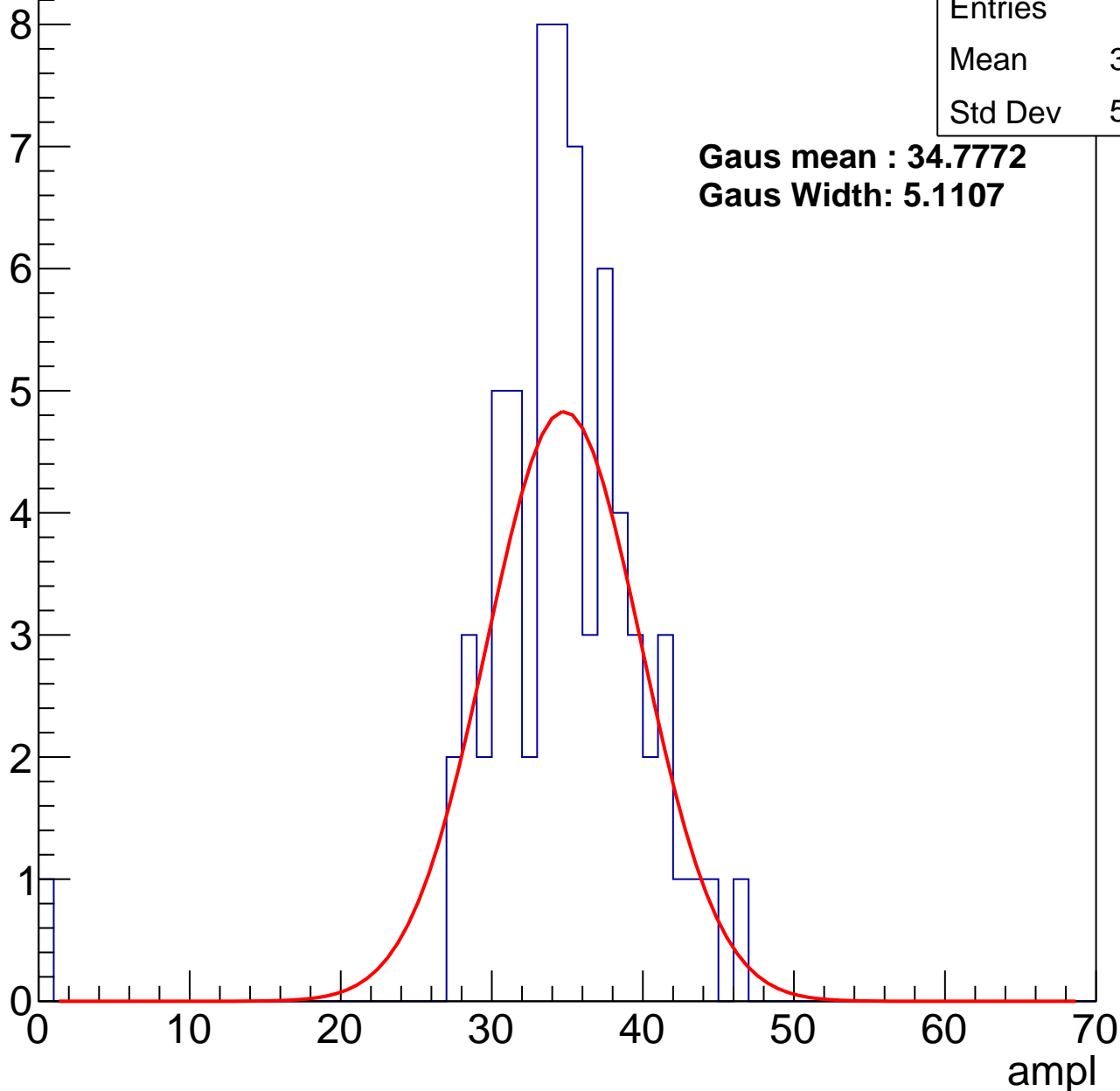
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	34.16
Std Dev	5.905

Gaus mean : 34.7772

Gaus Width: 5.1107



B1L103S, U14-ch58, adc2

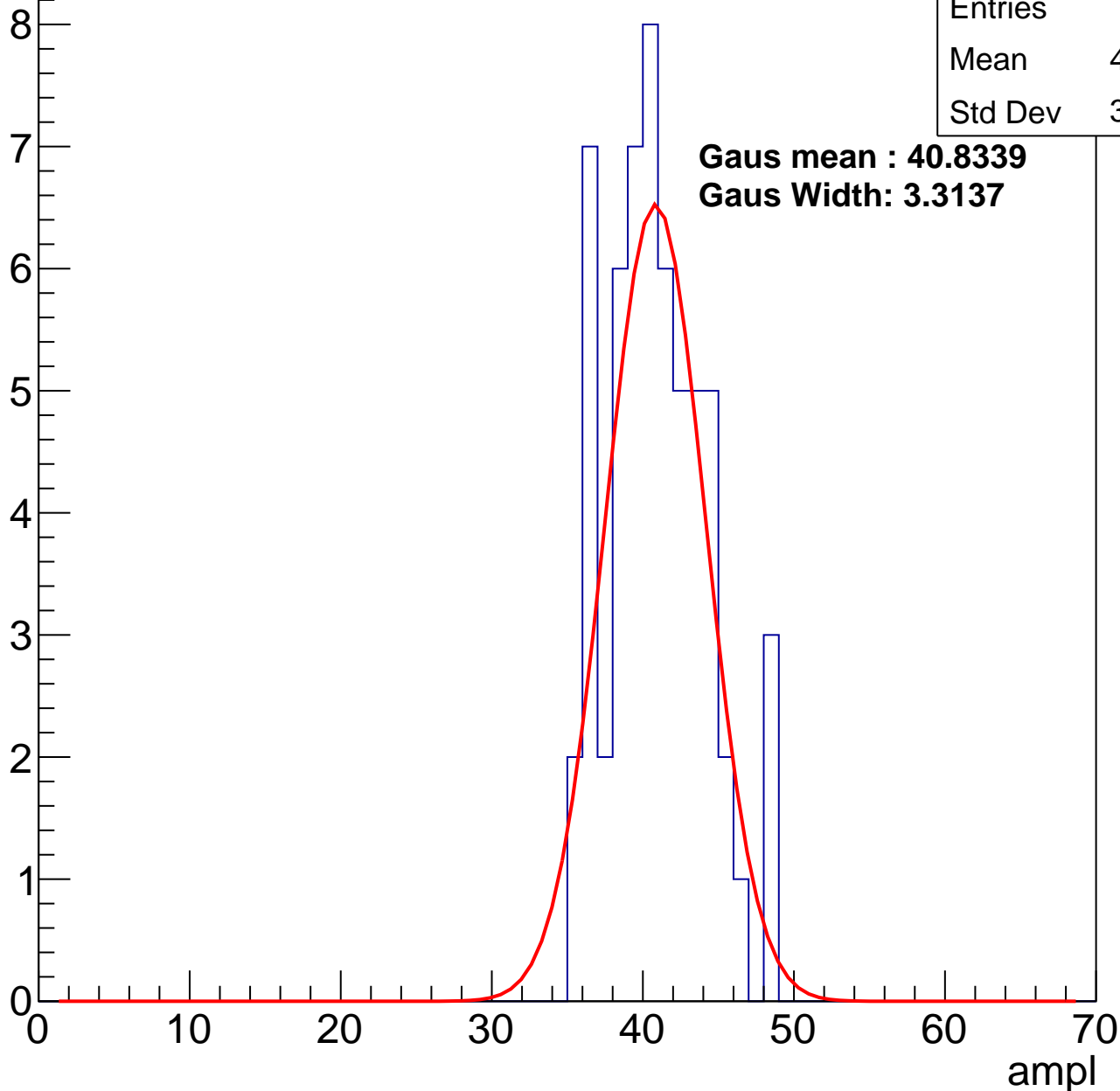
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	40.47
Std Dev	3.259

Gaus mean : 40.8339

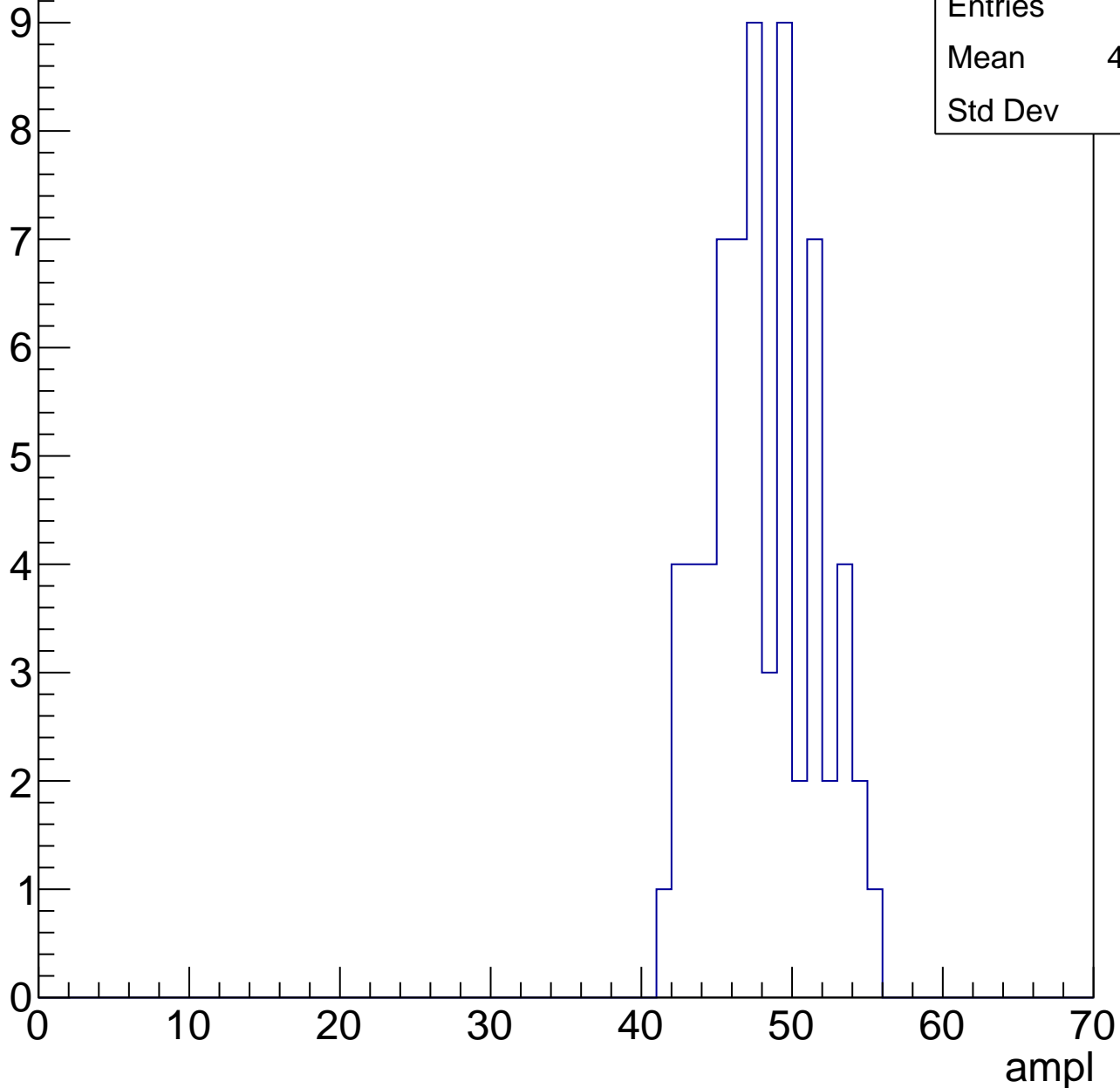
Gaus Width: 3.3137



B1L103S, U14-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

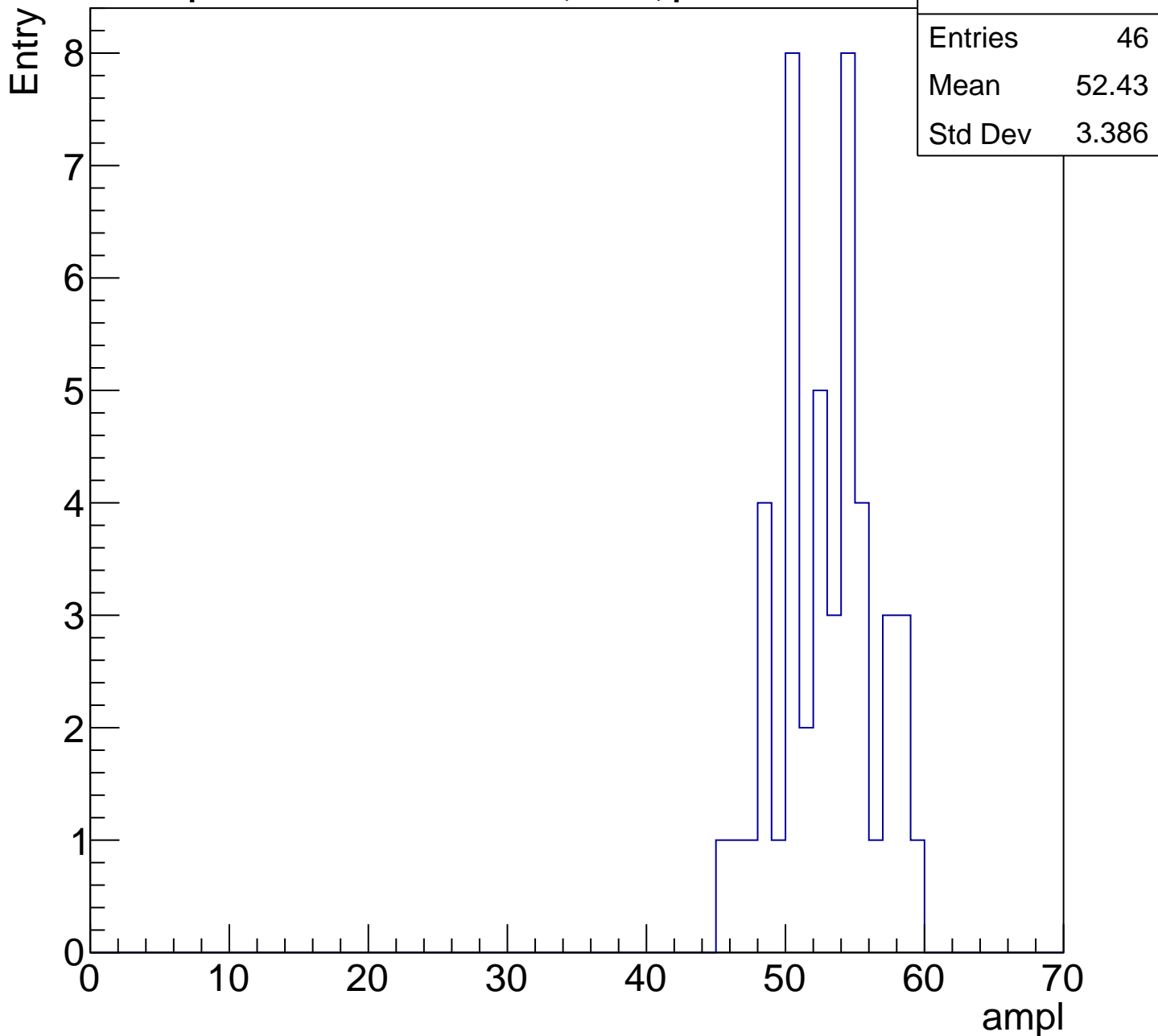
Entry



Entries	66
Mean	47.55
Std Dev	3.43

B1L103S, U14-ch58, adc4

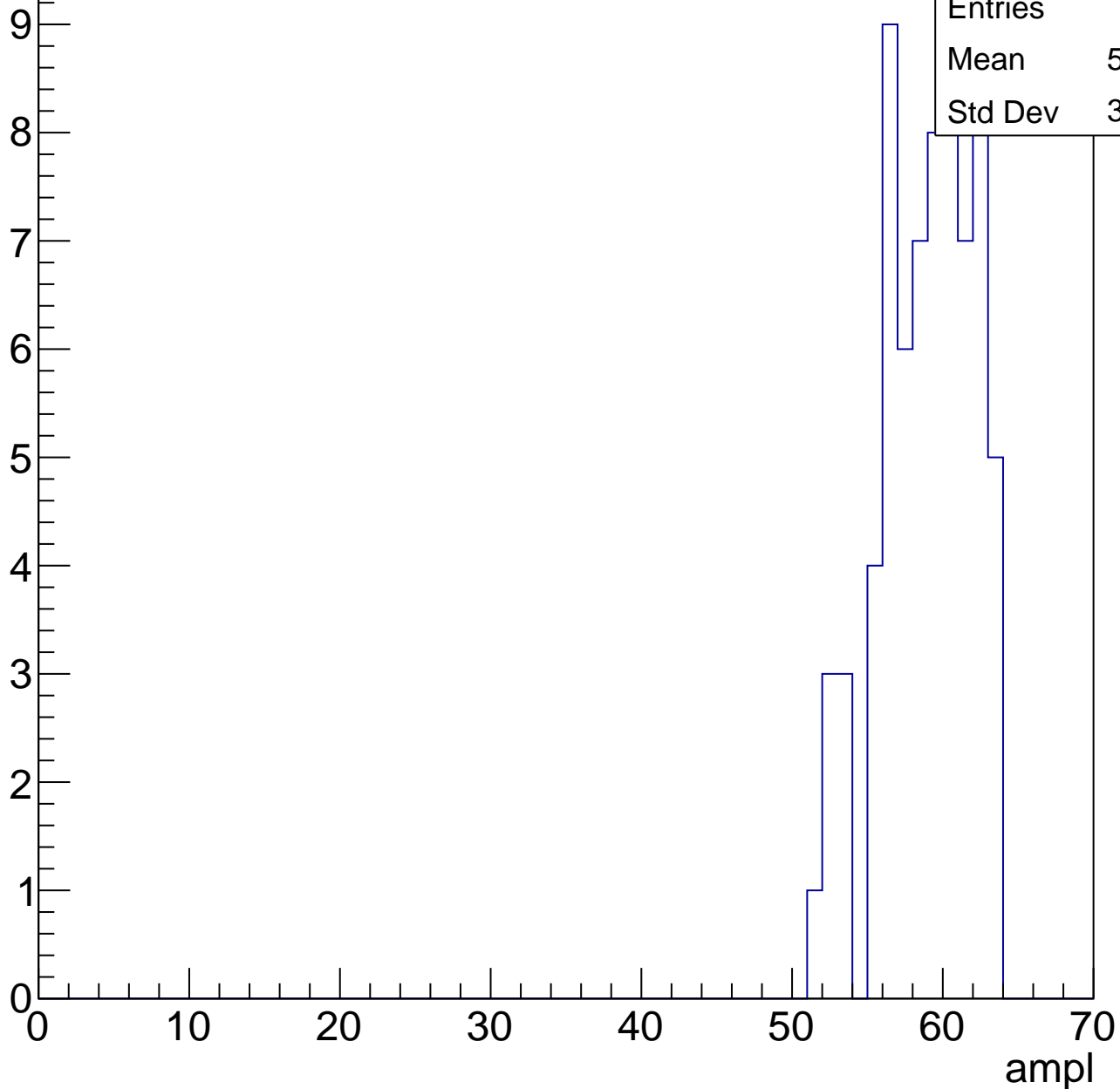
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U14-ch58, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



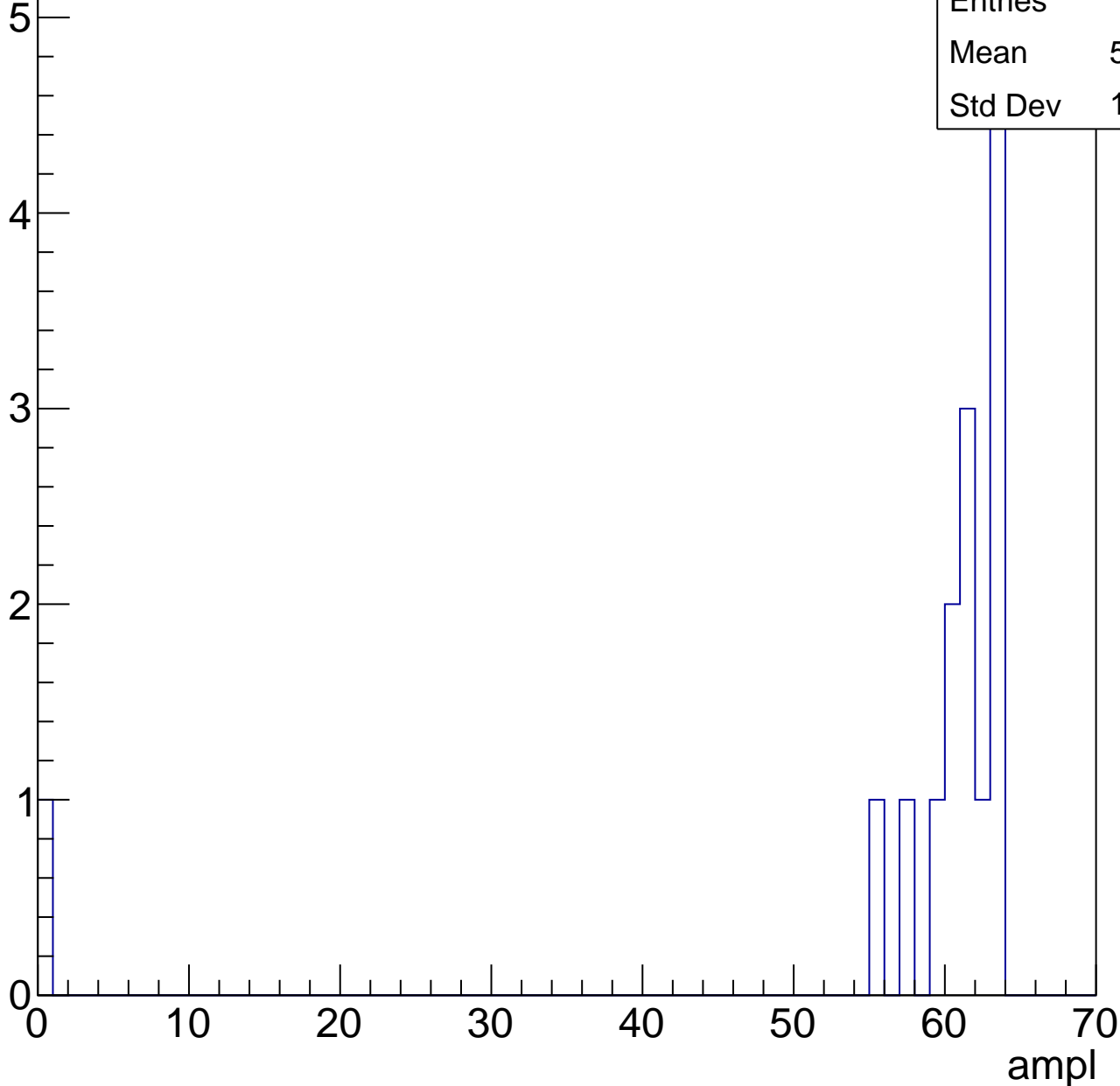
Entries	70
Mean	58.43
Std Dev	3.092

B1L103S, U14-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	56.73
Std Dev	15.33



B1L103S, U14-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L103S, U14-ch59, adc0

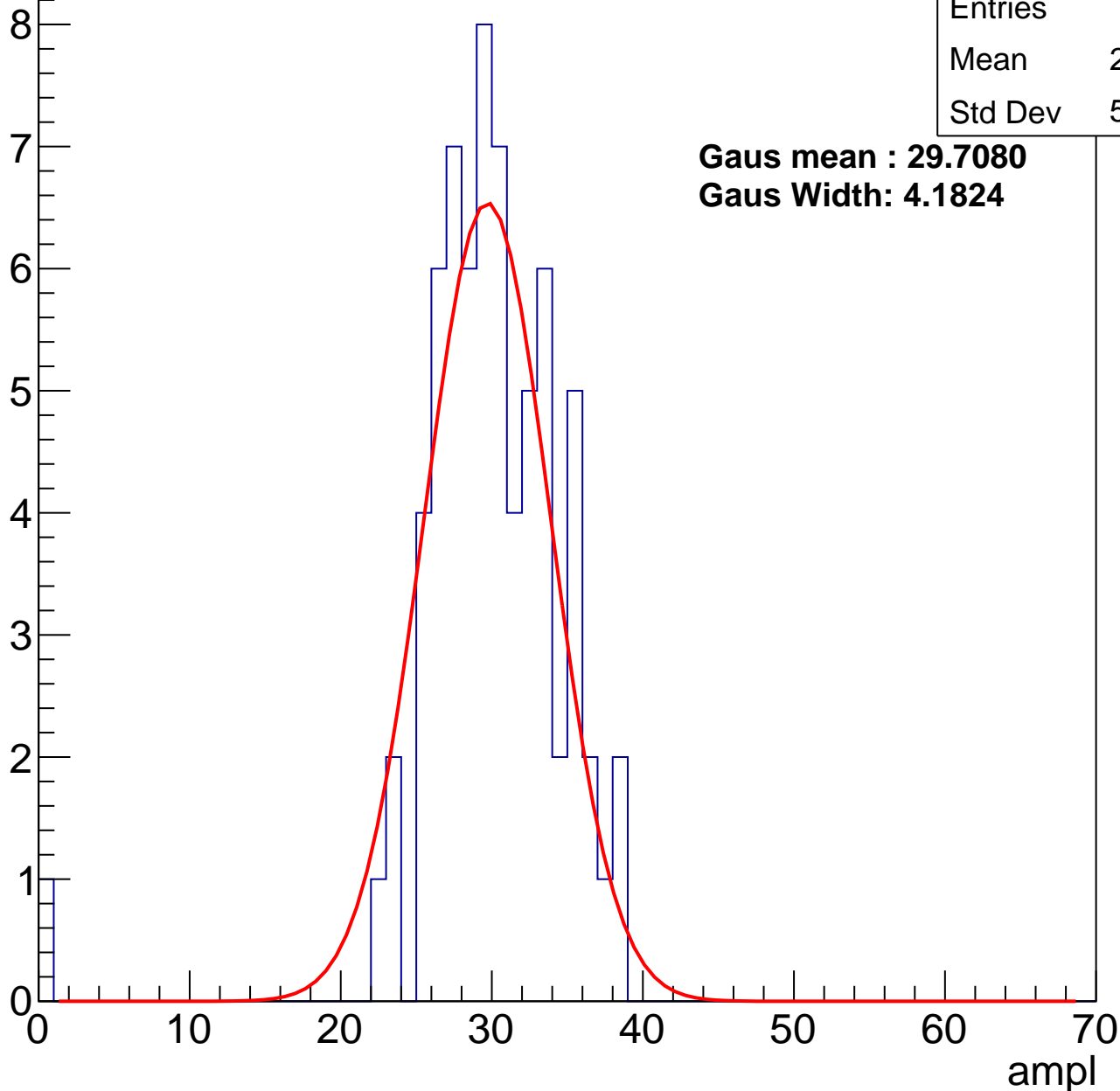
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	29.46
Std Dev	5.132

Gaus mean : 29.7080

Gaus Width: 4.1824



B1L103S, U14-ch59, adc1

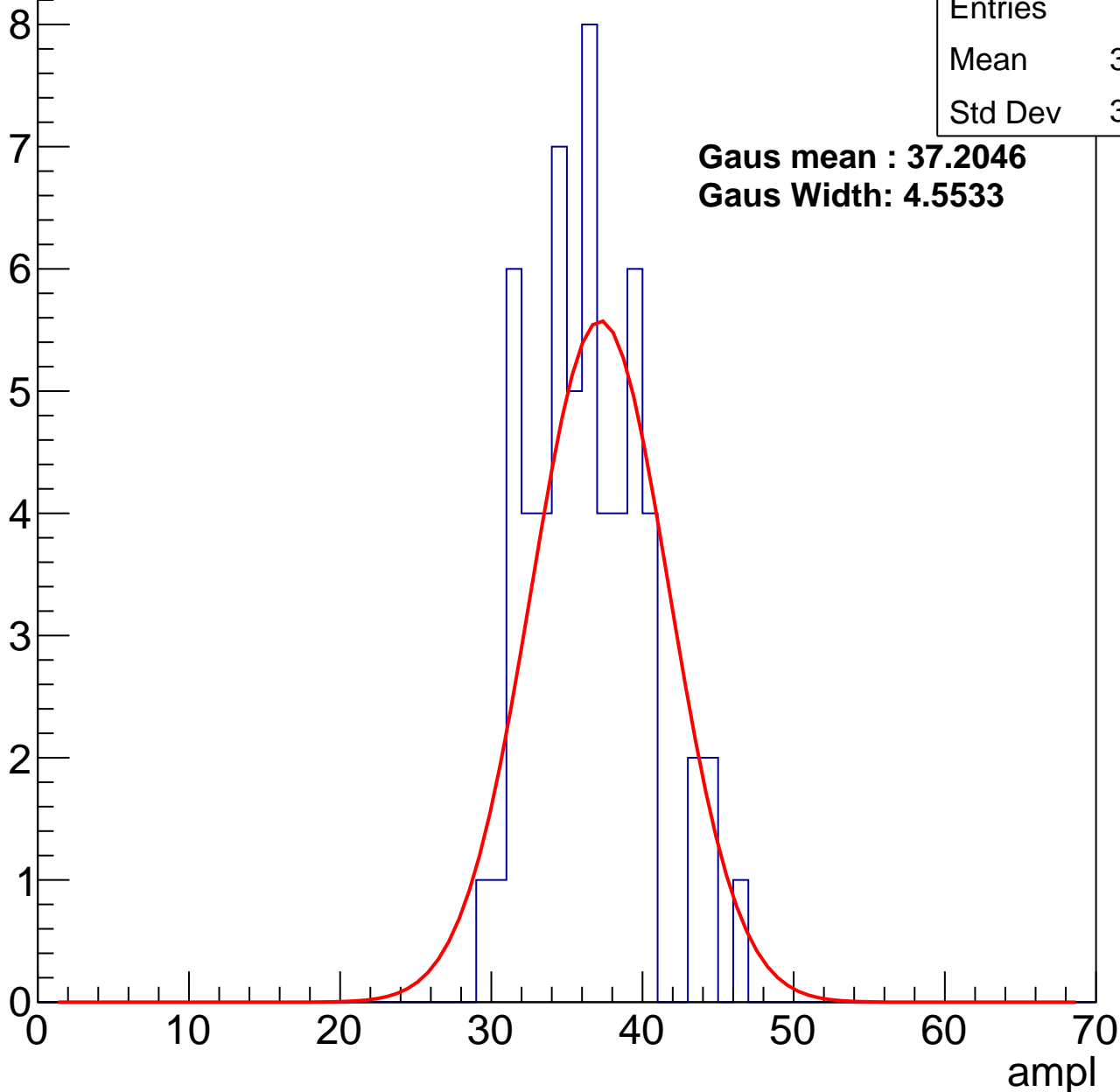
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	35.93
Std Dev	3.755

Gaus mean : 37.2046

Gaus Width: 4.5533



B1L103S, U14-ch59, adc2

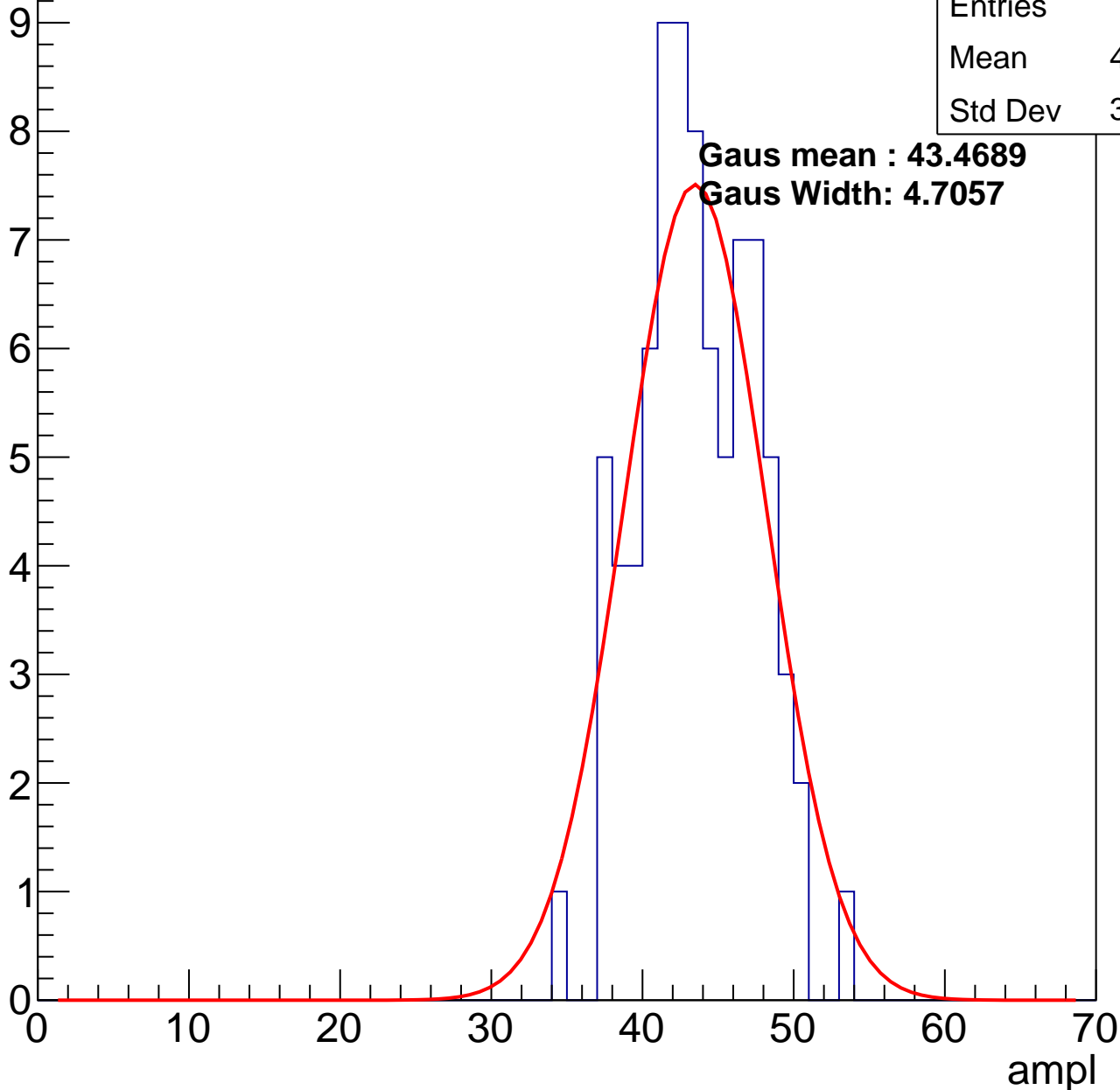
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	43.15
Std Dev	3.752

Gaus mean : 43.4689

Gaus Width: 4.7057

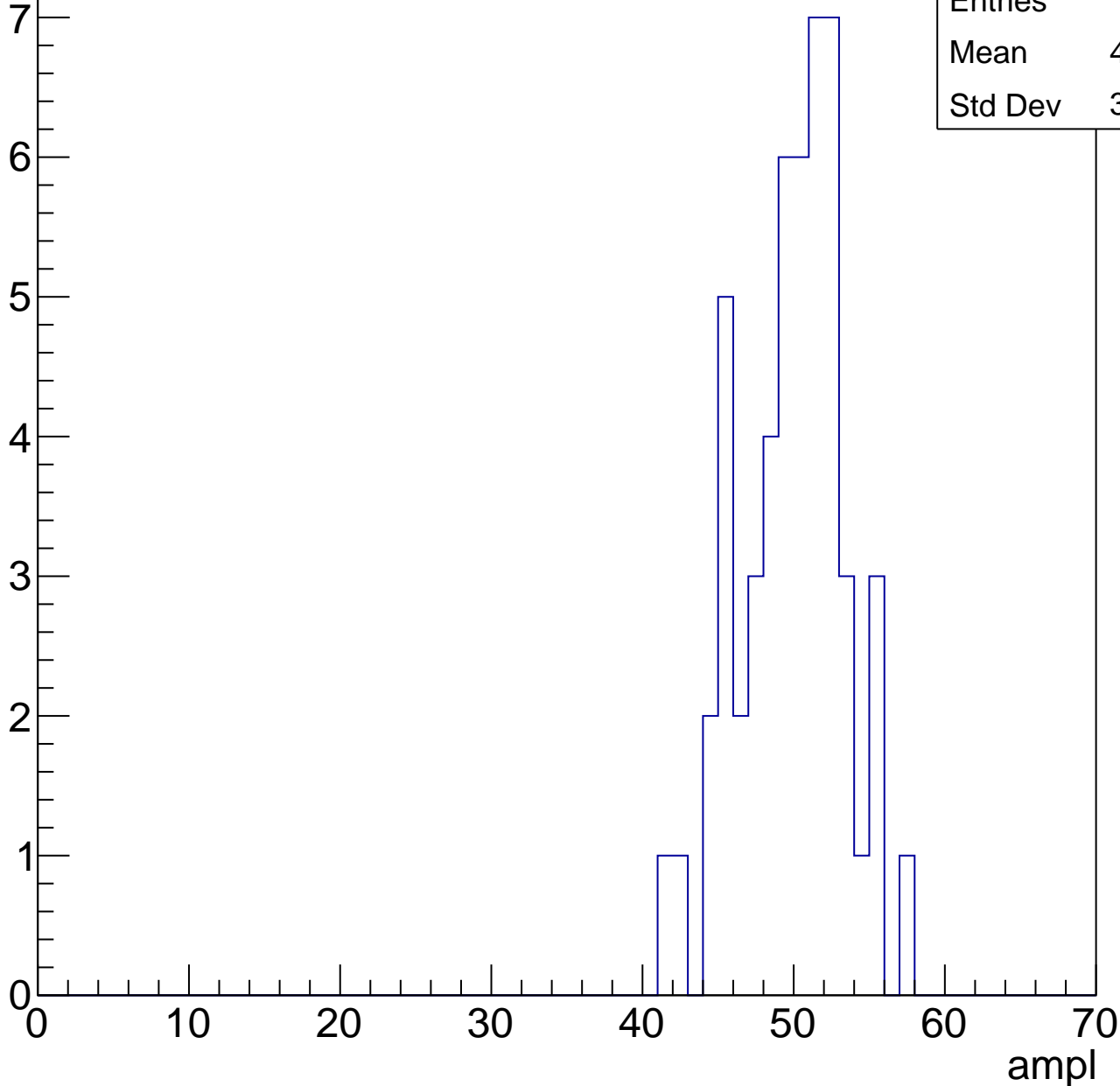


B1L103S, U14-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	49.44
Std Dev	3.422



B1L103S, U14-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	55.56
Std Dev	3.403

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L103S, U14-ch59, adc5

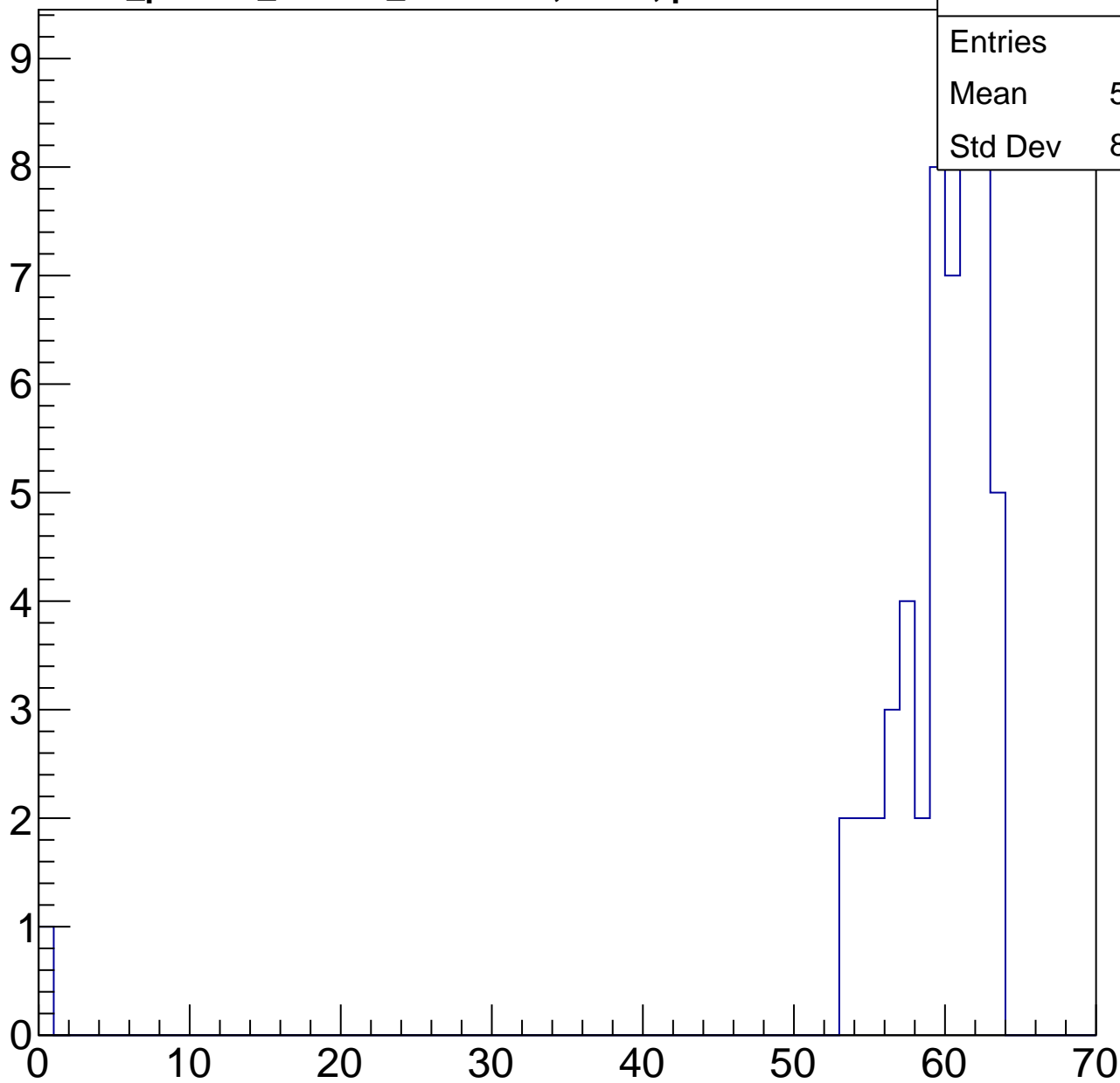
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.33
Std Dev	8.455

ampl



B1L103S, U14-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	62
Std Dev	1.225

B1L103S, U14-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch60, adc0

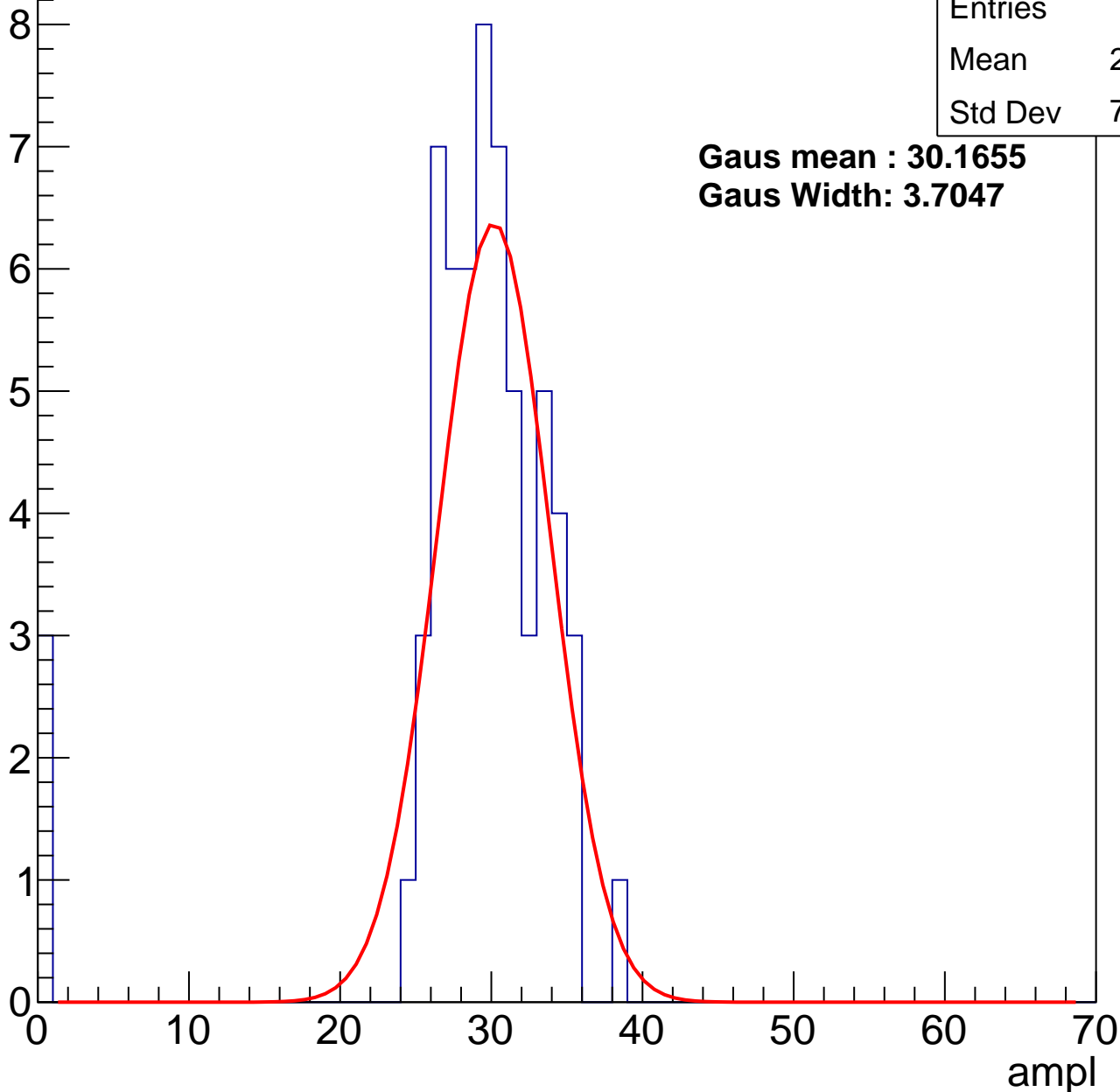
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.19
Std Dev	7.039

Gaus mean : 30.1655

Gaus Width: 3.7047



B1L103S, U14-ch60, adc1

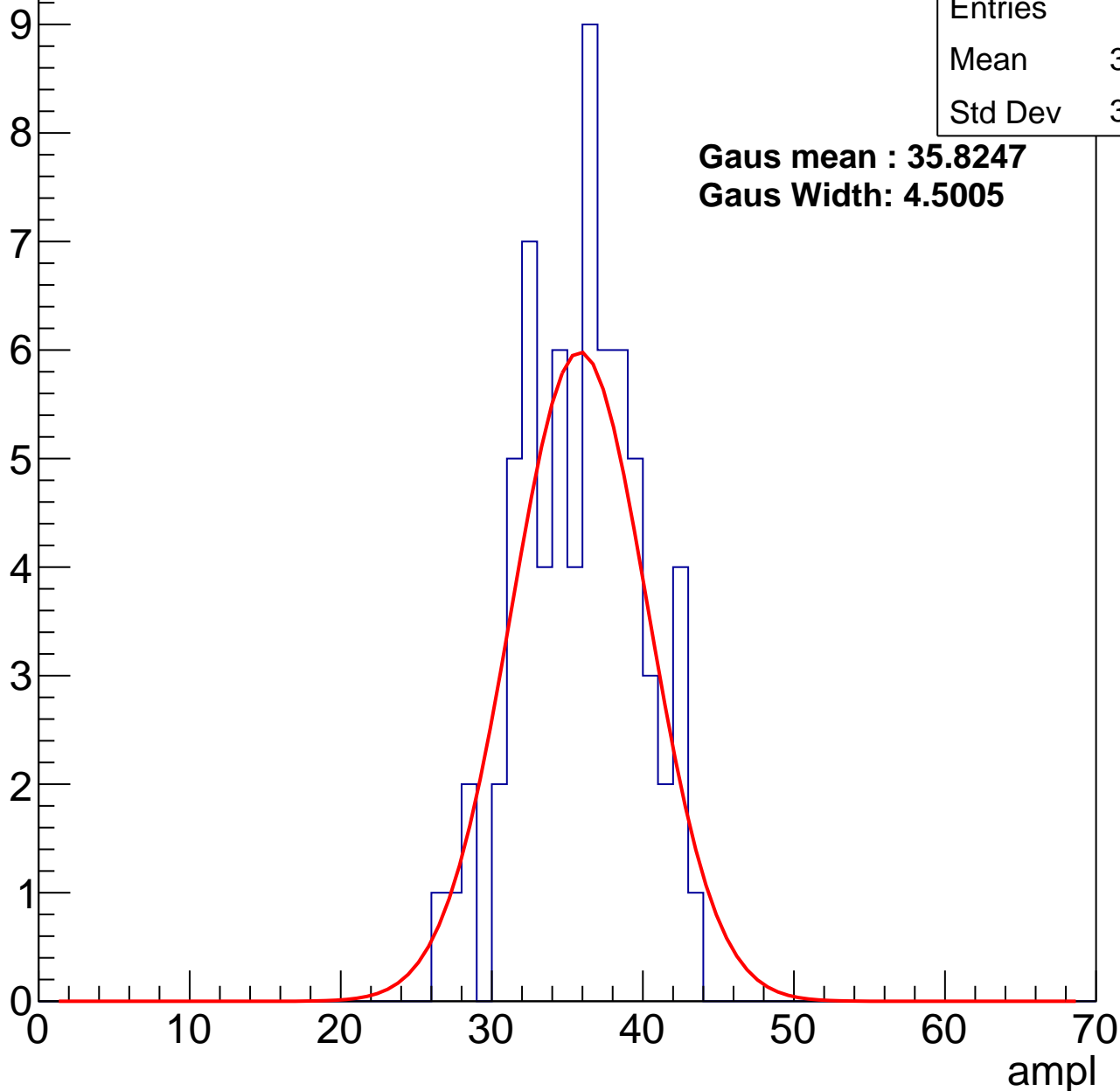
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.38
Std Dev	3.877

Gaus mean : 35.8247

Gaus Width: 4.5005



B1L103S, U14-ch60, adc2

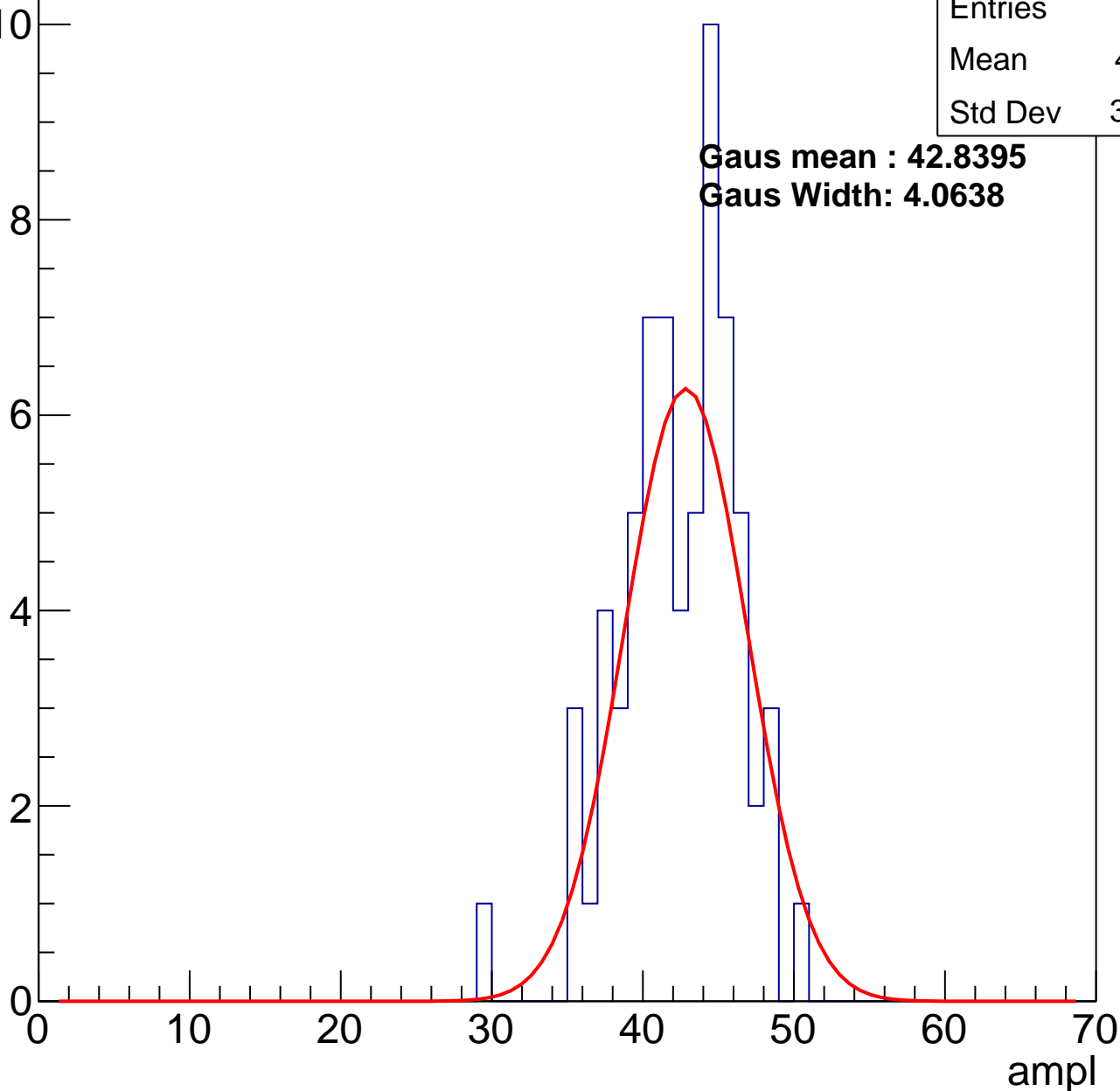
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	41.91
Std Dev	3.815

Gaus mean : 42.8395

Gaus Width: 4.0638

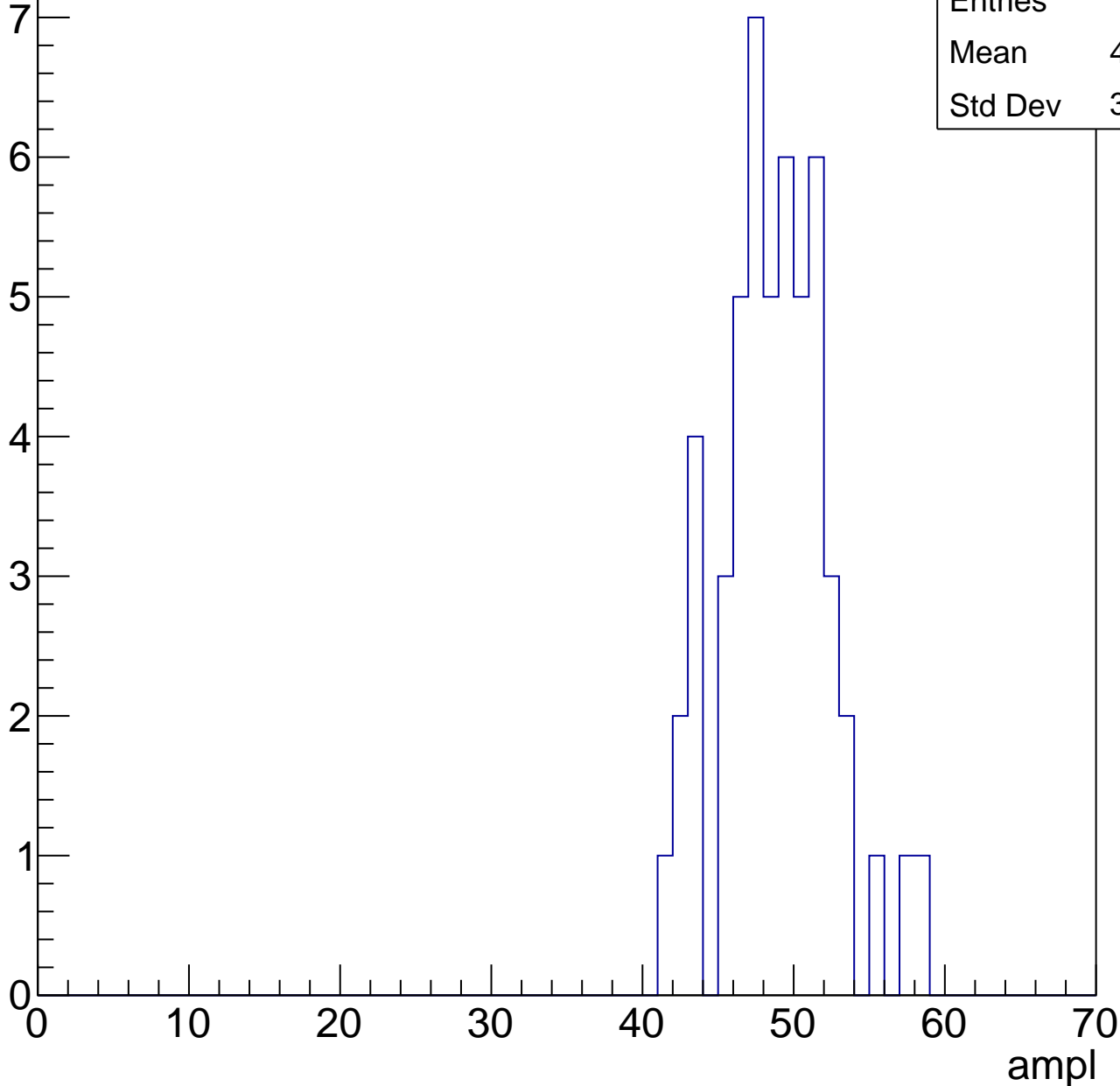


B1L103S, U14-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	48.33
Std Dev	3.609



B1L103S, U14-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

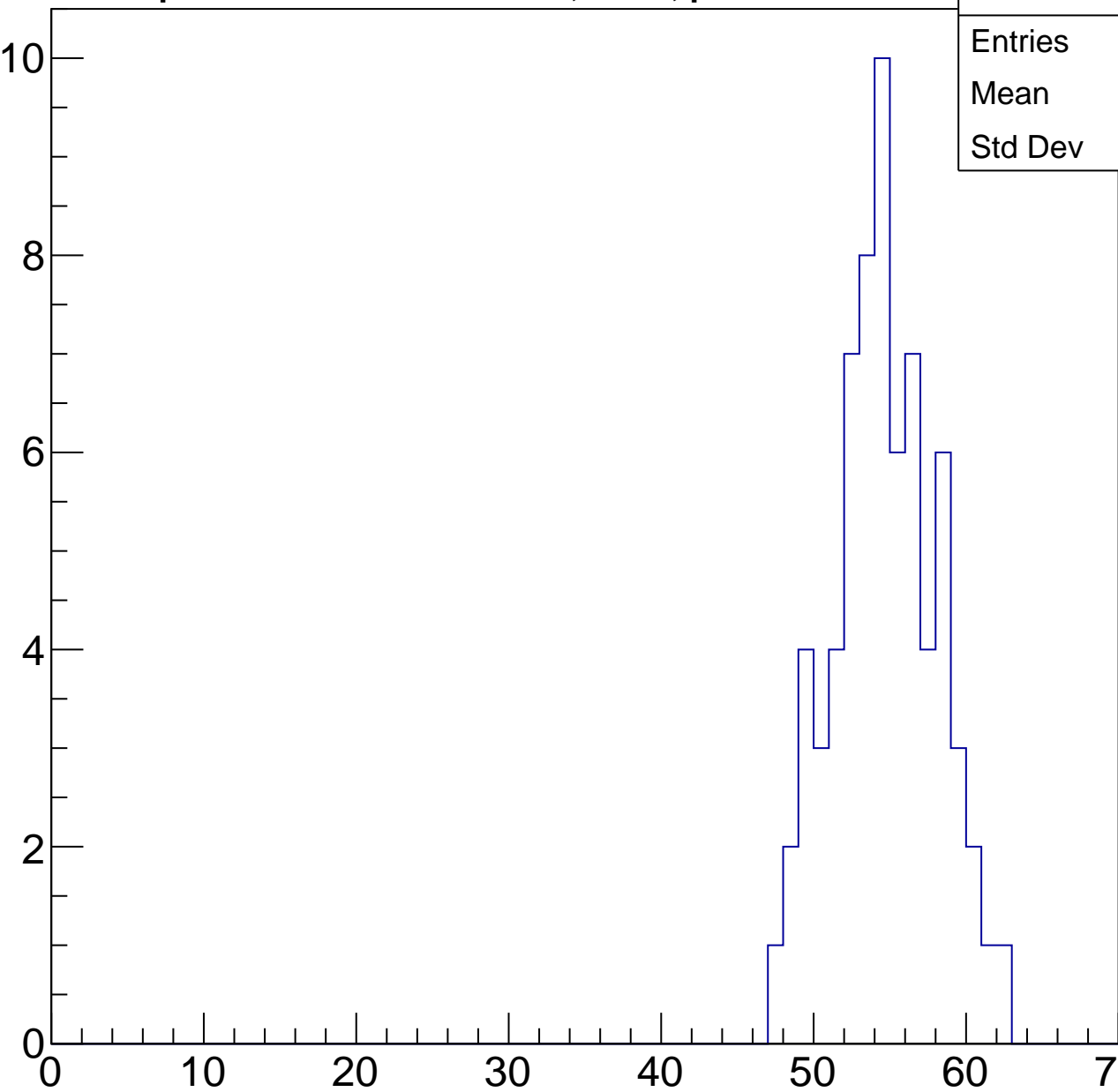
Entries	69
Mean	54.19
Std Dev	3.329

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

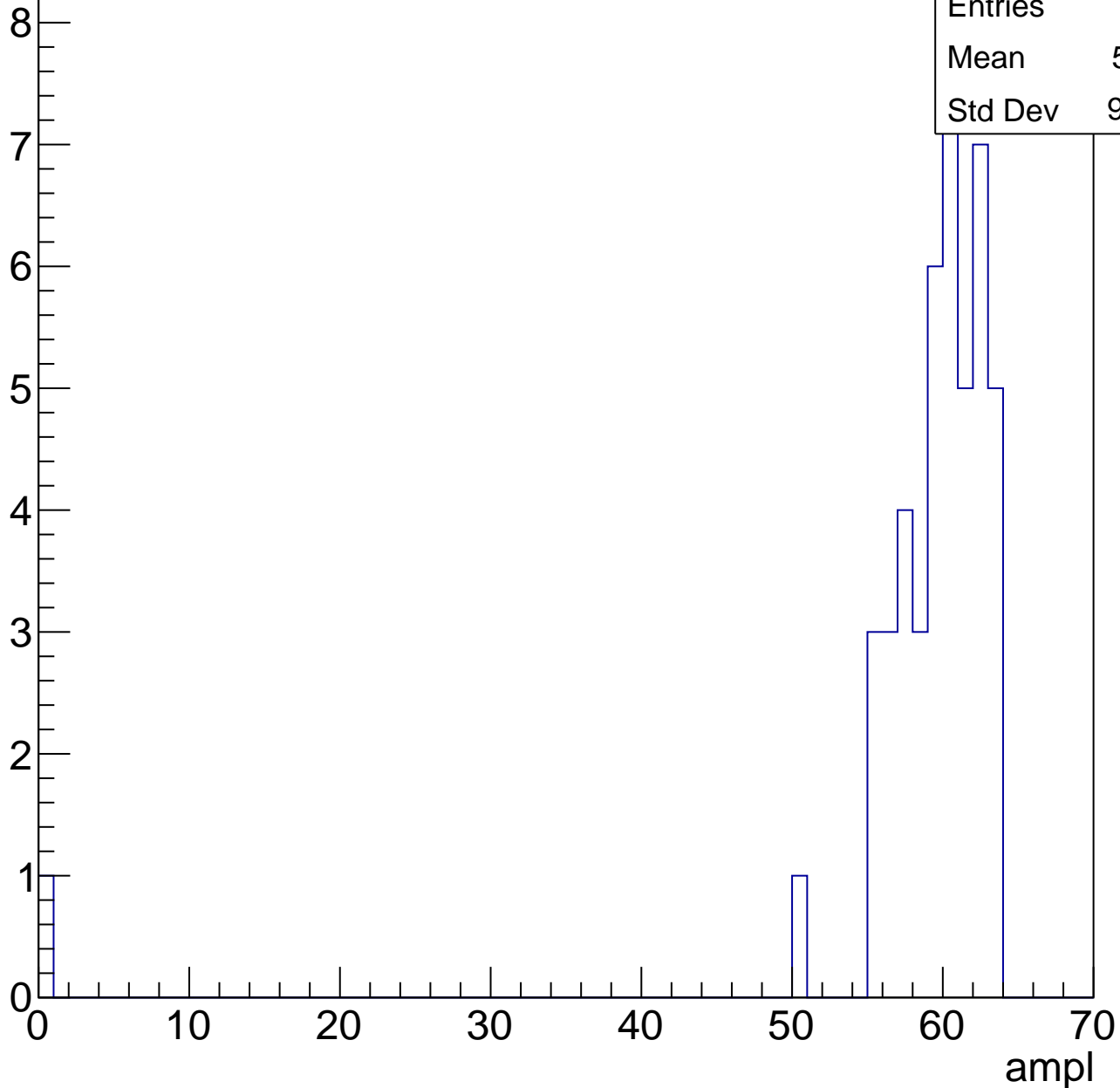


B1L103S, U14-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

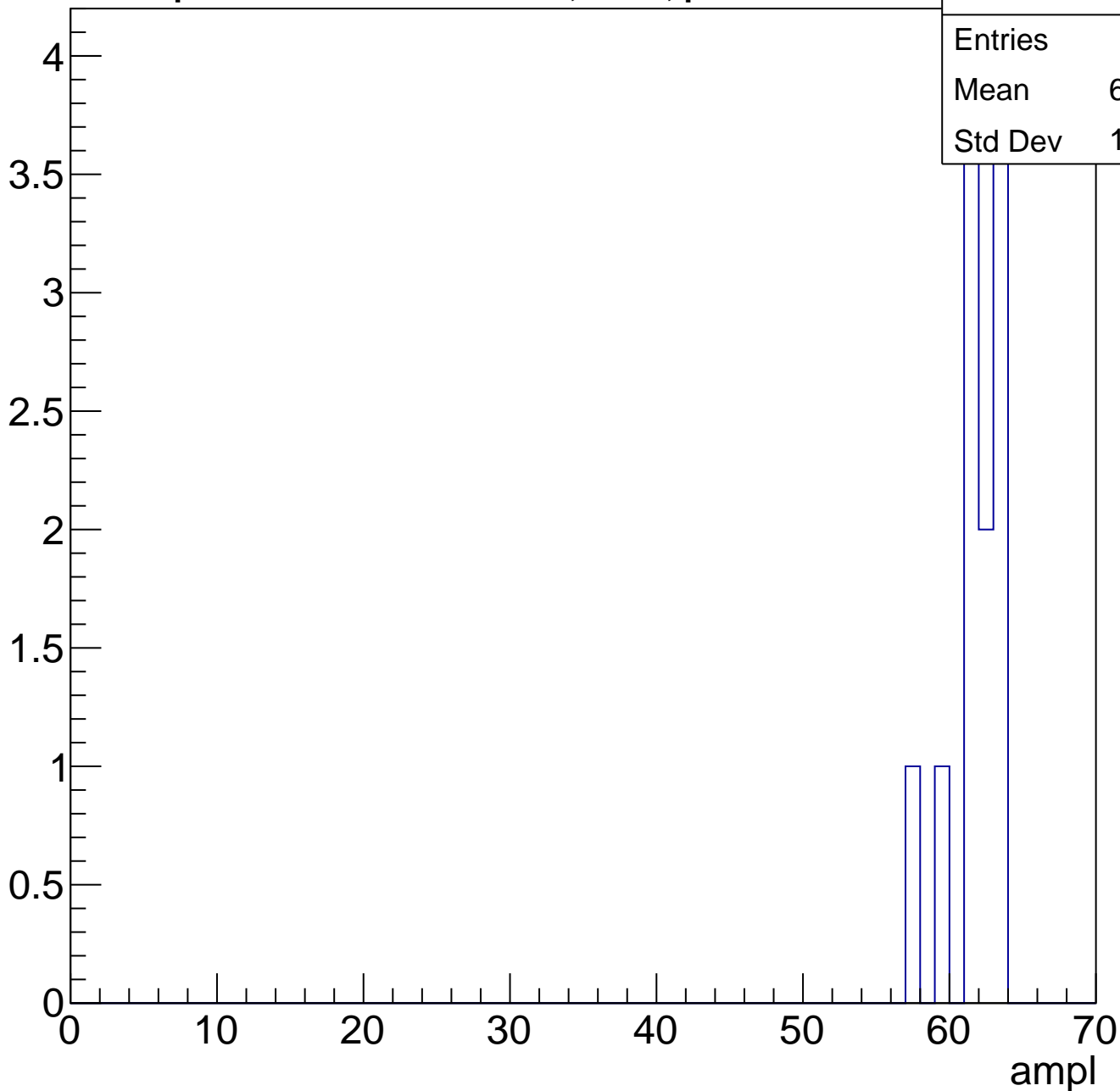
Entries	46
Mean	58.11
Std Dev	9.078



B1L103S, U14-ch60, adc6

calib_packv5_042523_0143.root, FC#7, port C2

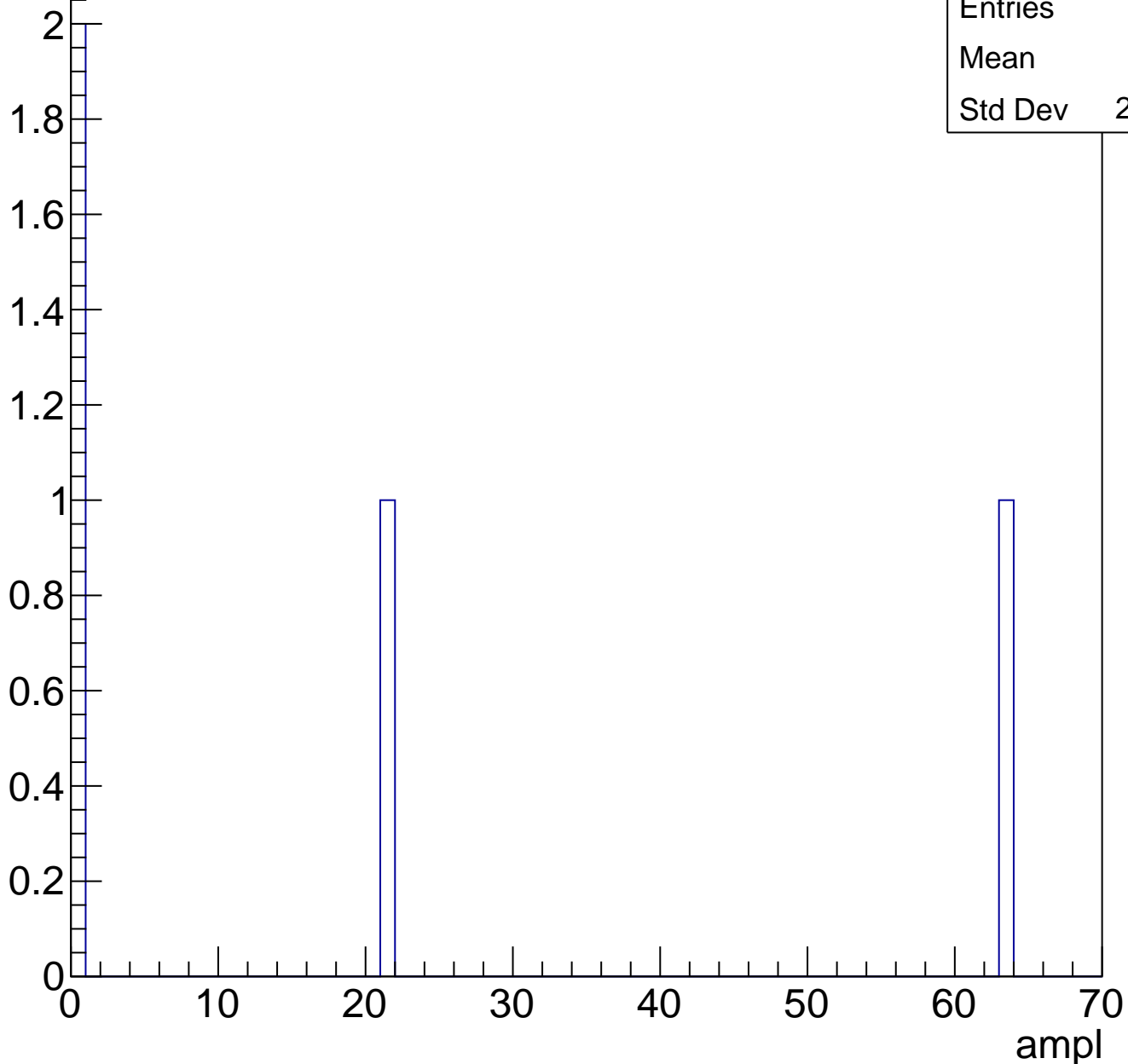
Entry



B1L103S, U14-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	21
Std Dev	25.72

B1L103S, U14-ch61, adc0

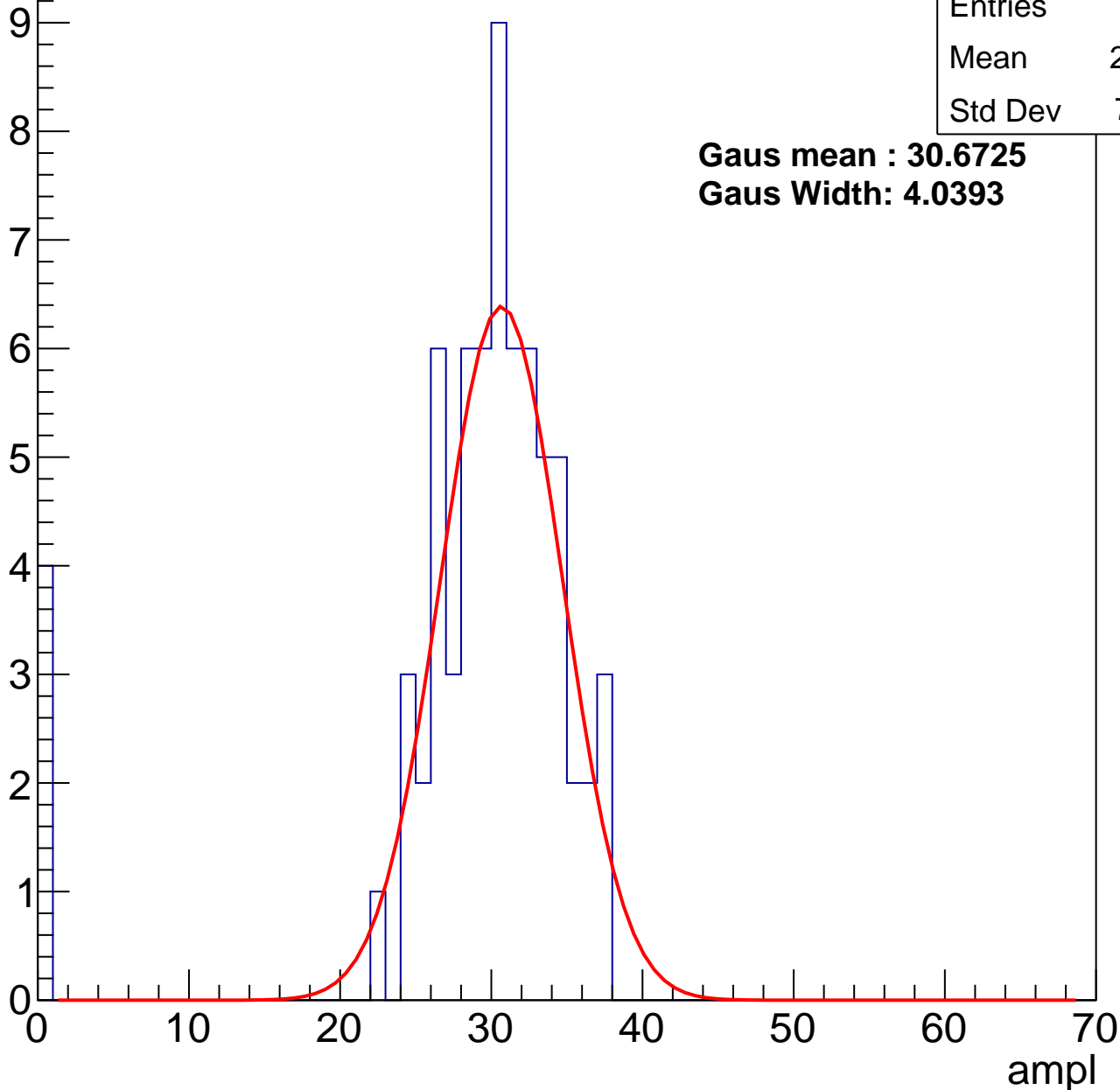
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	28.39
Std Dev	7.821

Gaus mean : 30.6725

Gaus Width: 4.0393



B1L103S, U14-ch61, adc1

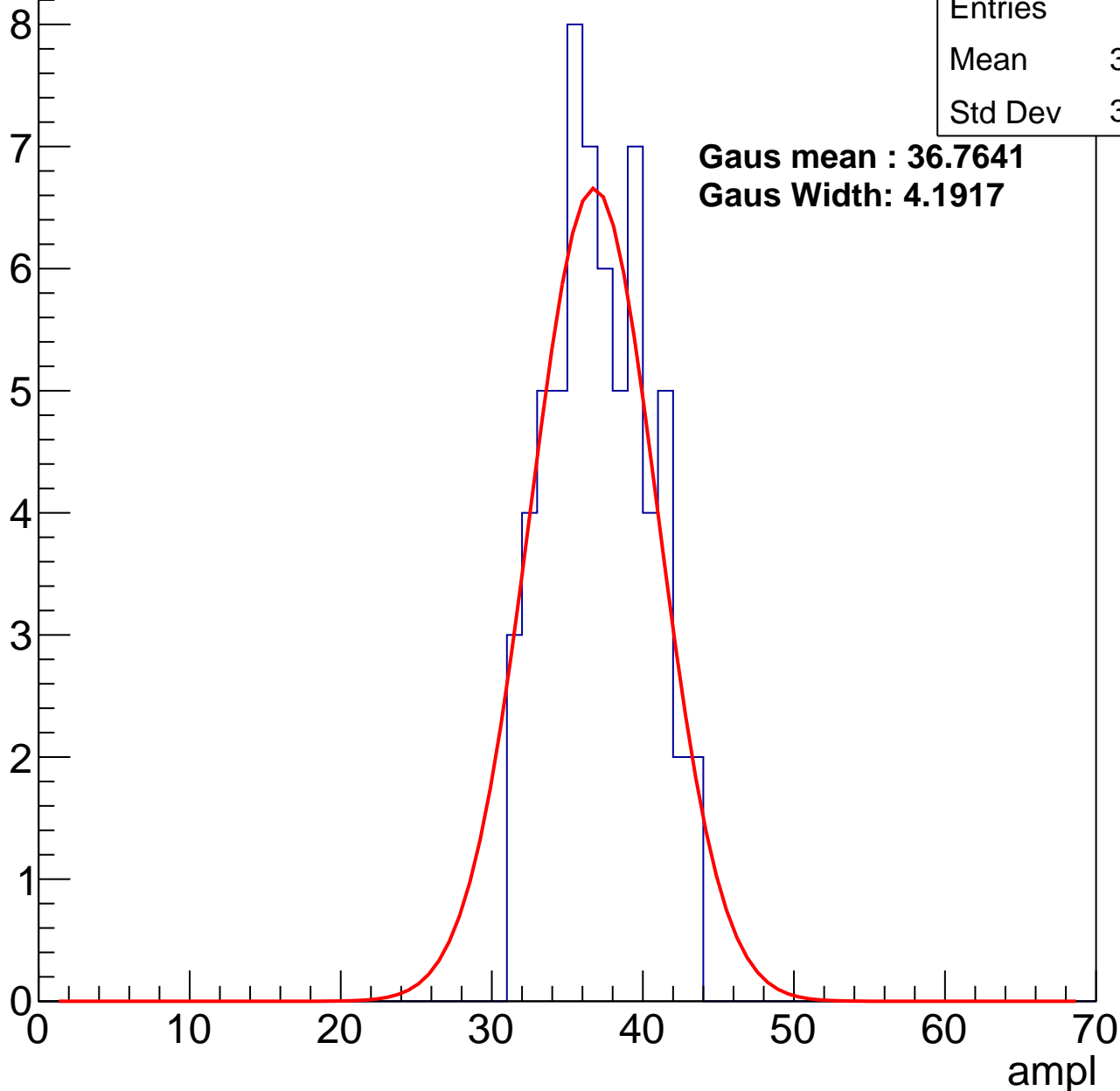
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	36.63
Std Dev	3.174

Gaus mean : 36.7641

Gaus Width: 4.1917



B1L103S, U14-ch61, adc2

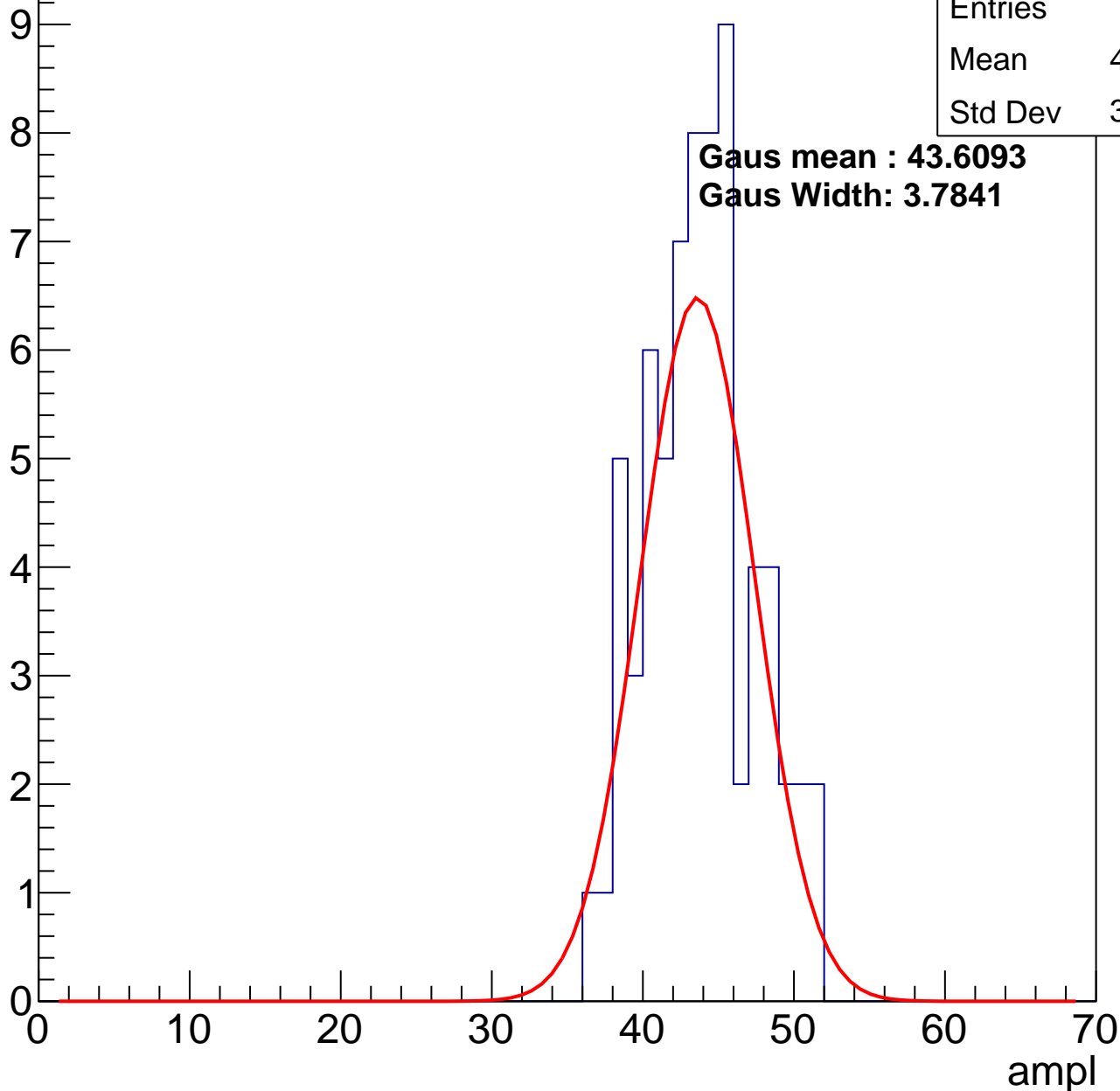
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	43.36
Std Dev	3.518

Gaus mean : 43.6093

Gaus Width: 3.7841

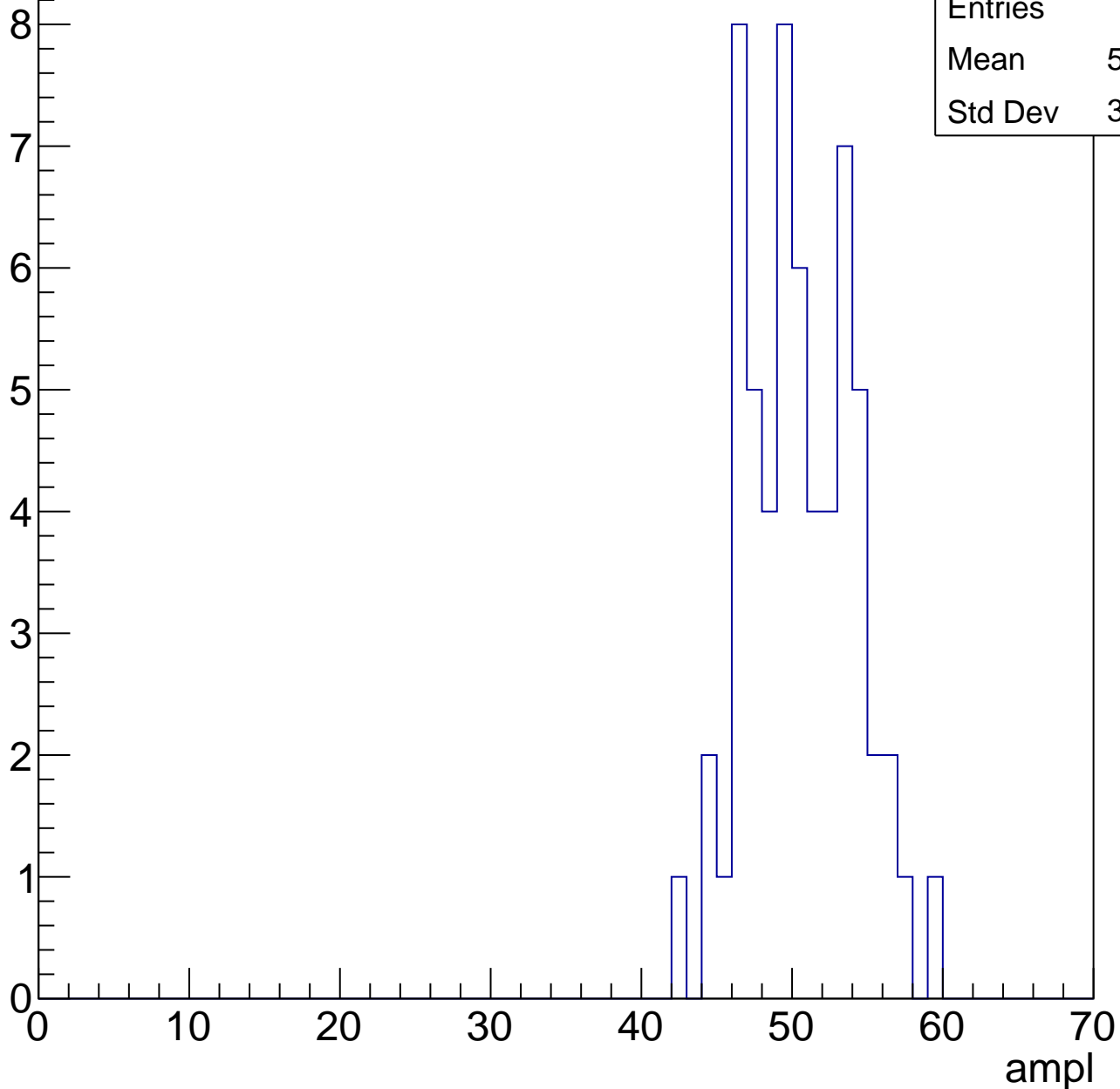


B1L103S, U14-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.05
Std Dev	3.559

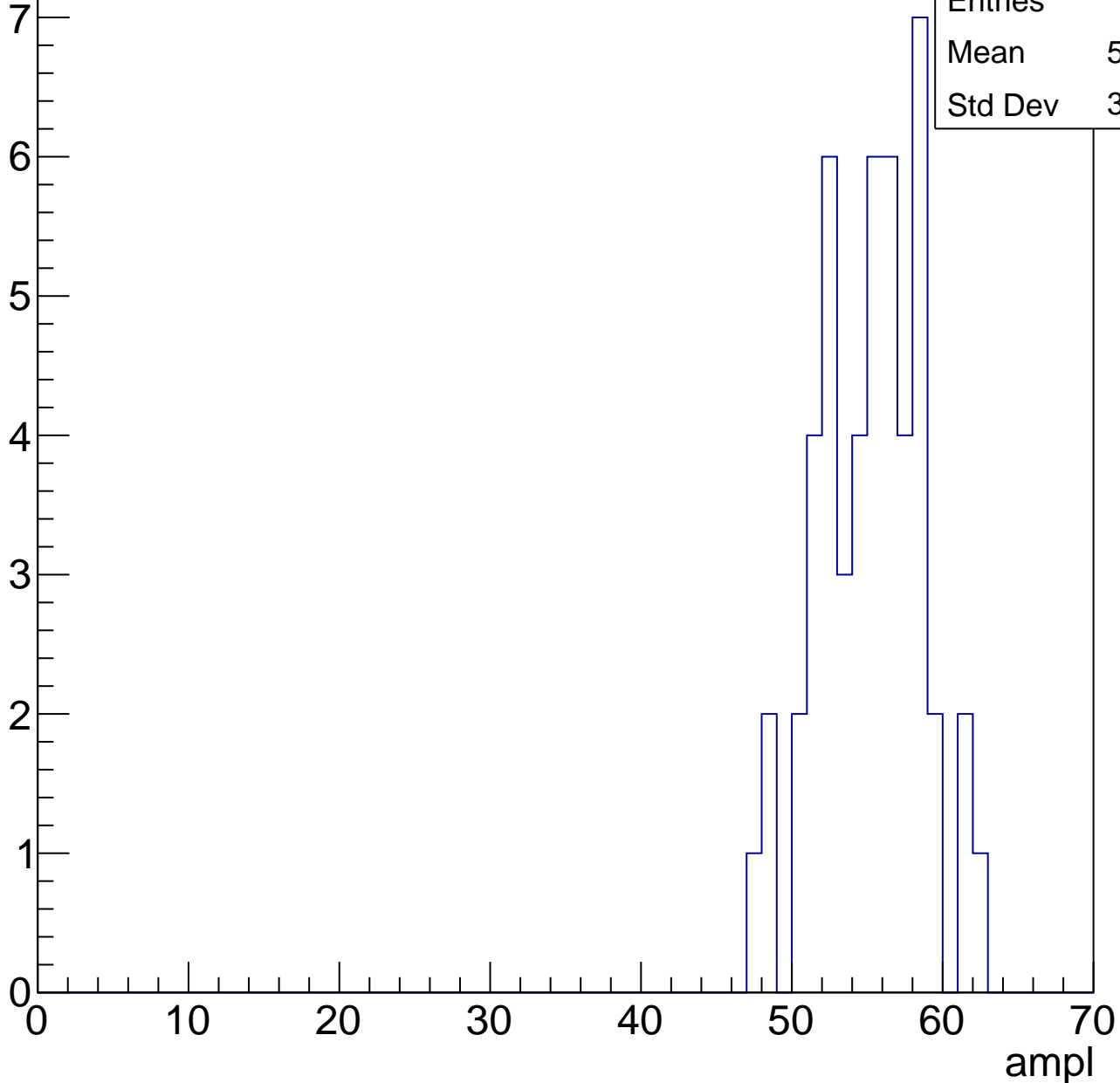


B1L103S, U14-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	54.72
Std Dev	3.406

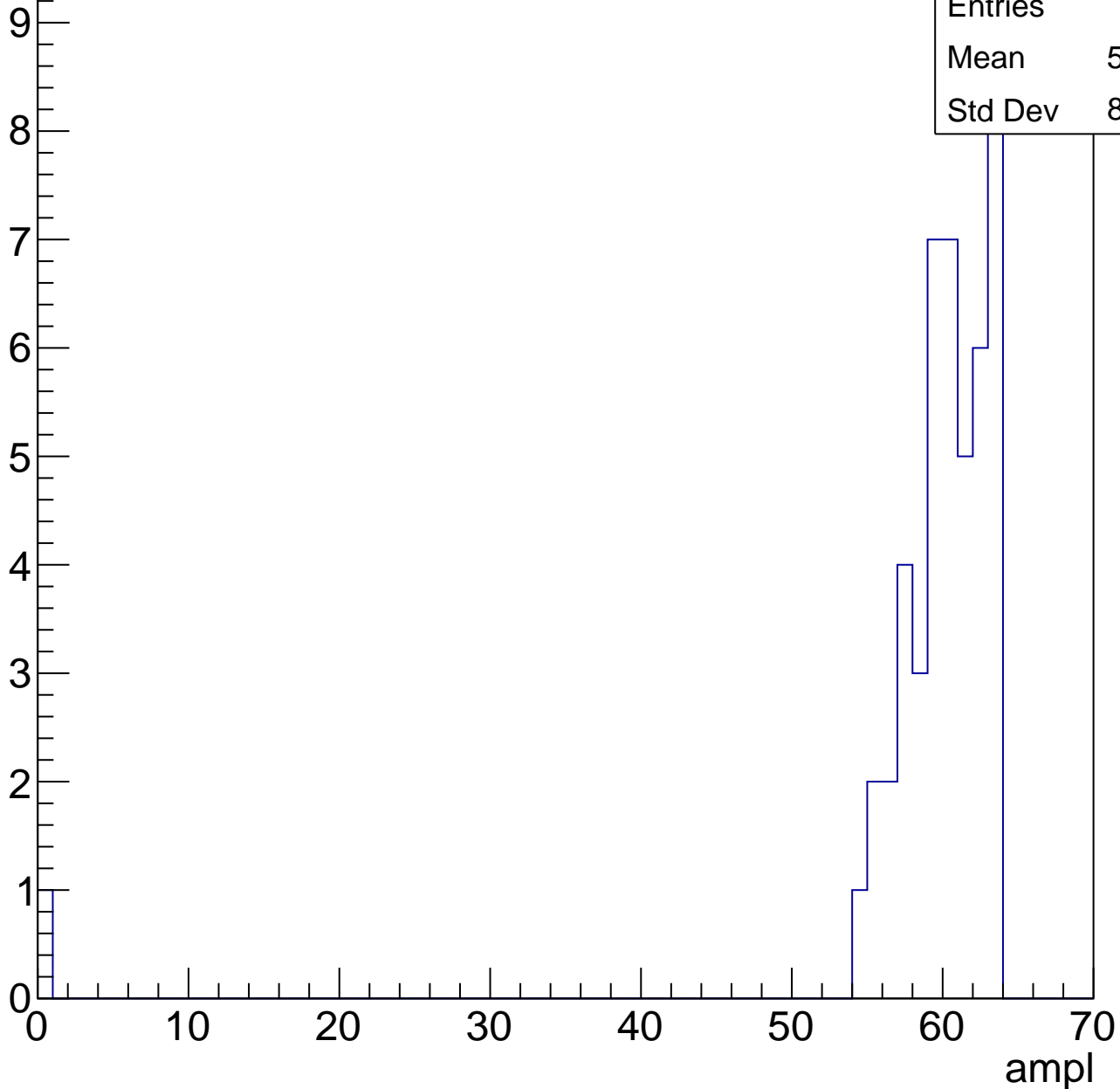


B1L103S, U14-ch61, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

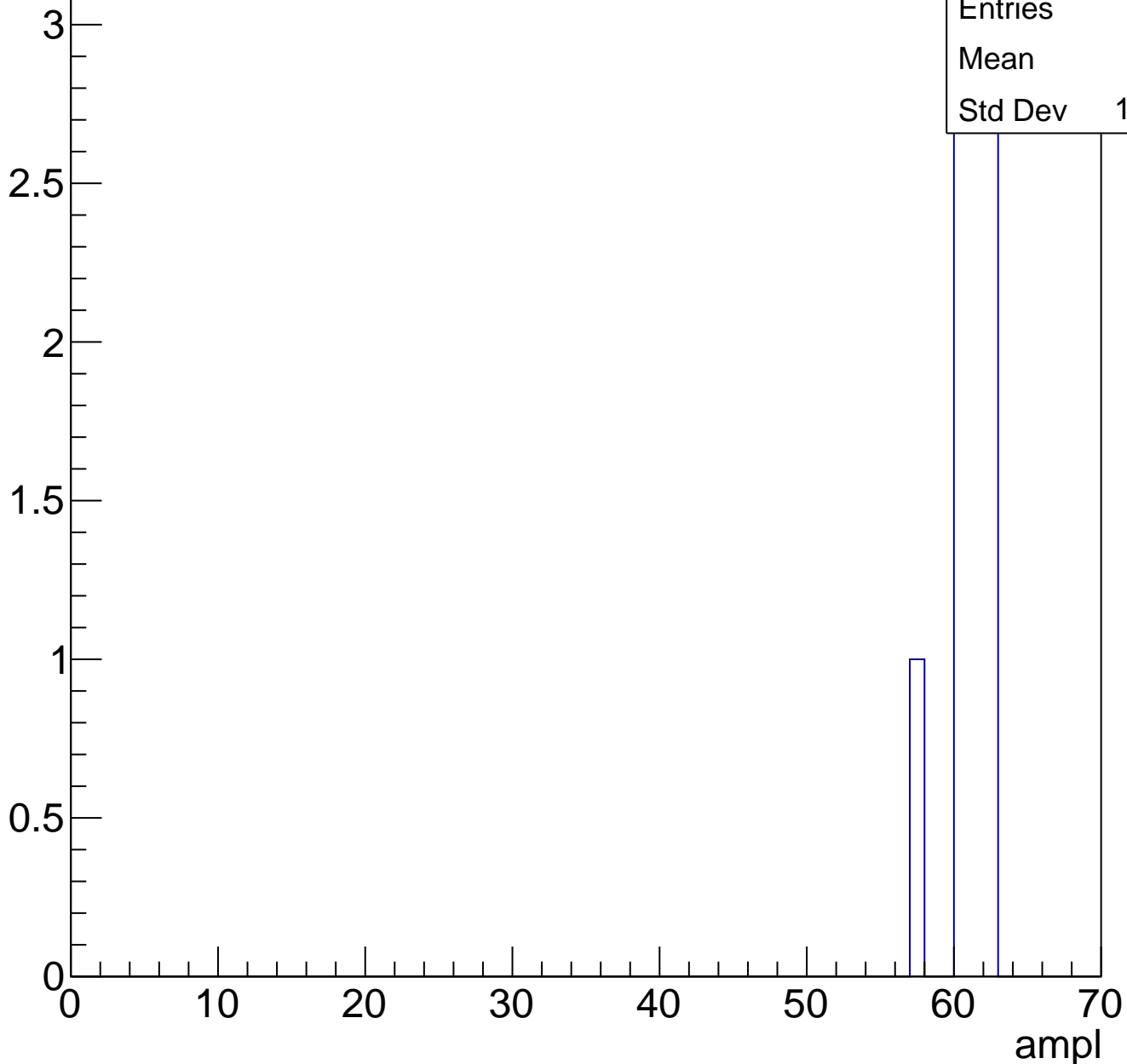
Entries	47
Mean	58.62
Std Dev	8.984



B1L103S, U14-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U14-ch62, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	29.23
Std Dev	4.899

Gaus mean : 30.1951

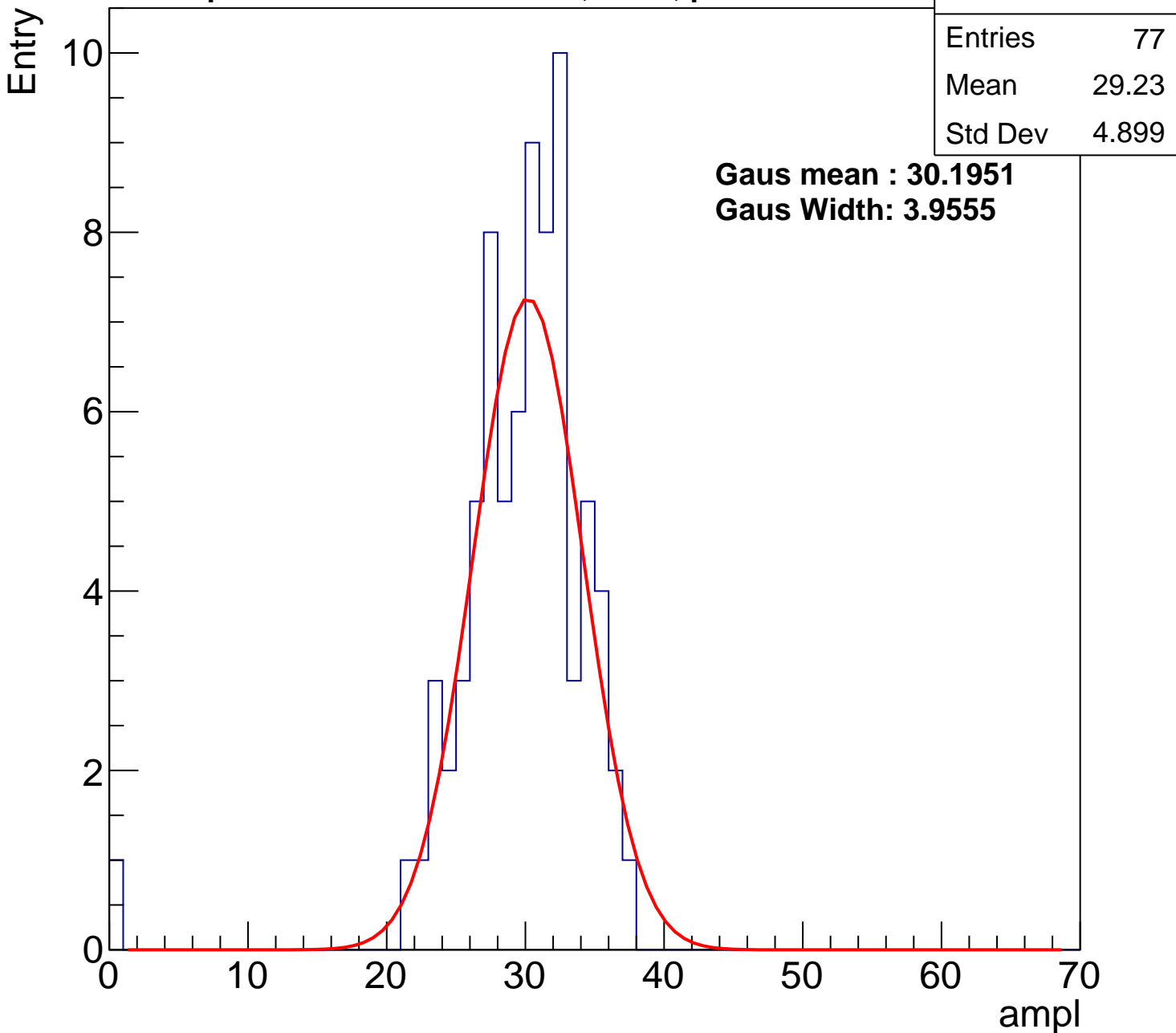
Gaus Width: 3.9555

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch62, adc1

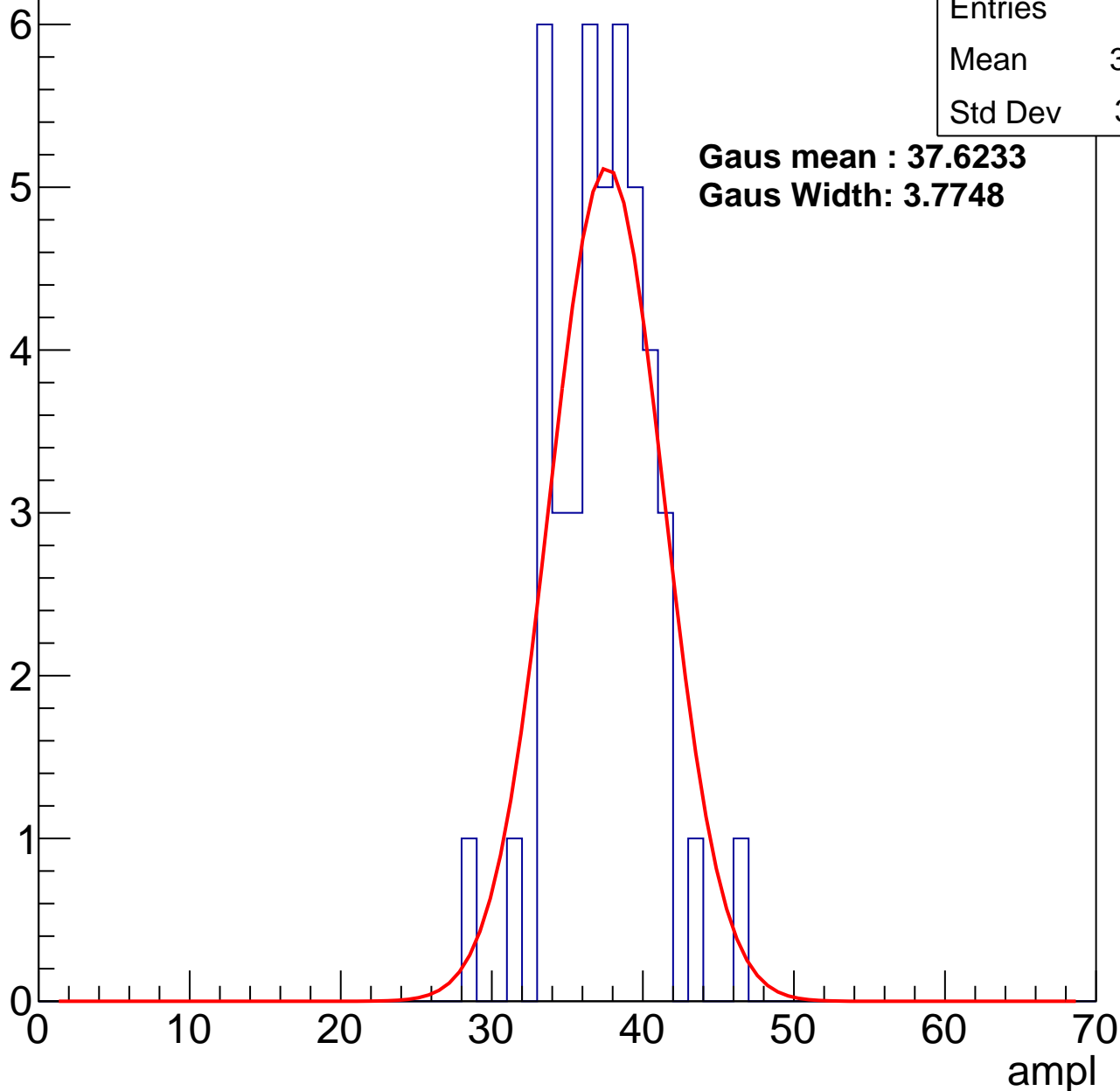
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	36.89
Std Dev	3.281

Gaus mean : 37.6233

Gaus Width: 3.7748



B1L103S, U14-ch62, adc2

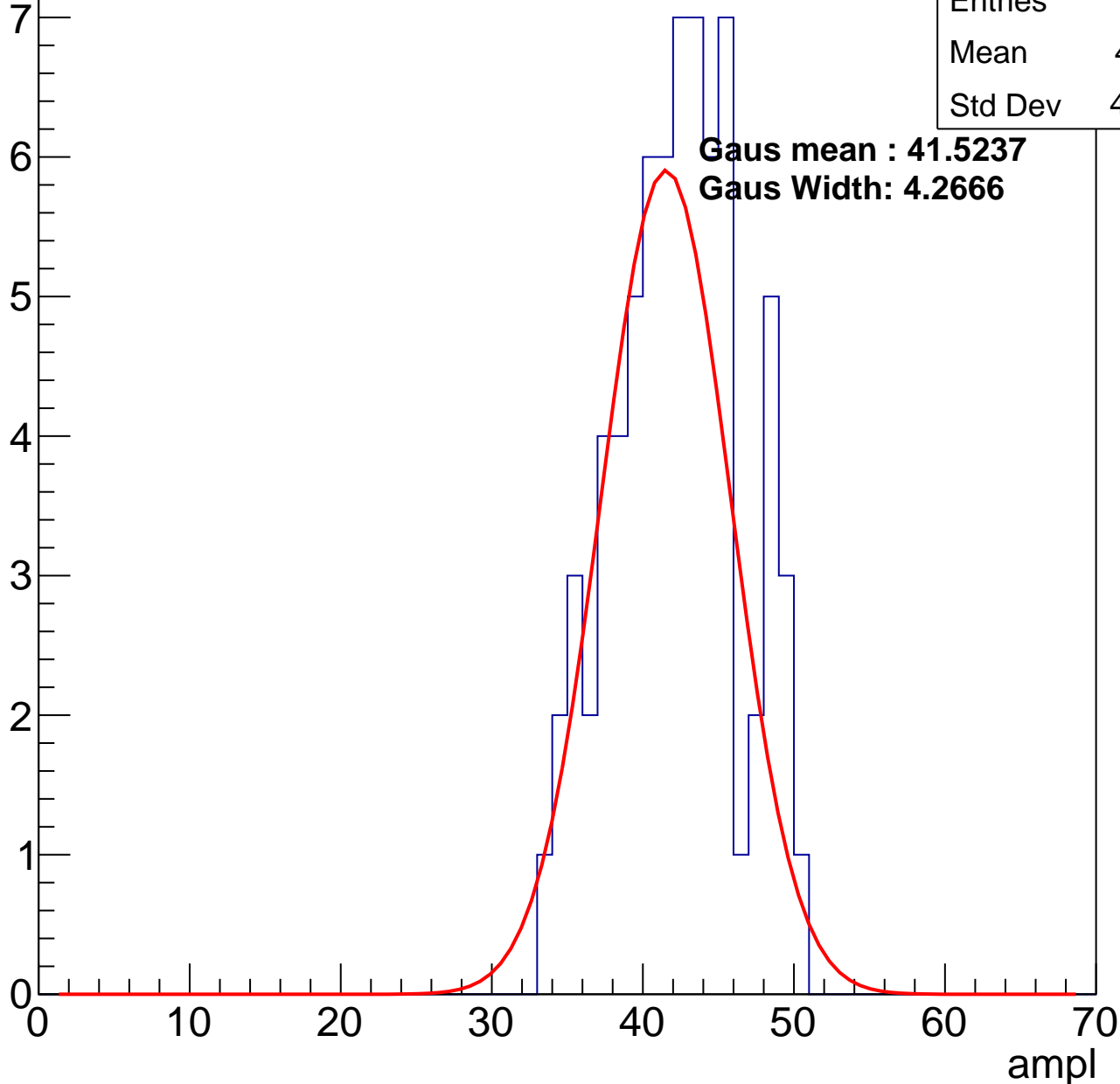
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	41.81
Std Dev	4.108

Gaus mean : 41.5237

Gaus Width: 4.2666

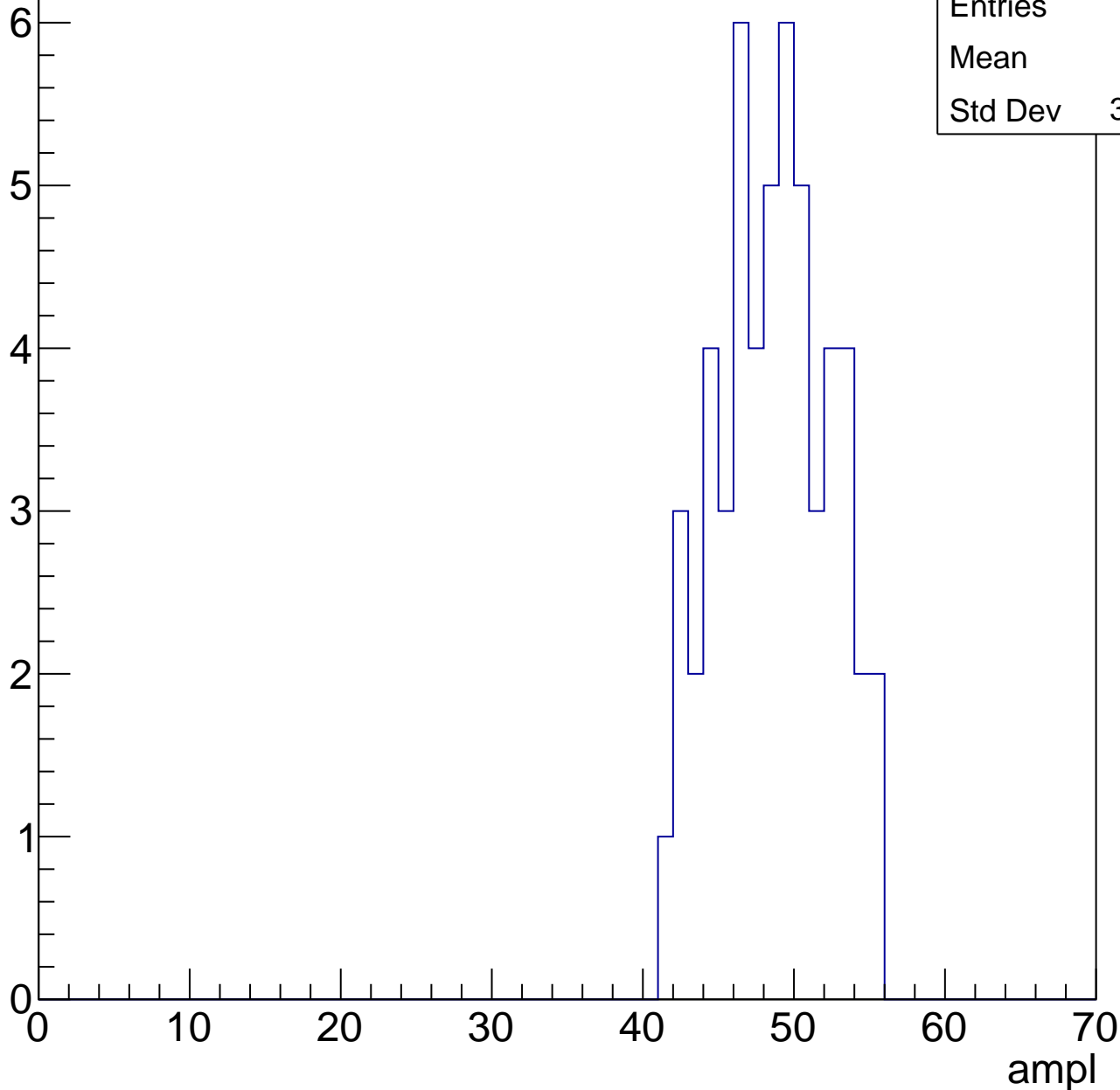


B1L103S, U14-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	48.2
Std Dev	3.628

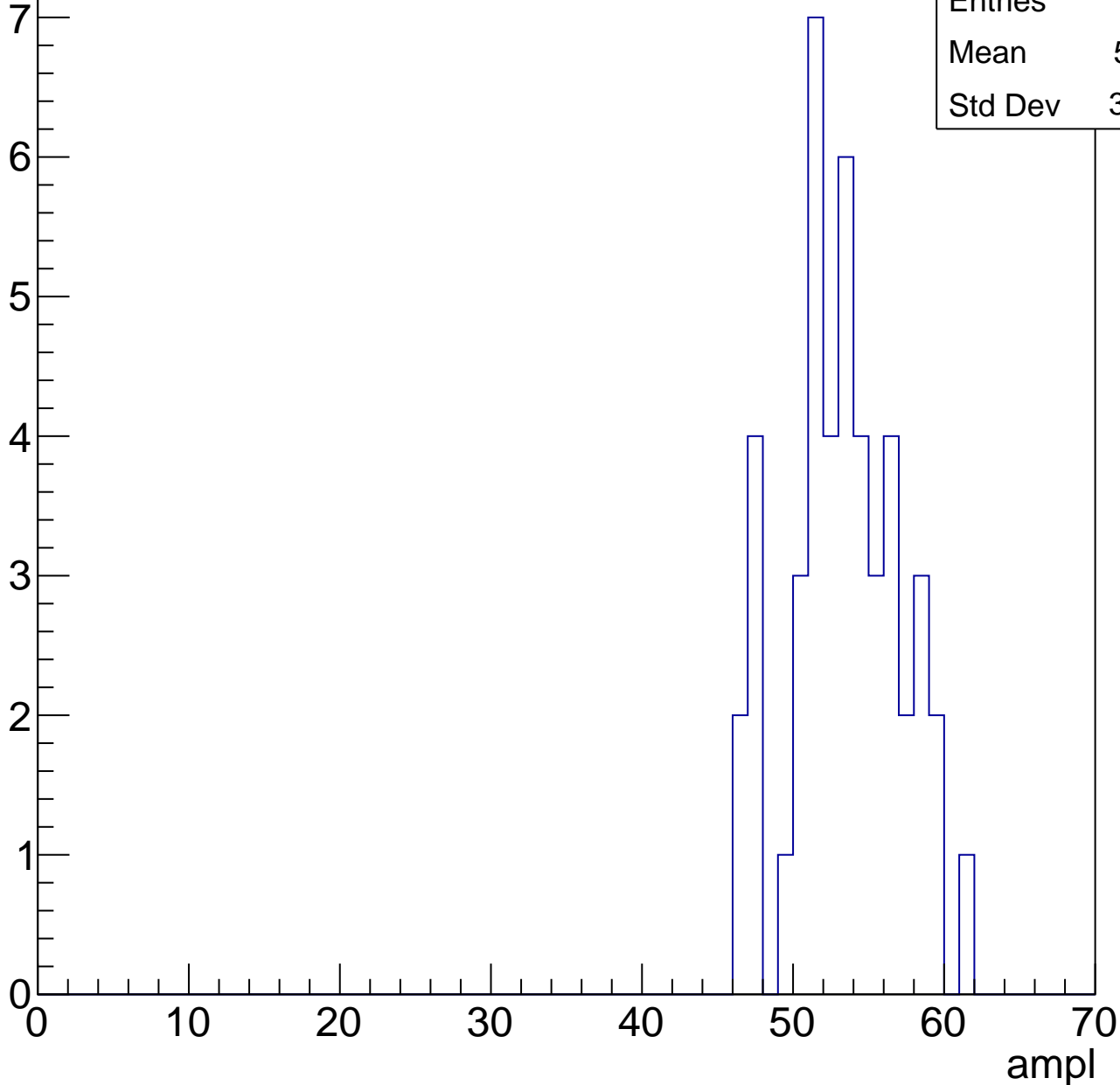


B1L103S, U14-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	52.91
Std Dev	3.646

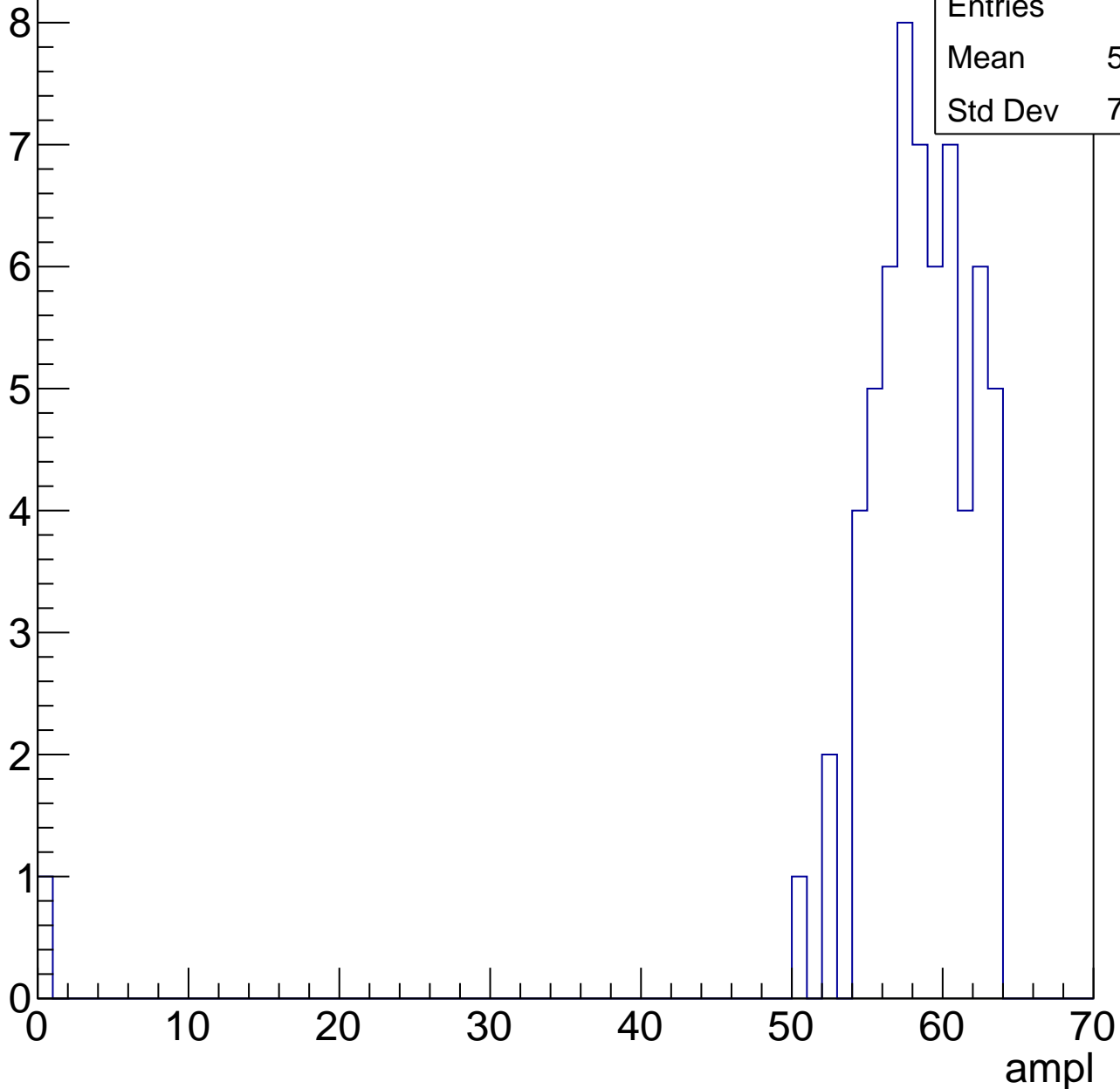


B1L103S, U14-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	57.23
Std Dev	7.926

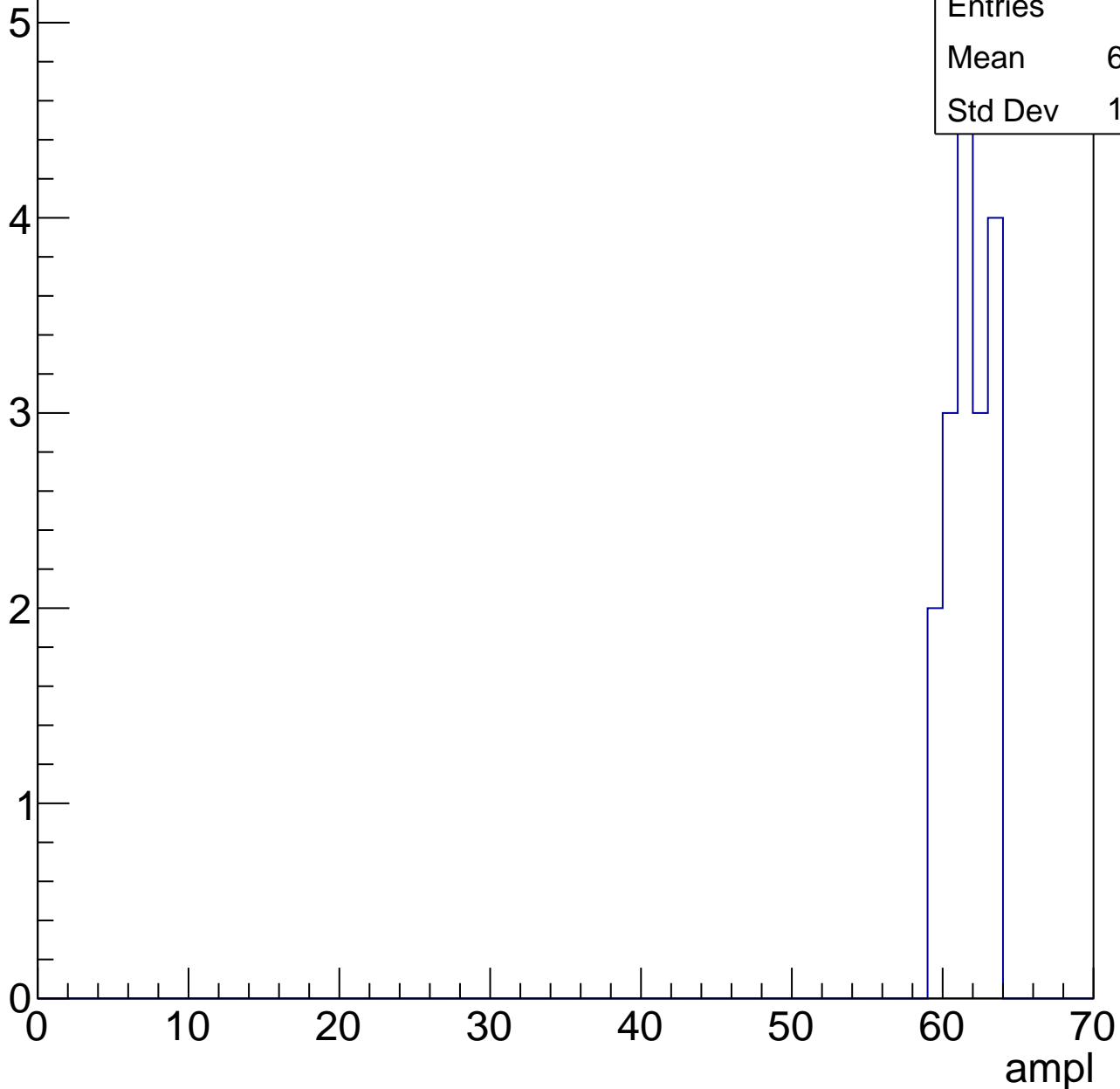


B1L103S, U14-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	61.24
Std Dev	1.307



B1L103S, U14-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch63, adc0

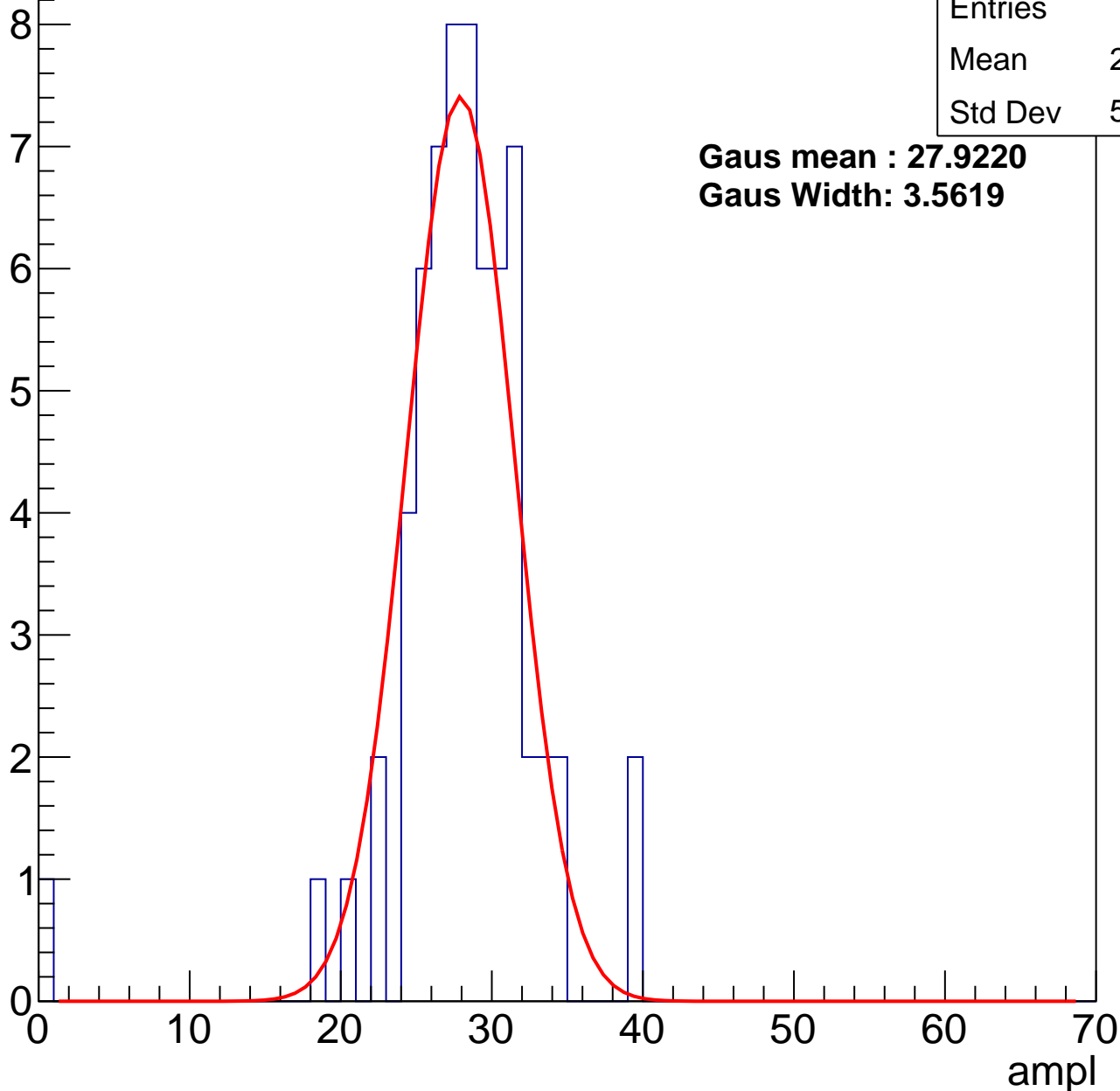
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	27.65
Std Dev	5.046

Gaus mean : 27.9220

Gaus Width: 3.5619



B1L103S, U14-ch63, adc1

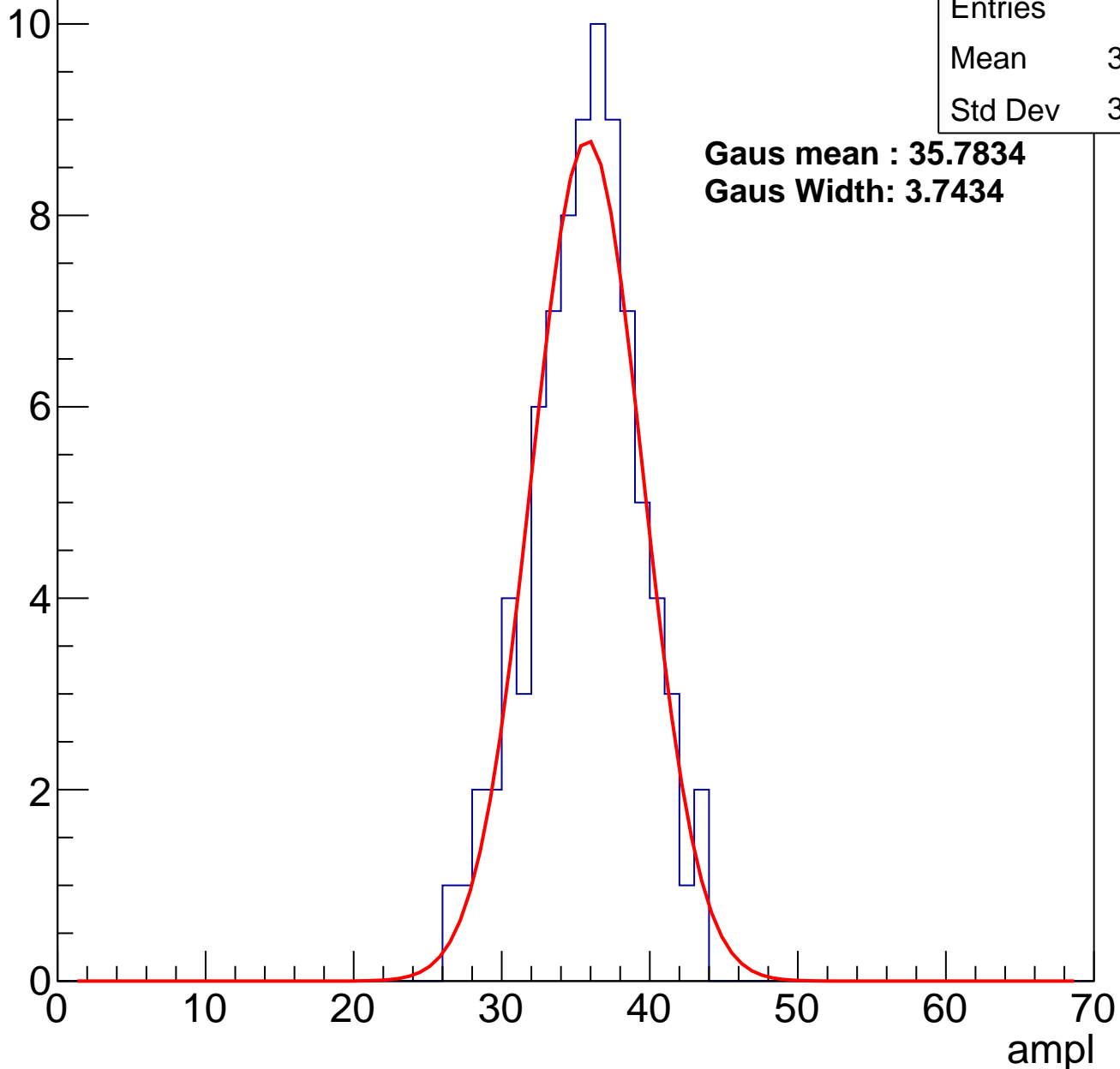
calib_packv5_042523_0143.root, FC#7, port C2

Entries	84
Mean	35.18
Std Dev	3.659

Gaus mean : 35.7834

Gaus Width: 3.7434

Entry



B1L103S, U14-ch63, adc2

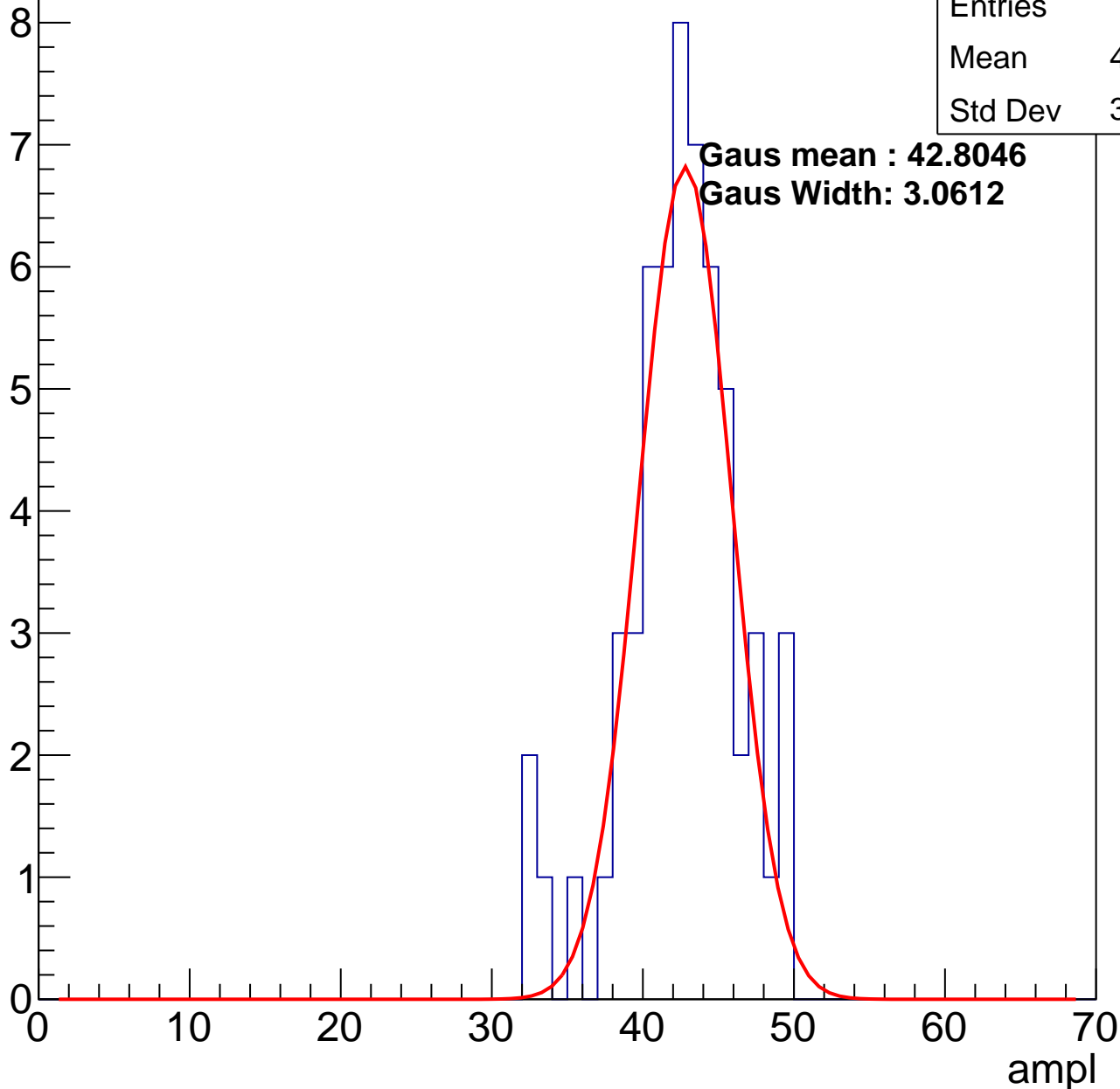
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	42.07
Std Dev	3.773

Gaus mean : 42.8046

Gaus Width: 3.0612

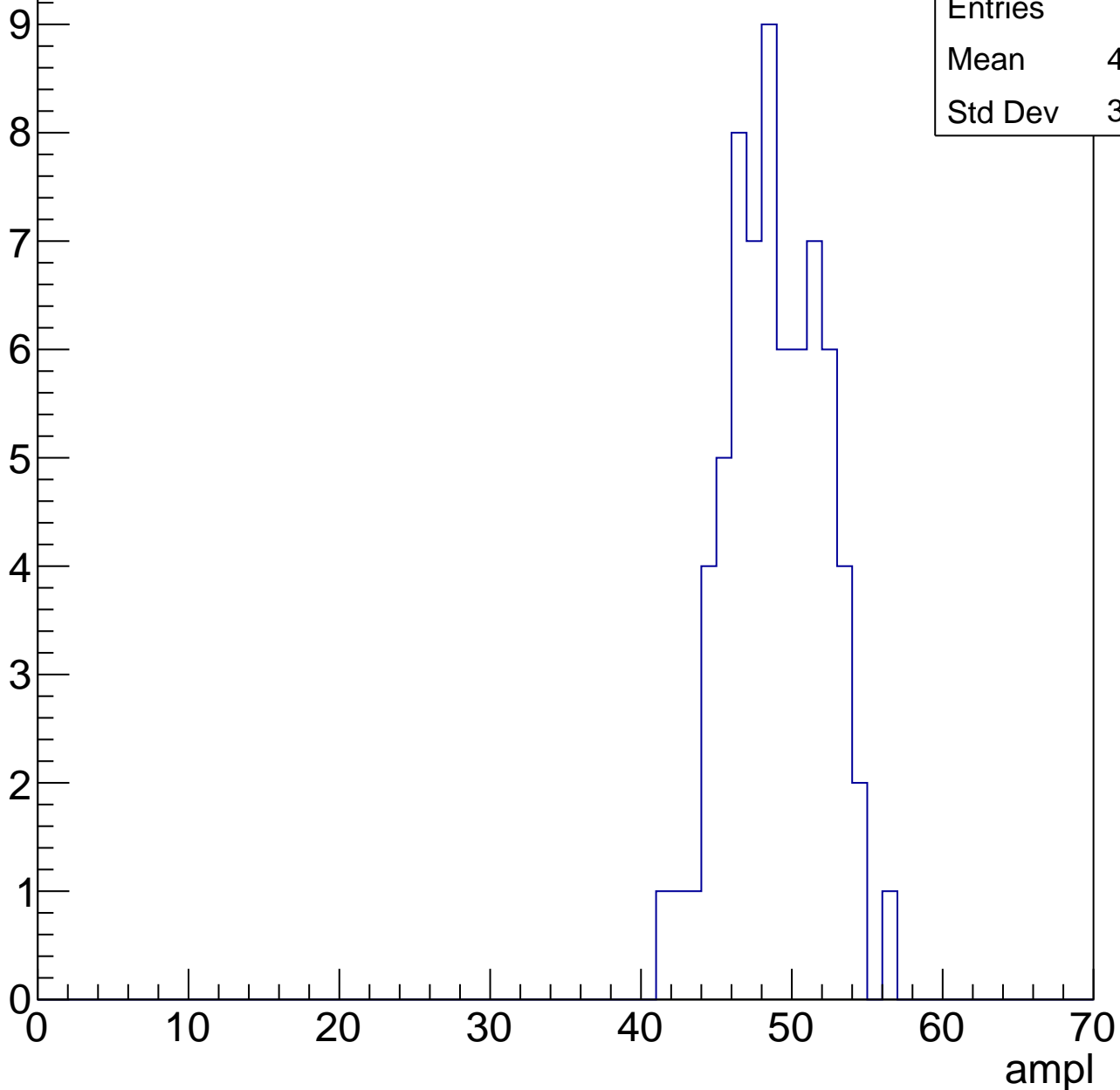


B1L103S, U14-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	48.46
Std Dev	3.136

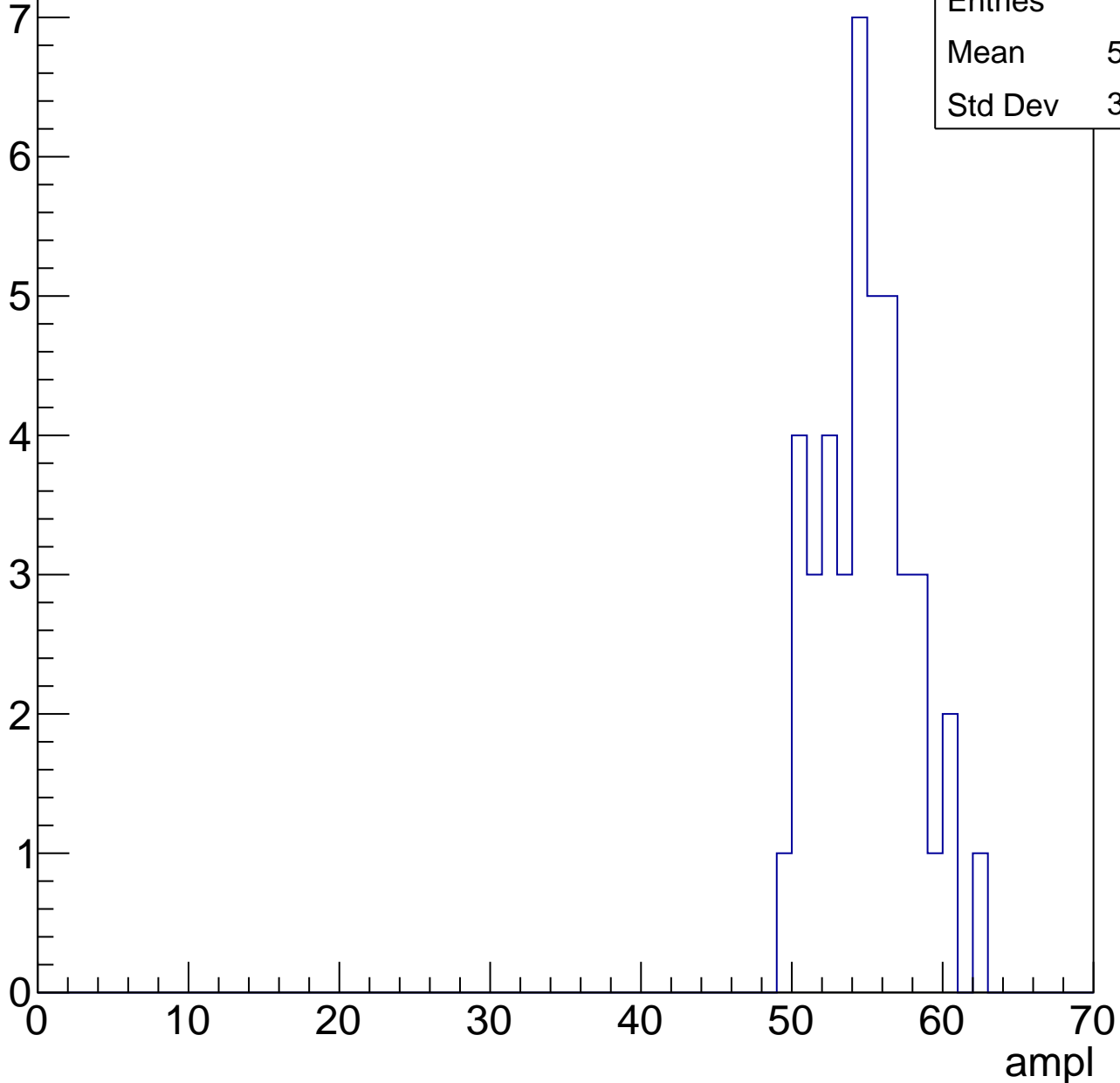


B1L103S, U14-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

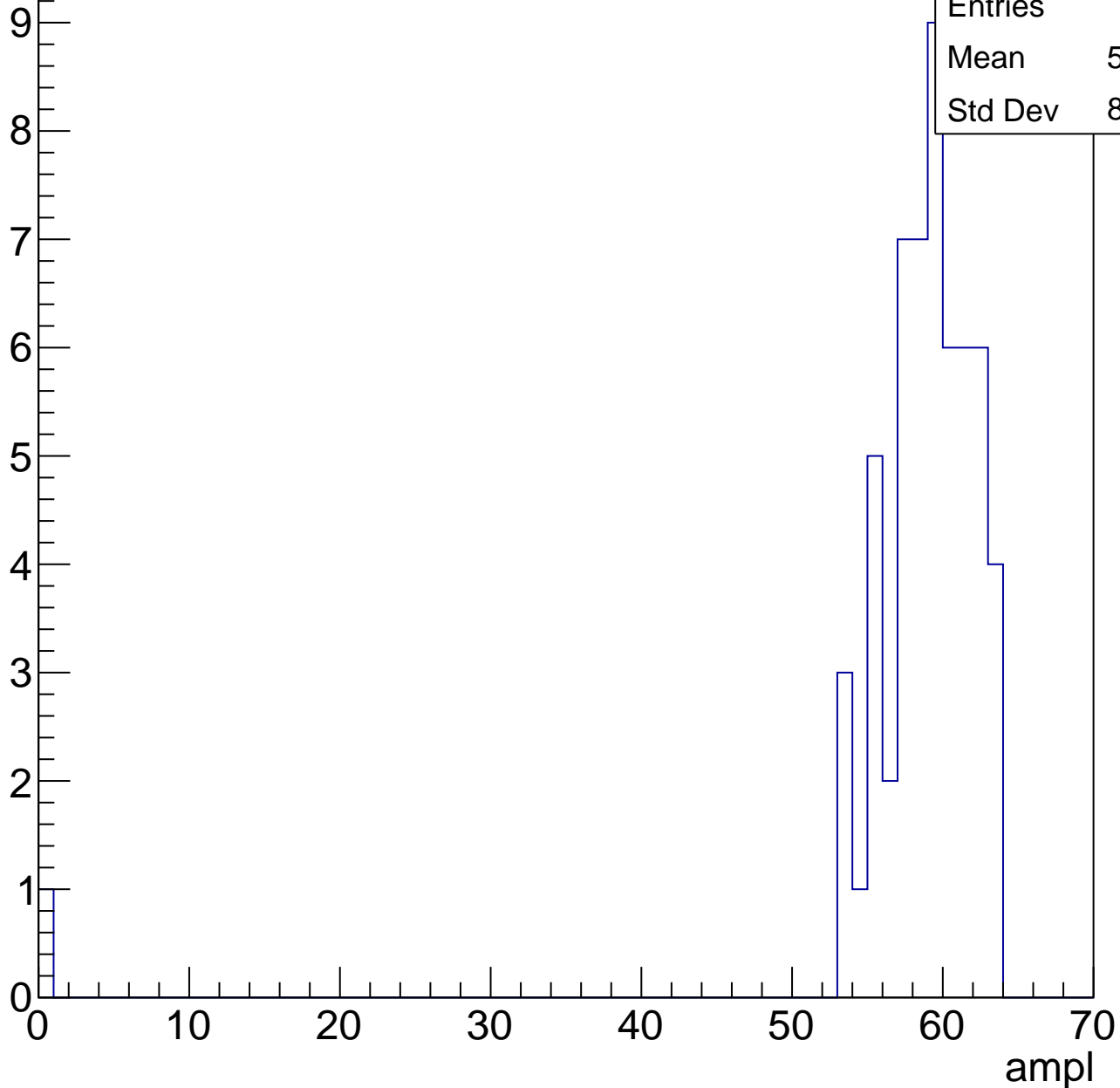
Entries	42
Mean	54.48
Std Dev	3.033



B1L103S, U14-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

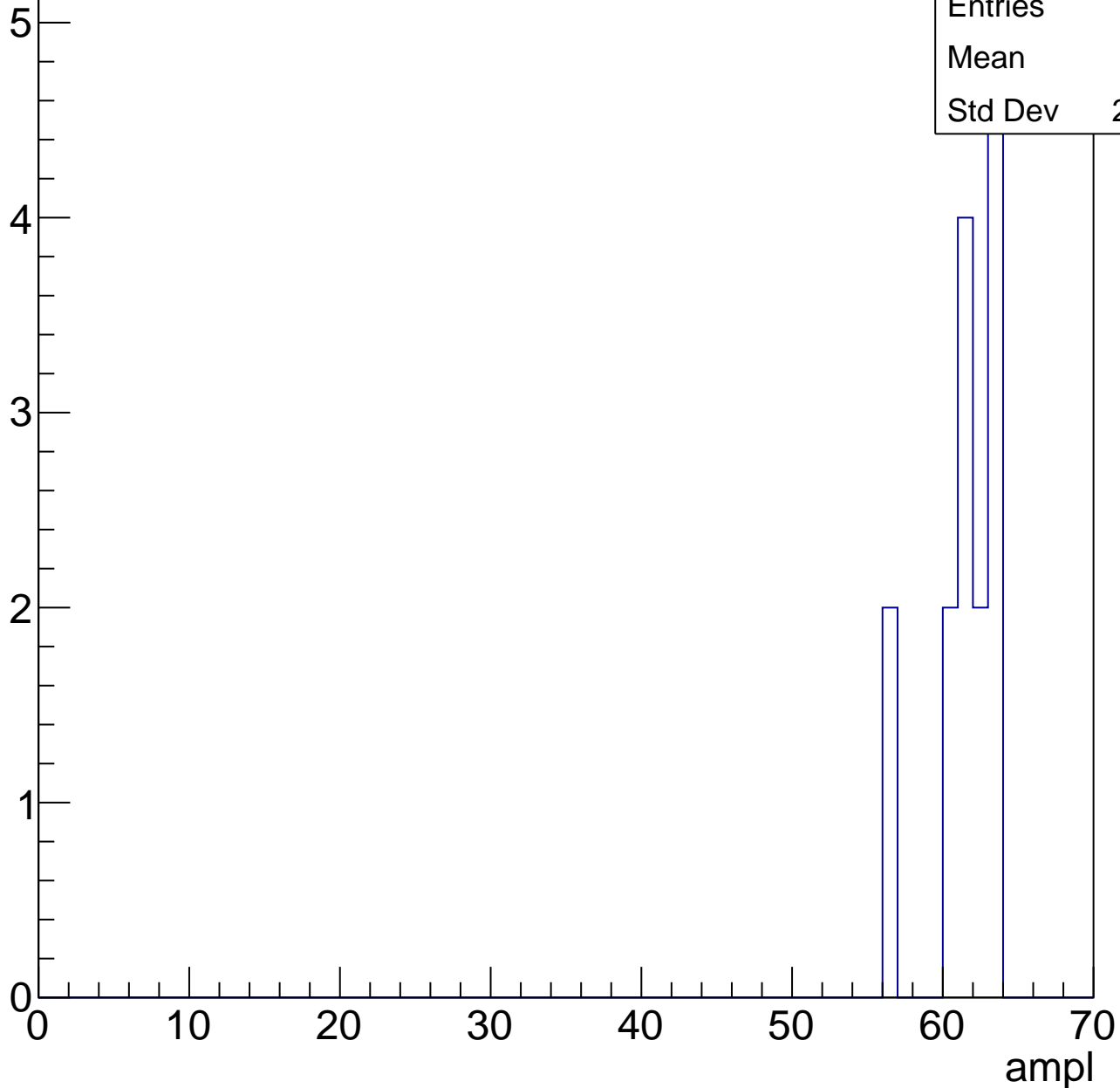


B1L103S, U14-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61
Std Dev	2.221



B1L103S, U14-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L103S, U14-ch64, adc0

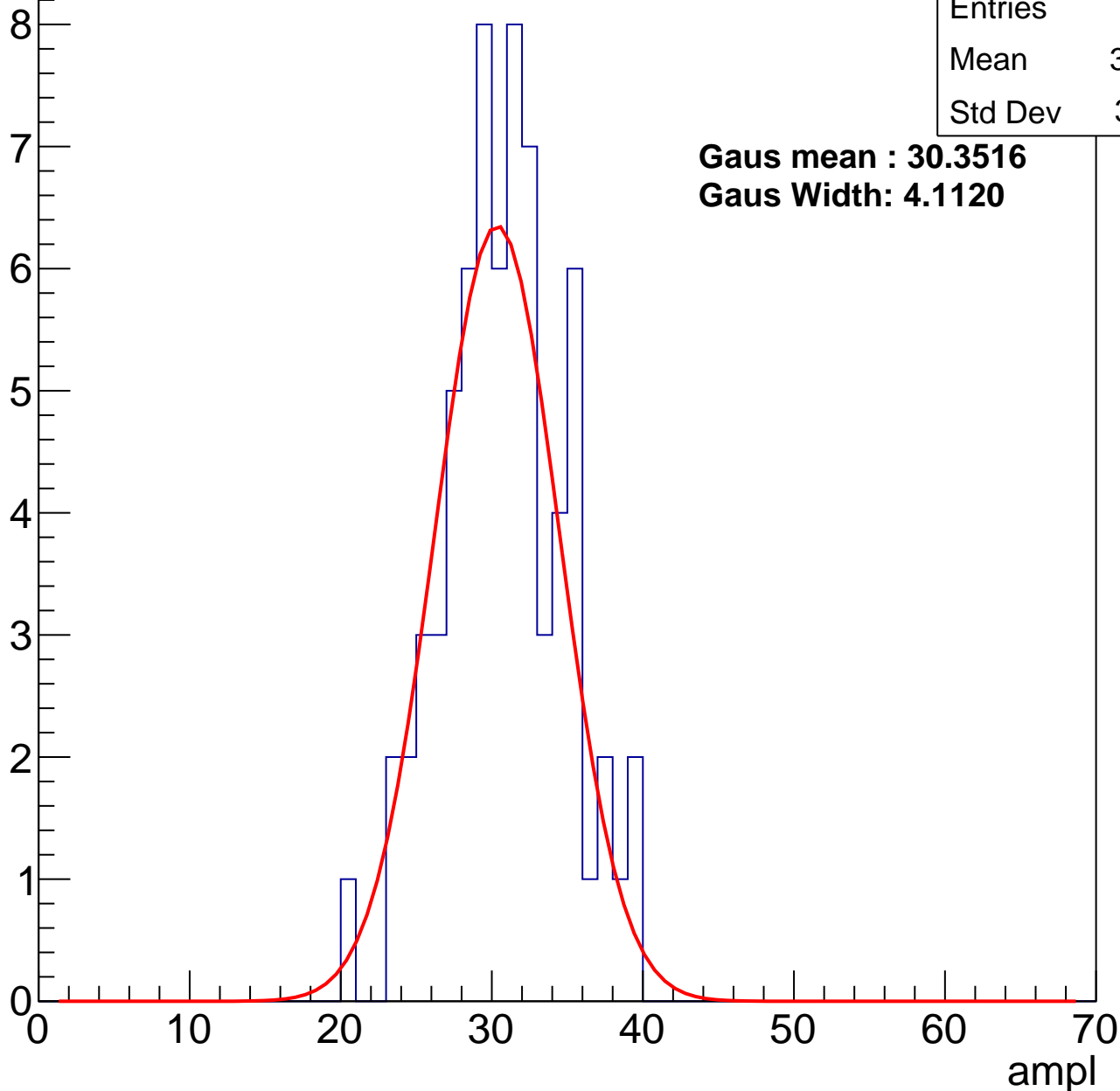
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	30.36
Std Dev	3.961

Gaus mean : 30.3516

Gaus Width: 4.1120



B1L103S, U14-ch64, adc1

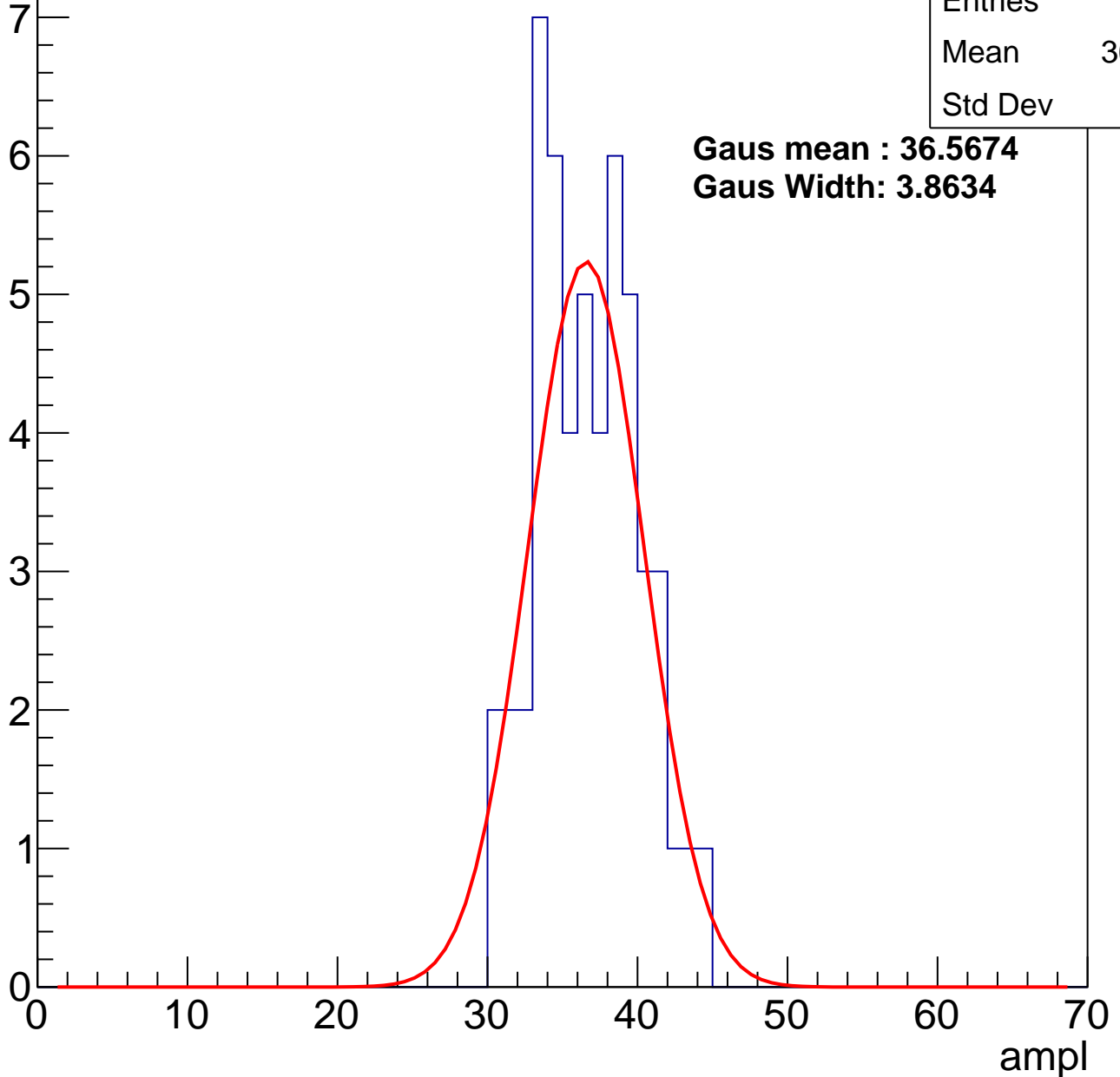
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	36.23
Std Dev	3.36

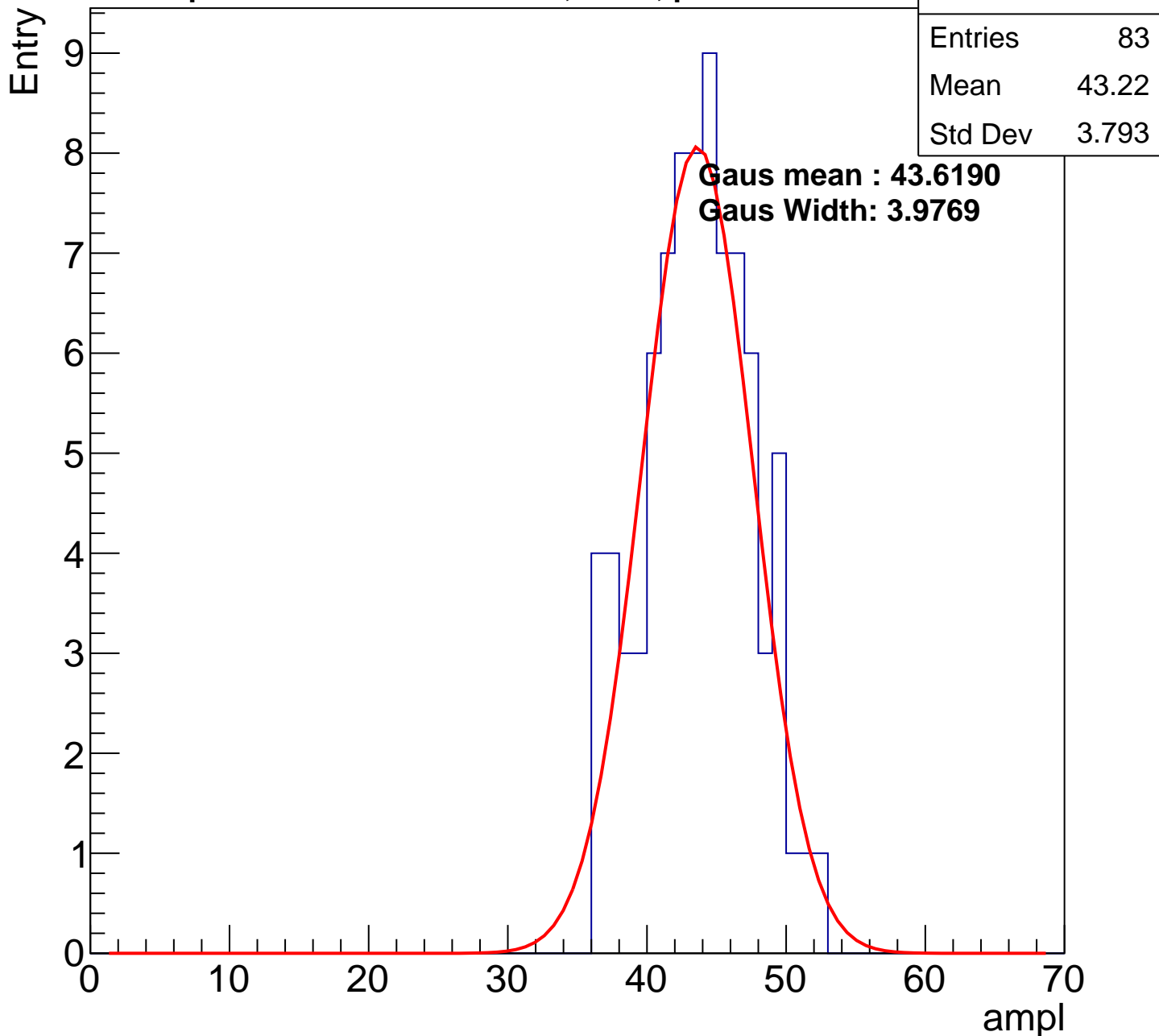
Gaus mean : 36.5674

Gaus Width: 3.8634



B1L103S, U14-ch64, adc2

calib_packv5_042523_0143.root, FC#7, port C2

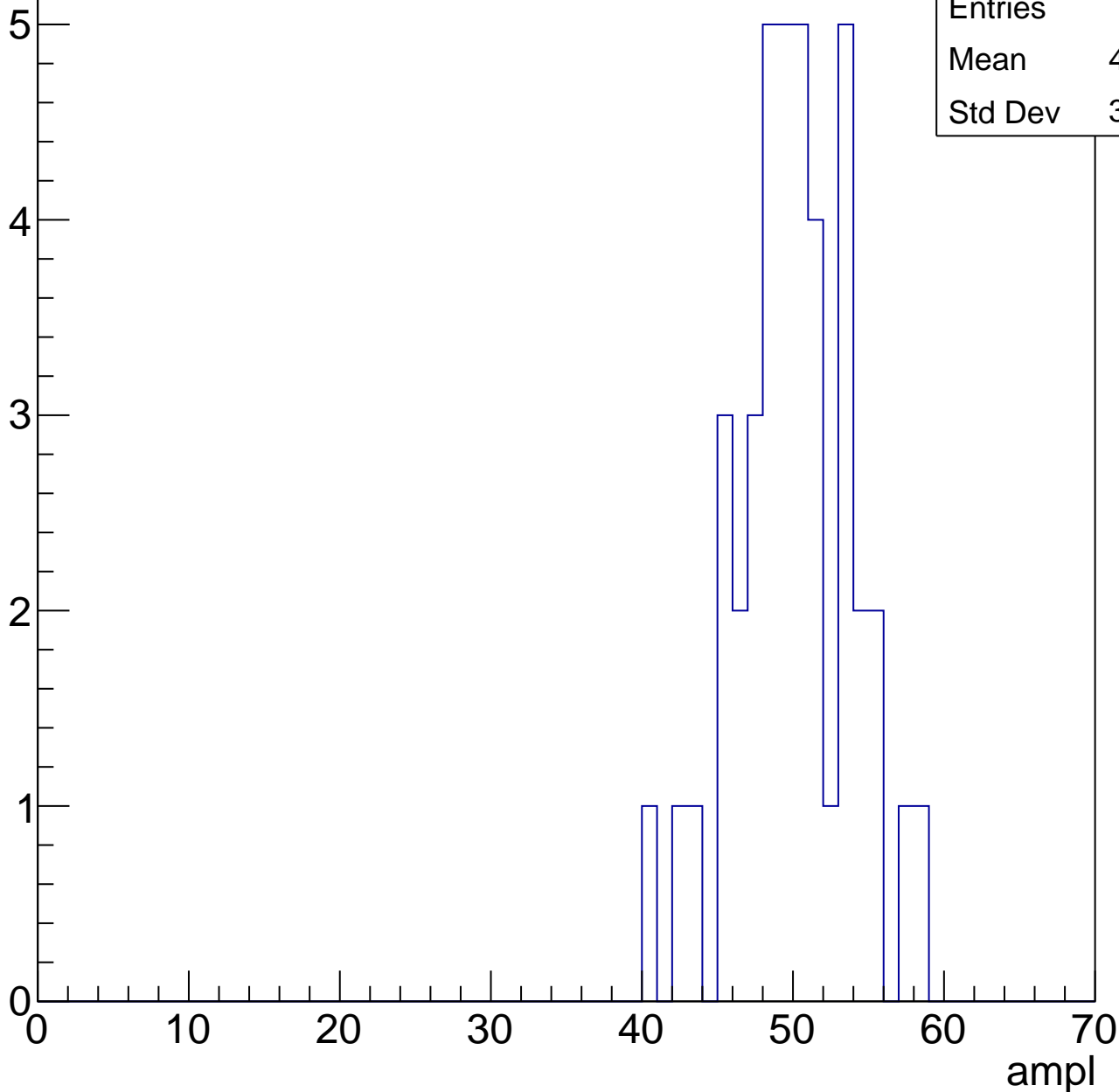


B1L103S, U14-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	49.57
Std Dev	3.818

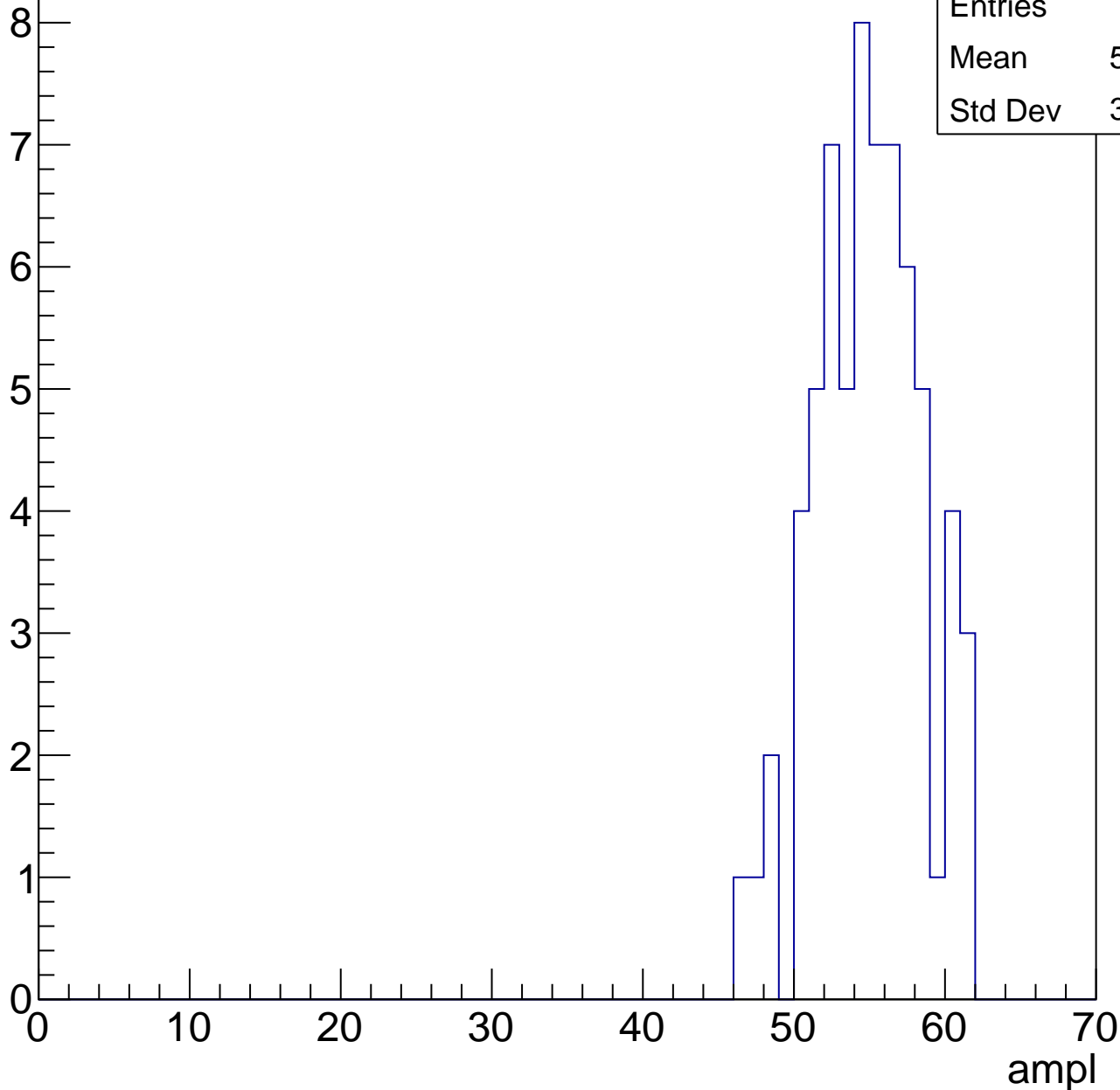


B1L103S, U14-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	54.48
Std Dev	3.474

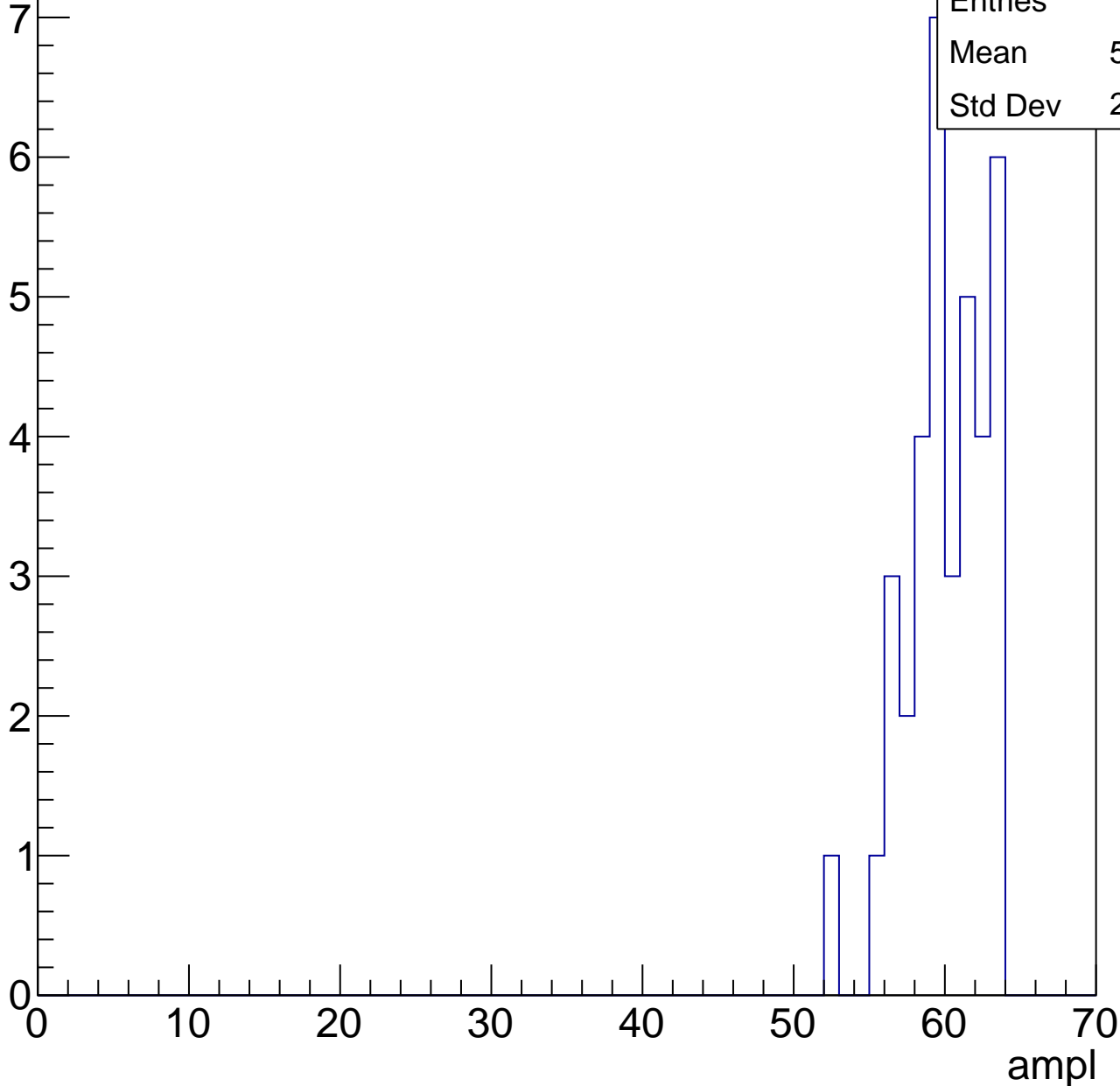


B1L103S, U14-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	59.58
Std Dev	2.618

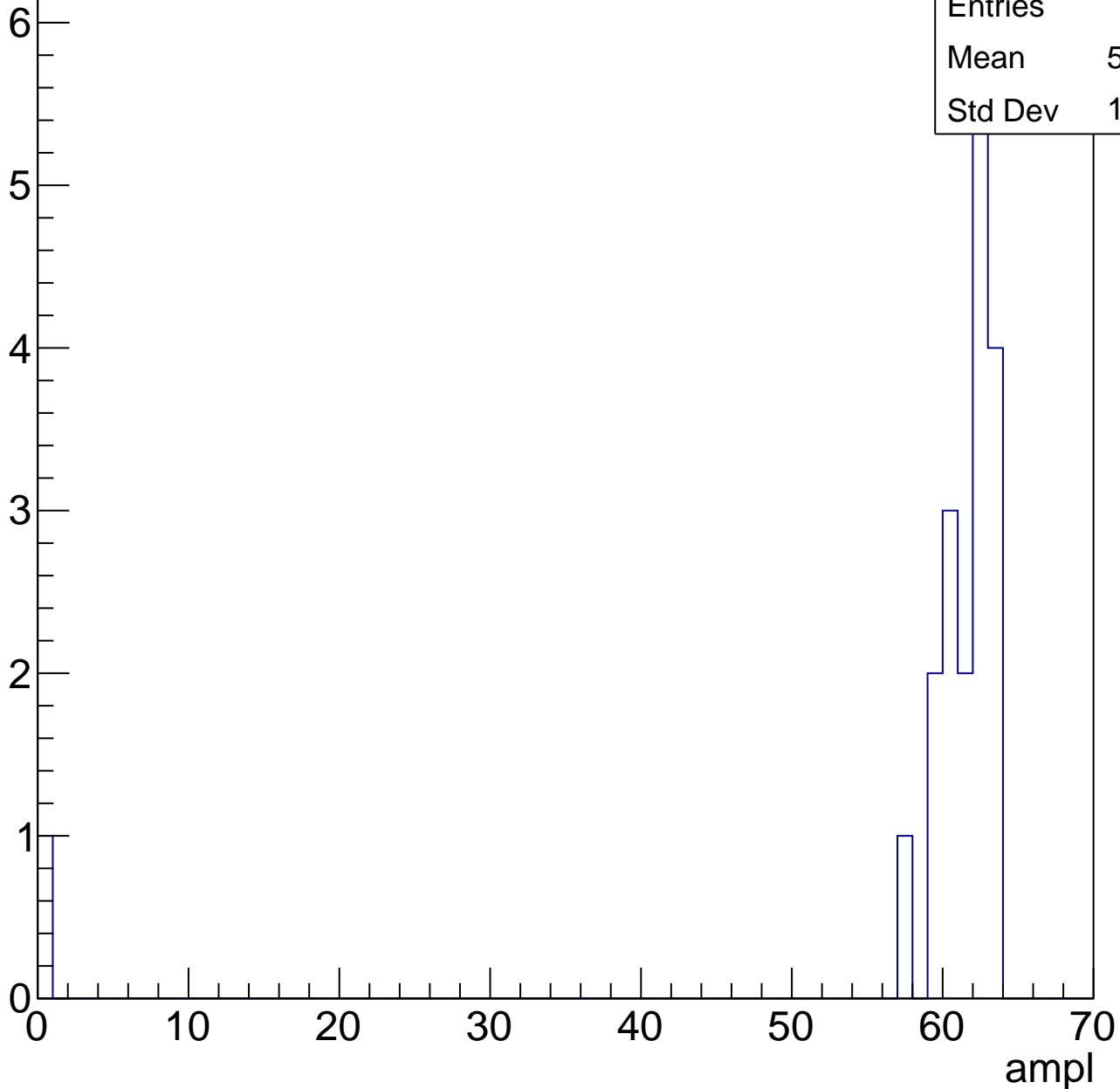


B1L103S, U14-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	57.95
Std Dev	13.75



B1L103S, U14-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch65, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	28.29
Std Dev	6.656

Gaus mean : 29.6227

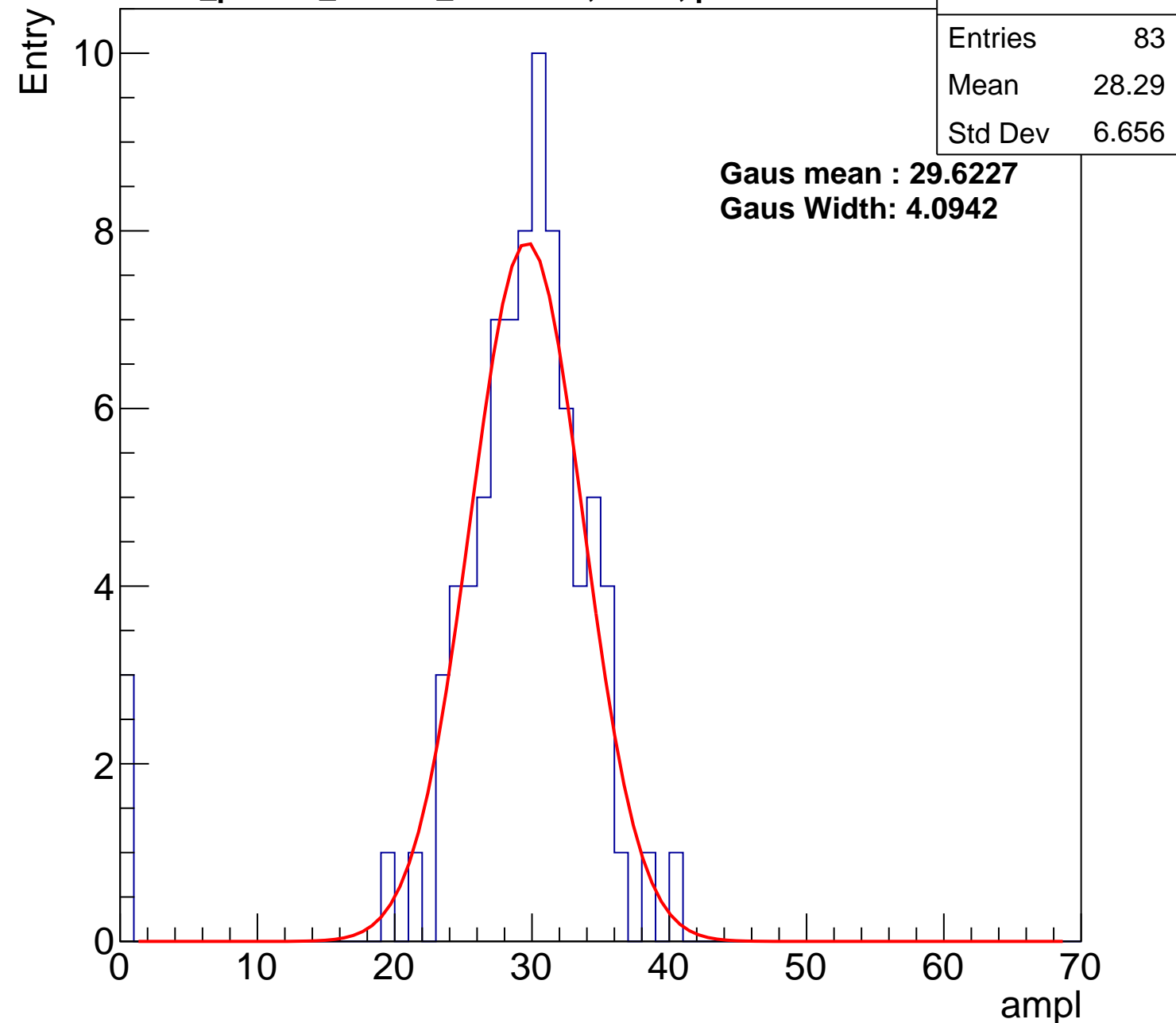
Gaus Width: 4.0942

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch65, adc1

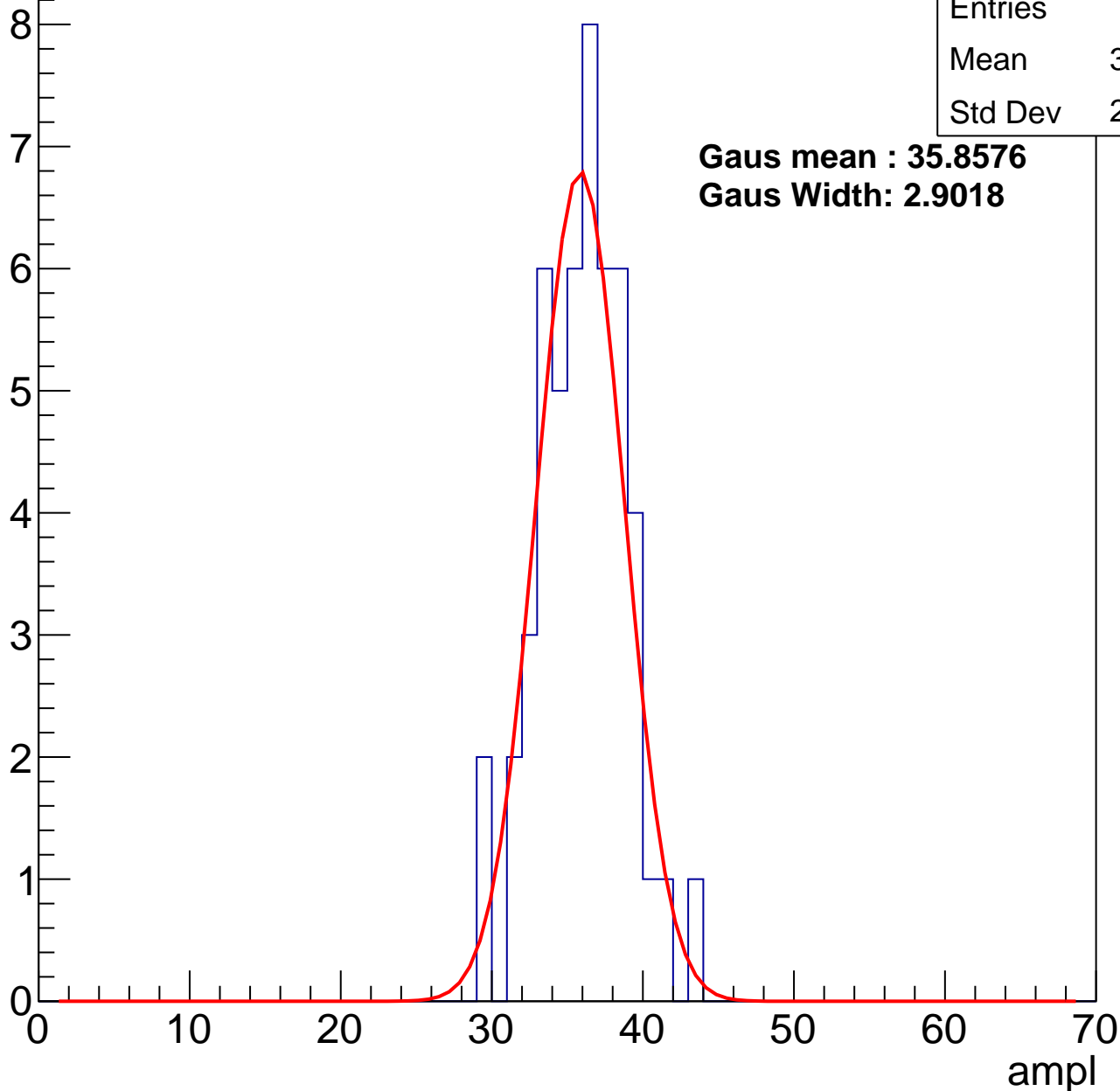
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	35.53
Std Dev	2.872

Gaus mean : 35.8576

Gaus Width: 2.9018



B1L103S, U14-ch65, adc2

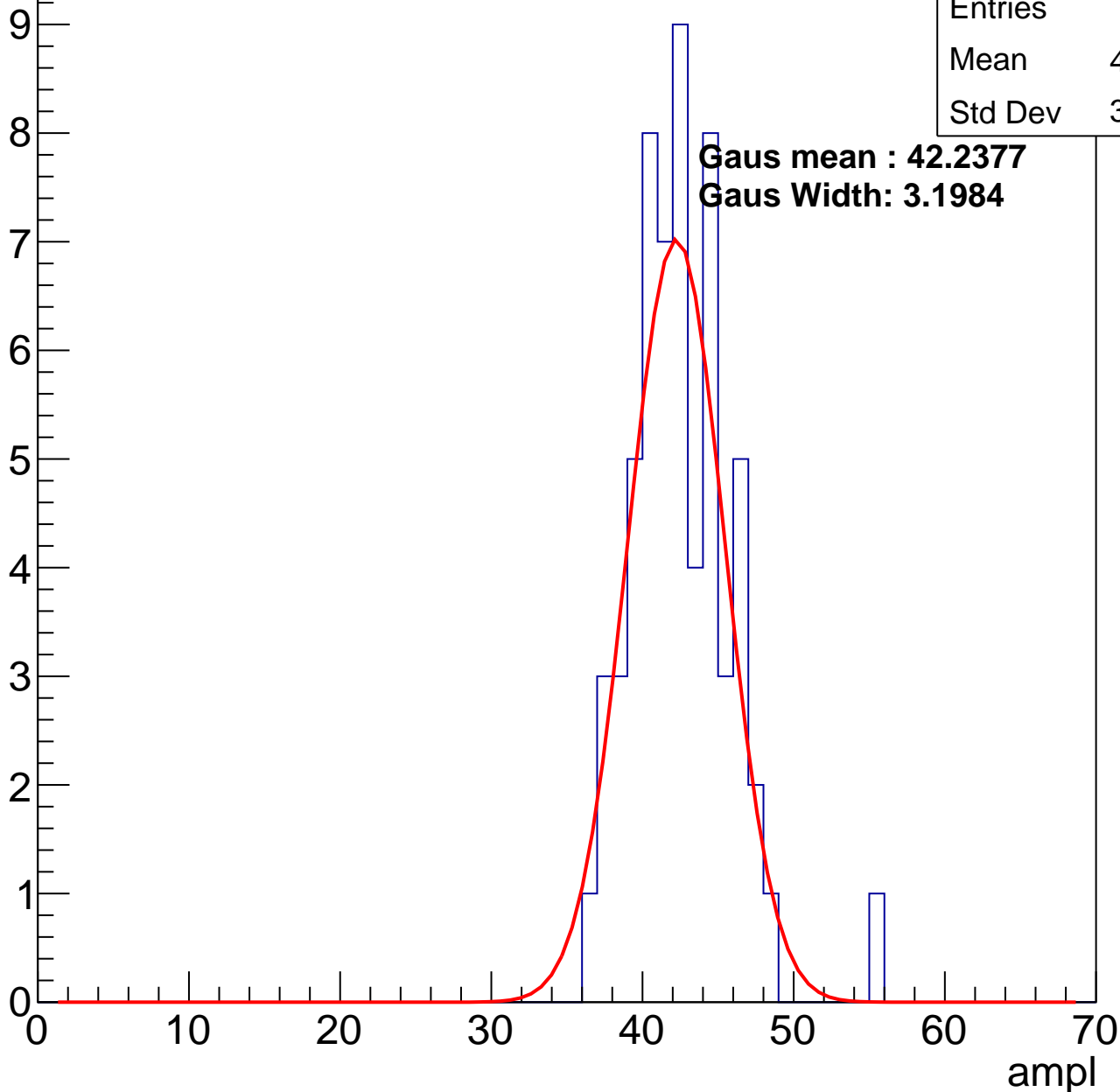
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.12
Std Dev	3.266

Gaus mean : 42.2377

Gaus Width: 3.1984

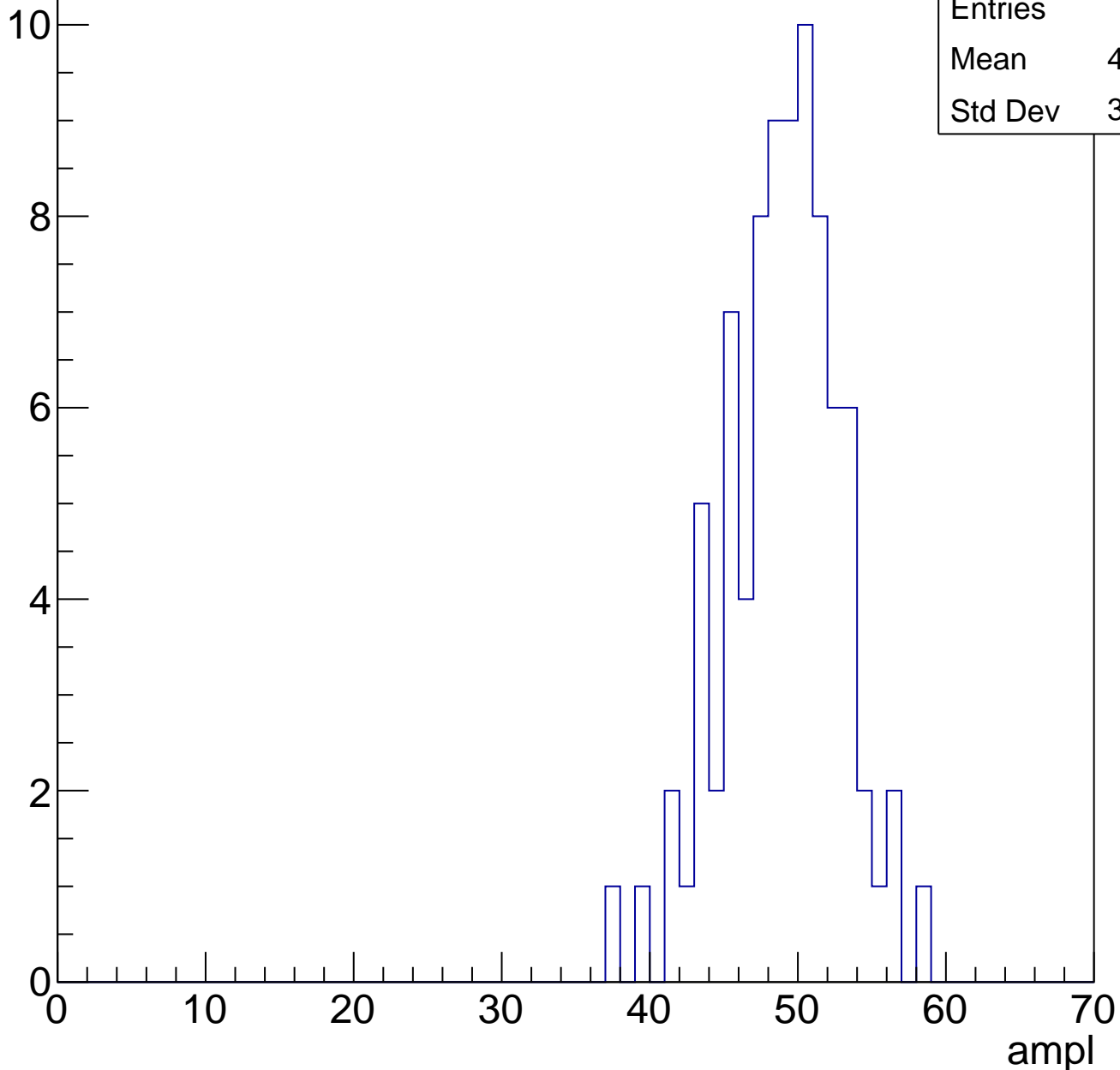


B1L103S, U14-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	85
Mean	48.49
Std Dev	3.873

Entry

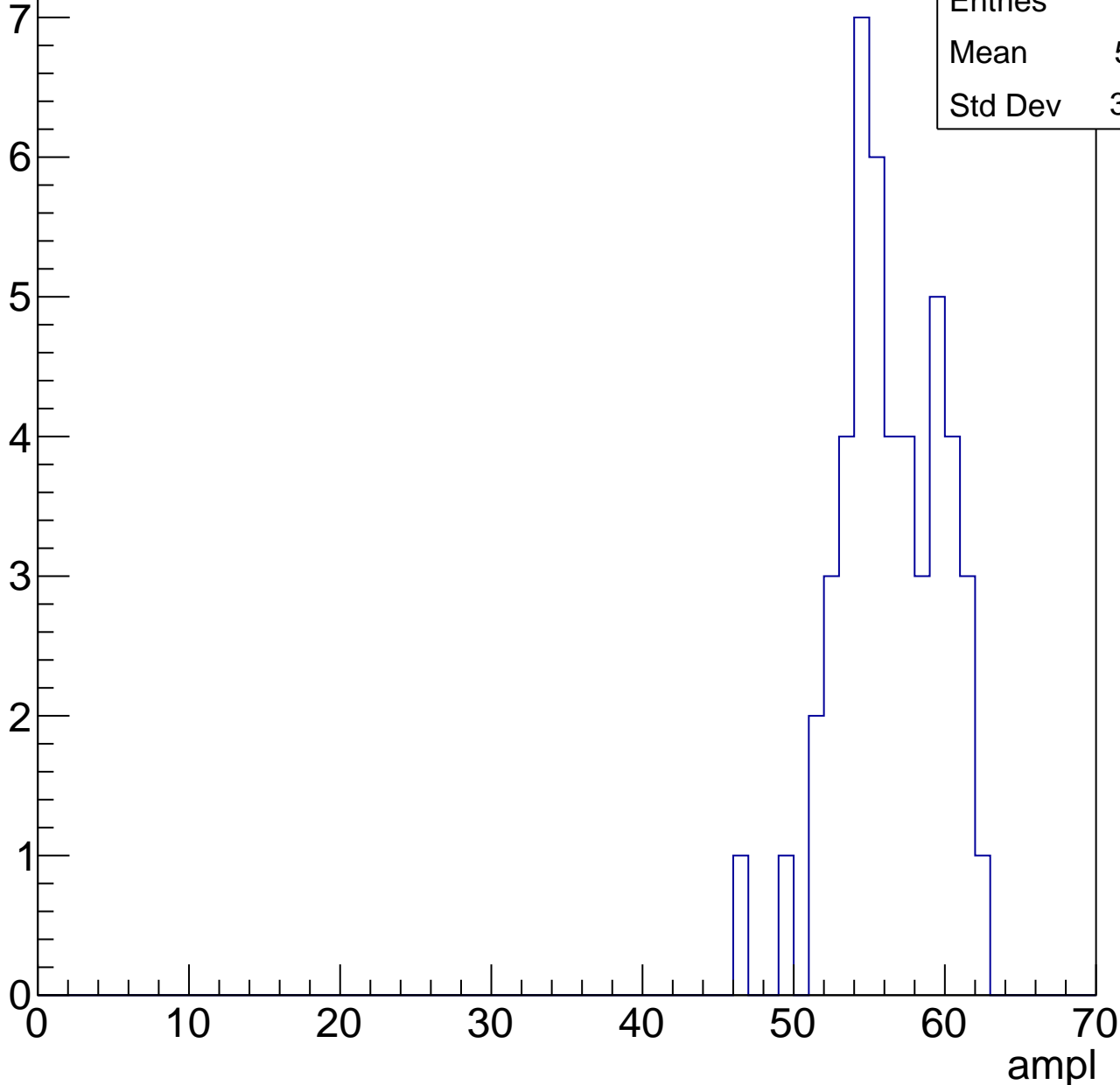


B1L103S, U14-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	55.81
Std Dev	3.389

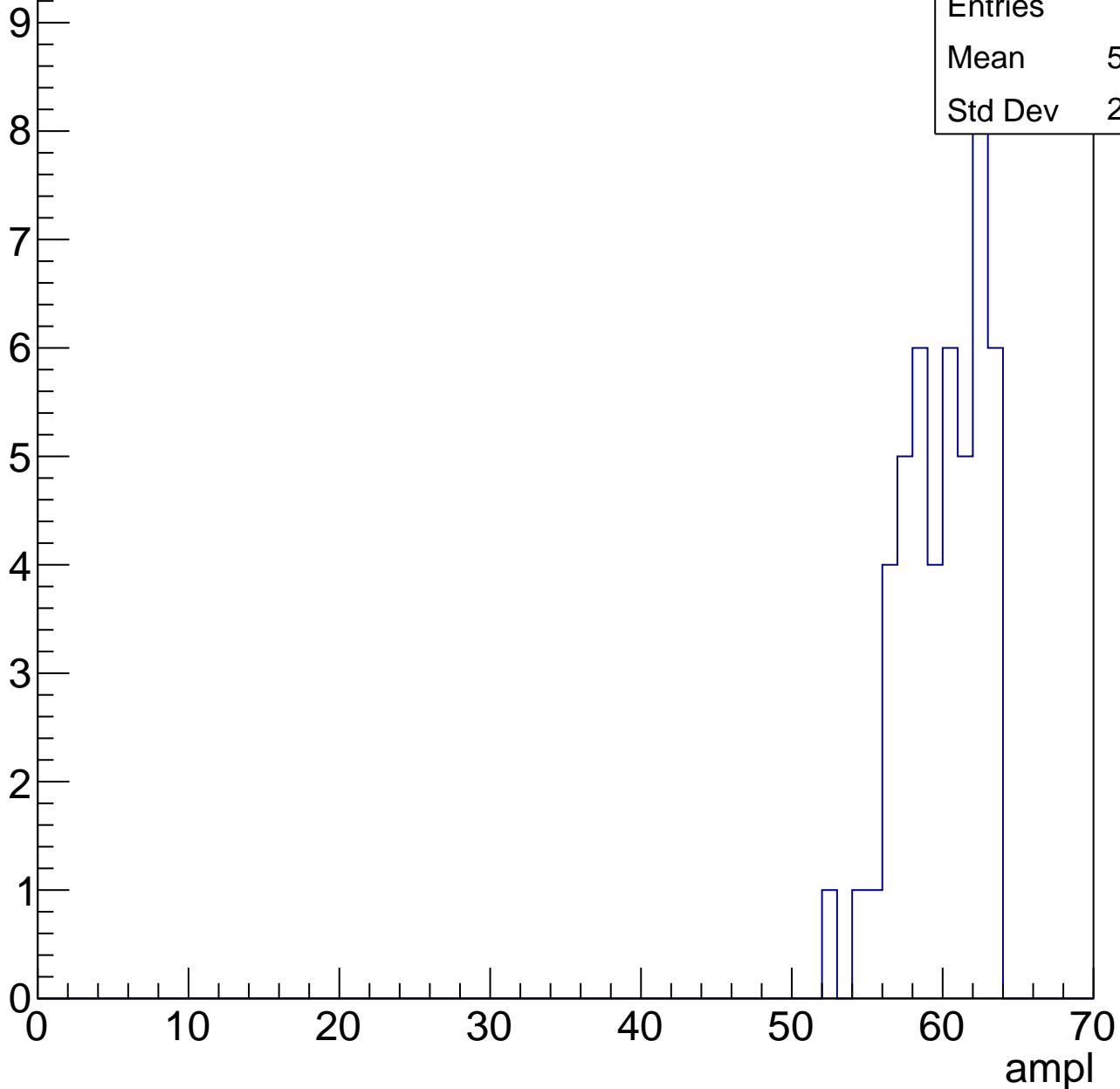


B1L103S, U14-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

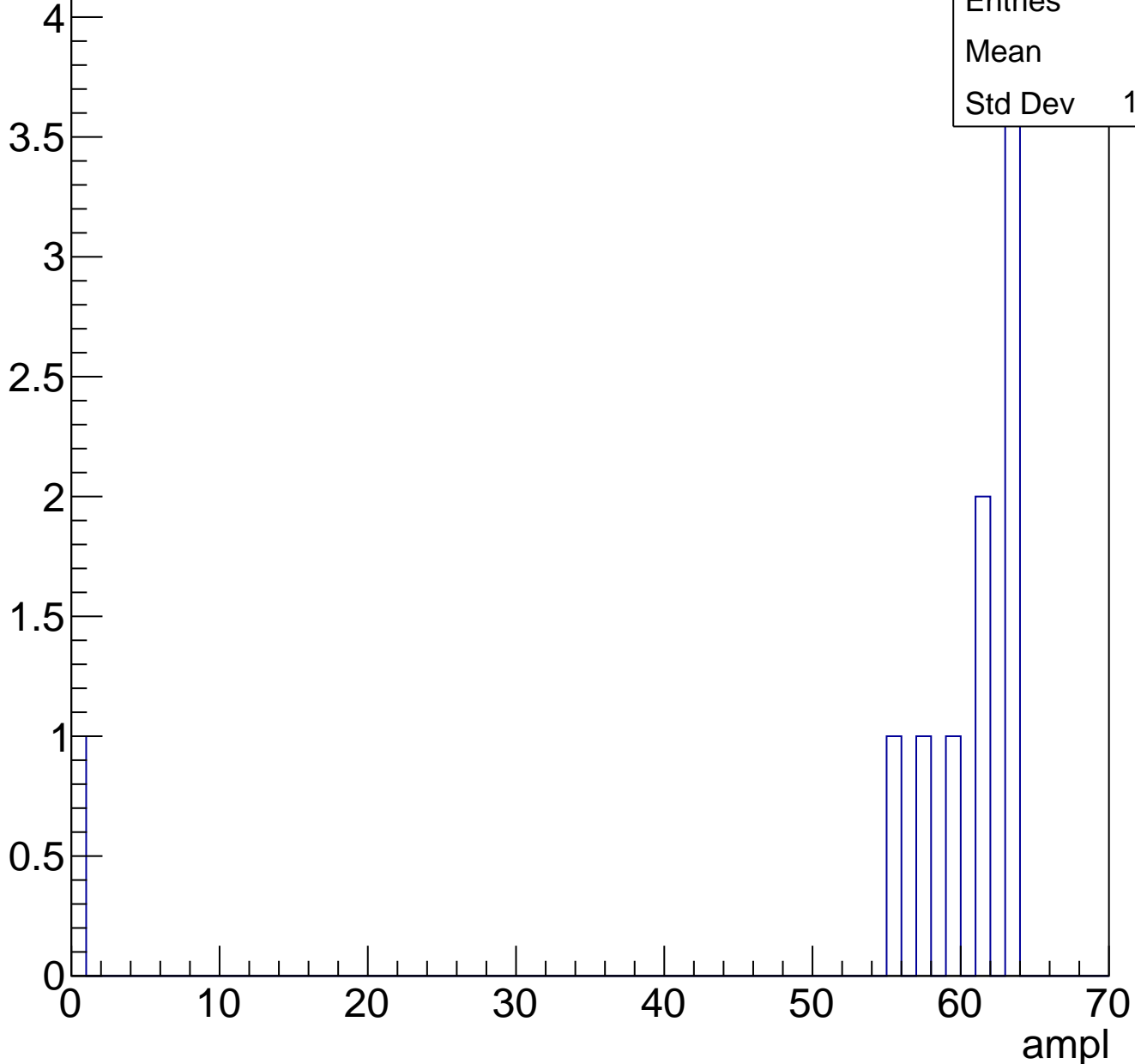
Entries	48
Mean	59.48
Std Dev	2.677



B1L103S, U14-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	54.5
Std Dev	18.36

B1L103S, U14-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch66, adc0

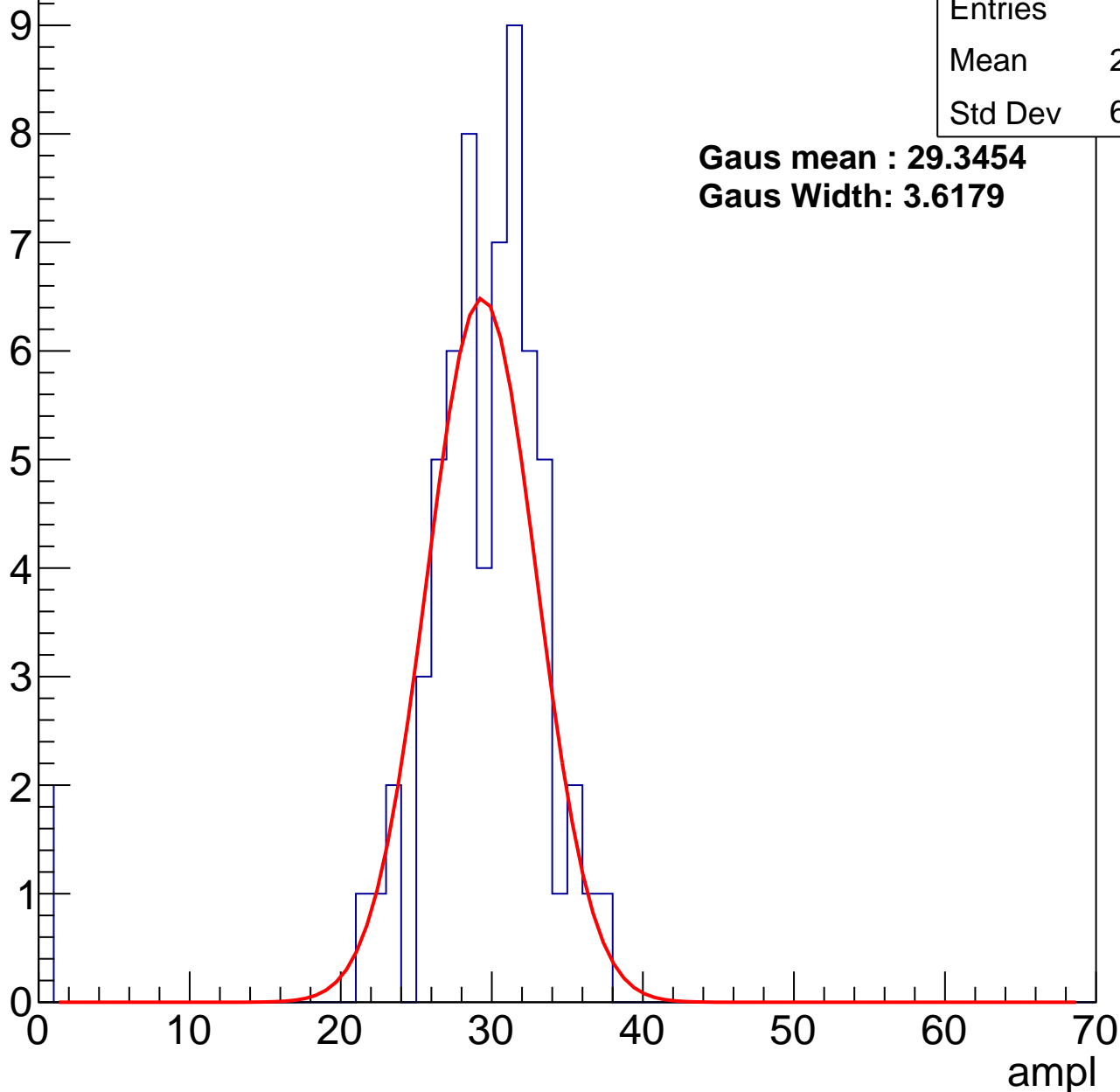
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.42
Std Dev	6.069

Gaus mean : 29.3454

Gaus Width: 3.6179



B1L103S, U14-ch66, adc1

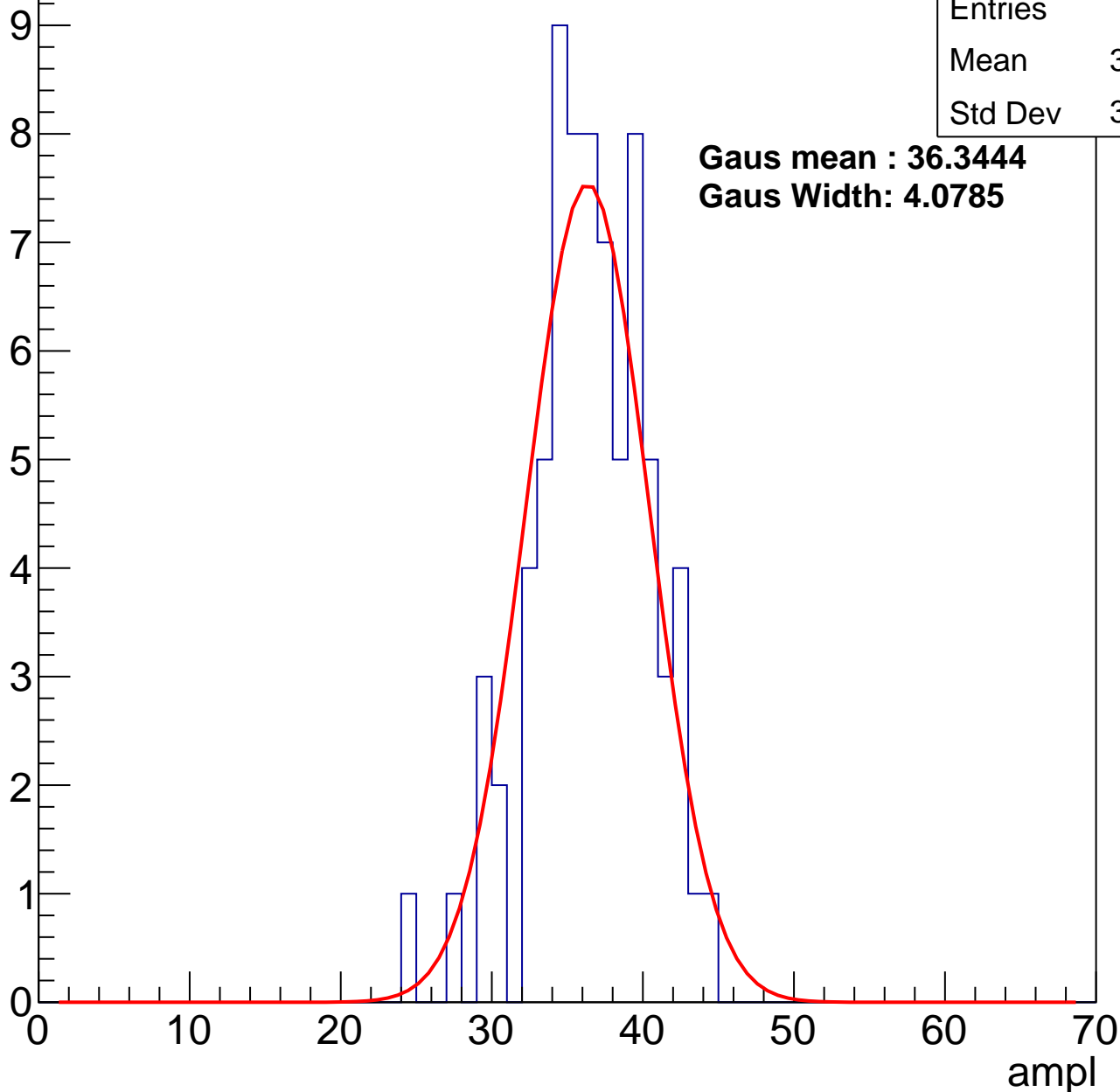
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.05
Std Dev	3.843

Gaus mean : 36.3444

Gaus Width: 4.0785



B1L103S, U14-ch66, adc2

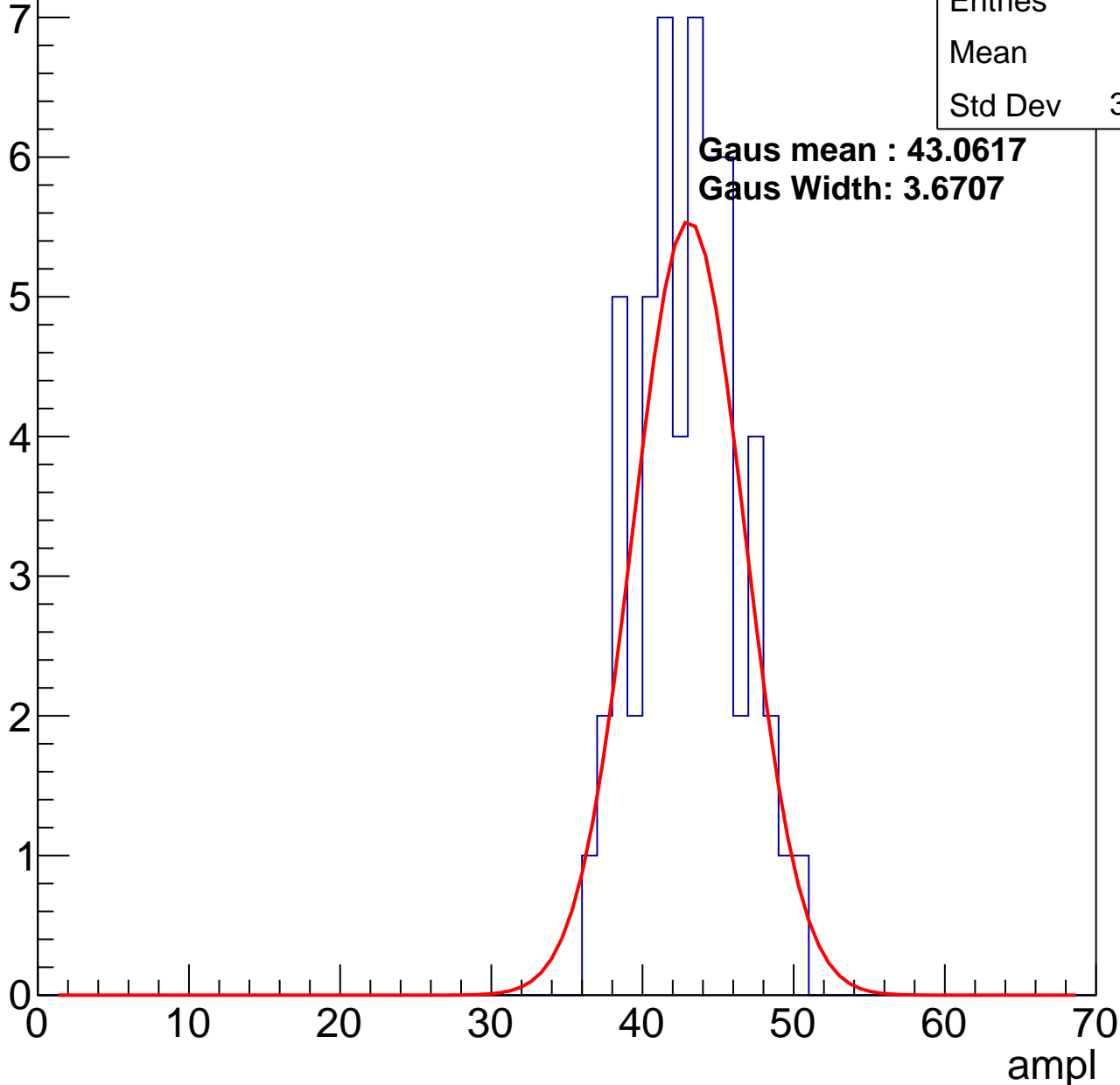
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.6
Std Dev	3.284

Gaus mean : 43.0617

Gaus Width: 3.6707

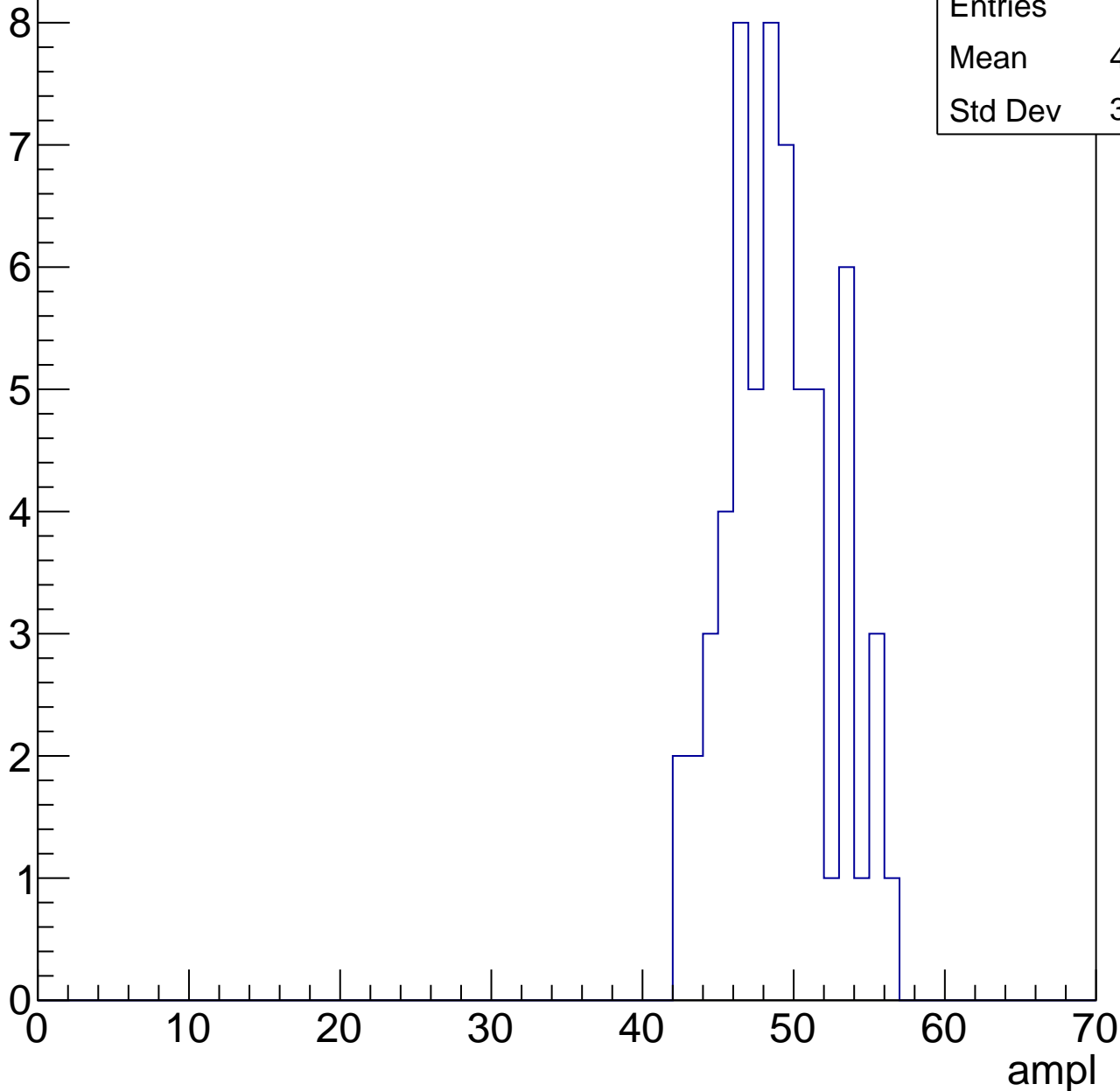


B1L103S, U14-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

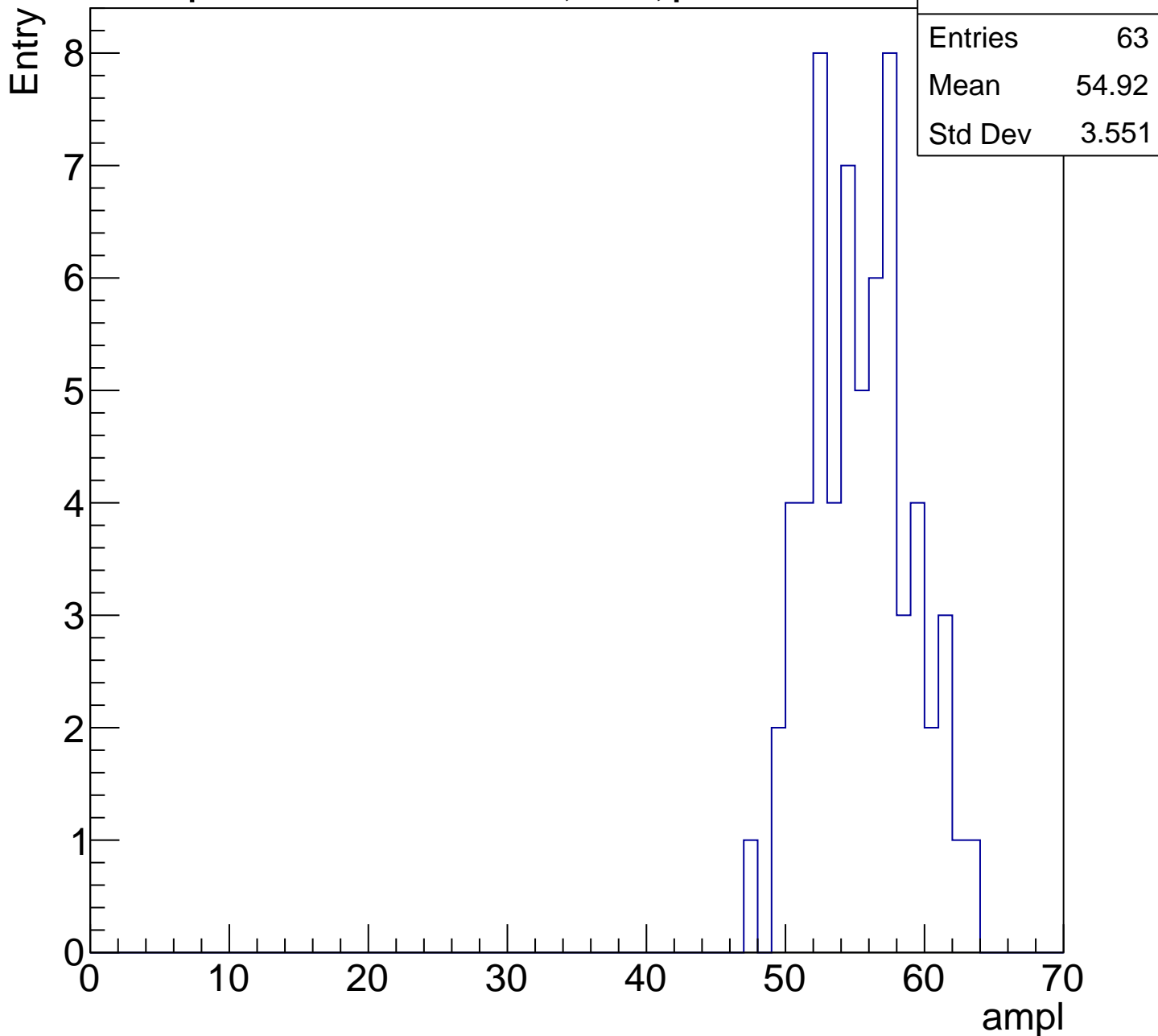
Entry

Entries	61
Mean	48.56
Std Dev	3.409



B1L103S, U14-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2

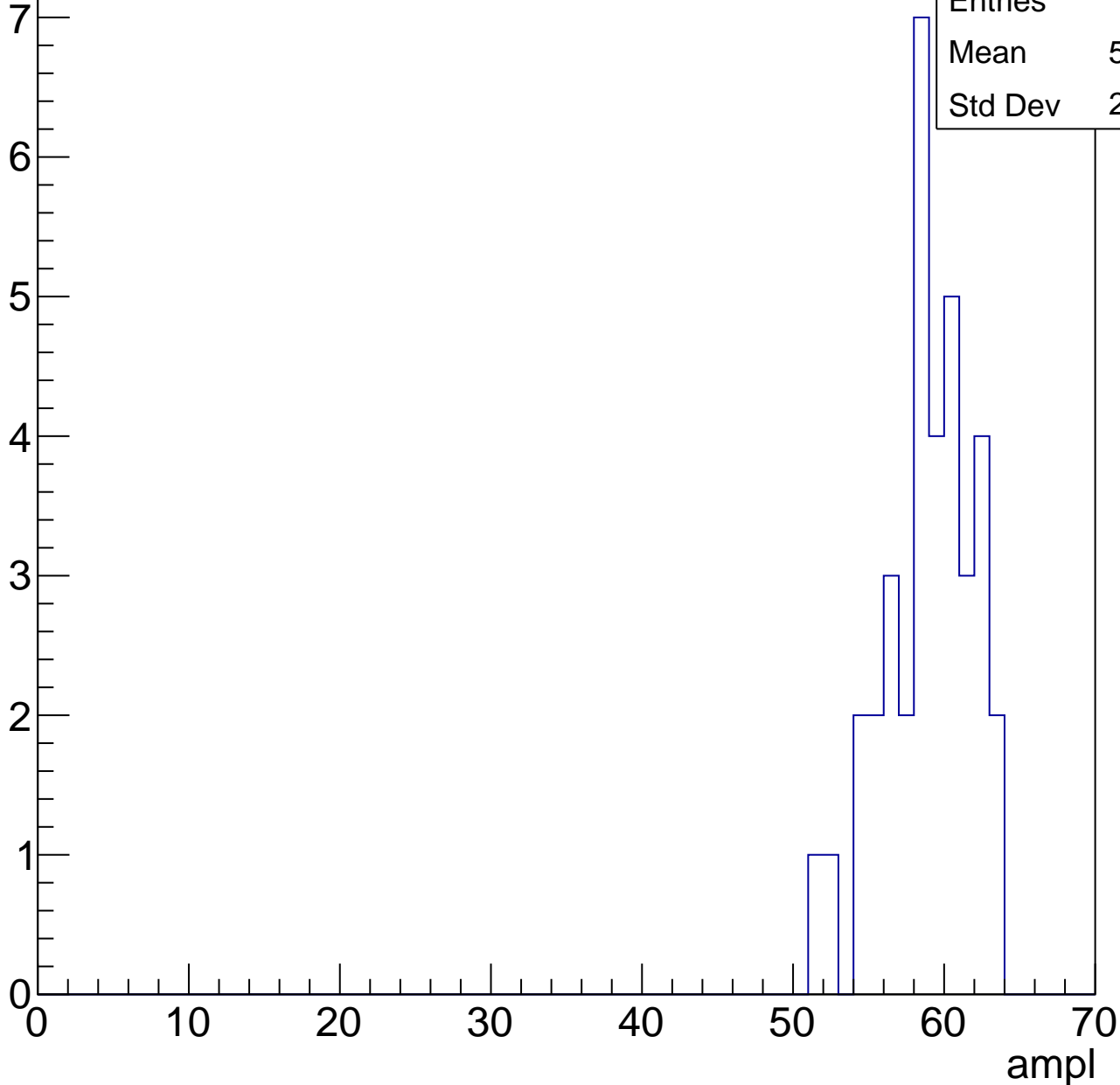


B1L103S, U14-ch66, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.39
Std Dev	2.928

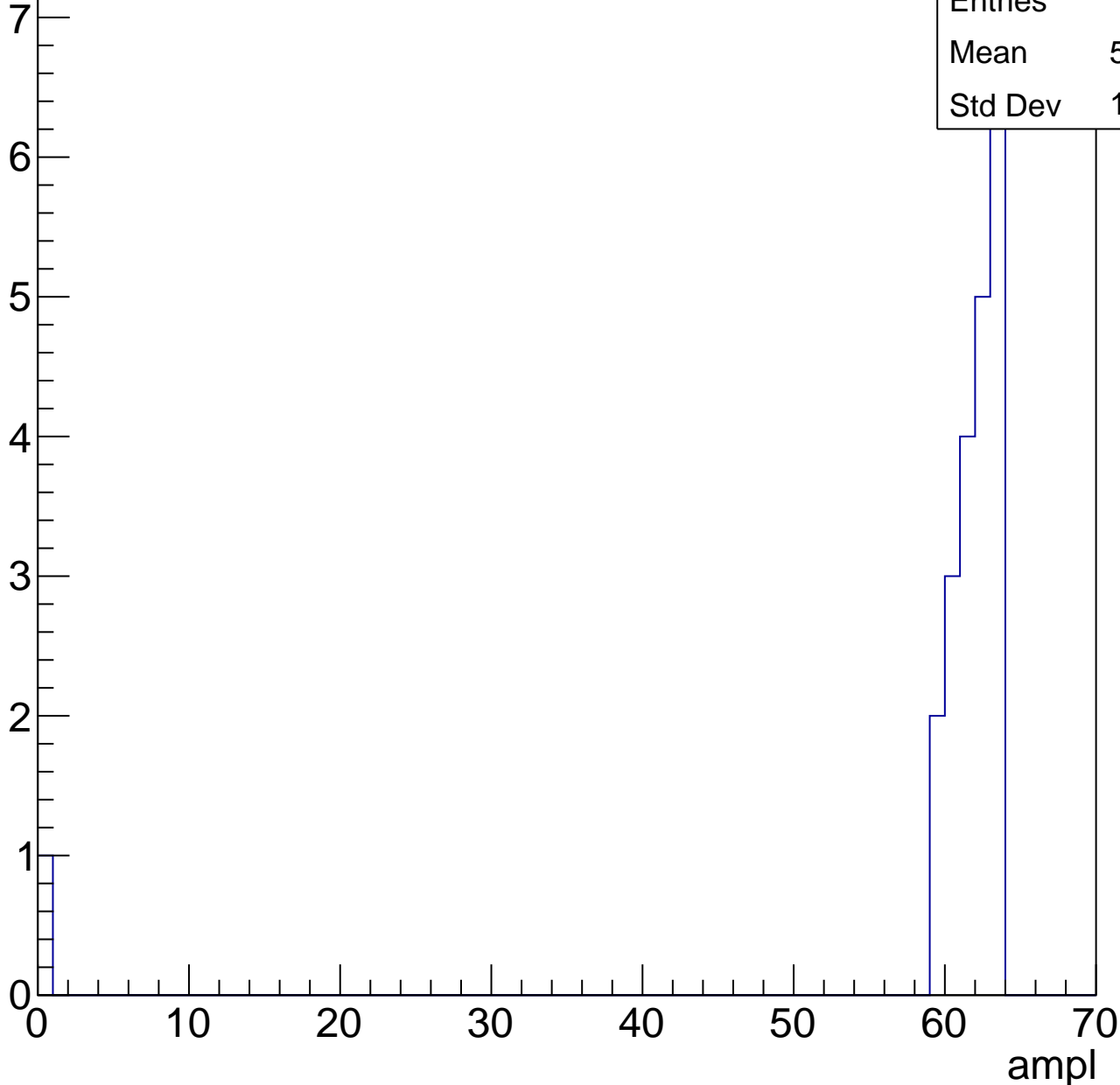


B1L103S, U14-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	22
Mean	58.77
Std Dev	12.89



B1L103S, U14-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch67, adc0

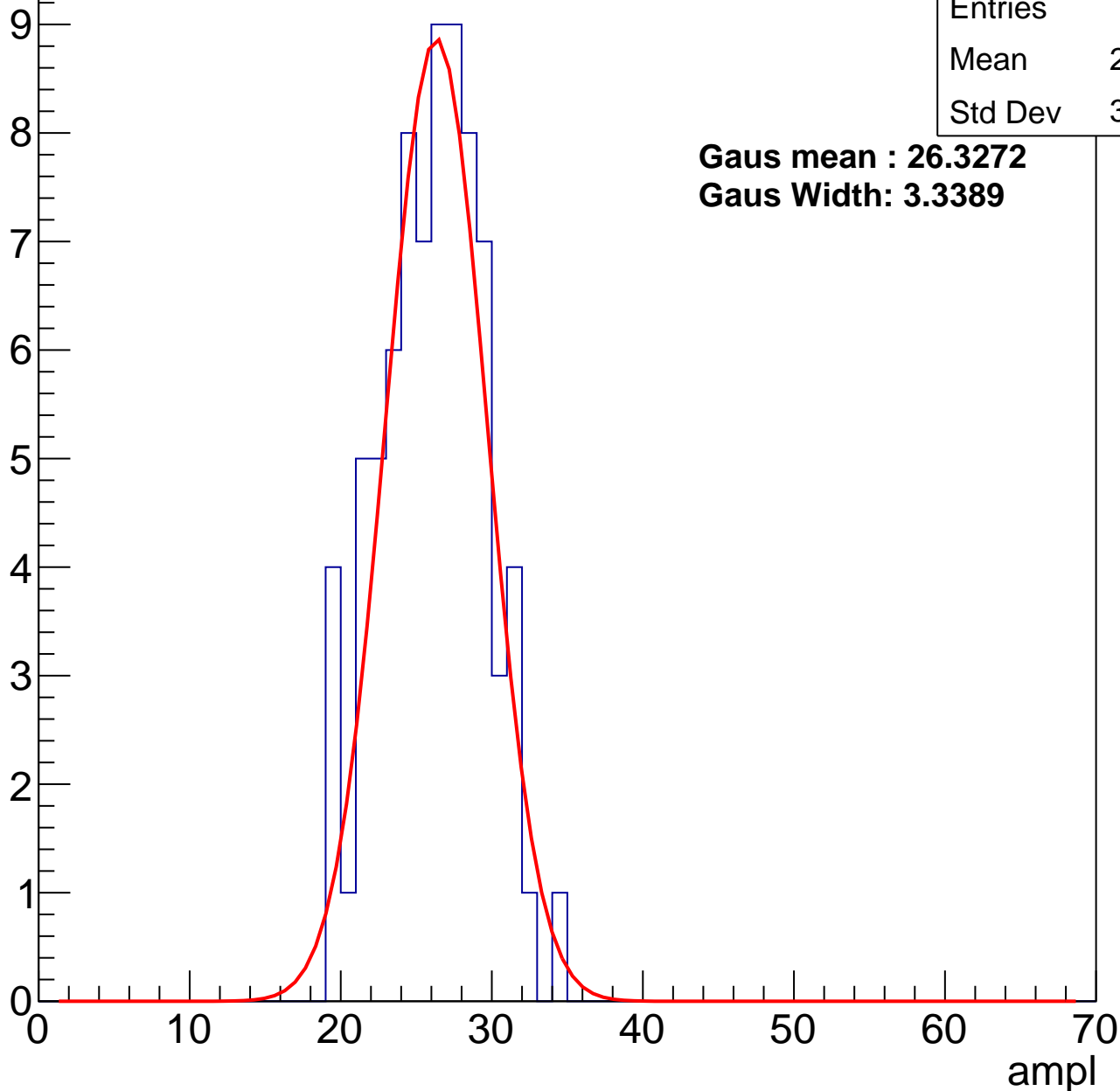
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	25.64
Std Dev	3.343

Gaus mean : 26.3272

Gaus Width: 3.3389



B1L103S, U14-ch67, adc1

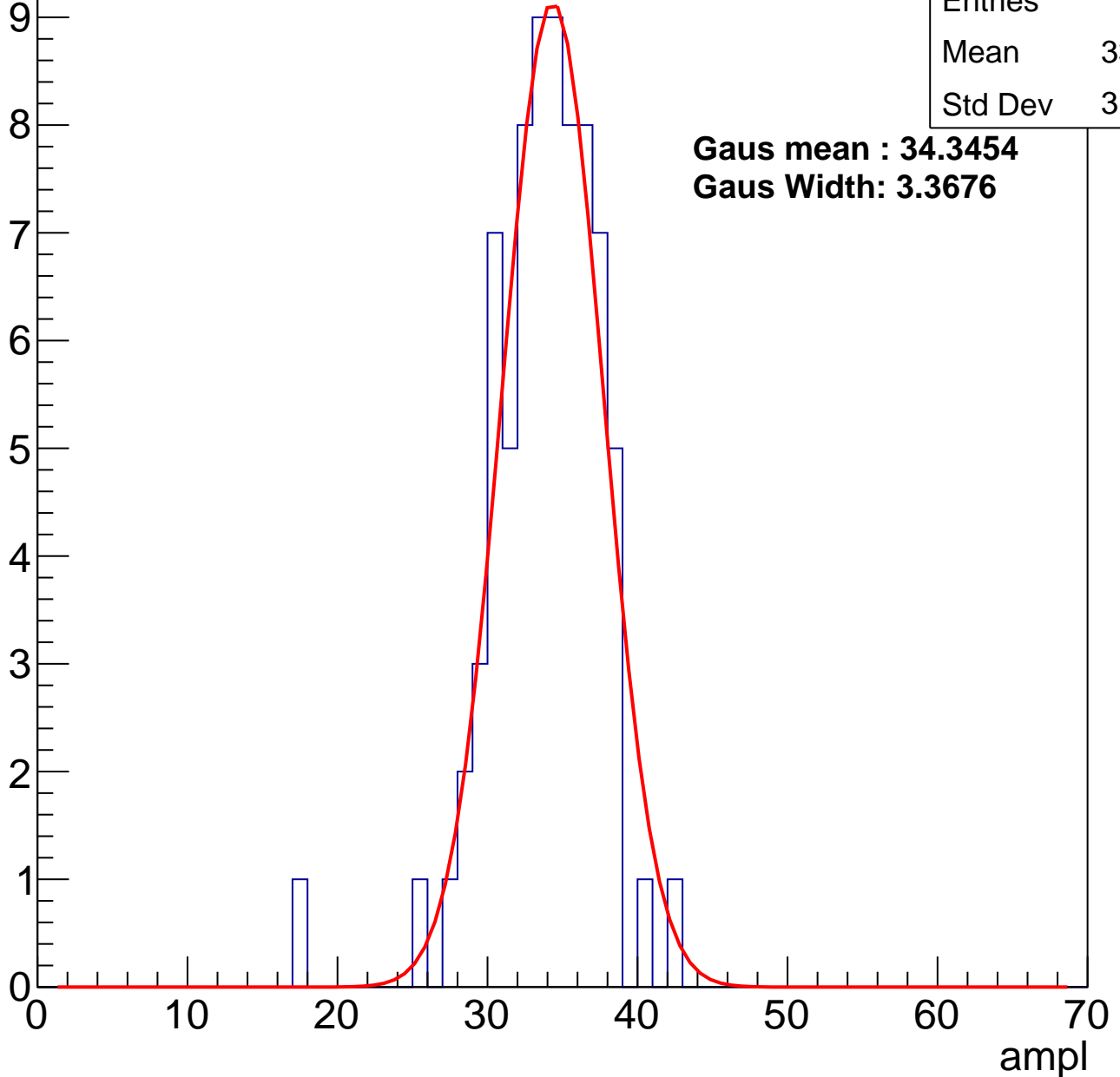
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	33.36
Std Dev	3.655

Gaus mean : 34.3454

Gaus Width: 3.3676



B1L103S, U14-ch67, adc2

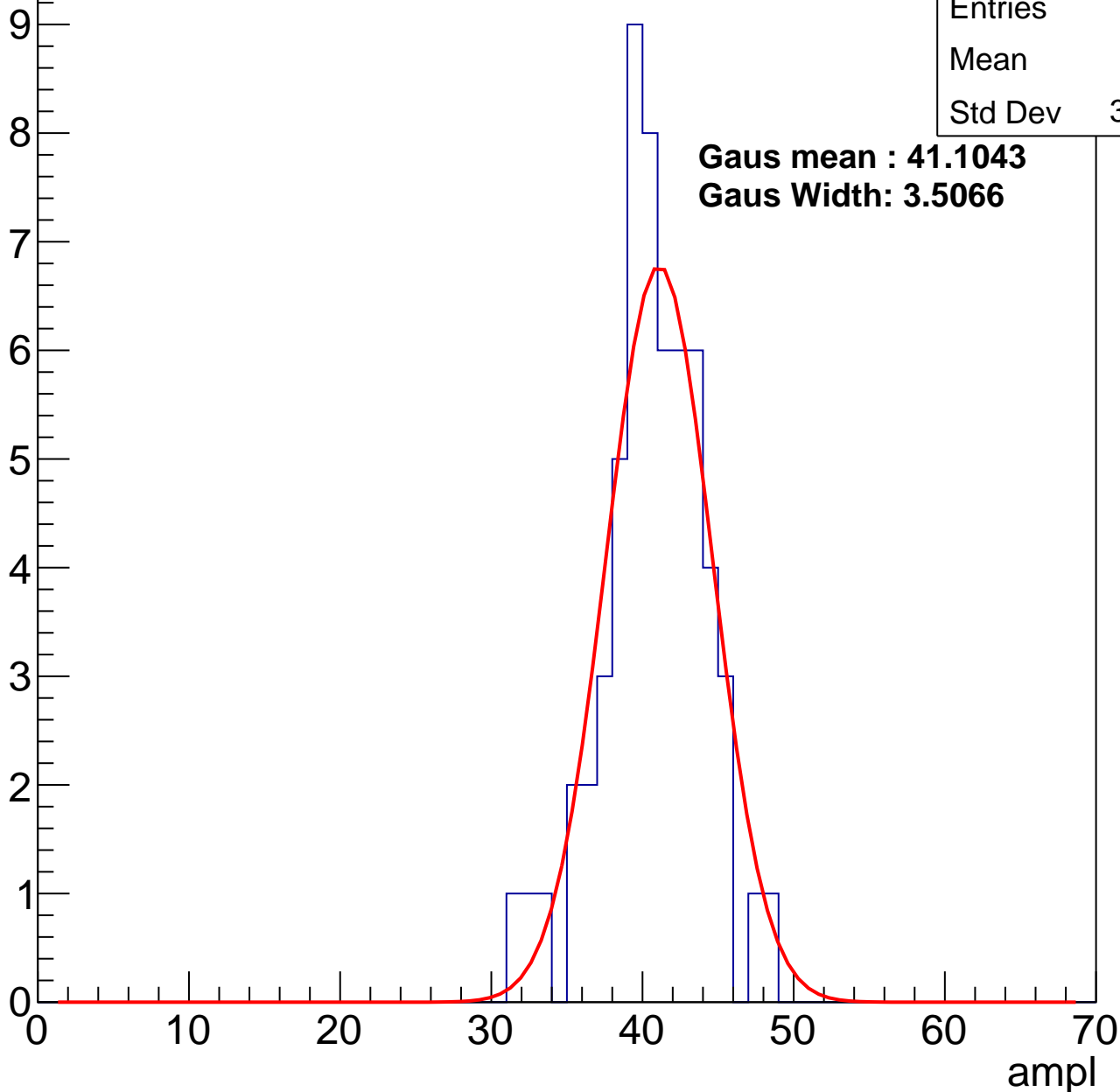
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	40.2
Std Dev	3.359

Gaus mean : 41.1043

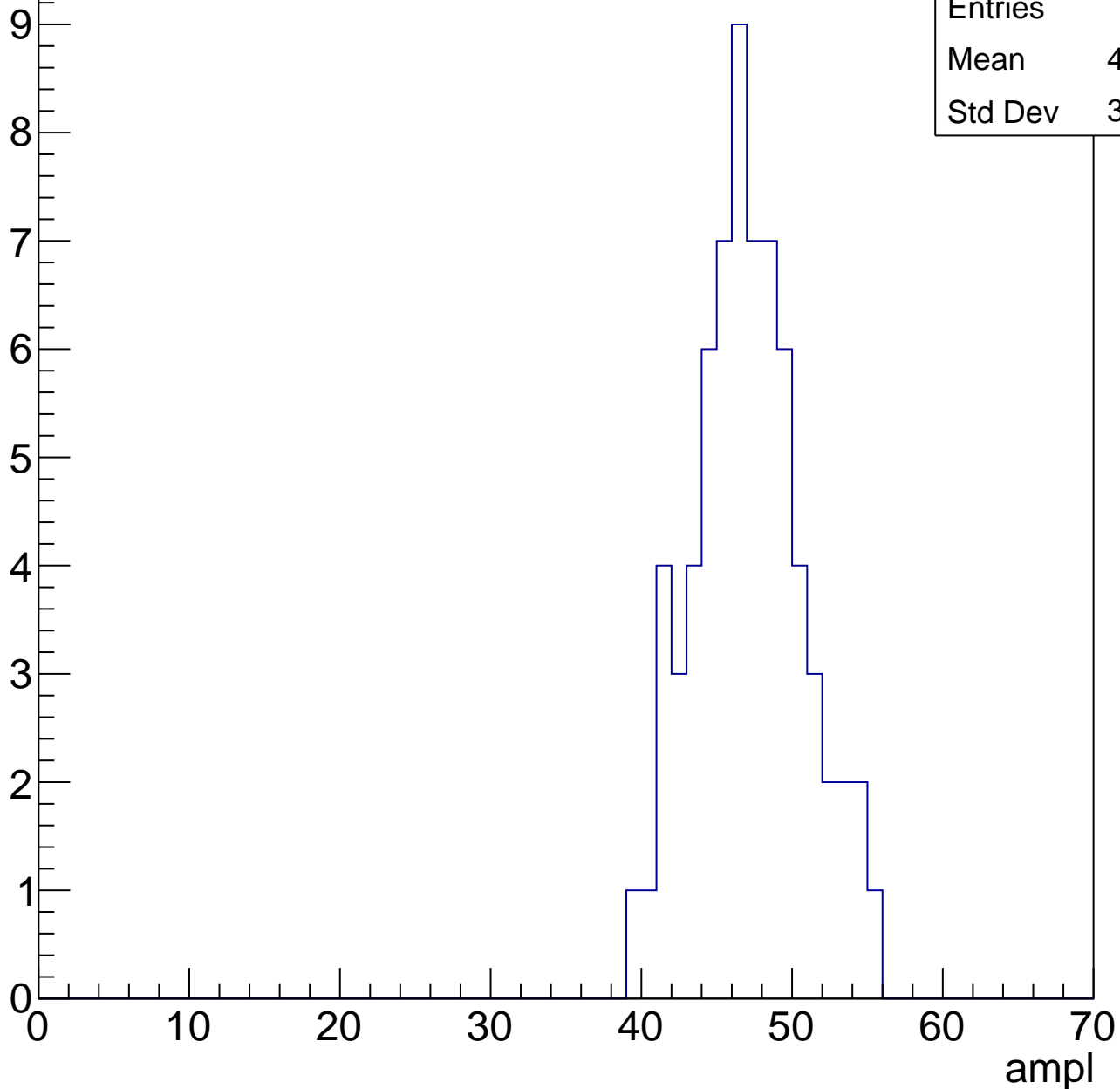
Gaus Width: 3.5066



B1L103S, U14-ch67, adc3

calib_packv5_042523_0143.root, FC#7, port C2

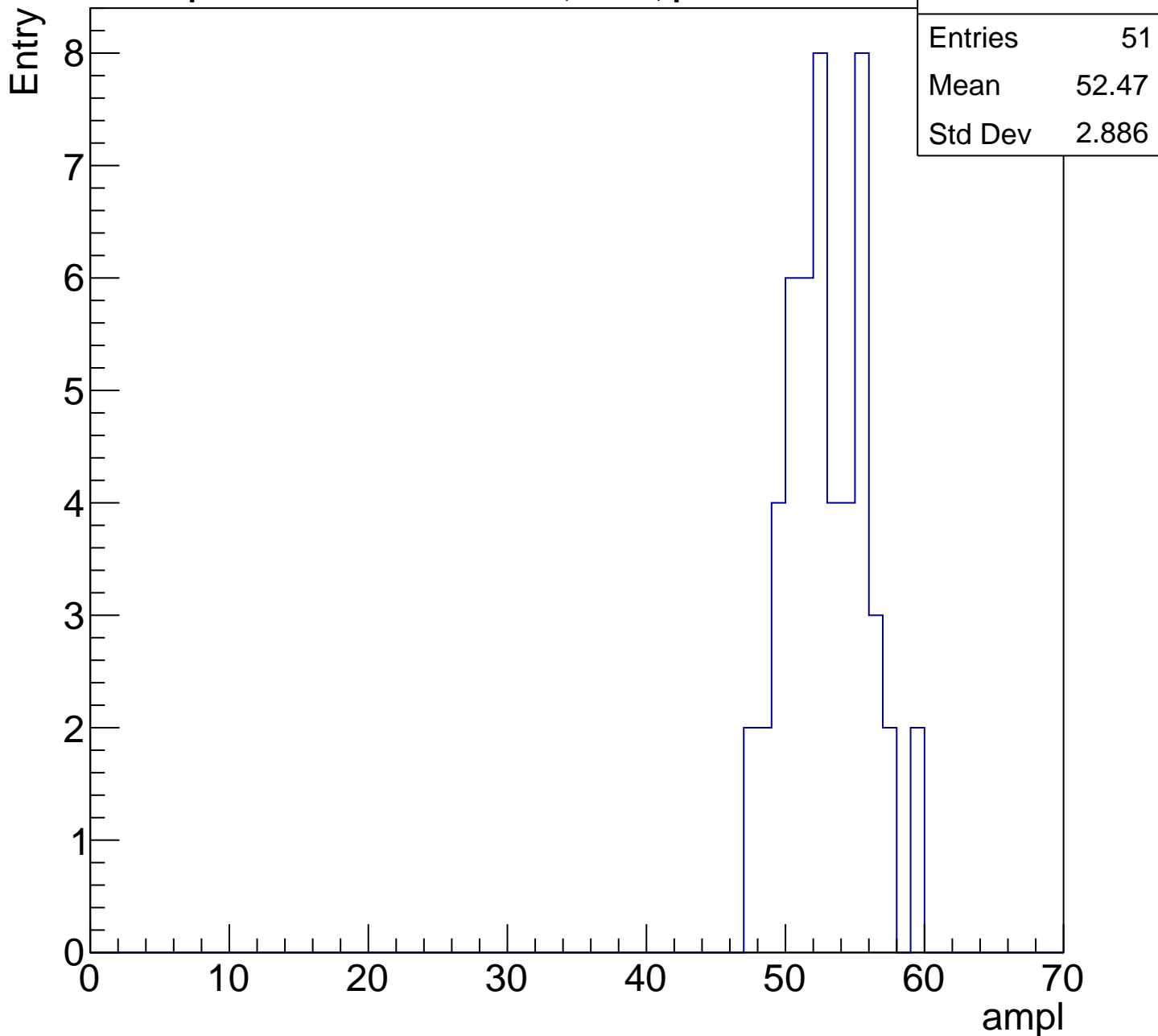
Entry



Entries	69
Mean	46.65
Std Dev	3.566

B1L103S, U14-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

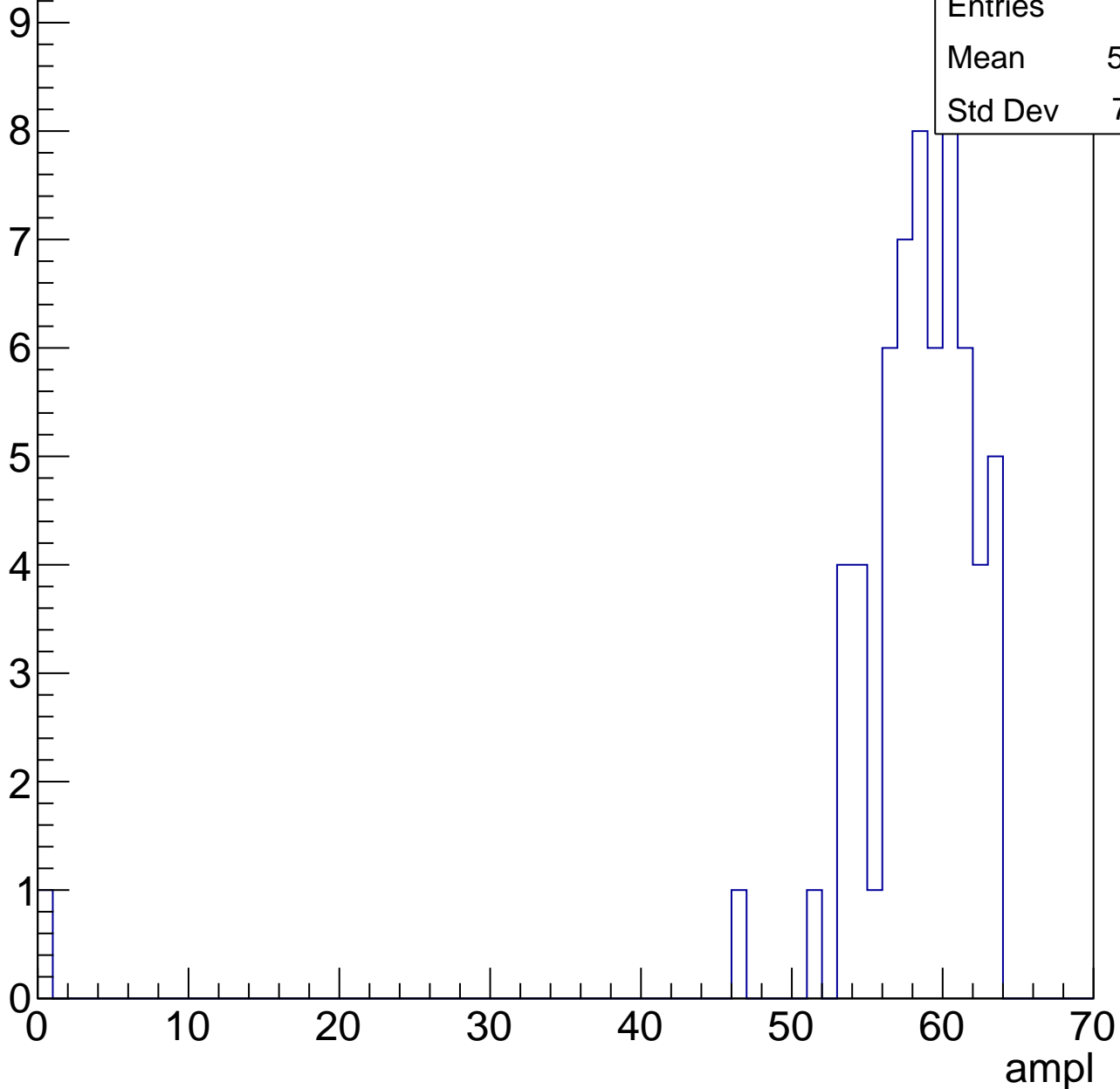


B1L103S, U14-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	57.17
Std Dev	7.971

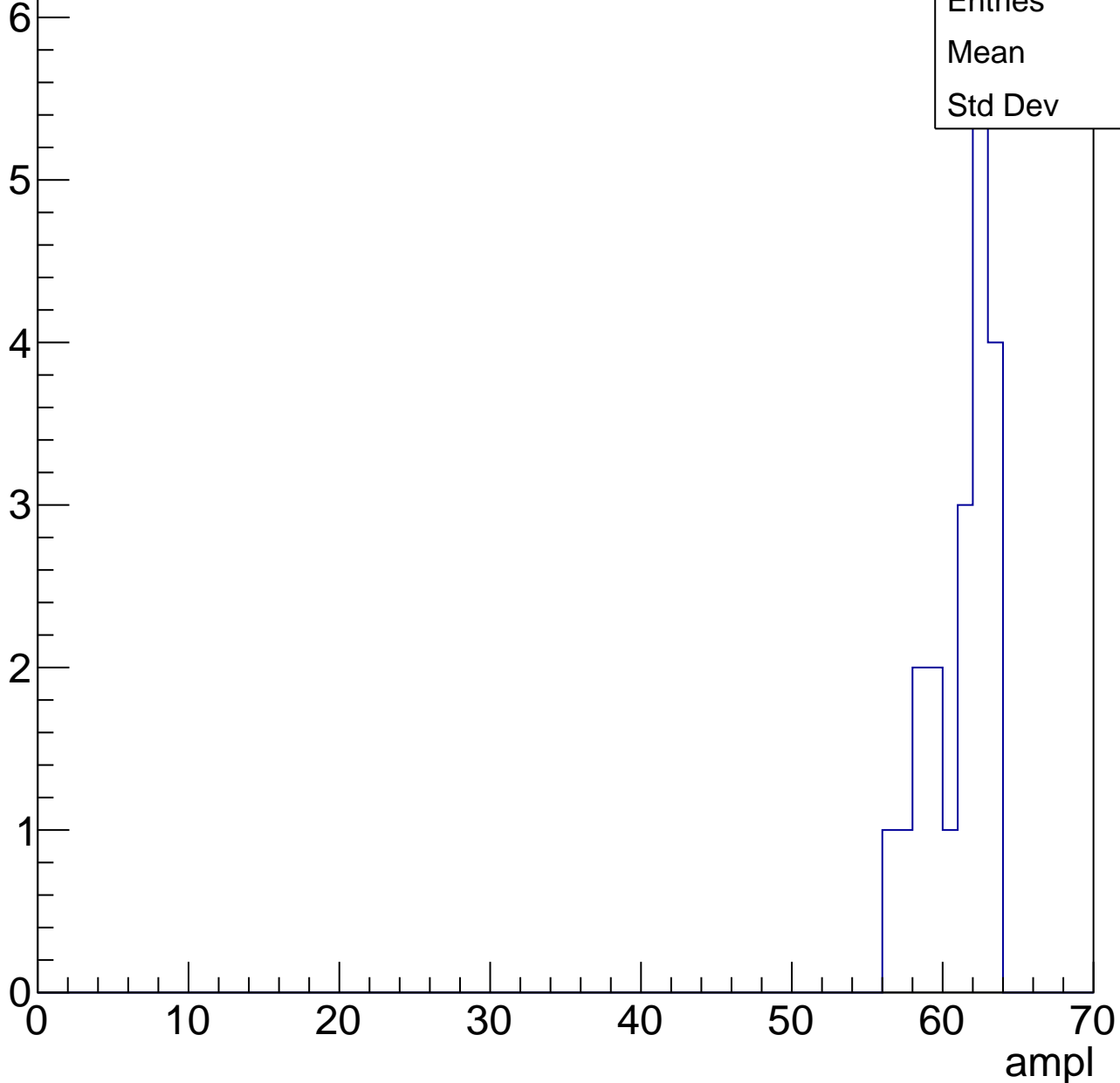


B1L103S, U14-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

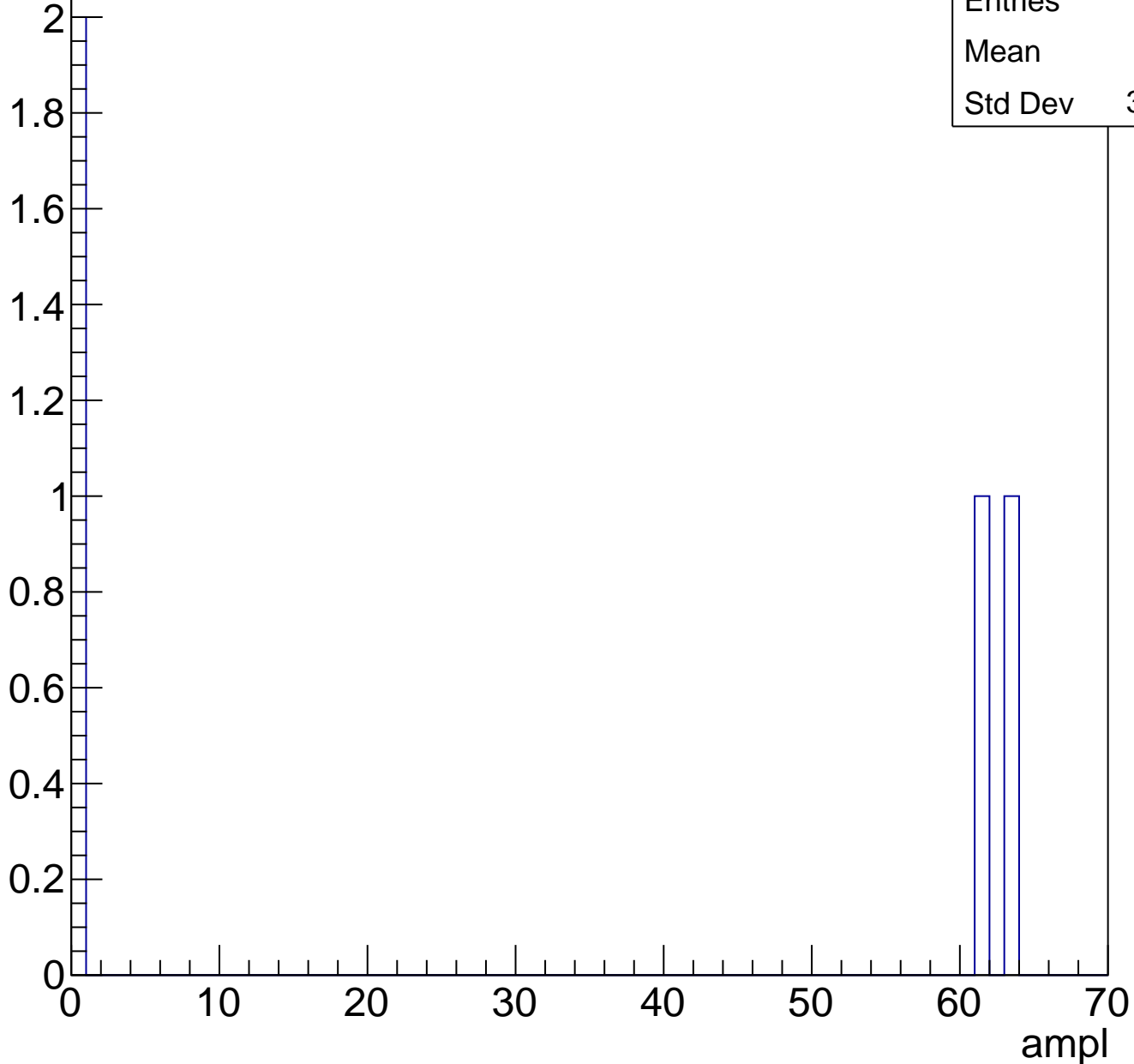
Entries	20
Mean	60.7
Std Dev	2.1



B1L103S, U14-ch67, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch68, adc0

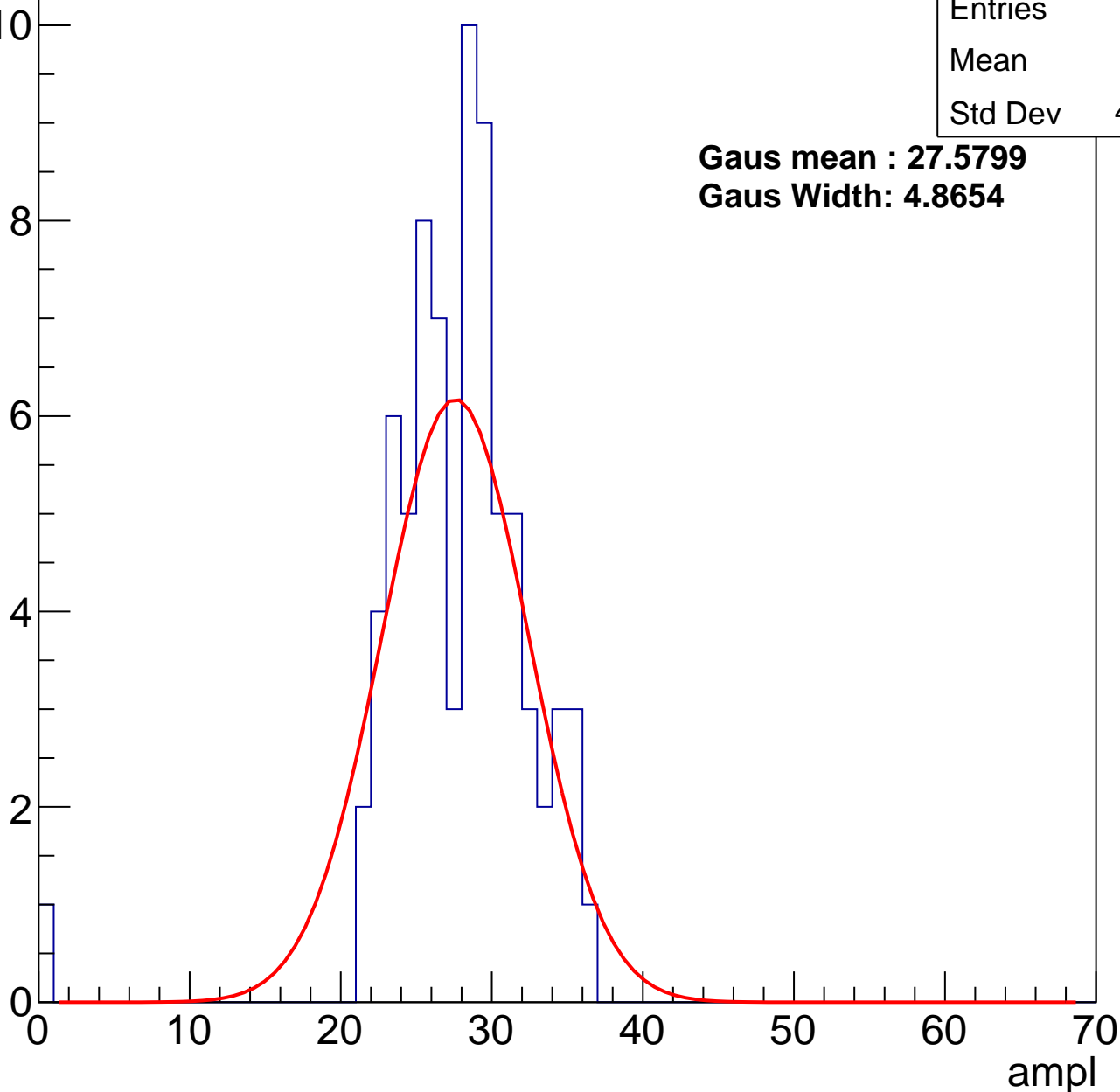
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	27.3
Std Dev	4.851

Gaus mean : 27.5799

Gaus Width: 4.8654



B1L103S, U14-ch68, adc1

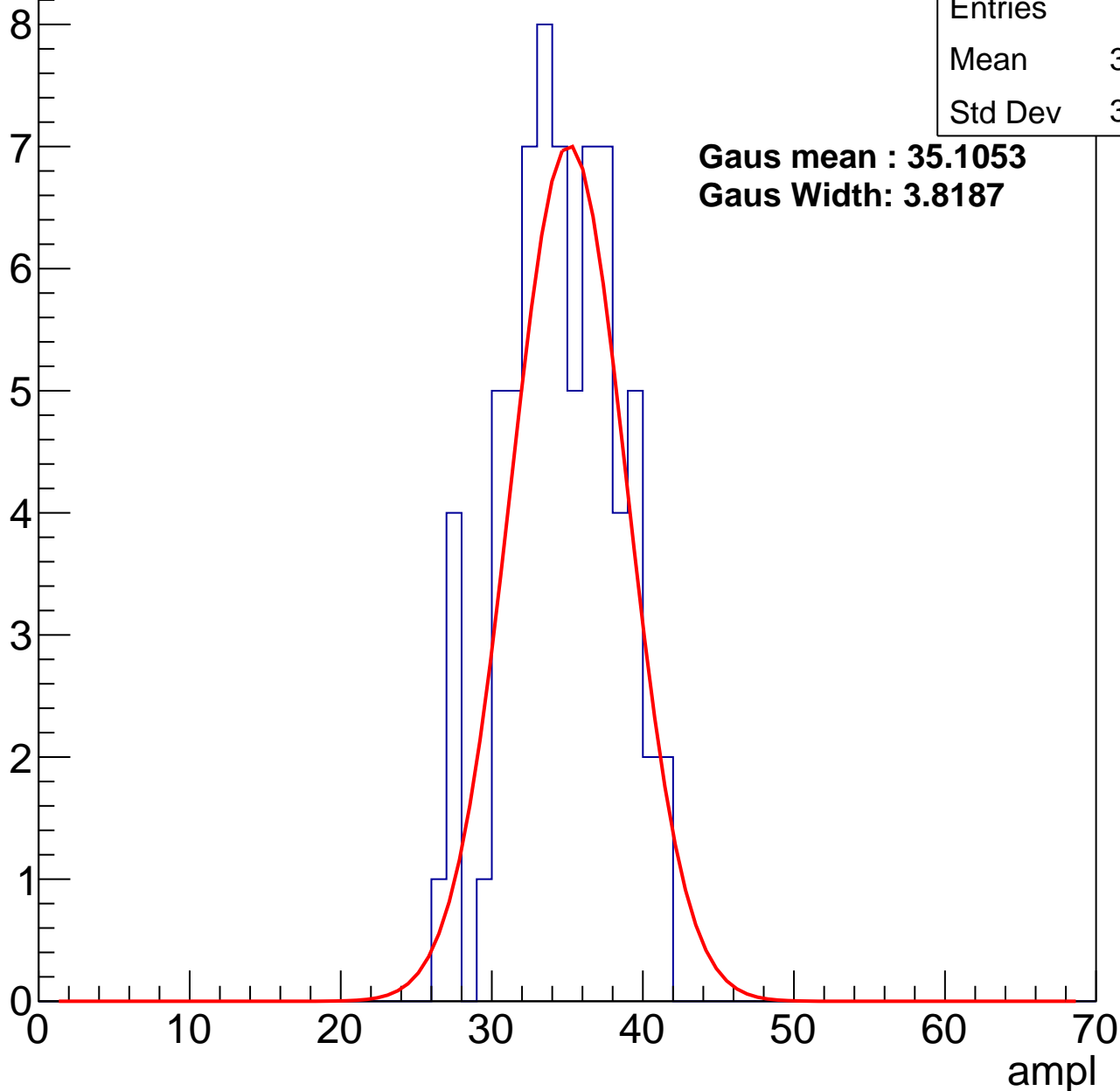
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	34.13
Std Dev	3.585

Gaus mean : 35.1053

Gaus Width: 3.8187



B1L103S, U14-ch68, adc2

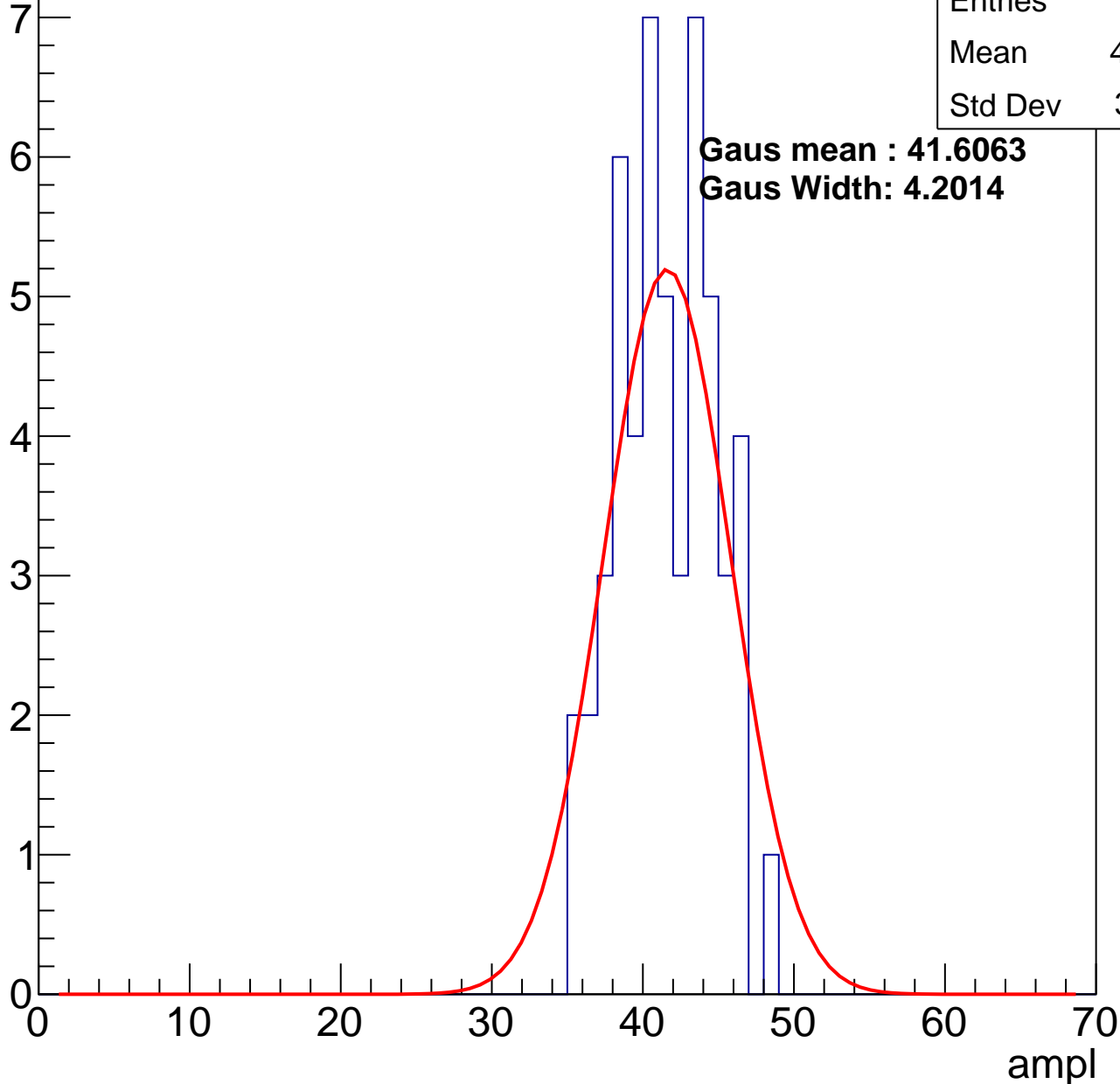
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	41.08
Std Dev	3.161

Gaus mean : 41.6063

Gaus Width: 4.2014

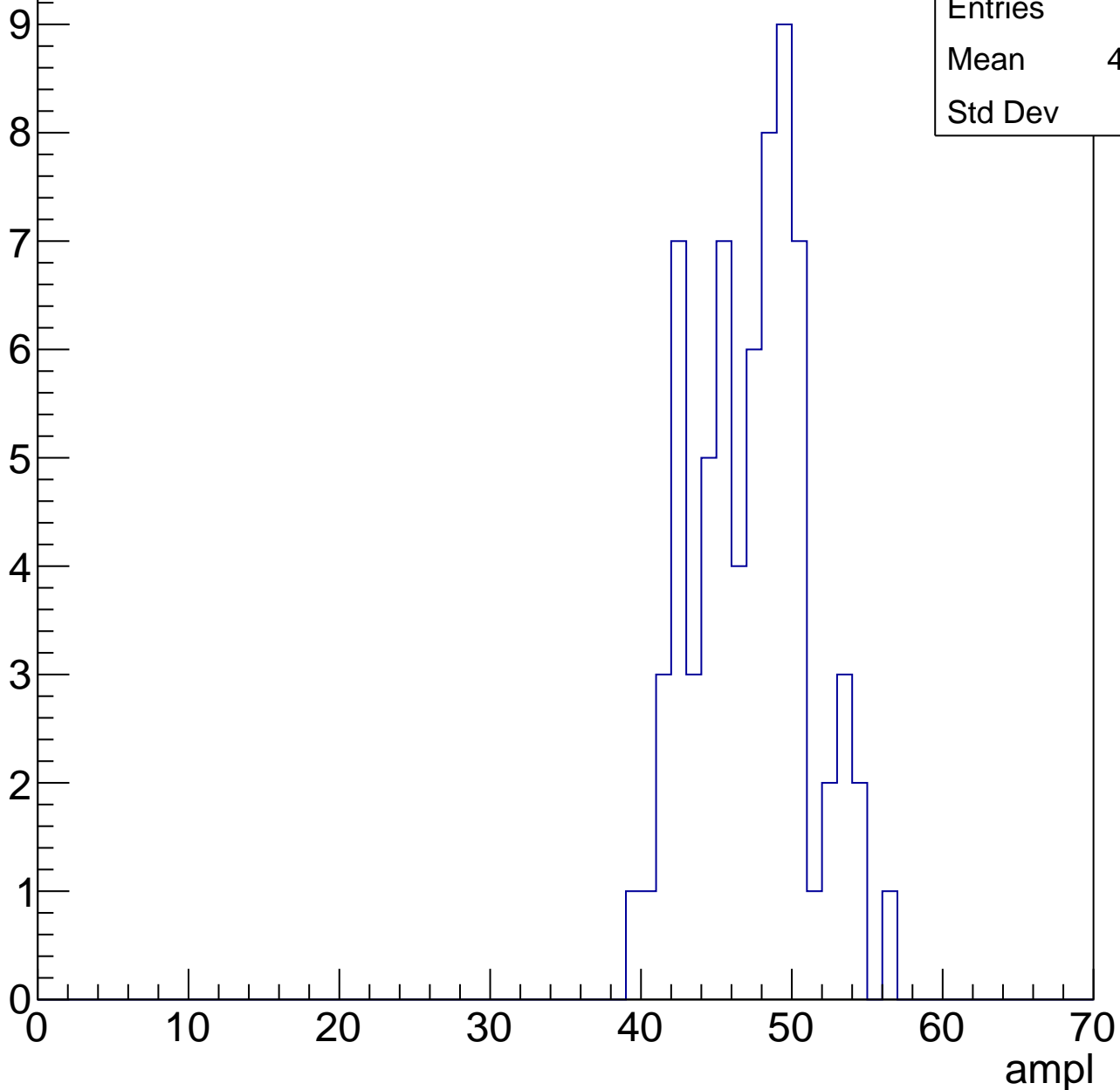


B1L103S, U14-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	46.84
Std Dev	3.74

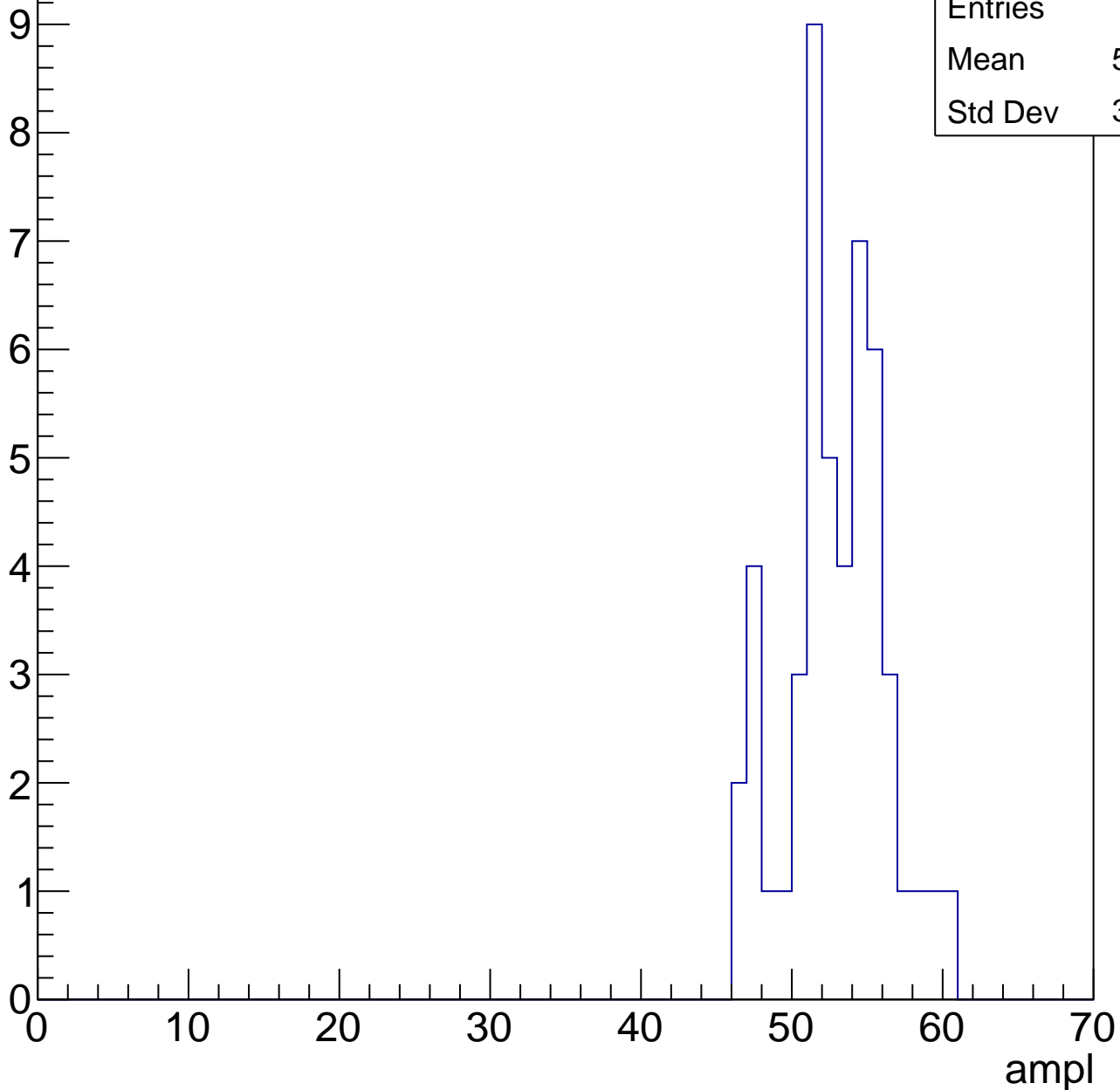


B1L103S, U14-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	52.41
Std Dev	3.251



B1L103S, U14-ch68, adc5

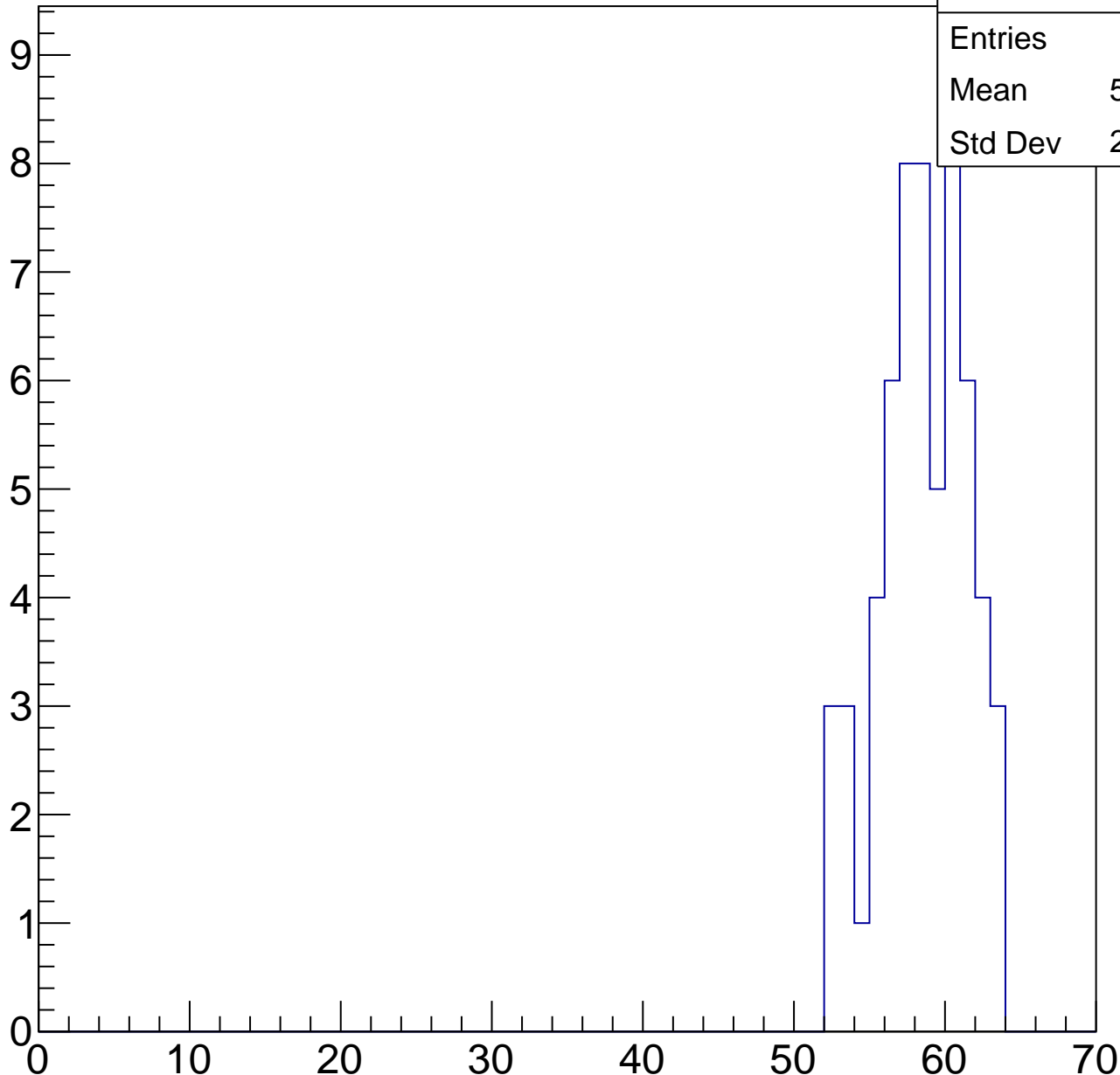
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.05
Std Dev	2.889

ampl

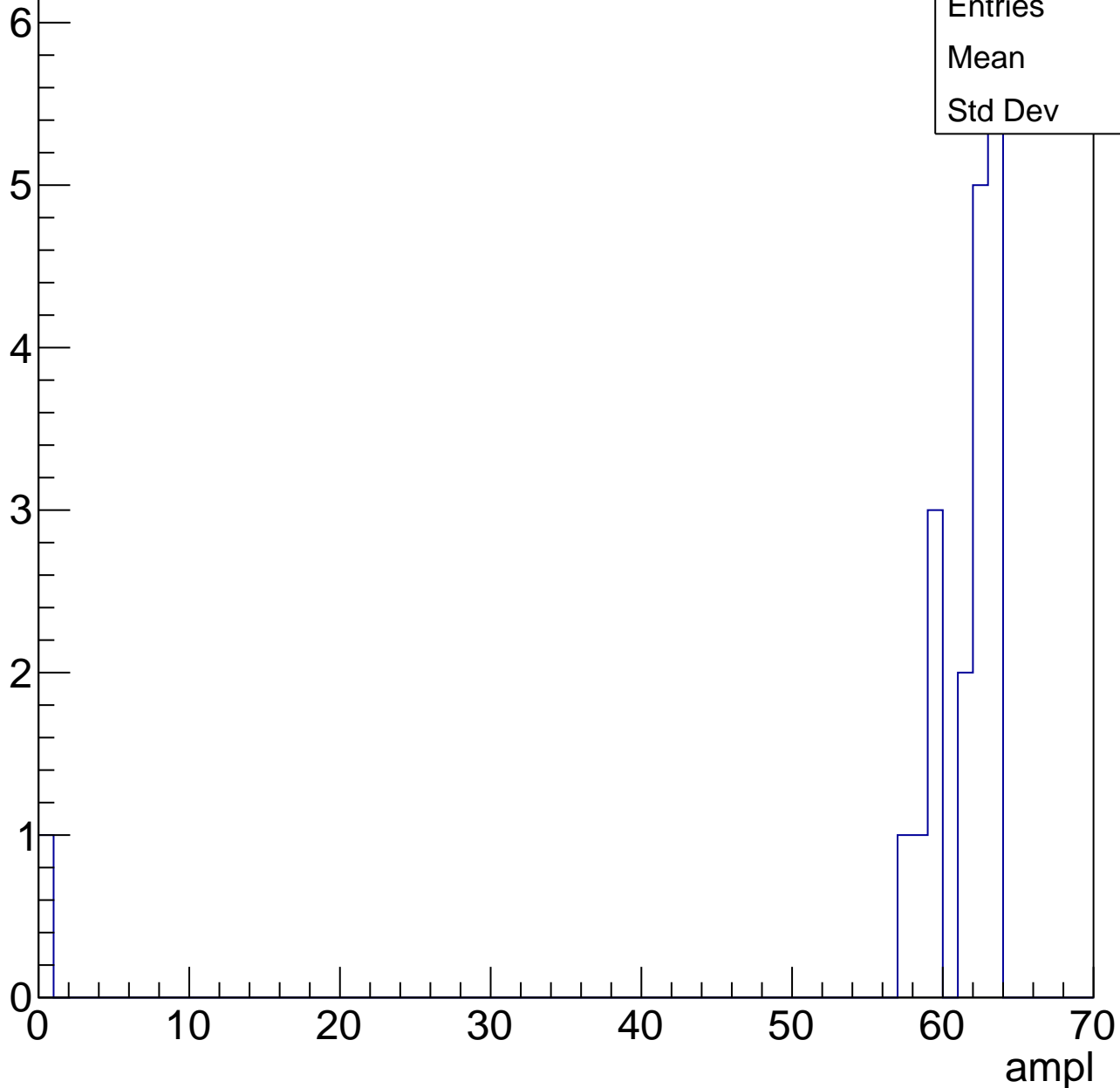


B1L103S, U14-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

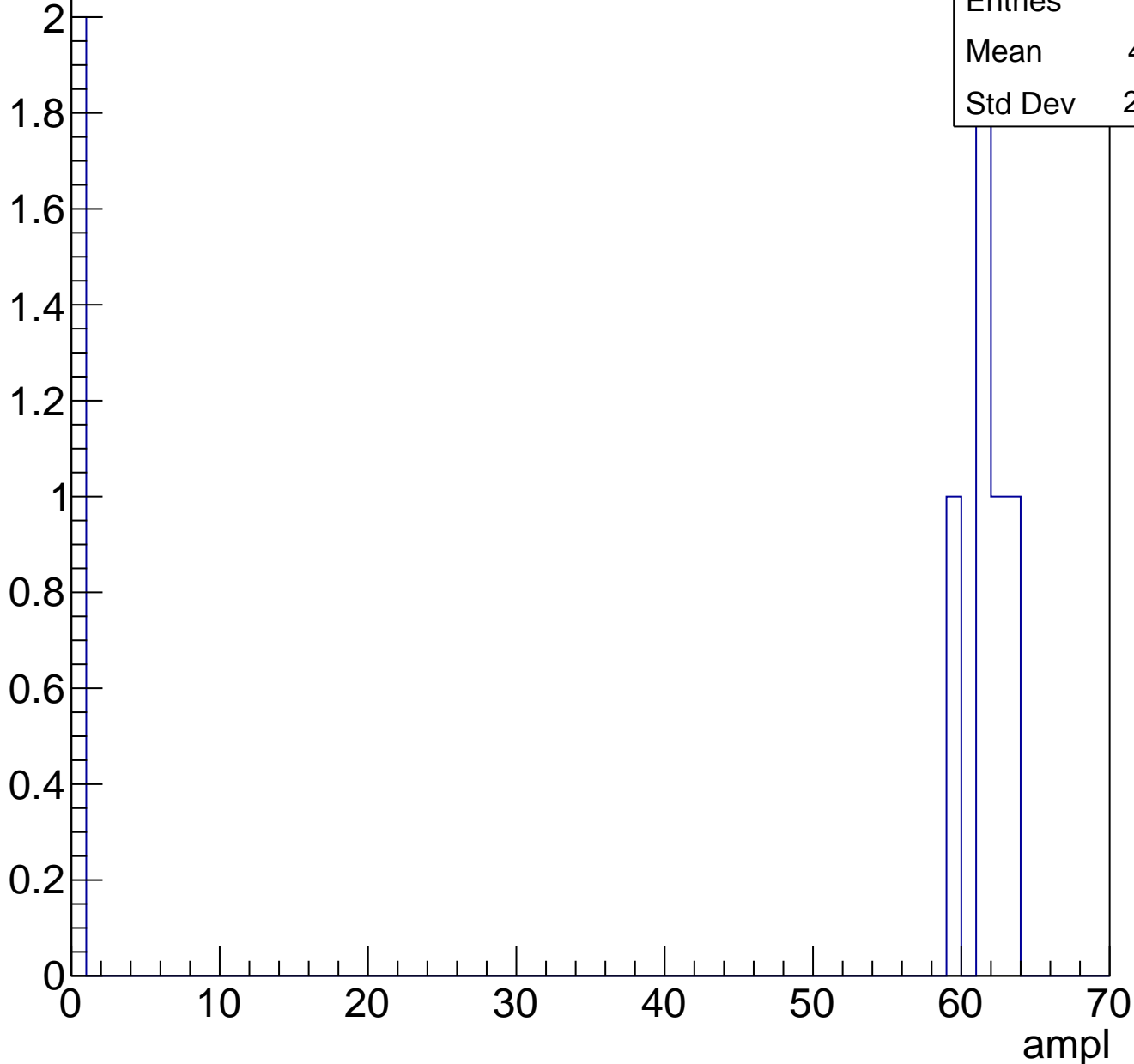
Entries	19
Mean	58
Std Dev	13.8



B1L103S, U14-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch69, adc0

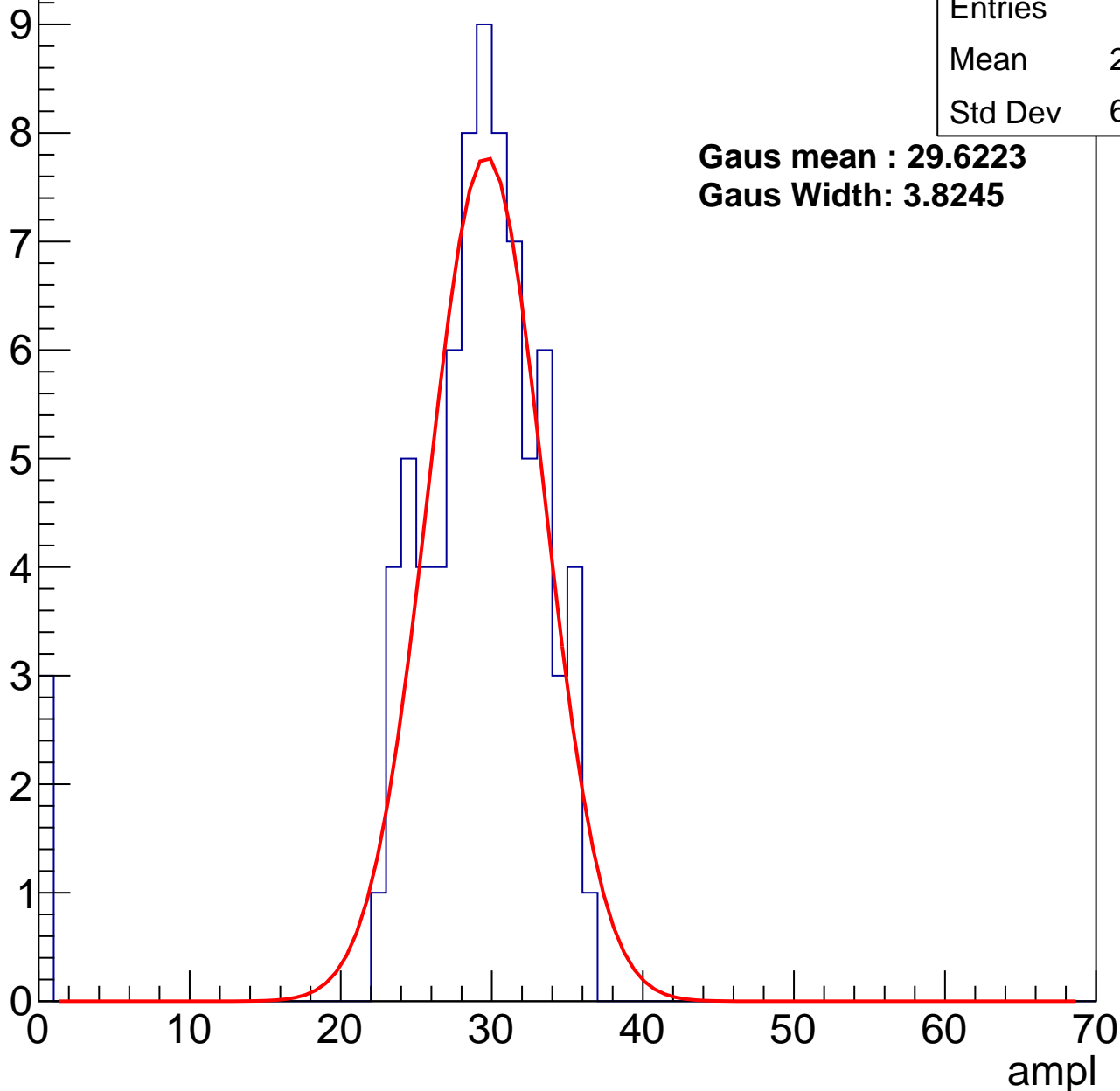
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	27.92
Std Dev	6.532

Gaus mean : 29.6223

Gaus Width: 3.8245



B1L103S, U14-ch69, adc1

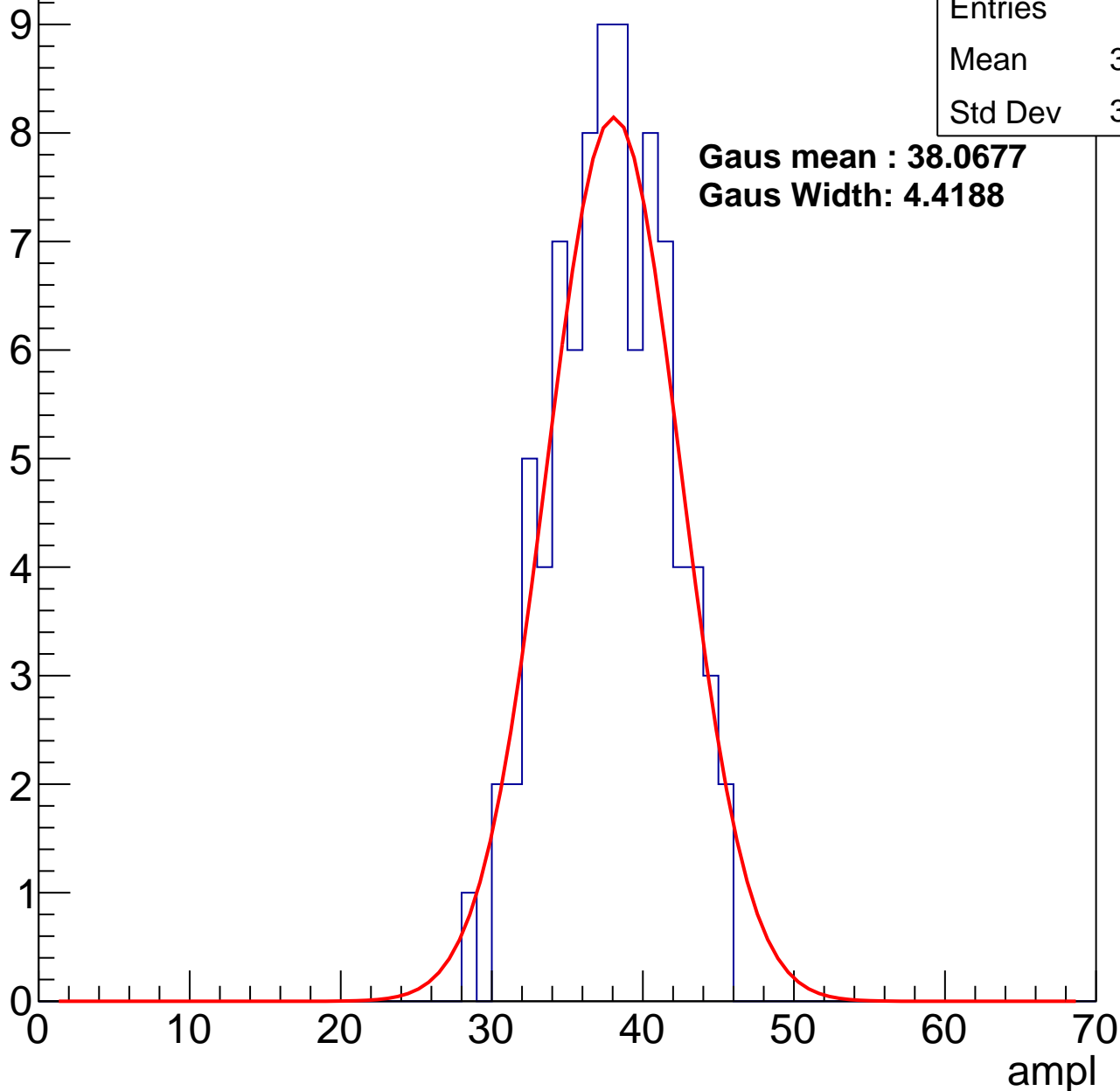
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	37.43
Std Dev	3.798

Gaus mean : 38.0677

Gaus Width: 4.4188



B1L103S, U14-ch69, adc2

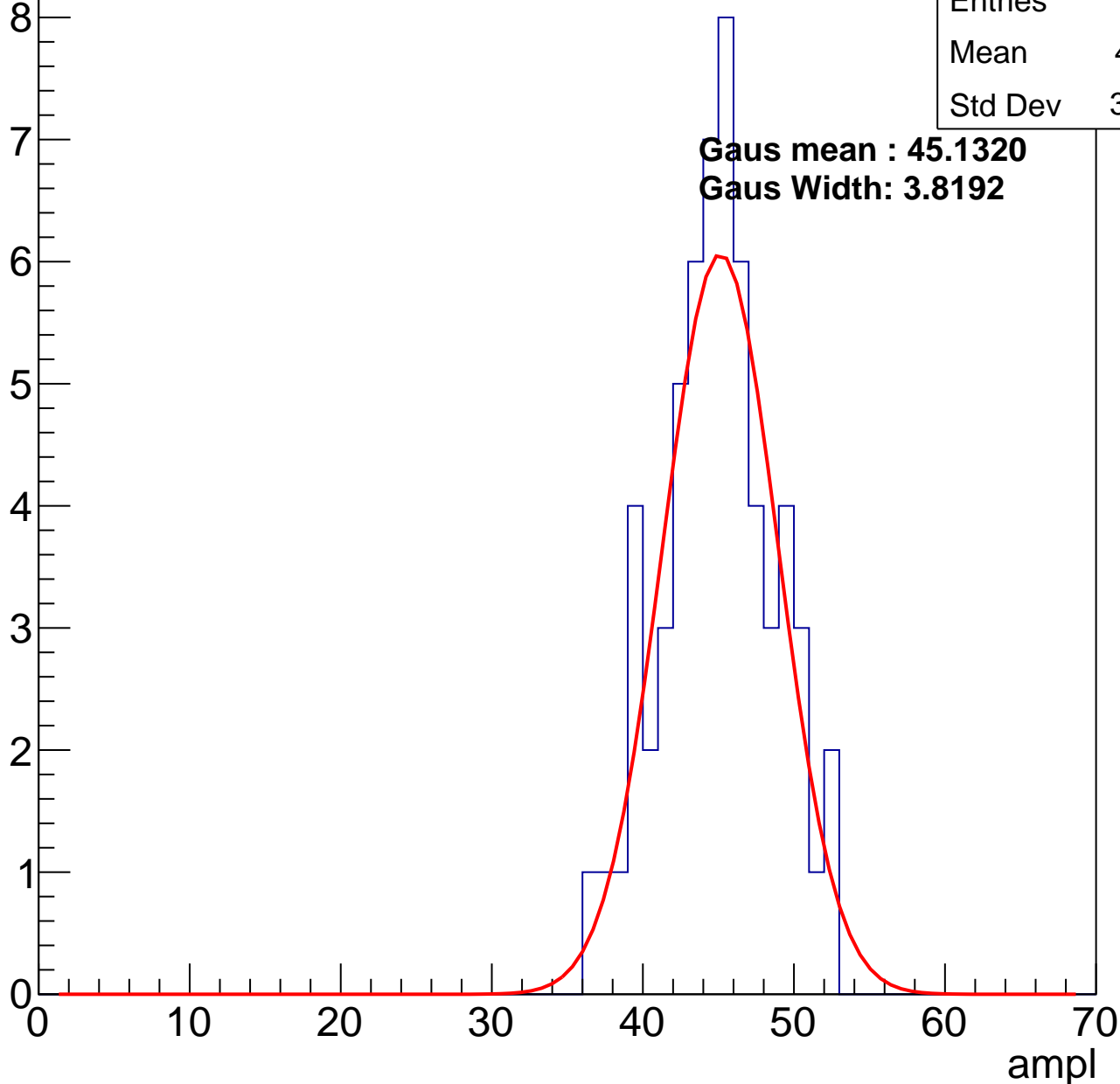
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	44.51
Std Dev	3.665

Gaus mean : 45.1320

Gaus Width: 3.8192

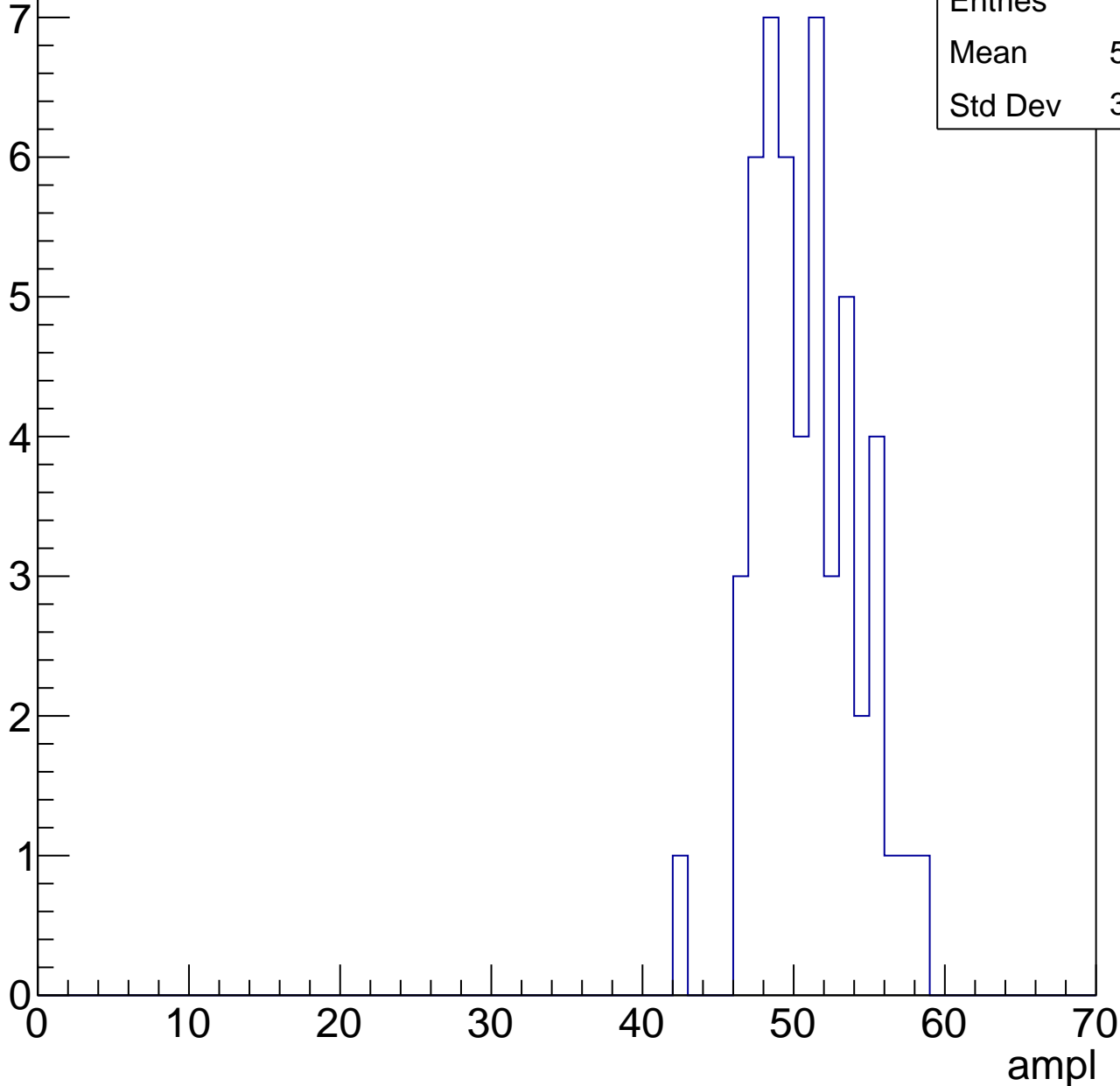


B1L103S, U14-ch69, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	50.37
Std Dev	3.248

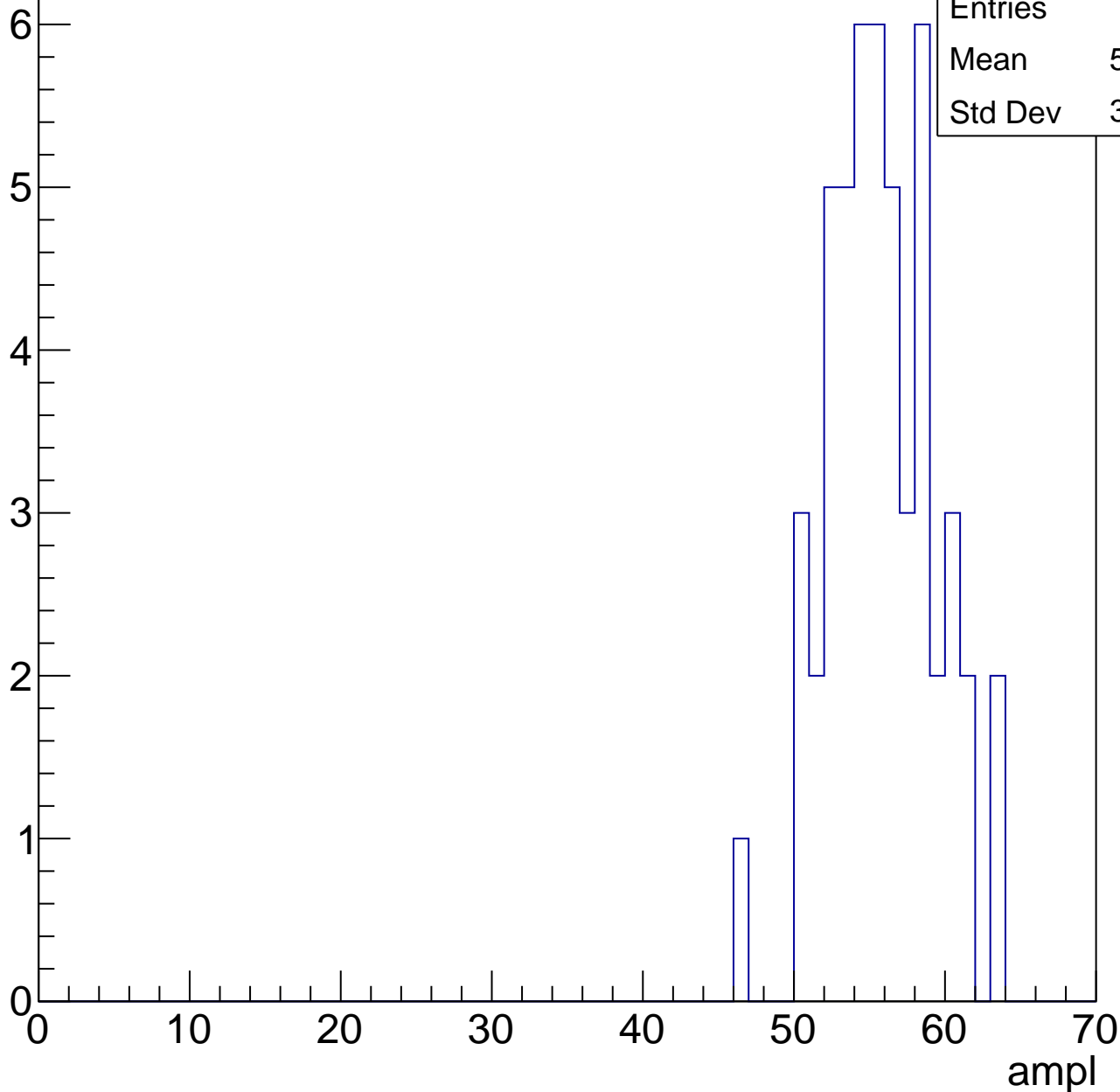


B1L103S, U14-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	55.33
Std Dev	3.518

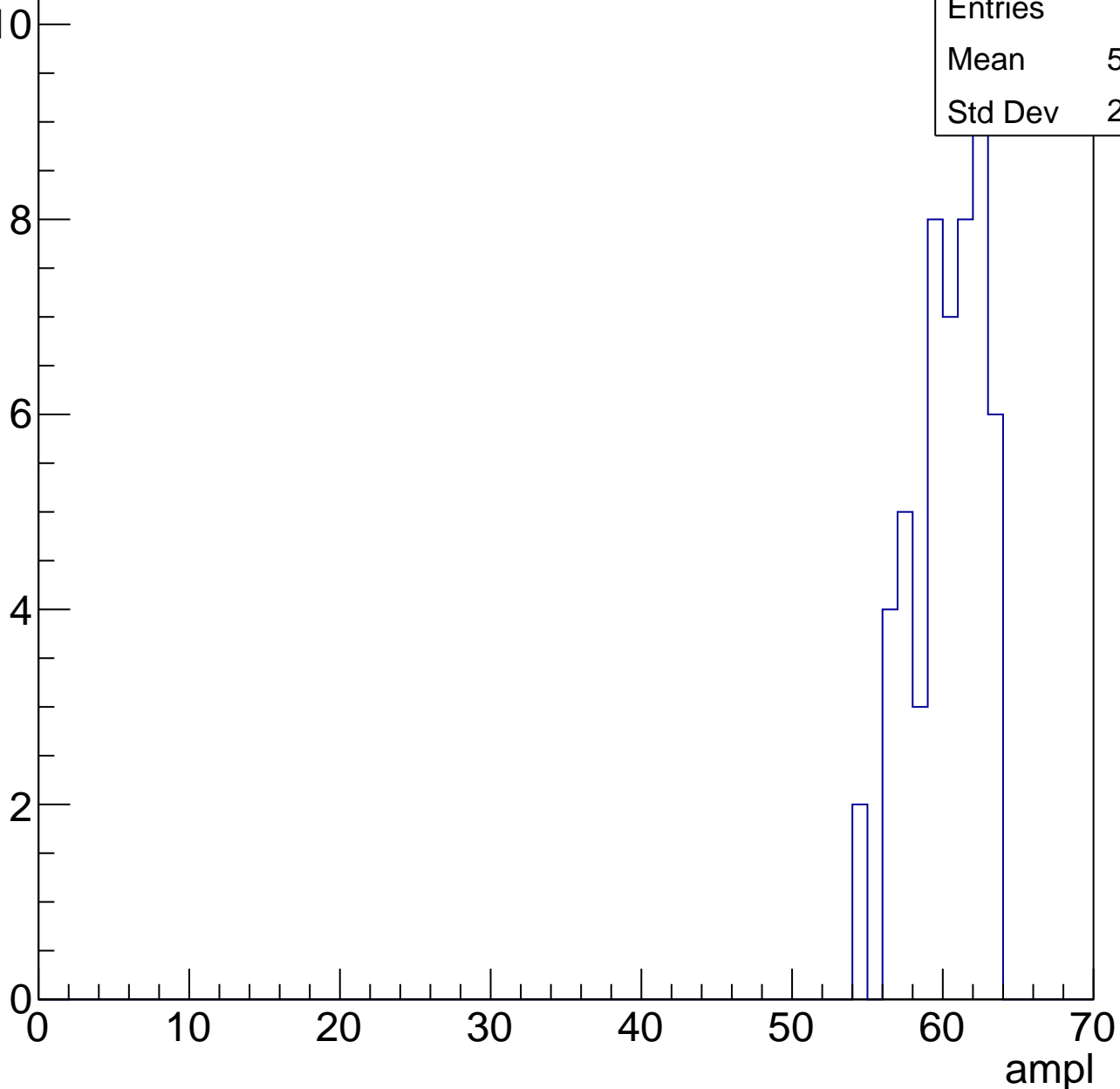


B1L103S, U14-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

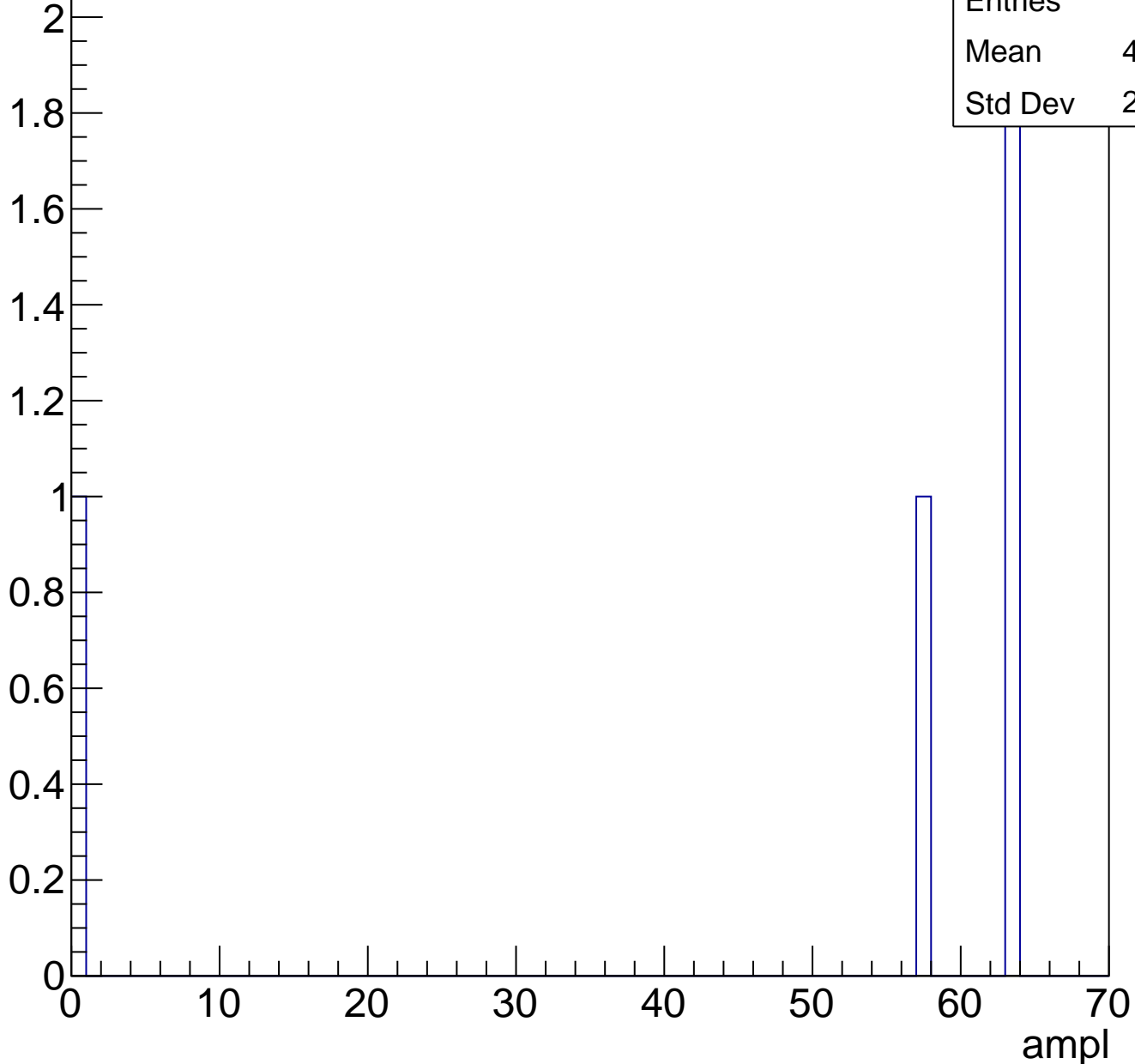
Entries	53
Mean	59.79
Std Dev	2.382



B1L103S, U14-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	45.75
Std Dev	26.53

B1L103S, U14-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch70, adc0

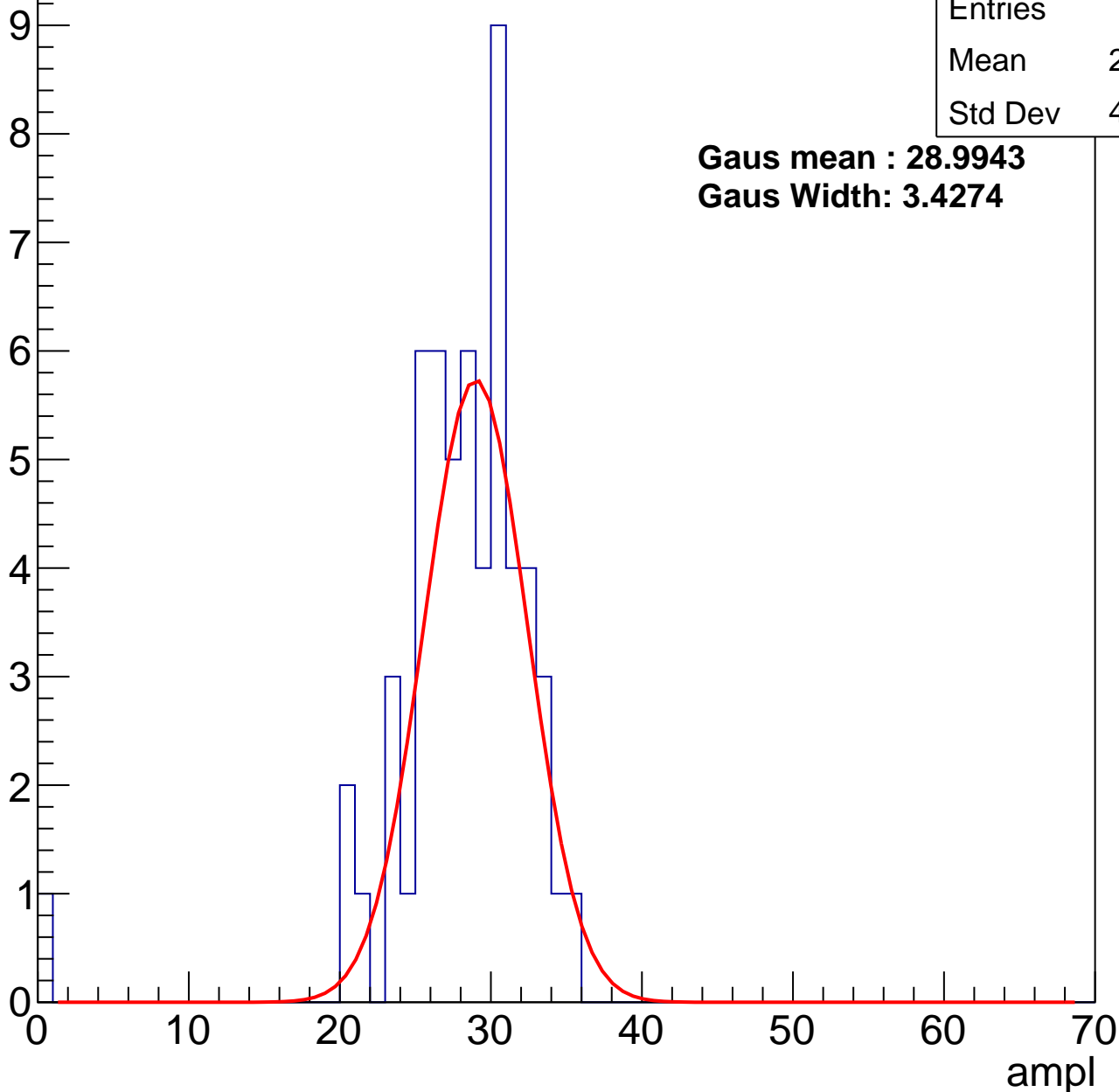
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	27.53
Std Dev	4.995

Gaus mean : 28.9943

Gaus Width: 3.4274



B1L103S, U14-ch70, adc1

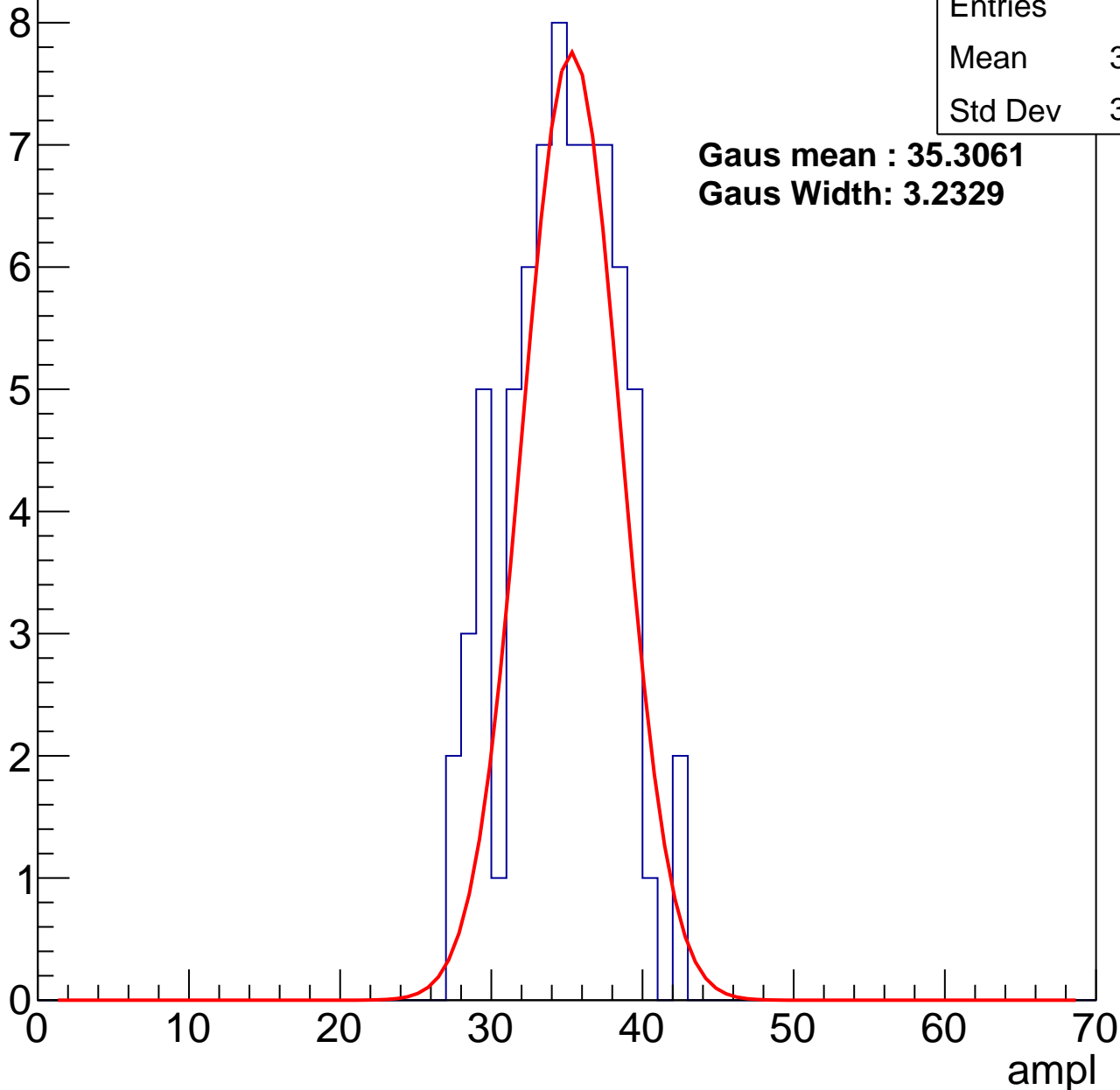
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	34.25
Std Dev	3.538

Gaus mean : 35.3061

Gaus Width: 3.2329



B1L103S, U14-ch70, adc2

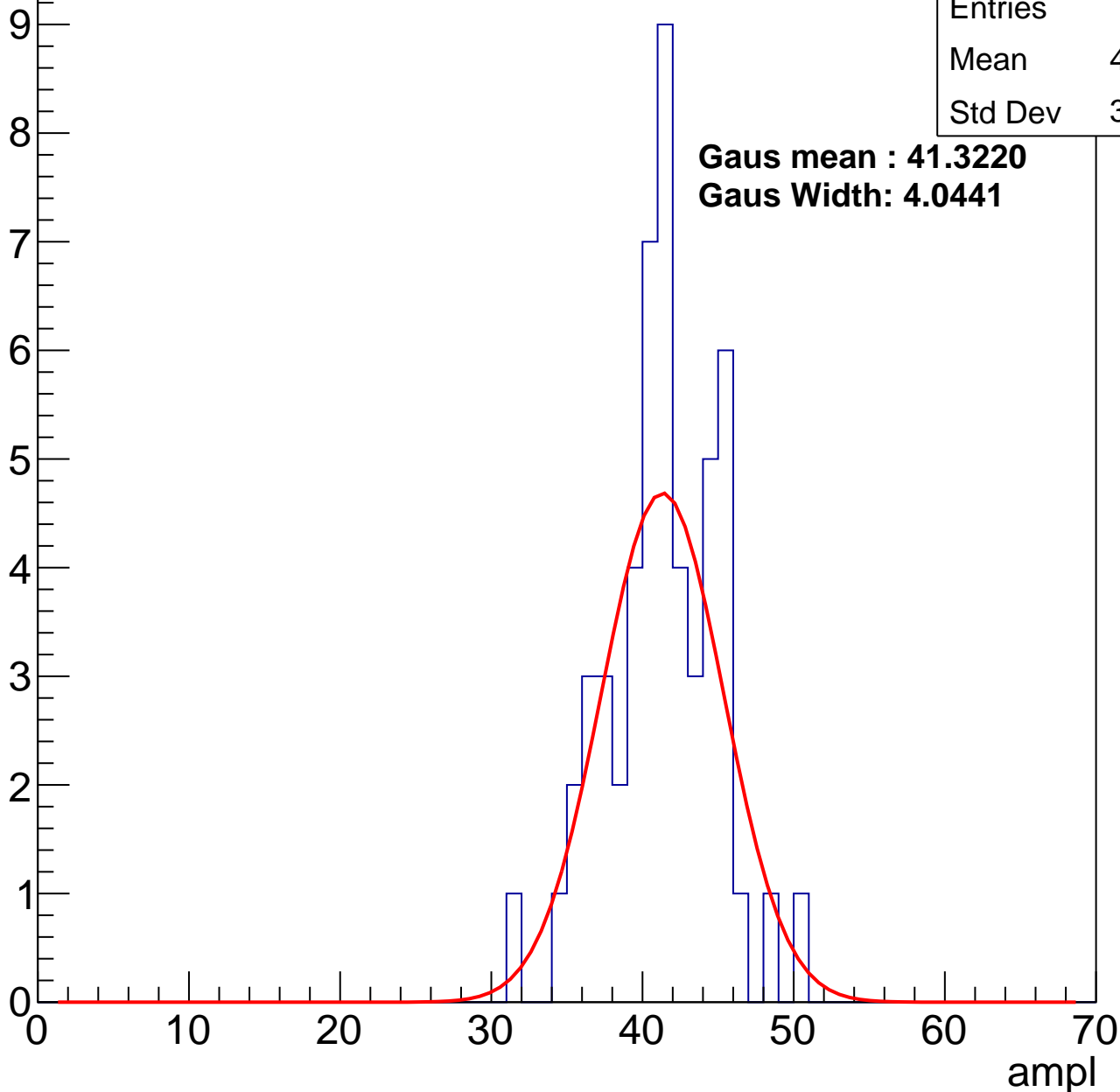
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	40.87
Std Dev	3.624

Gaus mean : 41.3220

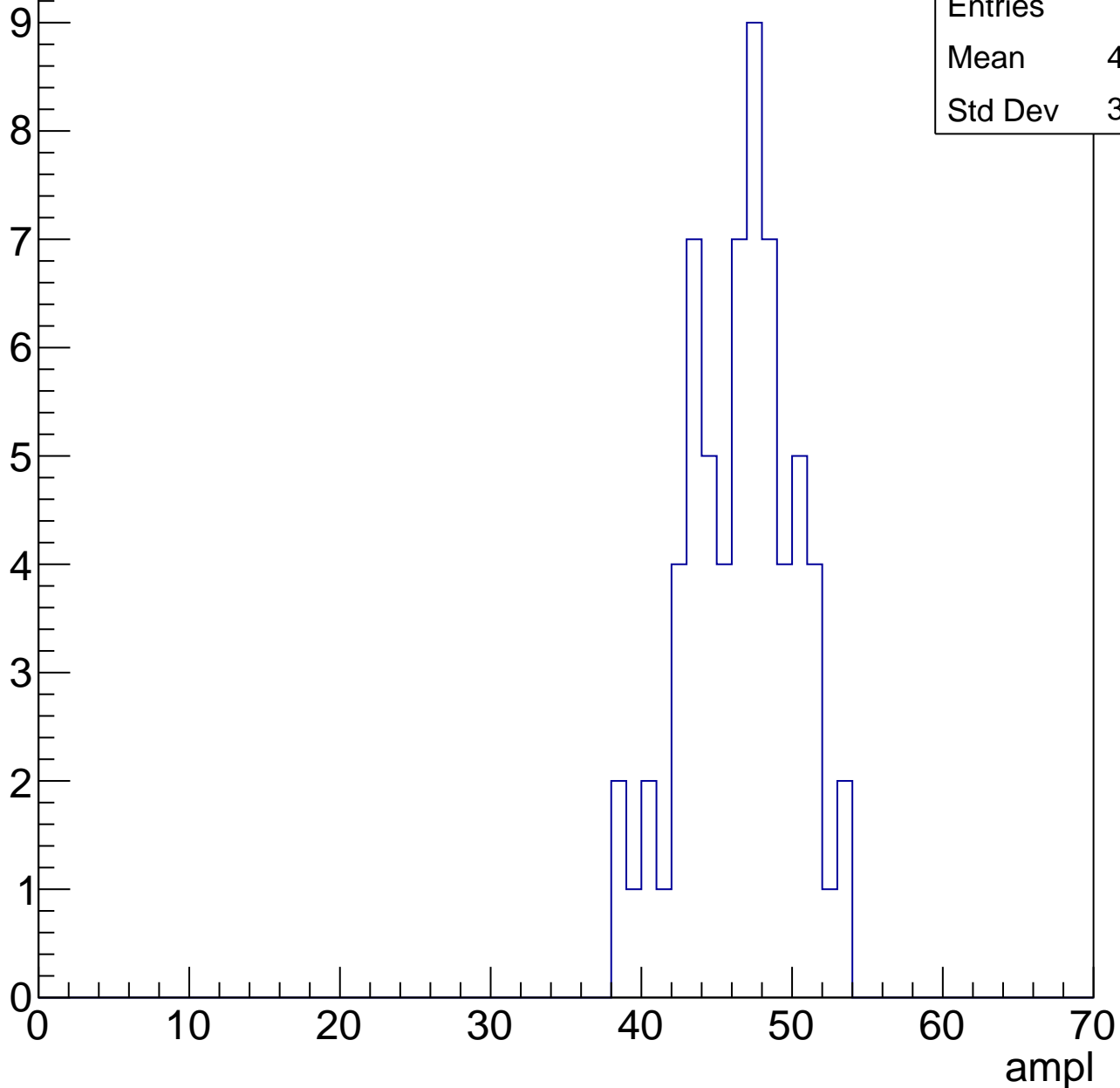
Gaus Width: 4.0441



B1L103S, U14-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



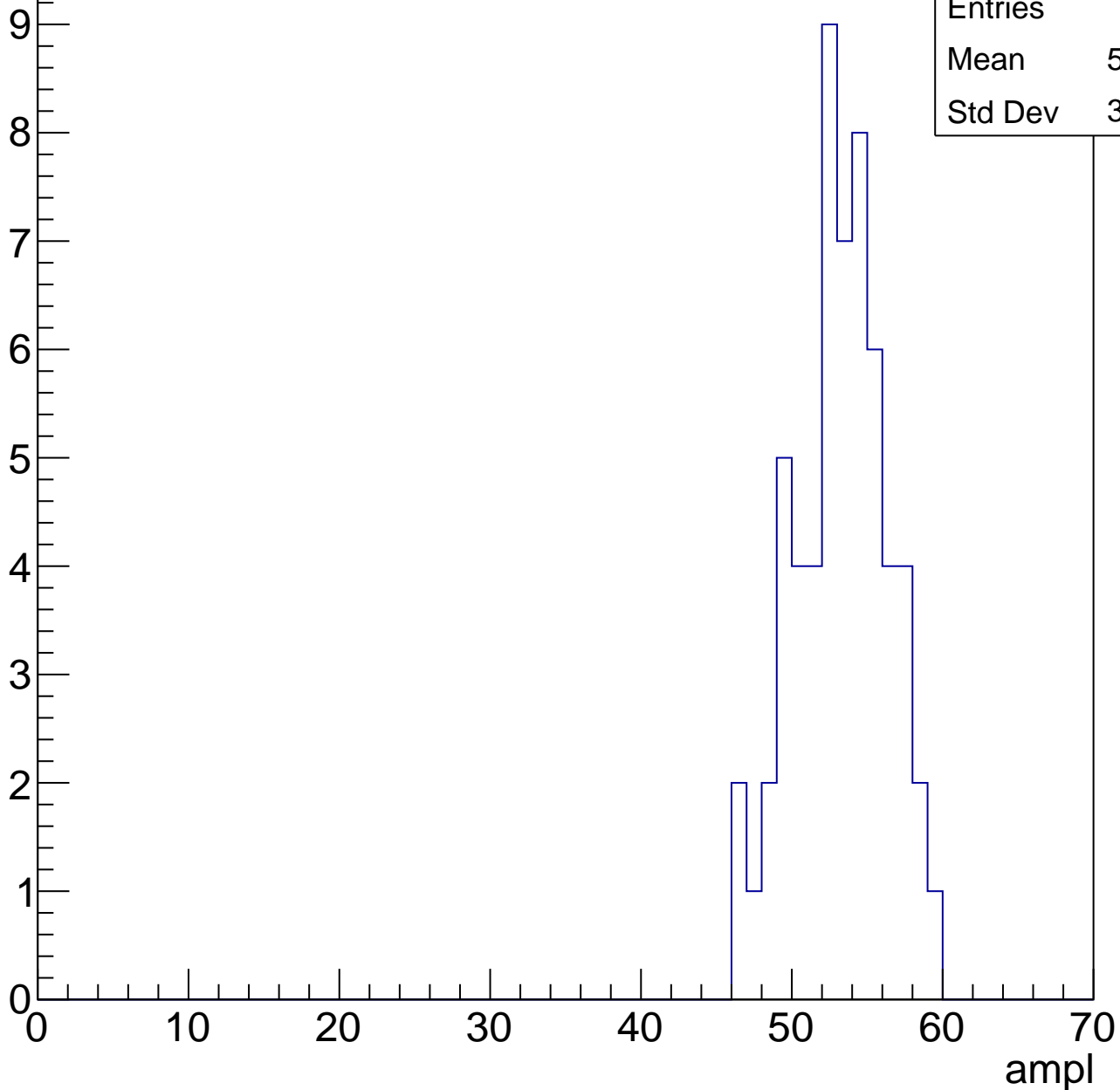
Entries	65
Mean	46.06
Std Dev	3.534

B1L103S, U14-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	52.75
Std Dev	3.023

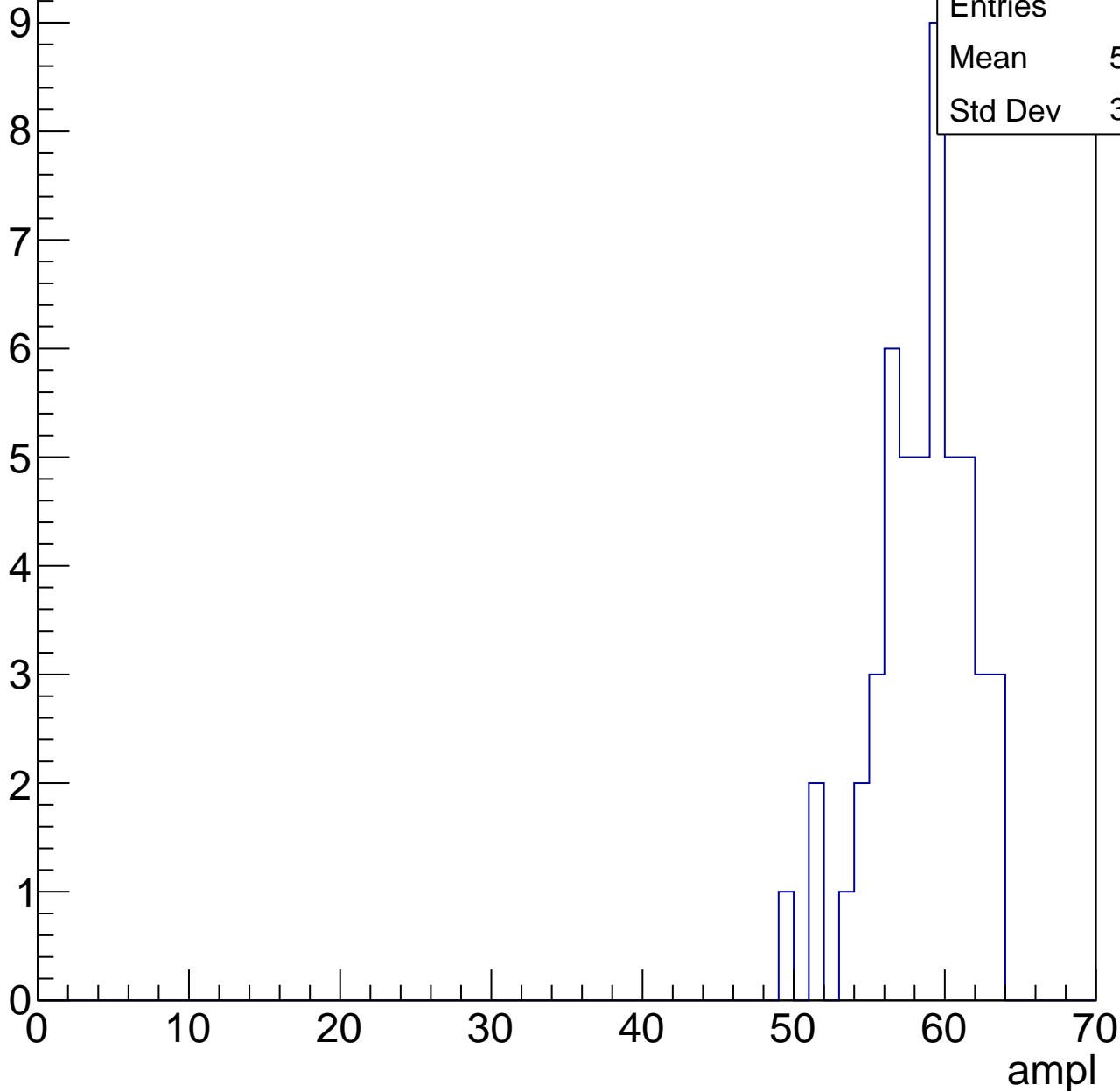


B1L103S, U14-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	57.98
Std Dev	3.127

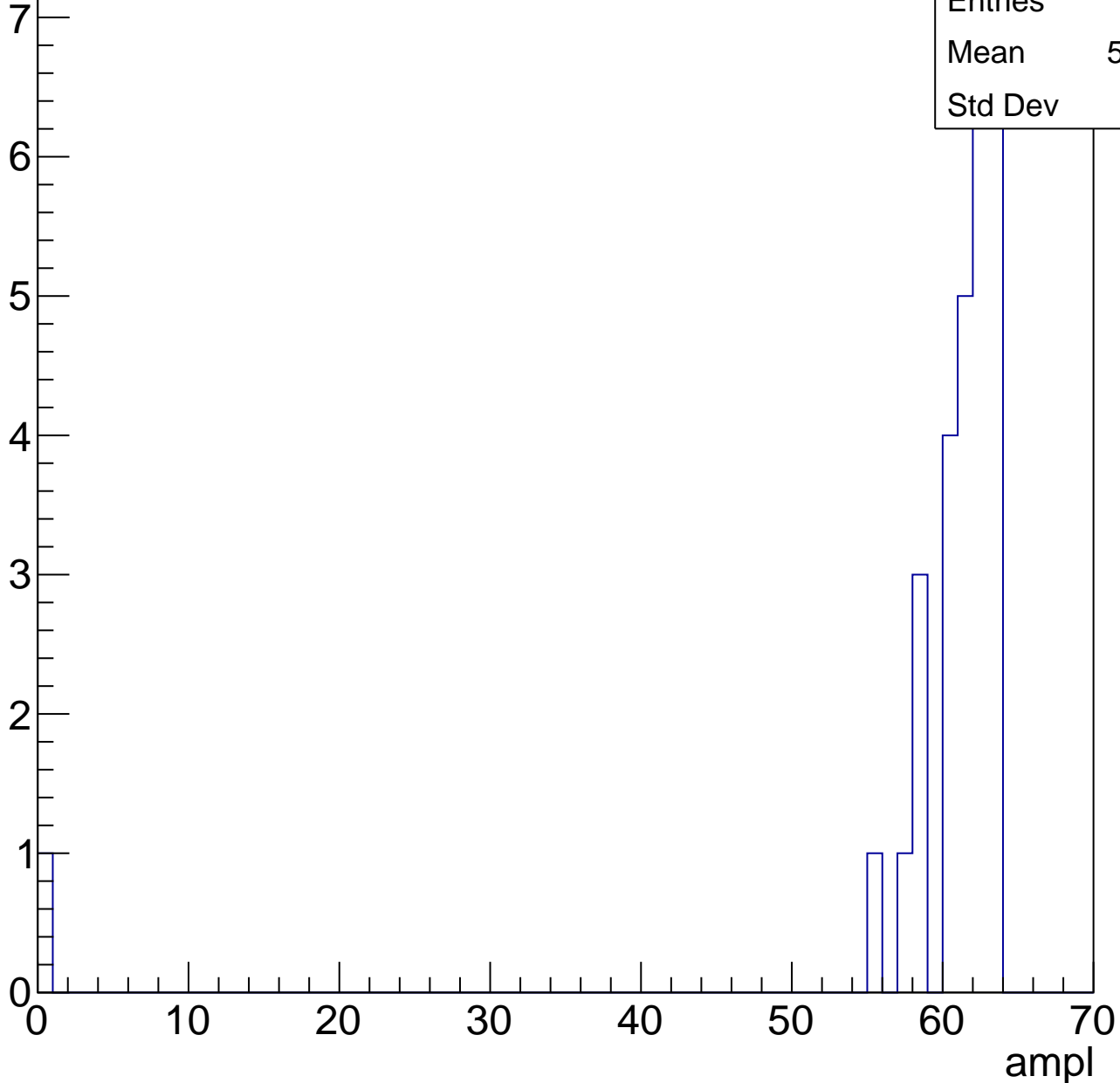


B1L103S, U14-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

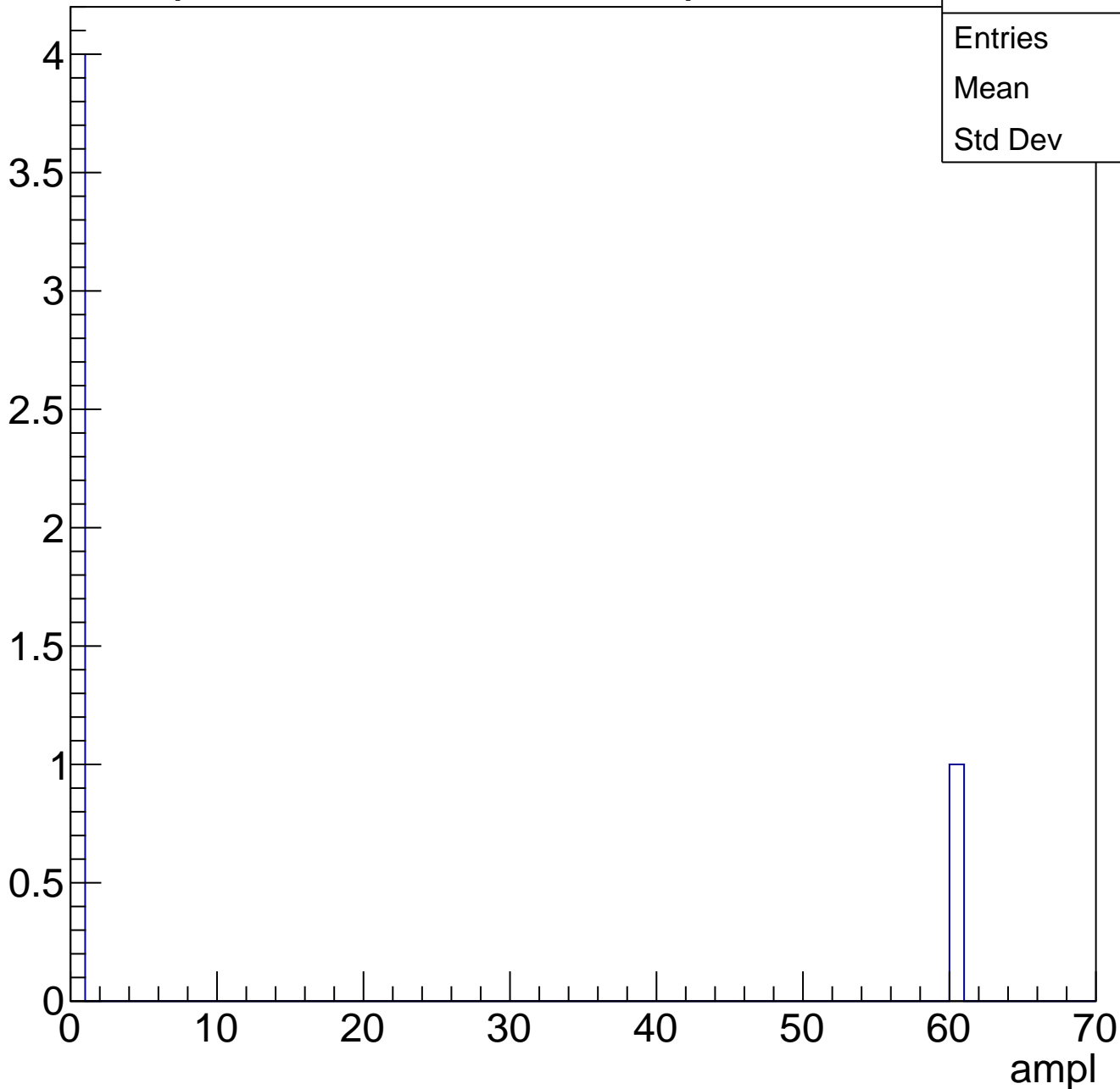
Entries	29
Mean	58.83
Std Dev	11.3



B1L103S, U14-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch71, adc0

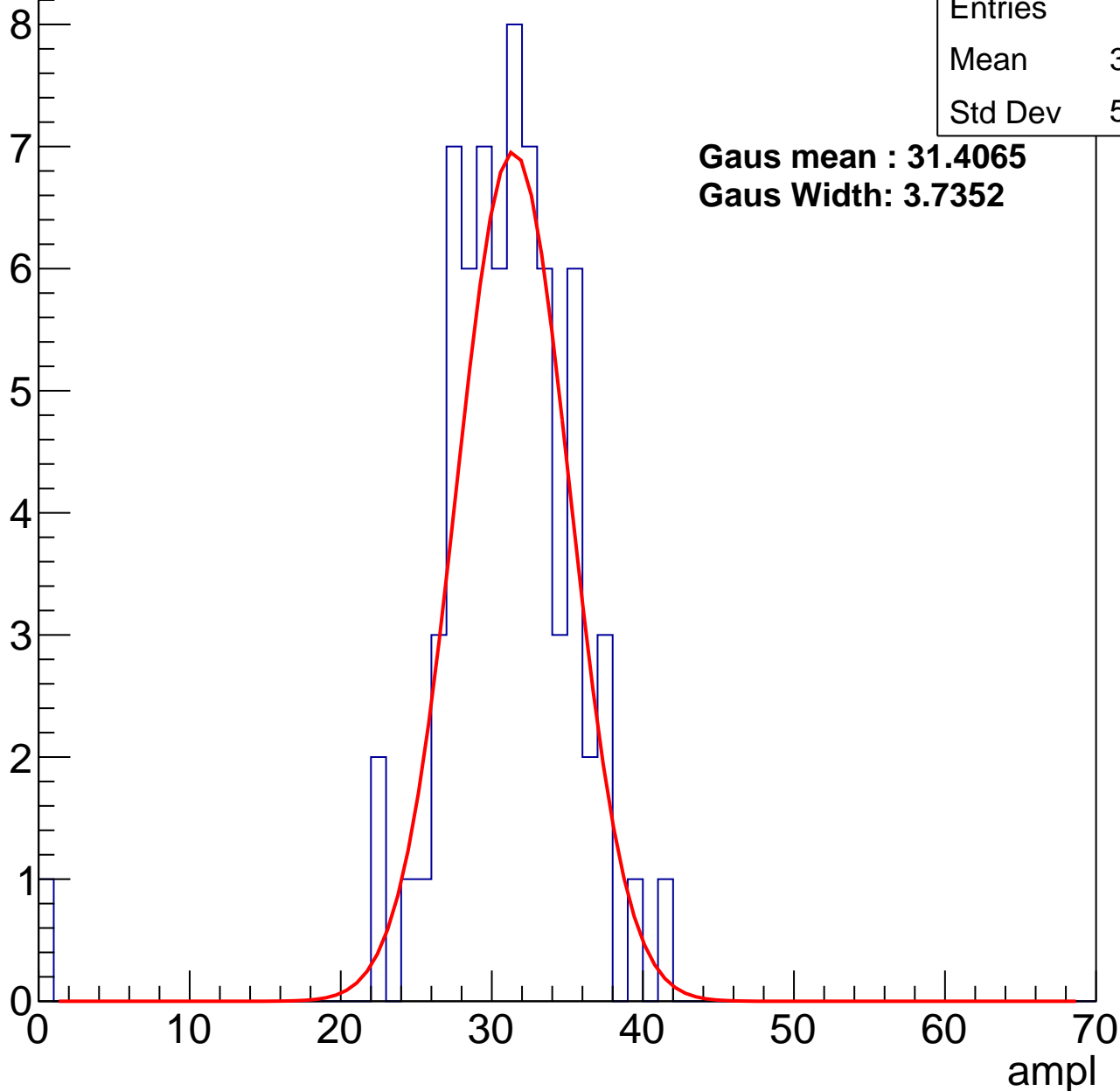
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.37
Std Dev	5.214

Gaus mean : 31.4065

Gaus Width: 3.7352



B1L103S, U14-ch71, adc1

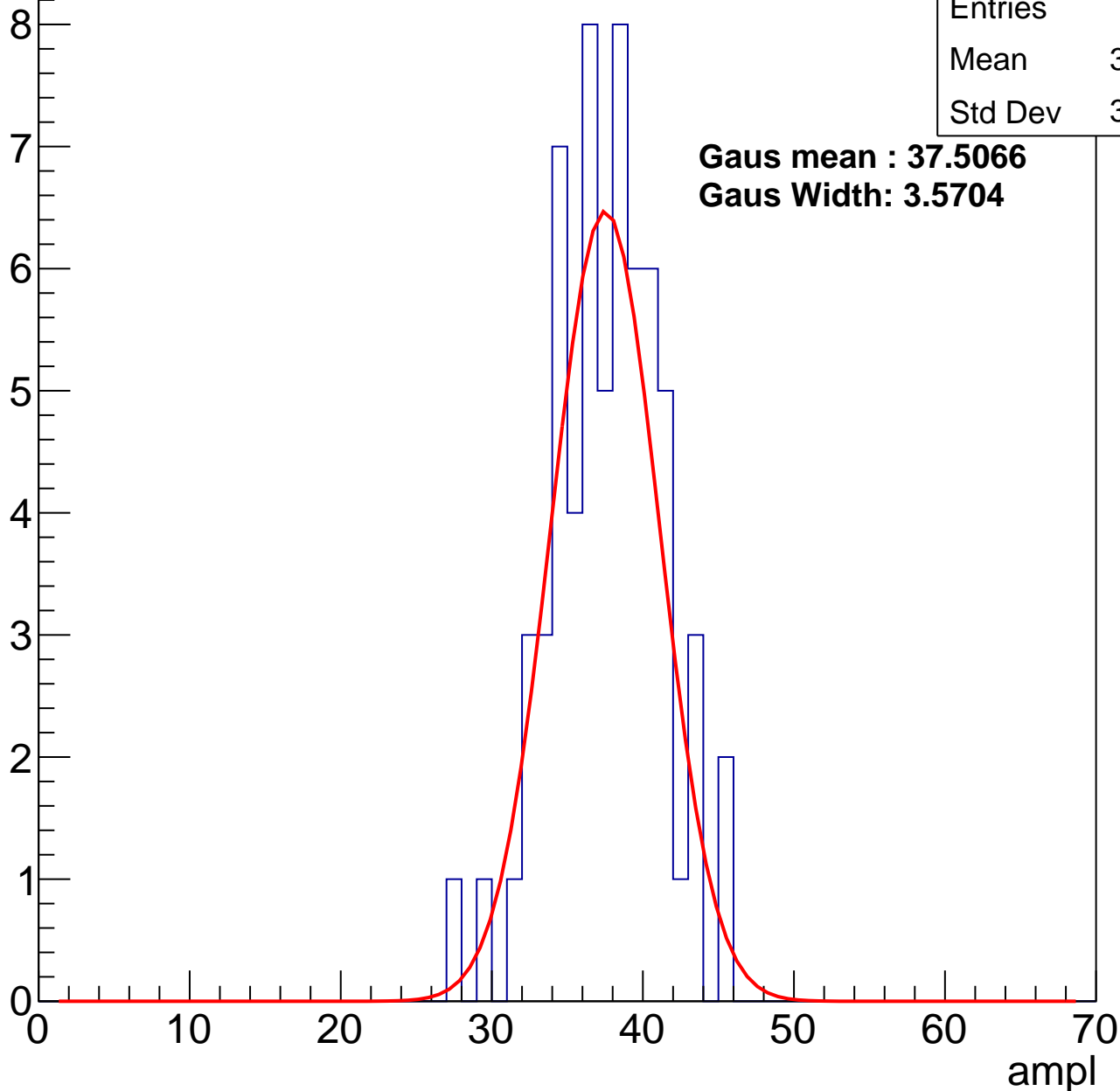
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	37.14
Std Dev	3.614

Gaus mean : 37.5066

Gaus Width: 3.5704



B1L103S, U14-ch71, adc2

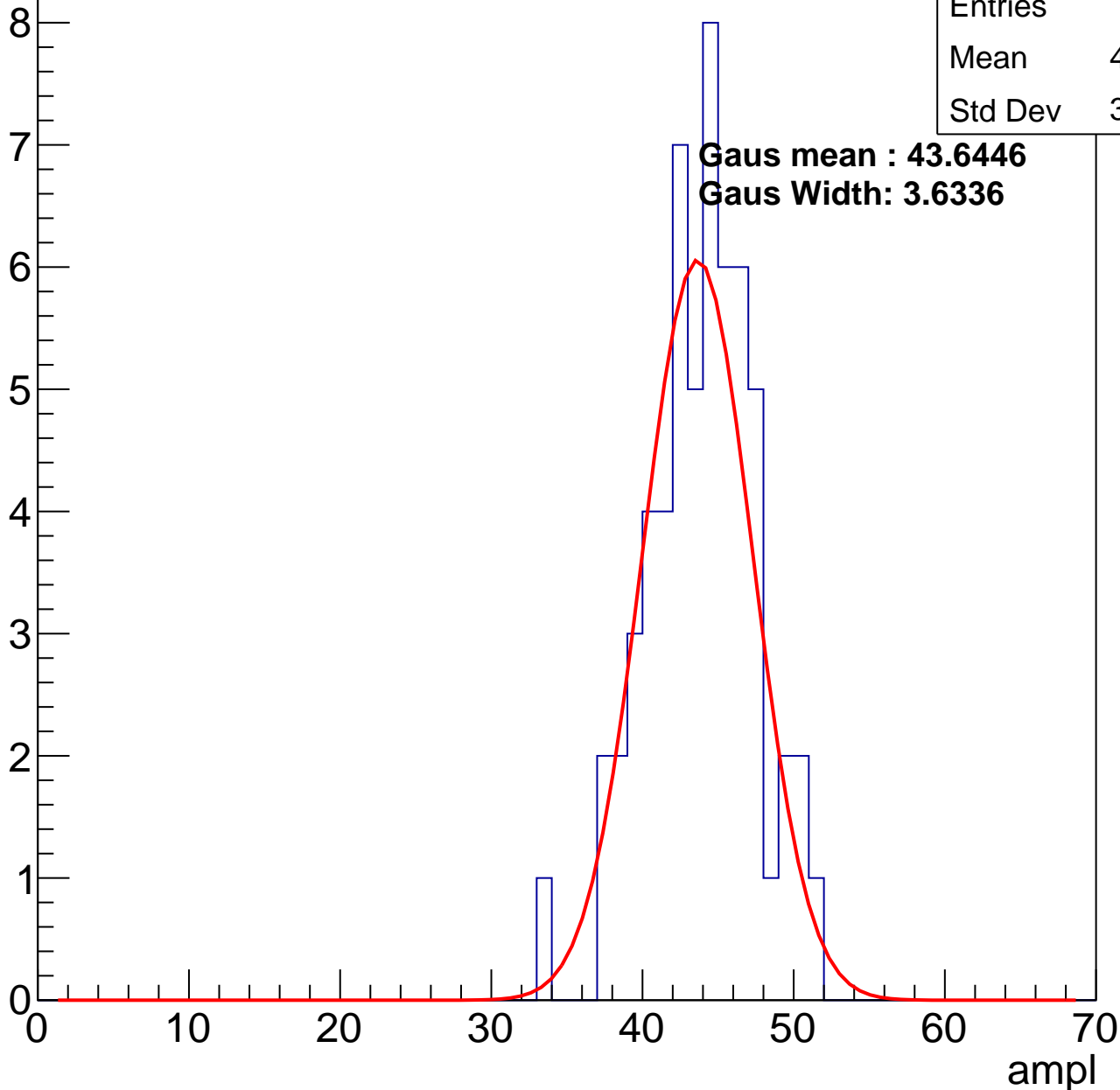
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.44
Std Dev	3.548

Gaus mean : 43.6446

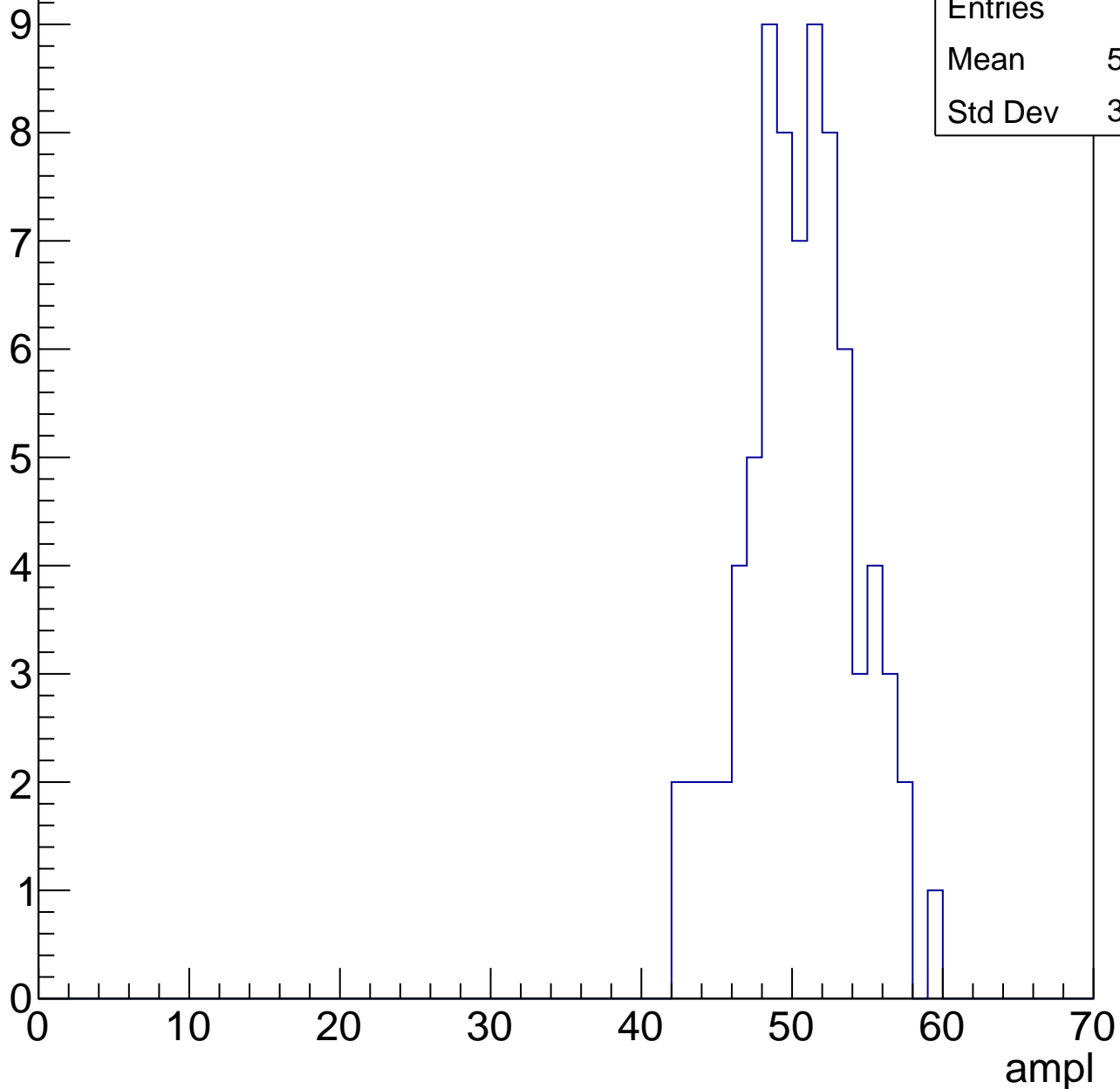
Gaus Width: 3.6336



B1L103S, U14-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

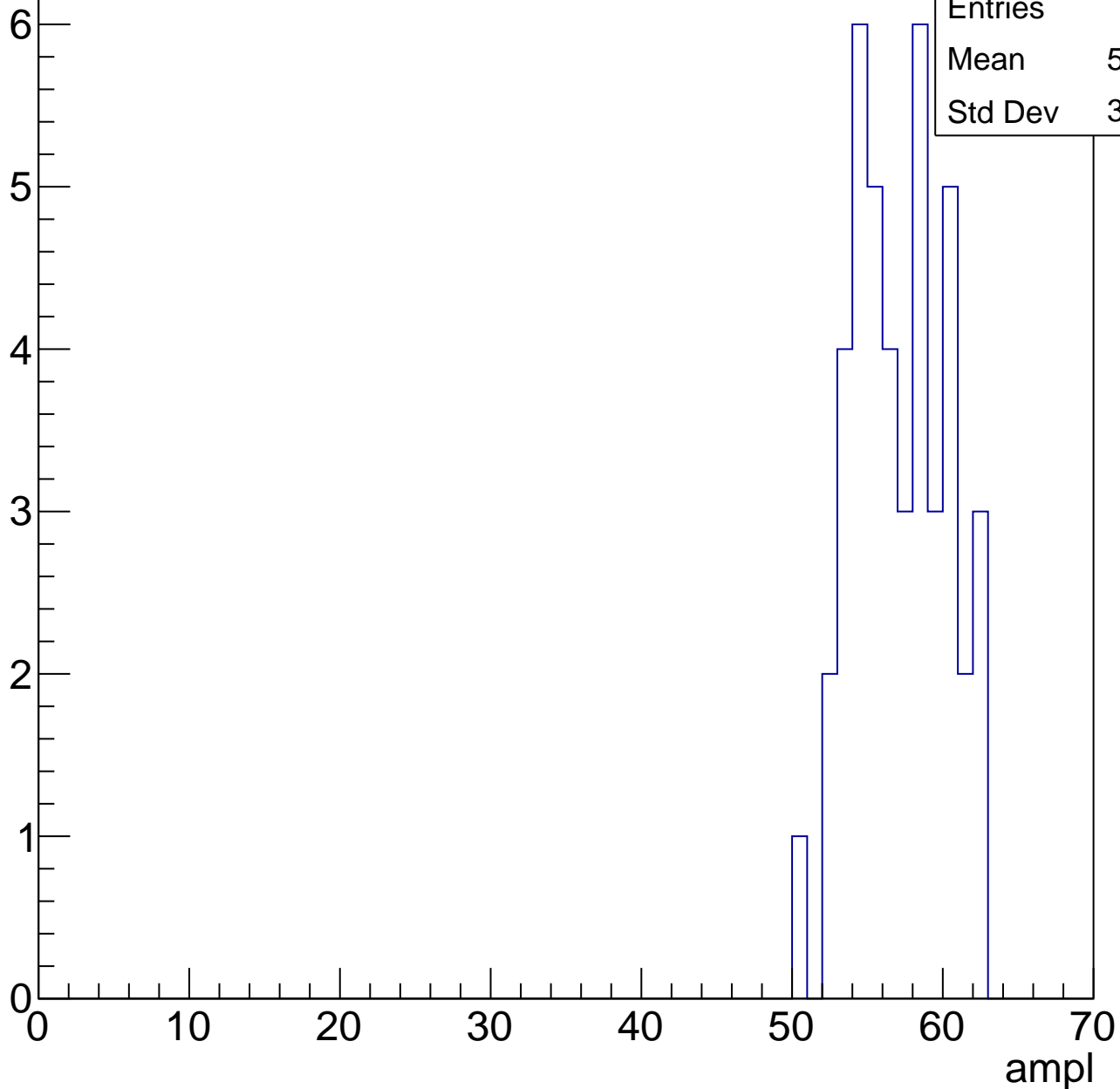


B1L103S, U14-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	56.66
Std Dev	3.037

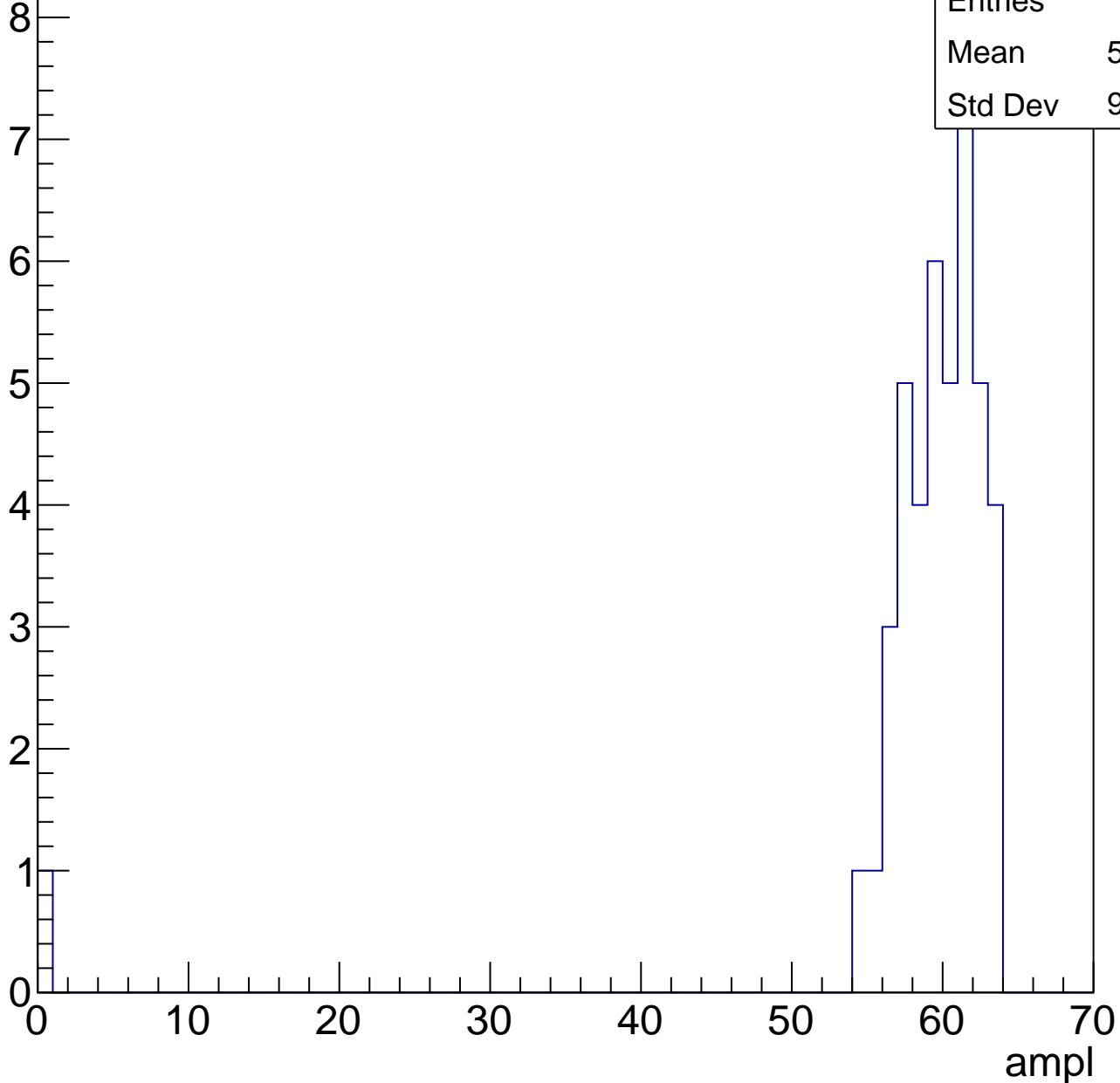


B1L103S, U14-ch71, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.09
Std Dev	9.256

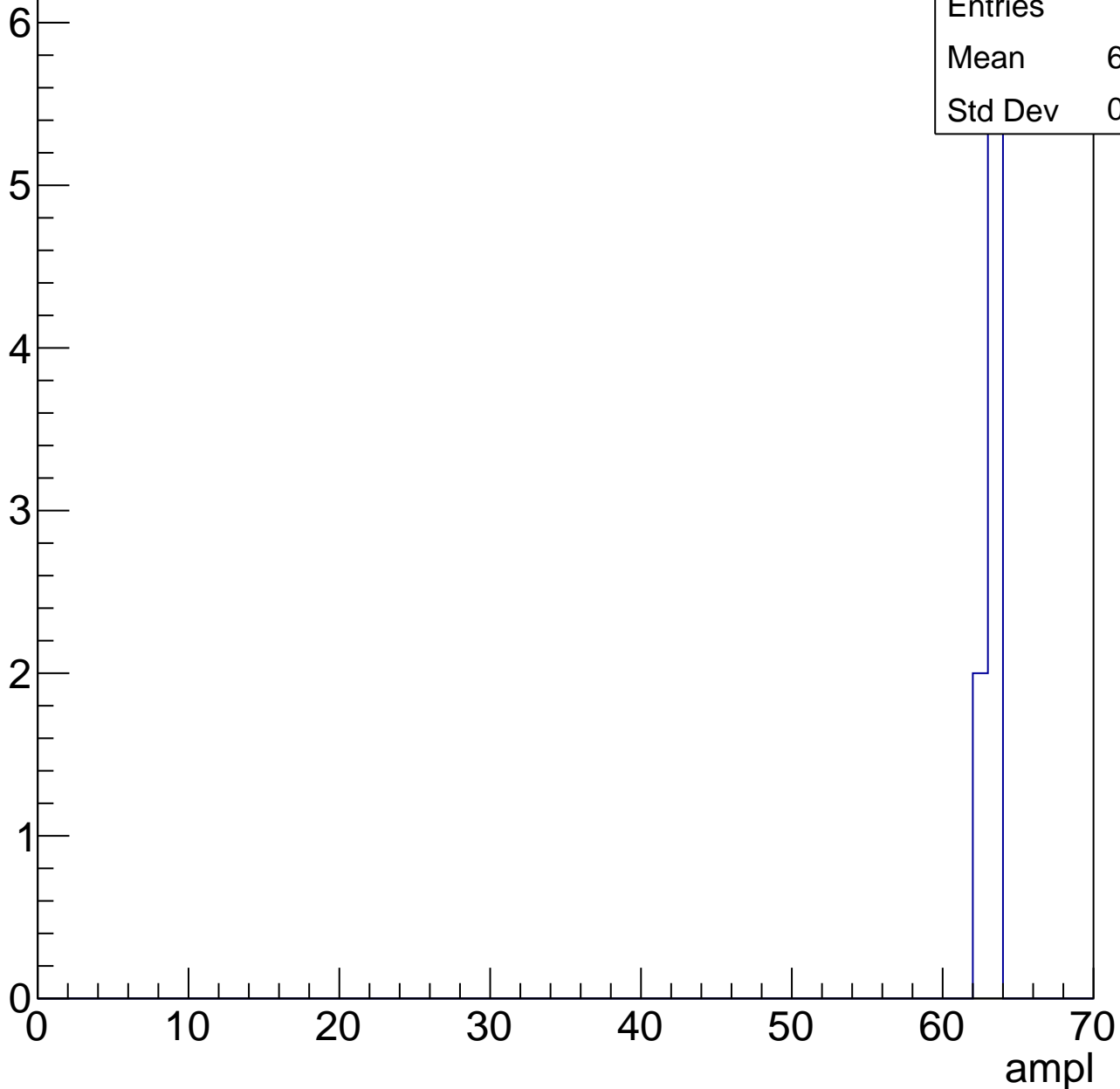


B1L103S, U14-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	62.75
Std Dev	0.433



B1L103S, U14-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch72, adc0

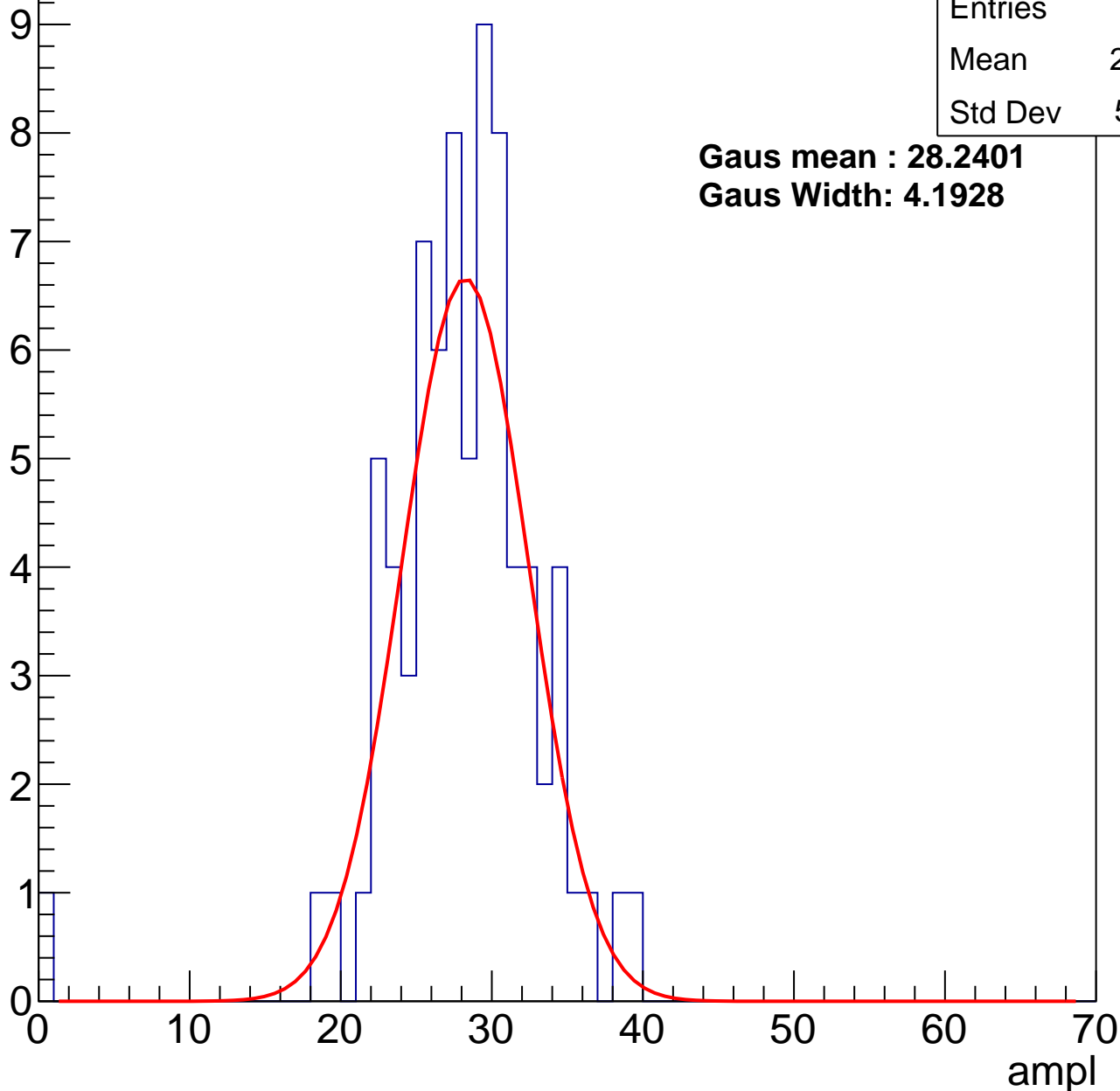
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	27.56
Std Dev	5.211

Gaus mean : 28.2401

Gaus Width: 4.1928



B1L103S, U14-ch72, adc1

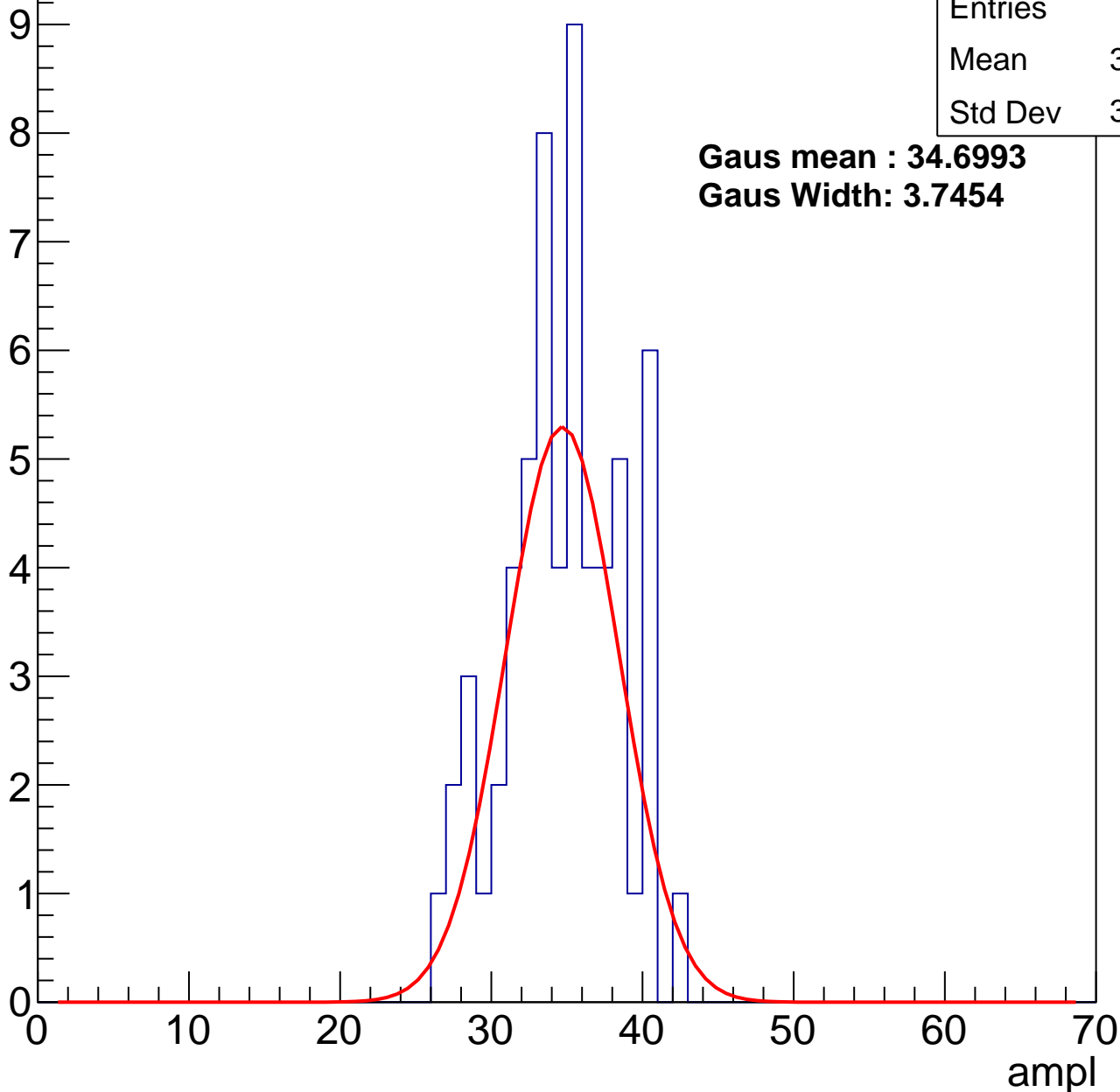
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	34.25
Std Dev	3.727

Gaus mean : 34.6993

Gaus Width: 3.7454



B1L103S, U14-ch72, adc2

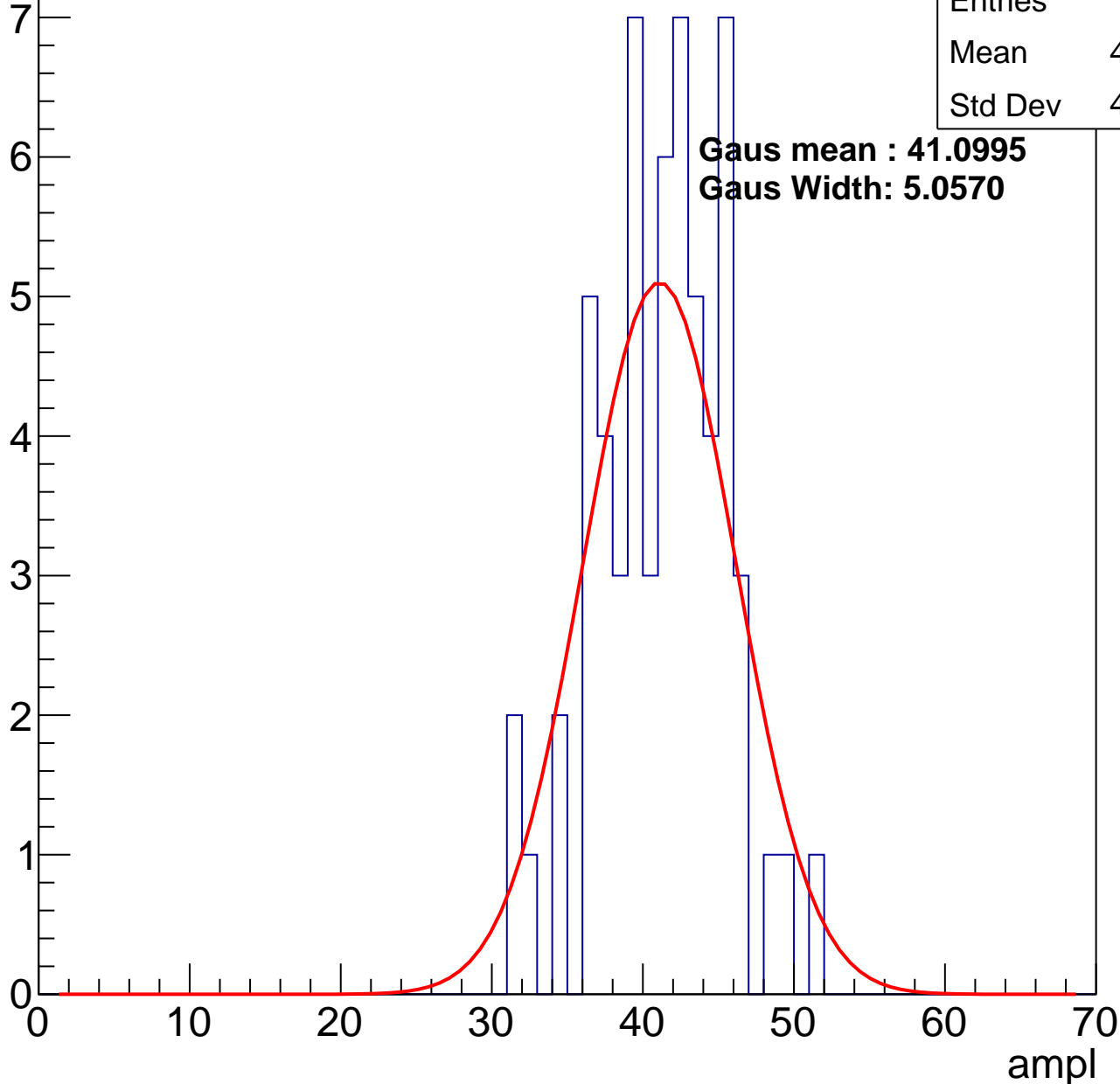
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	40.79
Std Dev	4.186

Gaus mean : 41.0995

Gaus Width: 5.0570

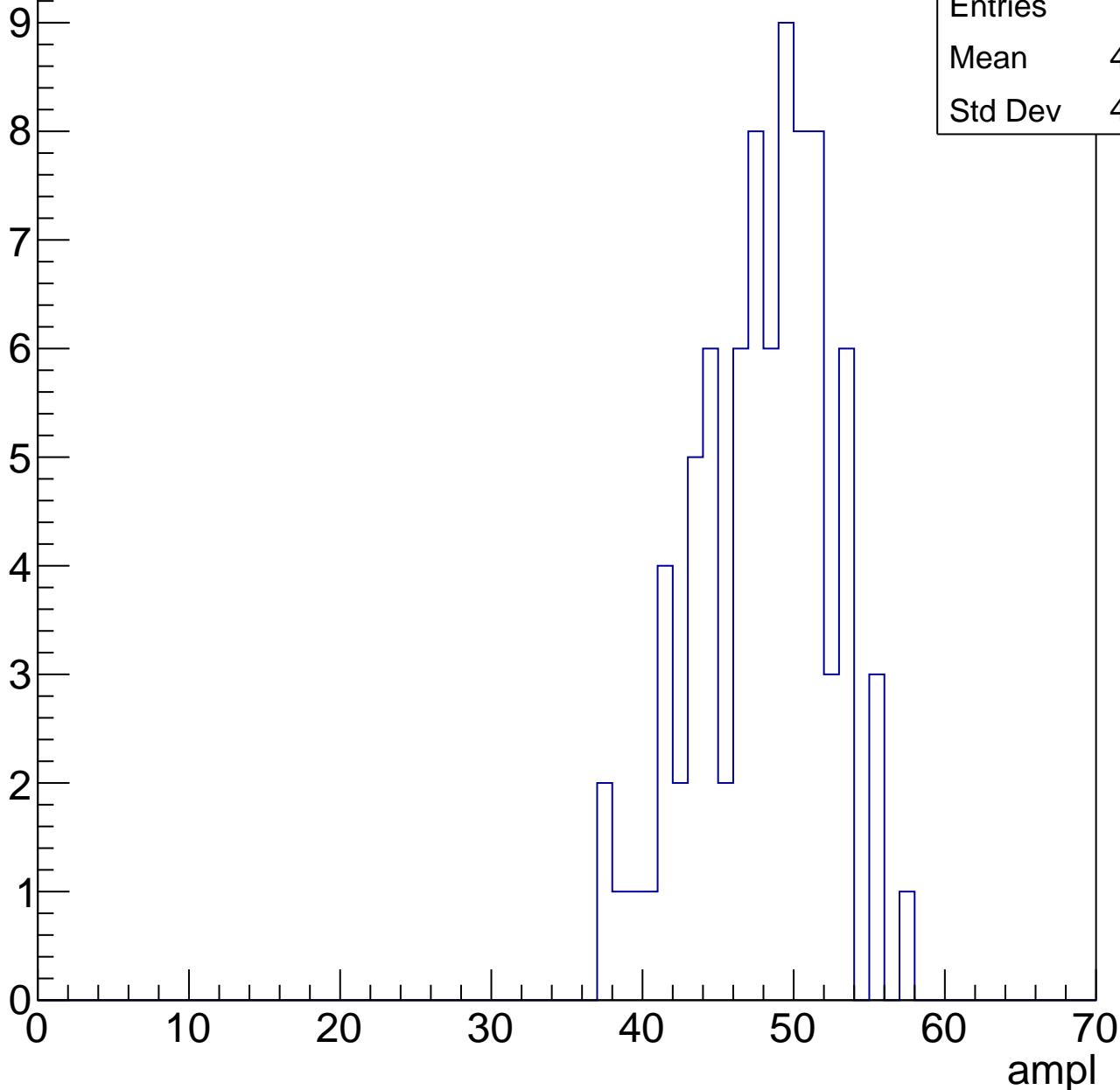


B1L103S, U14-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	47.48
Std Dev	4.337

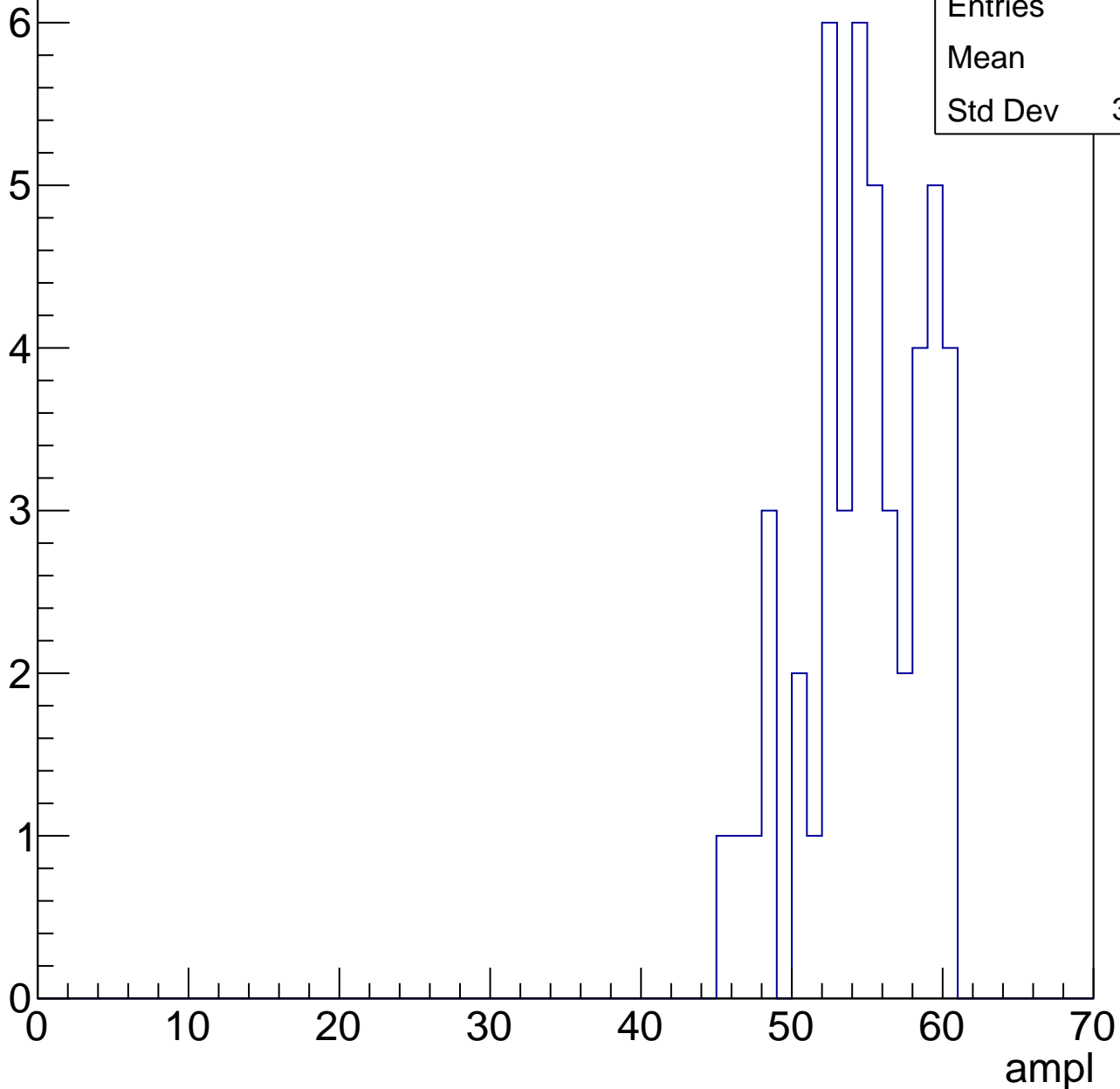


B1L103S, U14-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	54.3
Std Dev	3.951

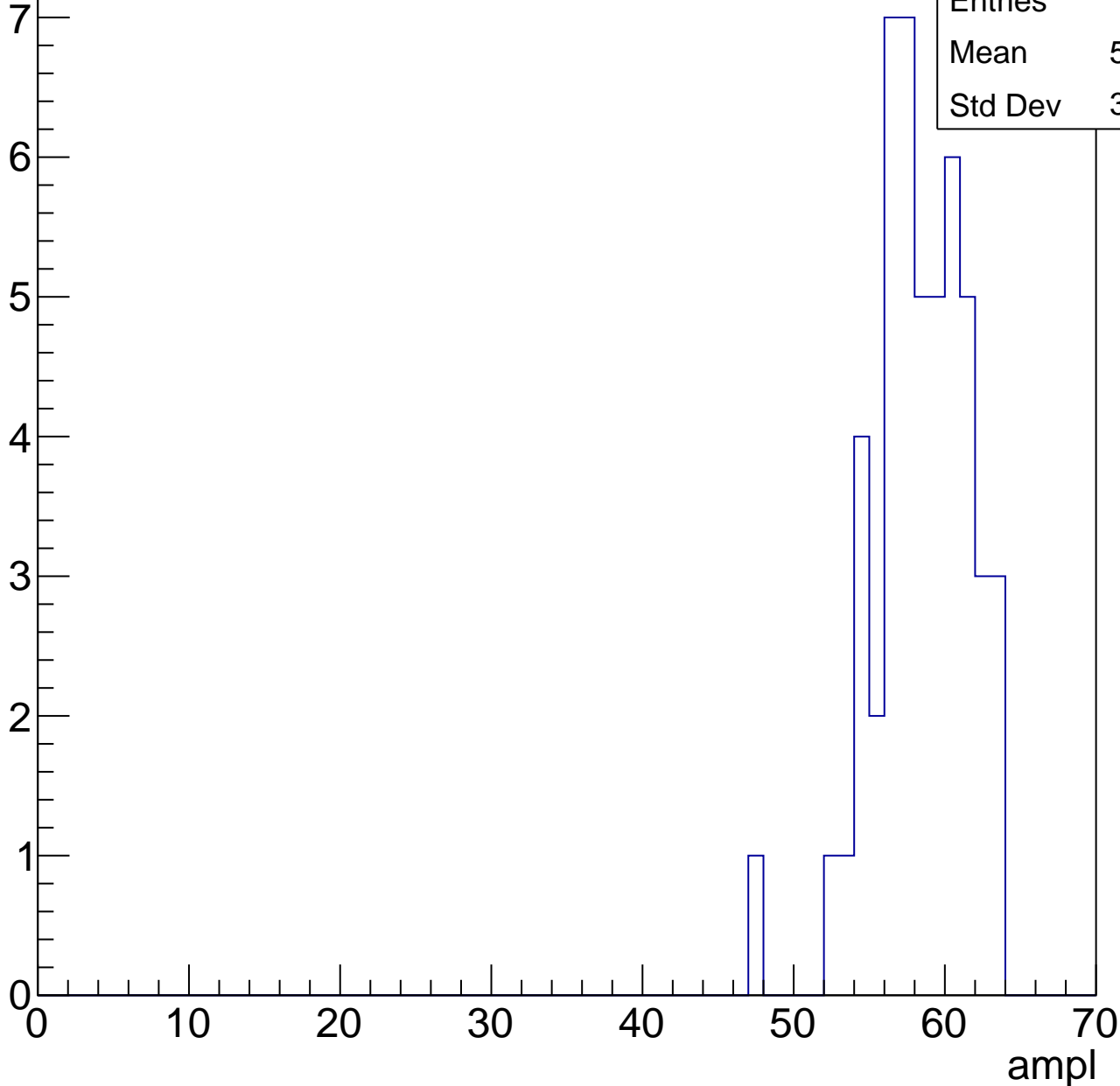


B1L103S, U14-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	57.88
Std Dev	3.147

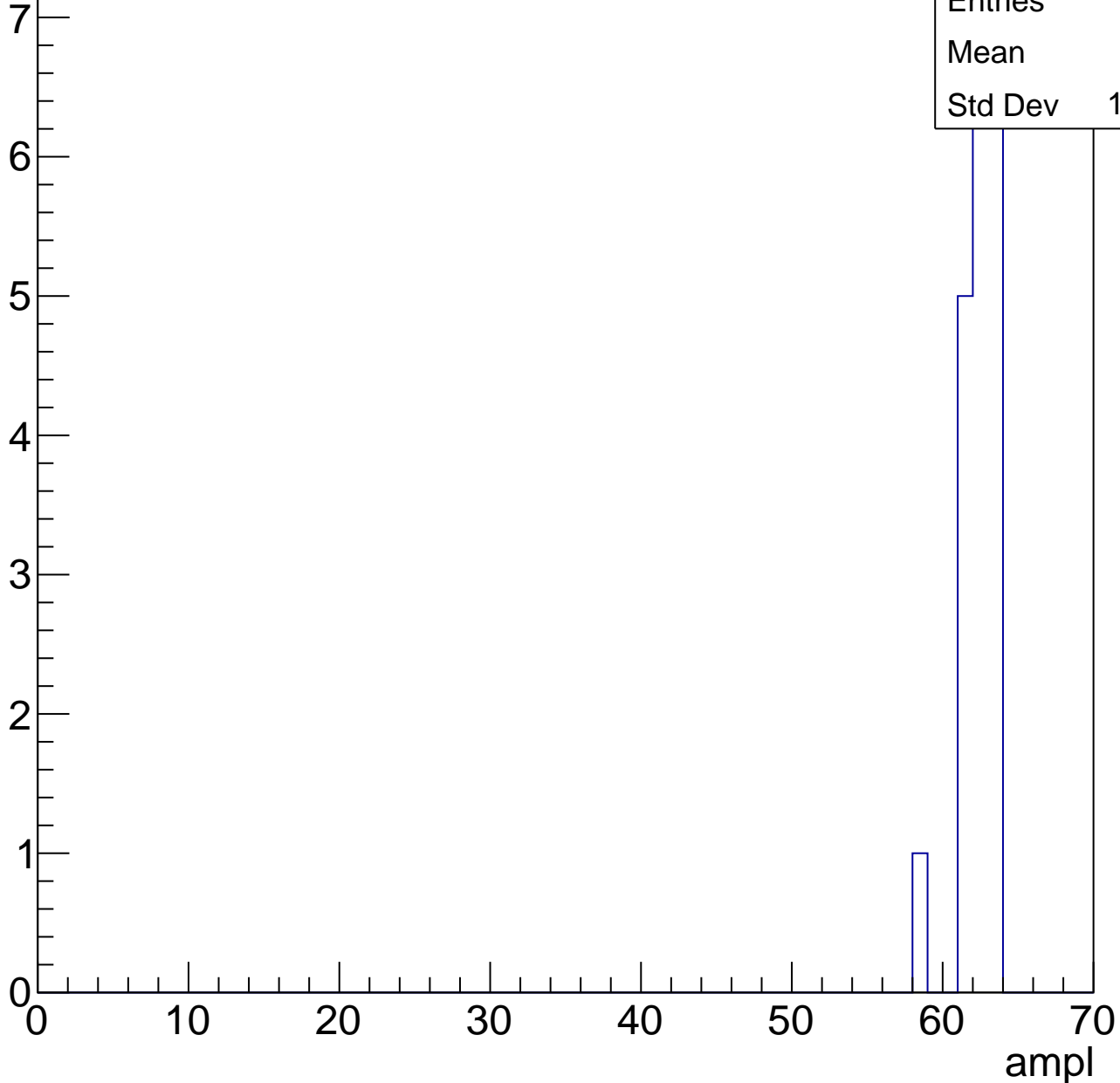


B1L103S, U14-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	20
Mean	61.9
Std Dev	1.179



B1L103S, U14-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch73, adc0

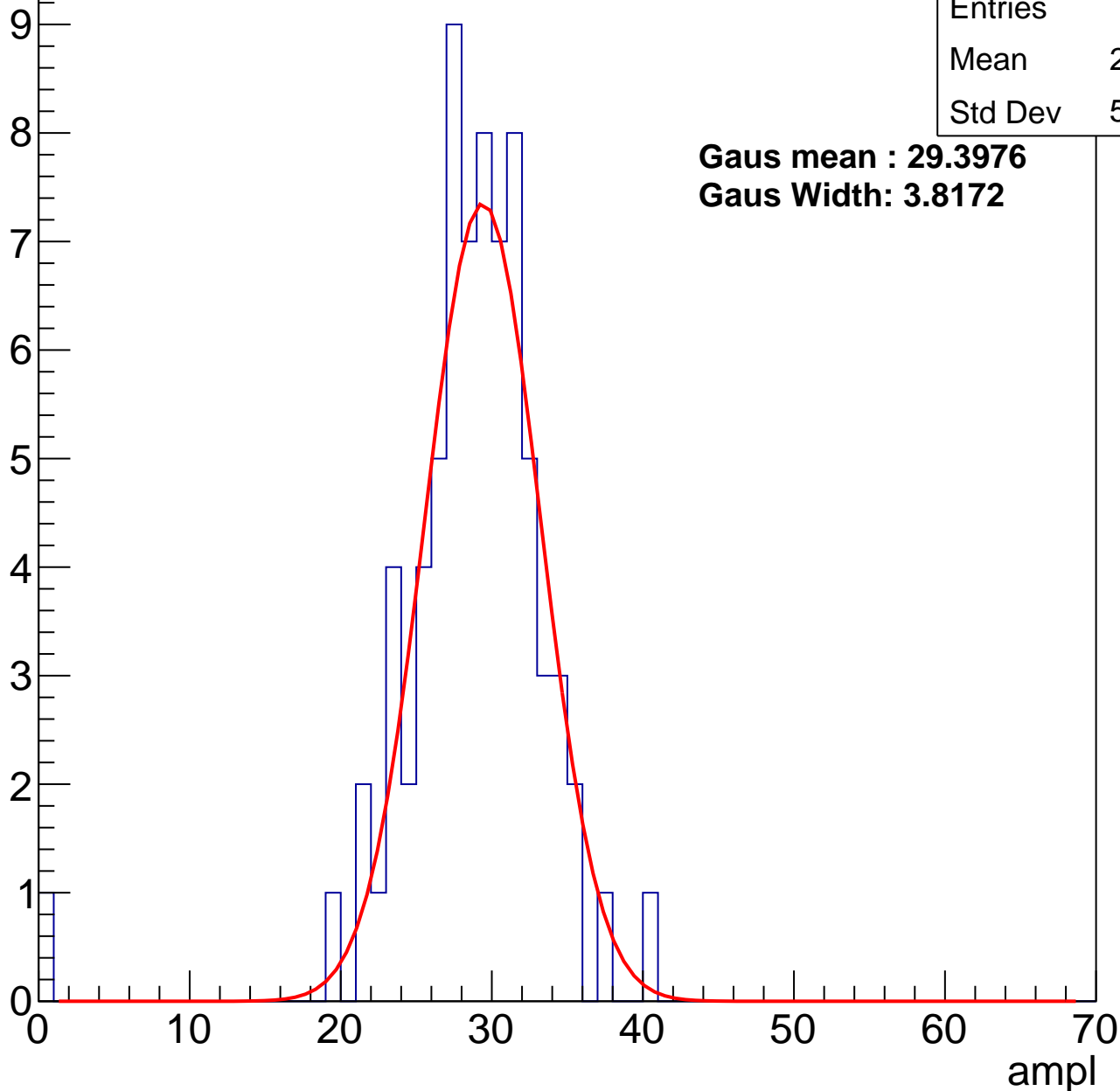
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.24
Std Dev	5.048

Gaus mean : 29.3976

Gaus Width: 3.8172



B1L103S, U14-ch73, adc1

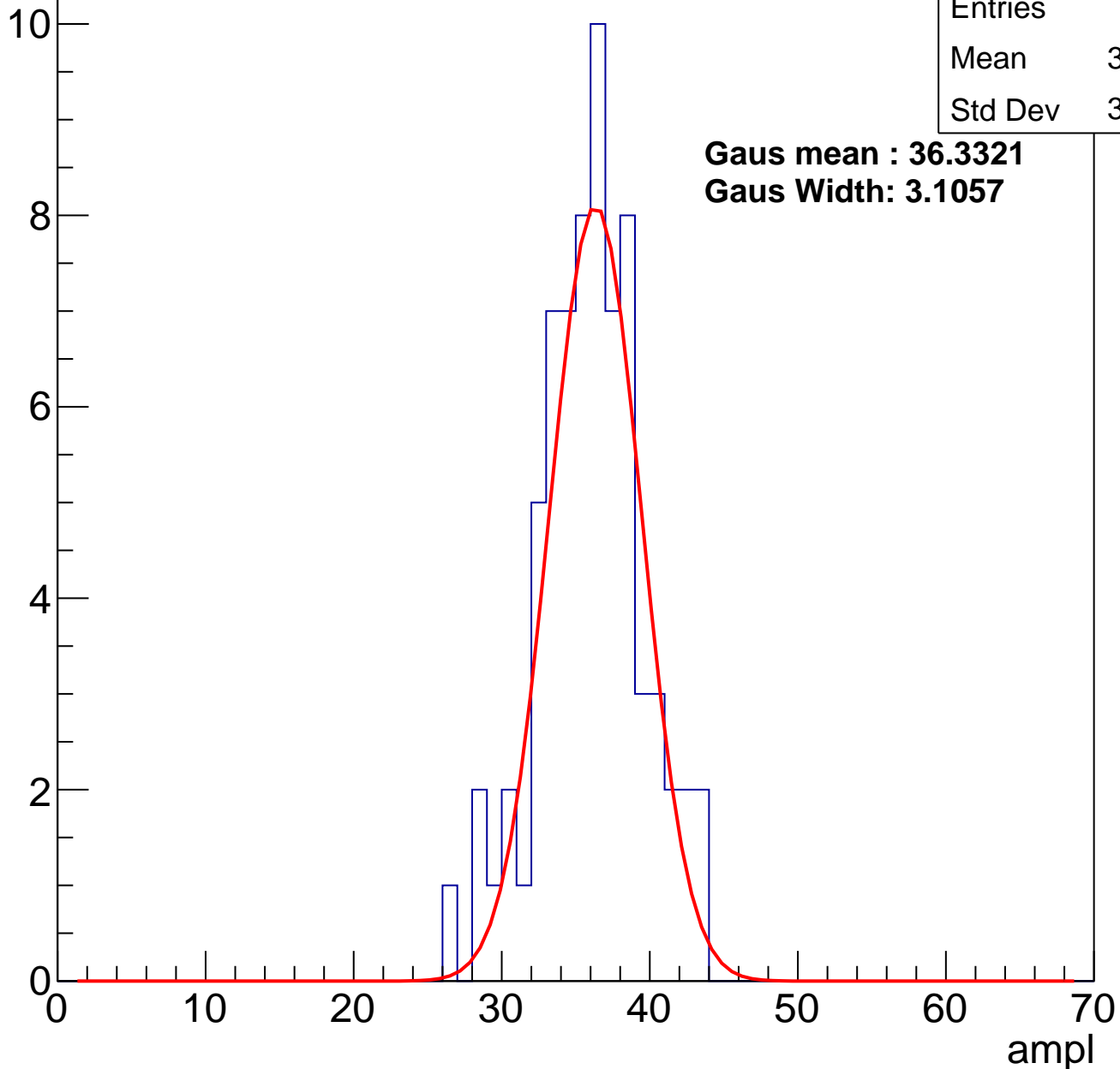
calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	35.54
Std Dev	3.504

Gaus mean : 36.3321

Gaus Width: 3.1057

Entry



B1L103S, U14-ch73, adc2

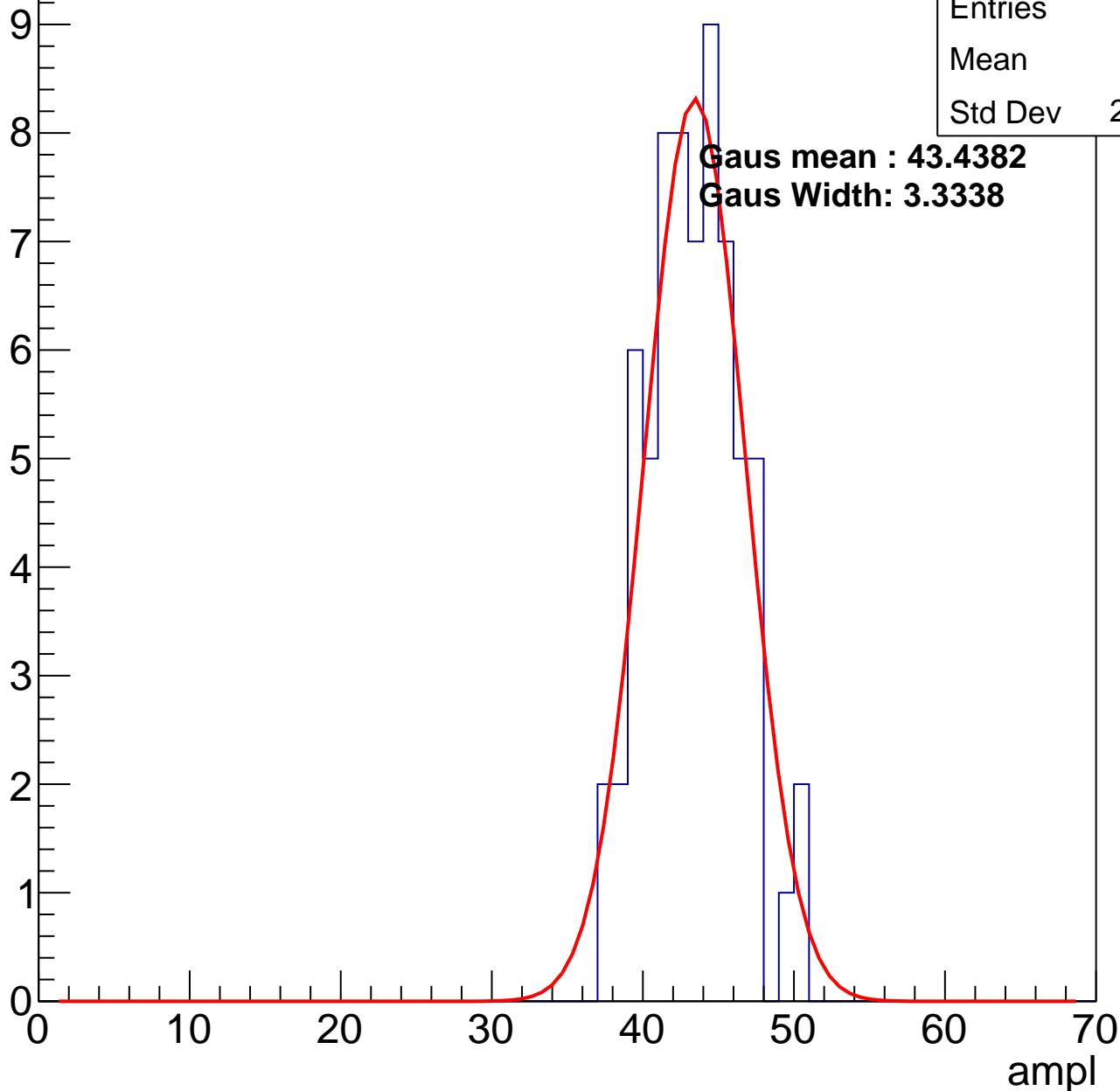
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.9
Std Dev	2.988

Gaus mean : 43.4382

Gaus Width: 3.3338

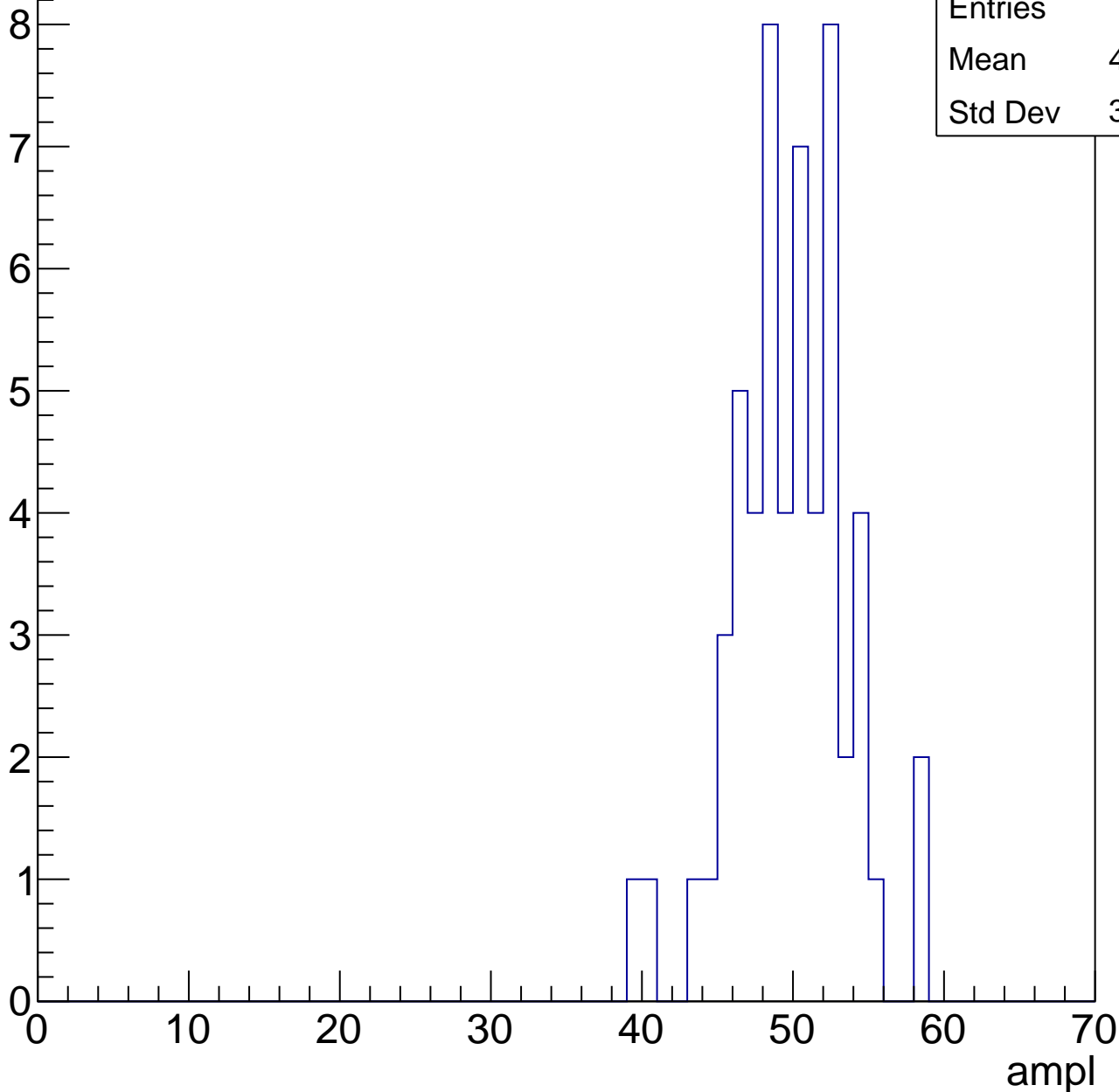


B1L103S, U14-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	49.32
Std Dev	3.728

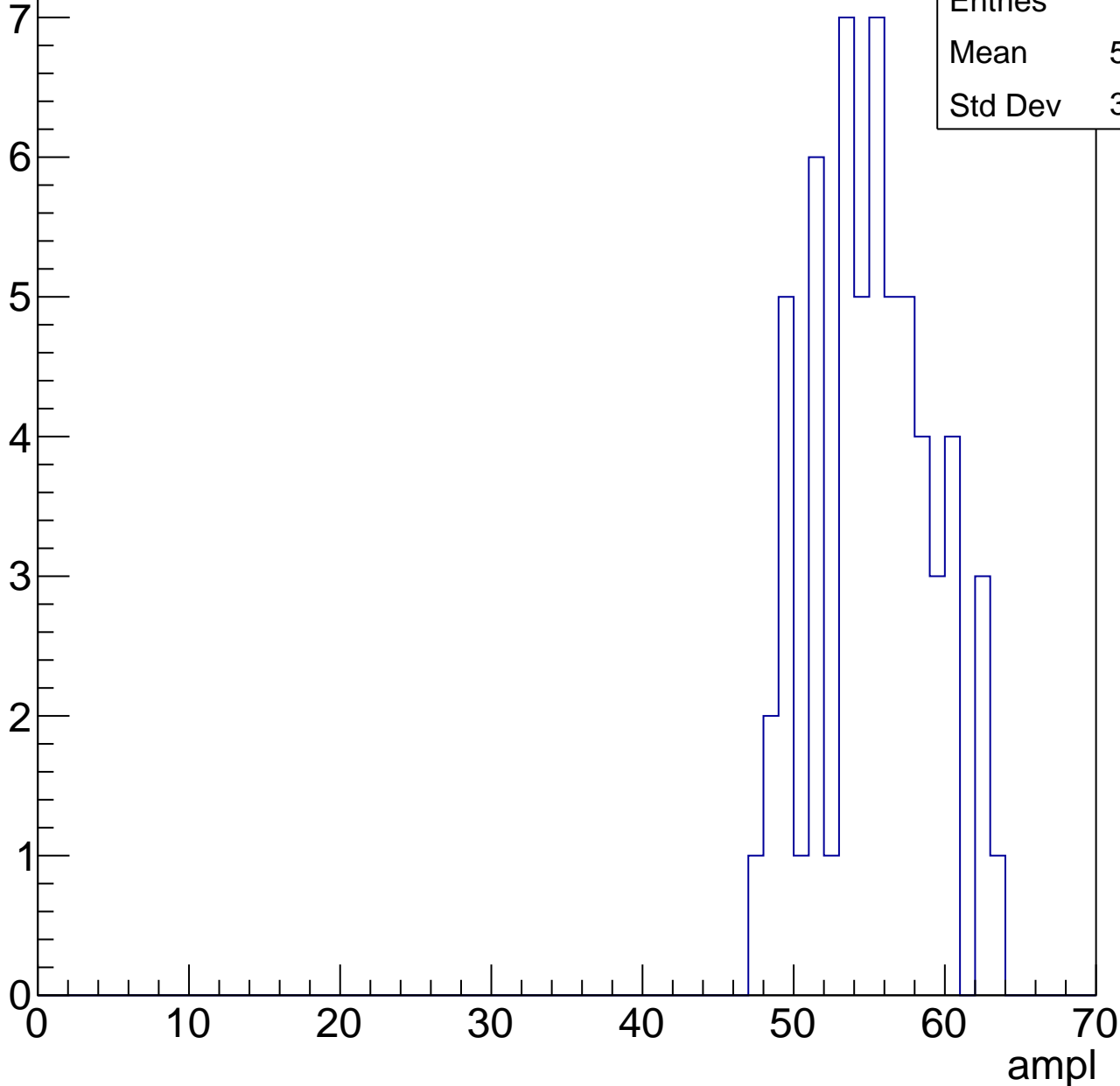


B1L103S, U14-ch73, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.75
Std Dev	3.918

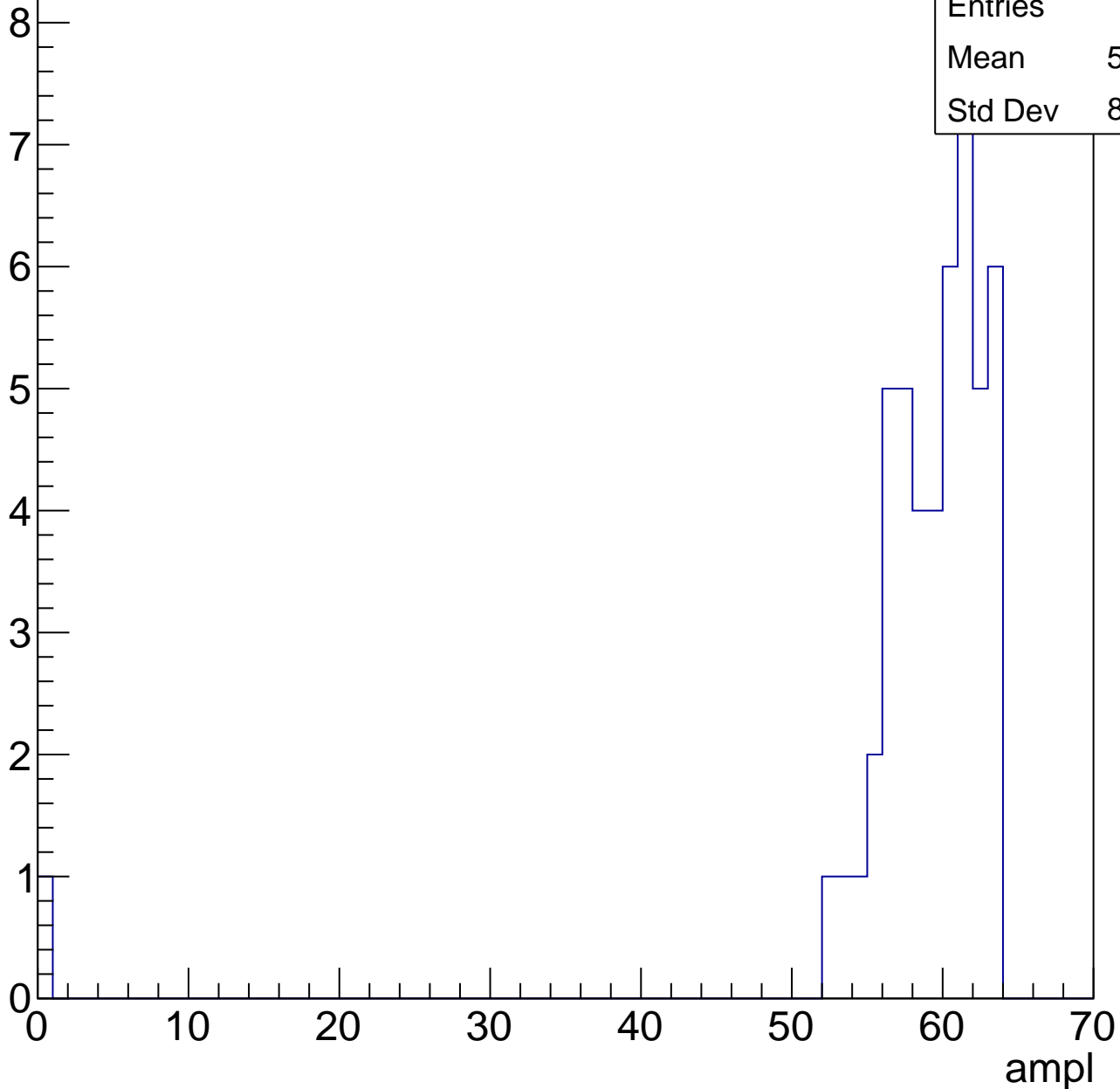


B1L103S, U14-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	57.92
Std Dev	8.822



B1L103S, U14-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	10
Mean	61.2
Std Dev	1.6

B1L103S, U14-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch74, adc0

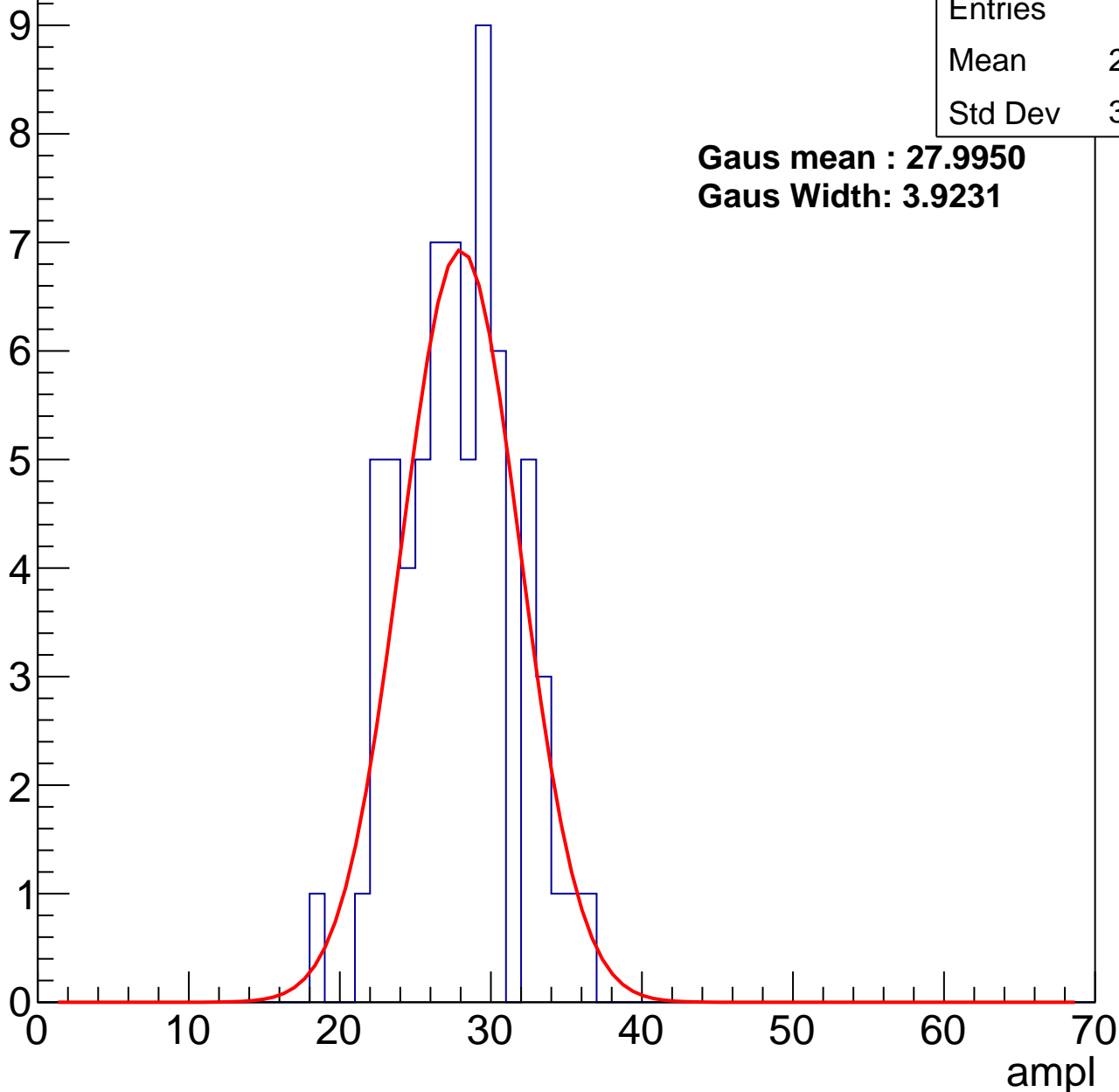
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	27.29
Std Dev	3.696

Gaus mean : 27.9950

Gaus Width: 3.9231



B1L103S, U14-ch74, adc1

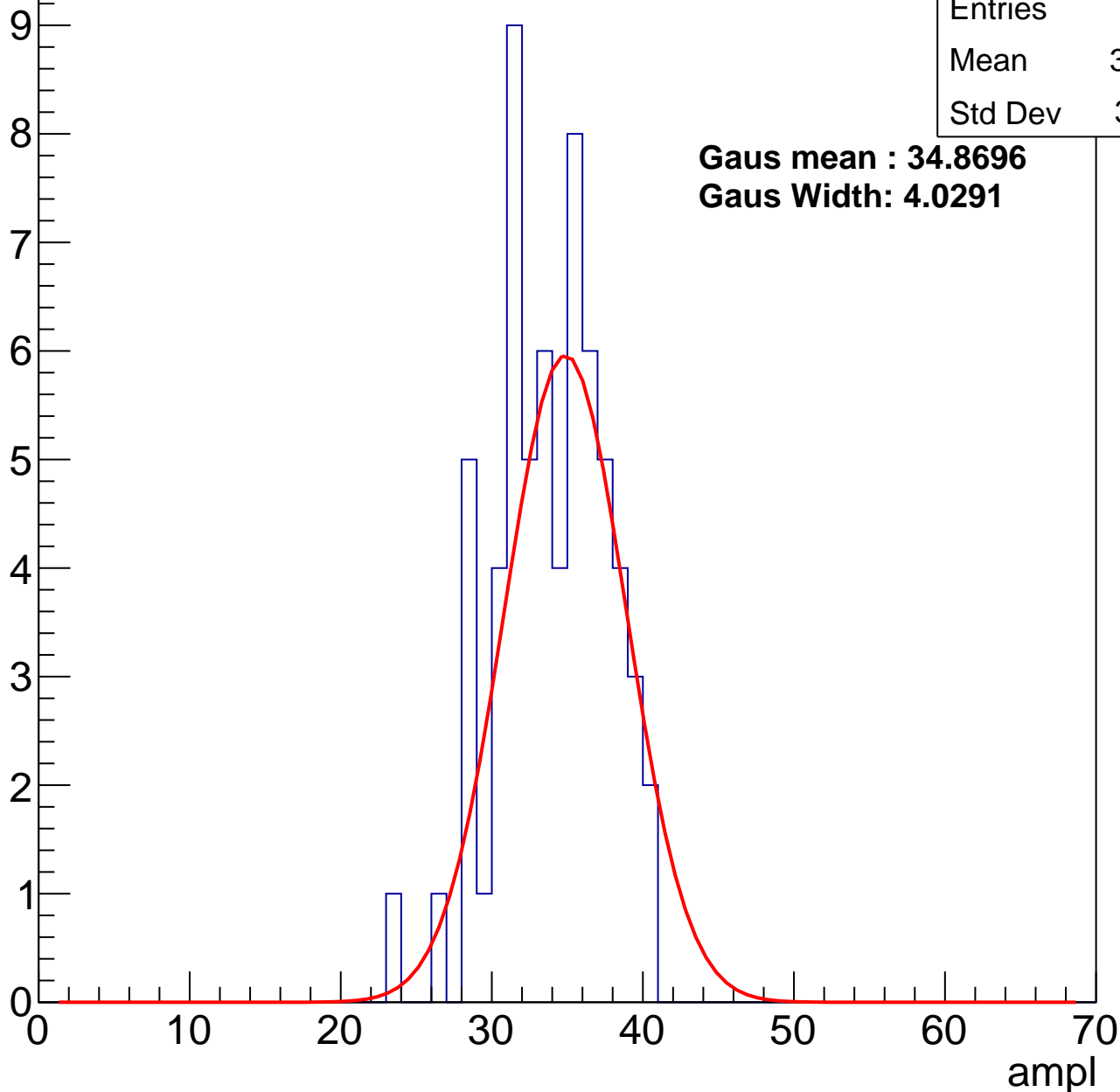
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	33.45
Std Dev	3.601

Gaus mean : 34.8696

Gaus Width: 4.0291



B1L103S, U14-ch74, adc2

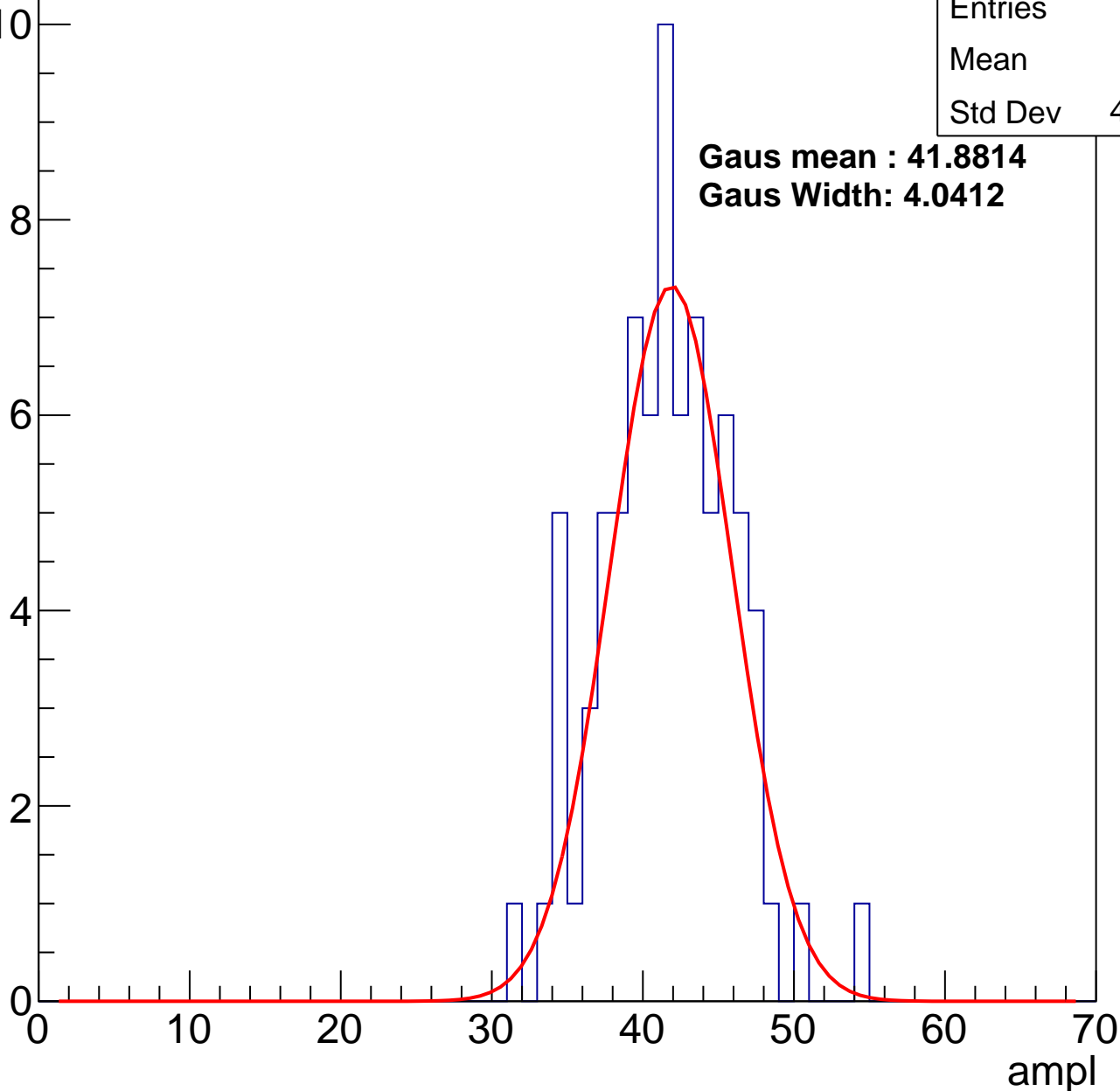
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	41.1
Std Dev	4.215

Gaus mean : 41.8814

Gaus Width: 4.0412



B1L103S, U14-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

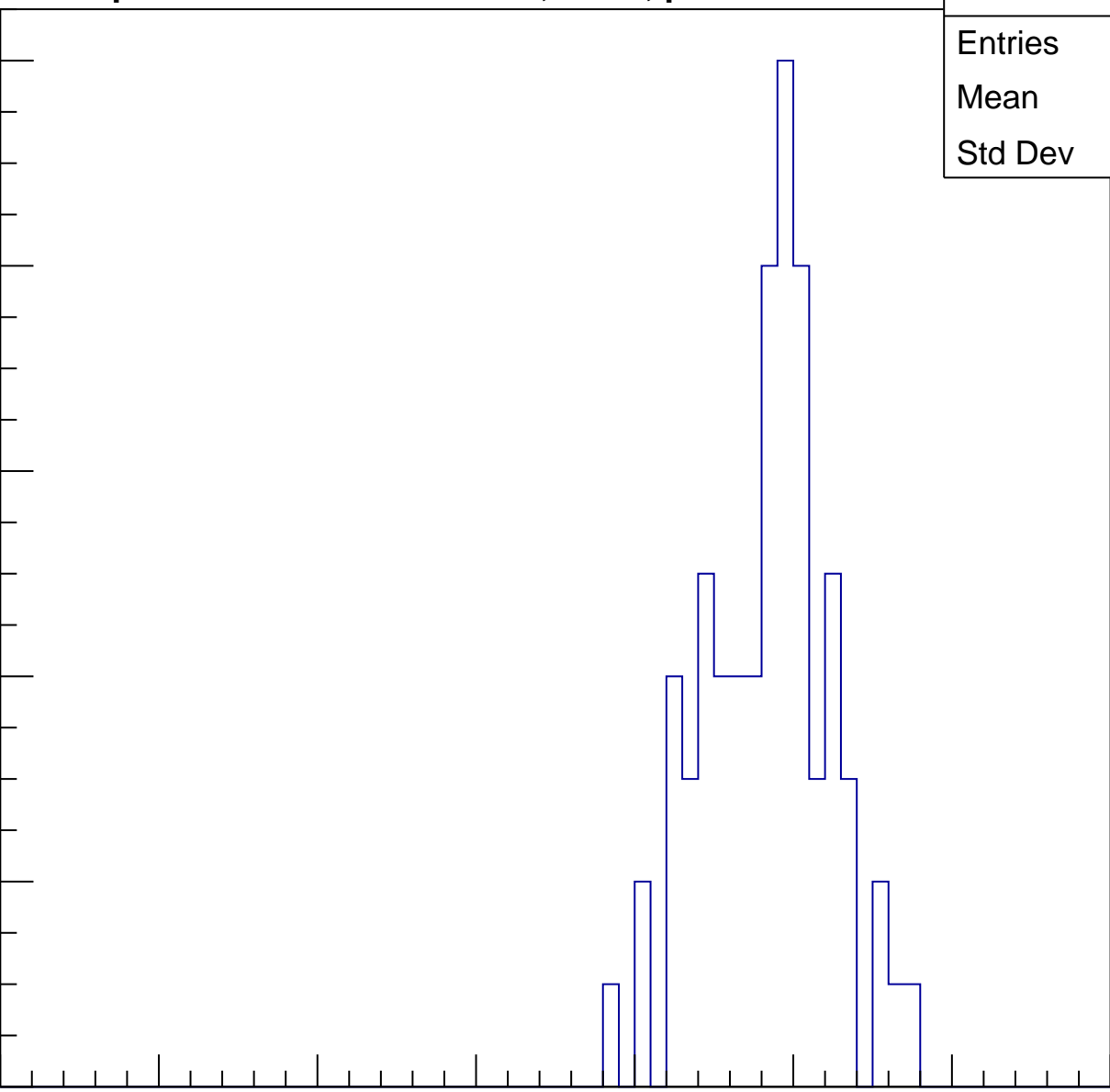
Entries	68
Mean	47.88
Std Dev	3.928

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

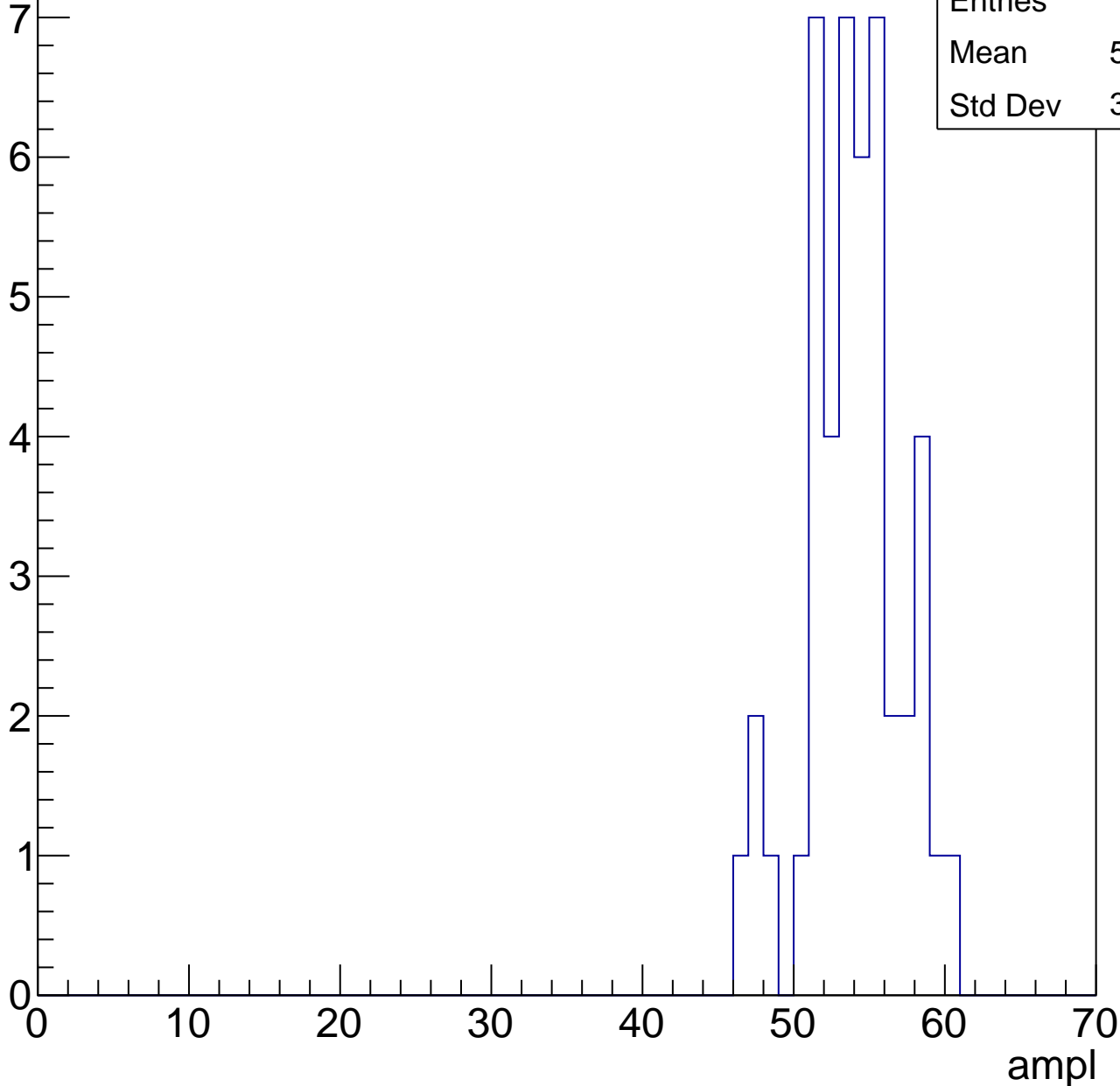


B1L103S, U14-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	53.48
Std Dev	3.112

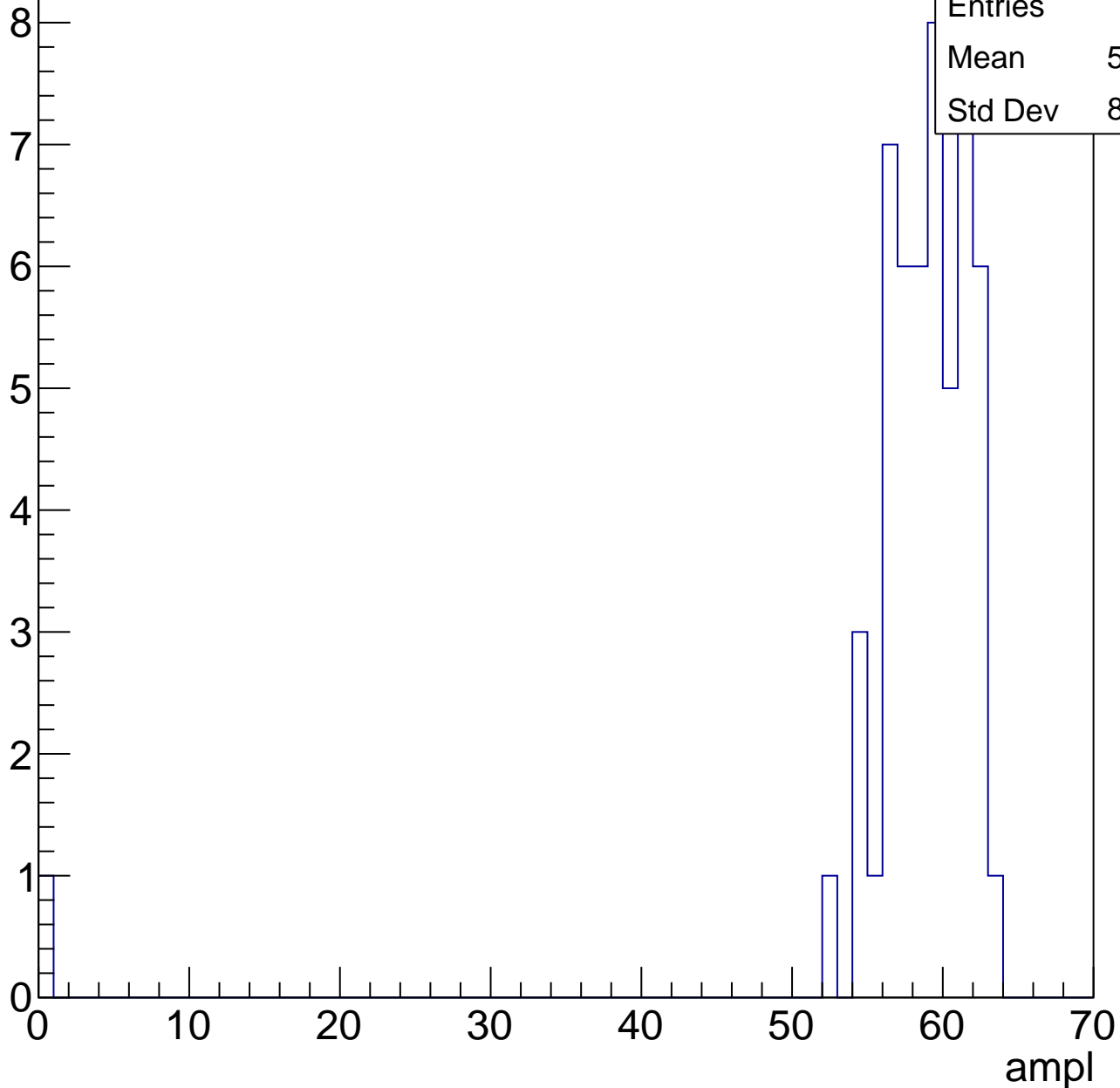


B1L103S, U14-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	57.47
Std Dev	8.352

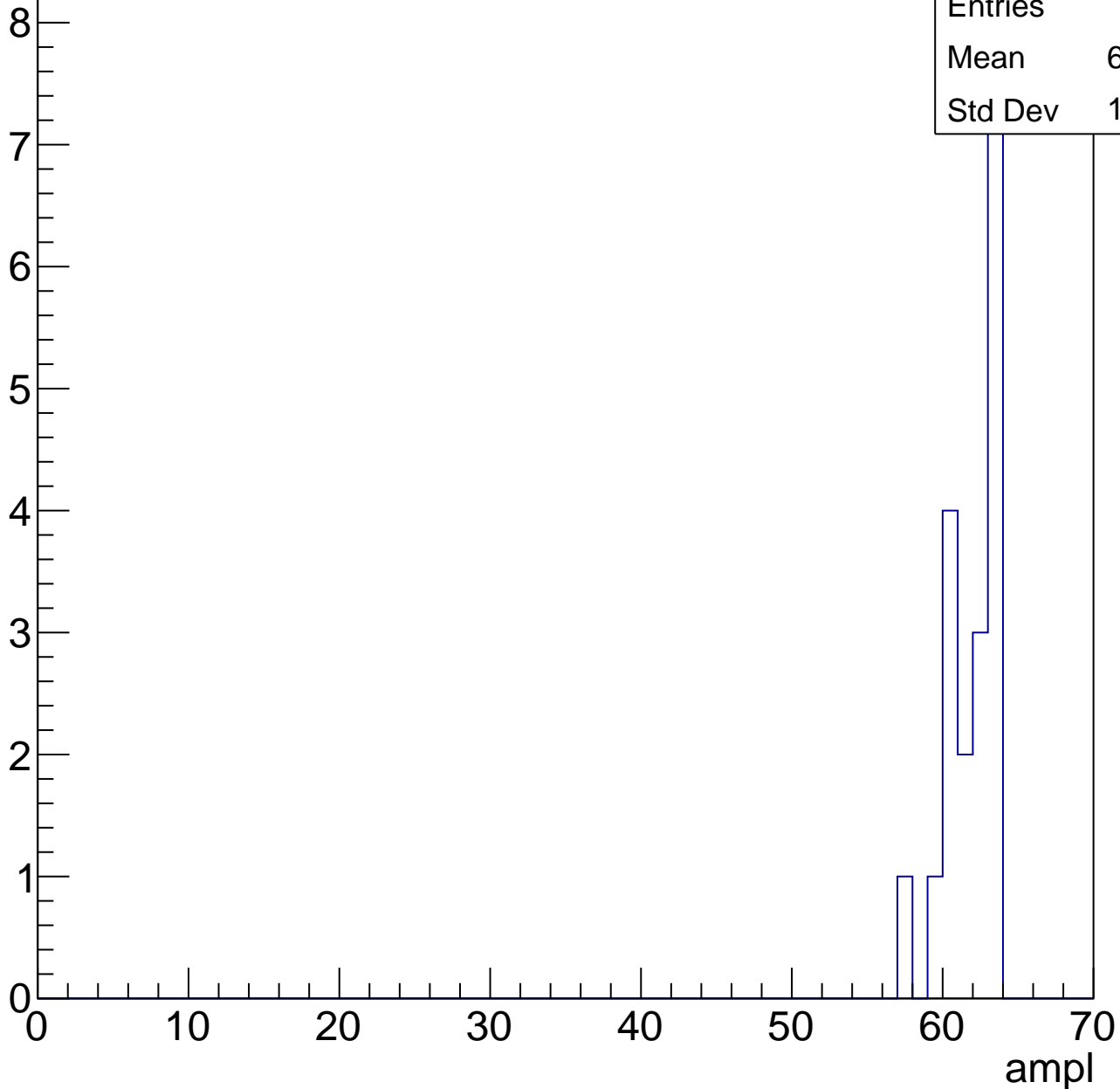


B1L103S, U14-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	61.47
Std Dev	1.697



B1L103S, U14-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch75, adc0

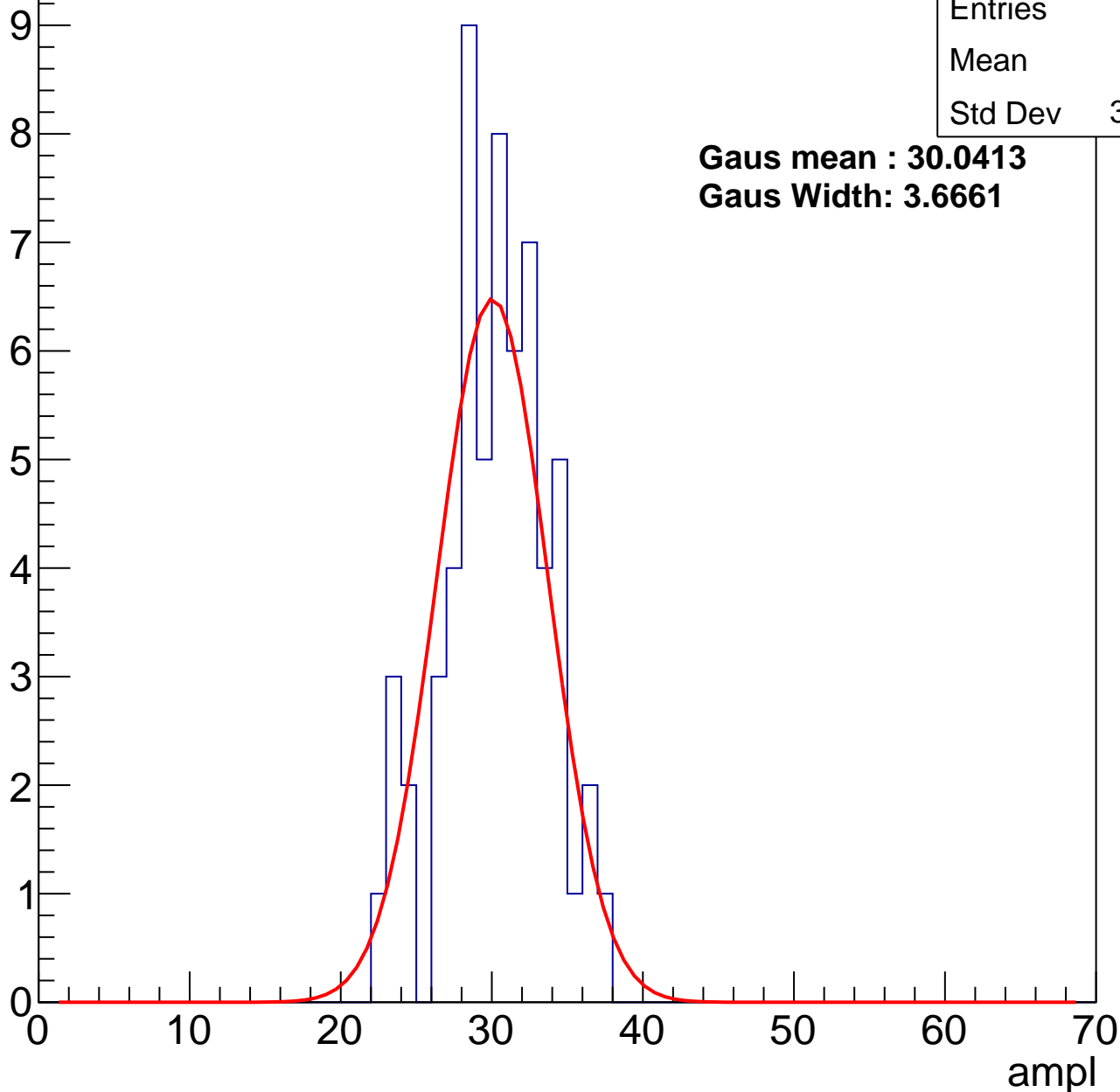
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	29.8
Std Dev	3.392

Gaus mean : 30.0413

Gaus Width: 3.6661



B1L103S, U14-ch75, adc1

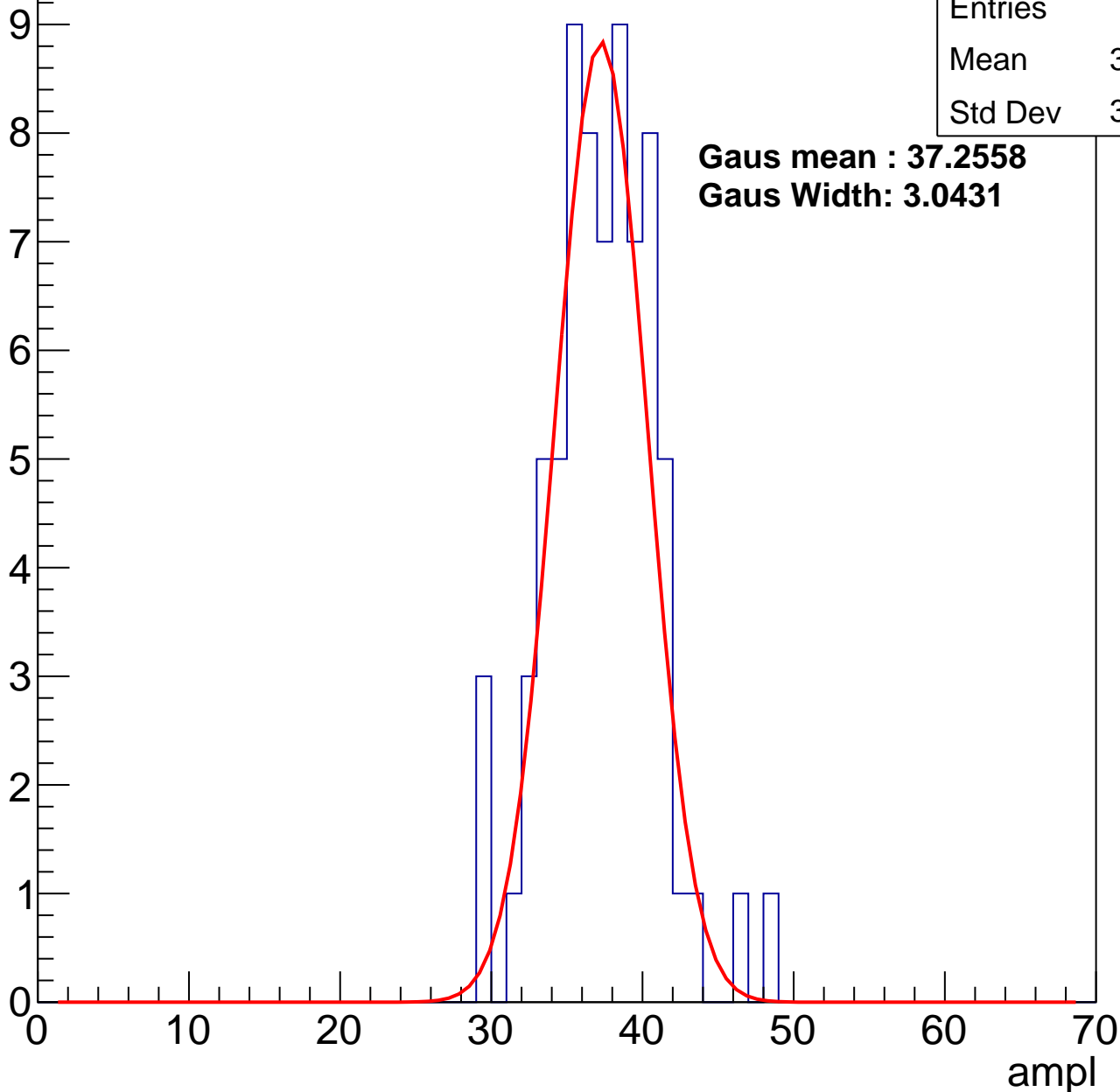
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.89
Std Dev	3.532

Gaus mean : 37.2558

Gaus Width: 3.0431



B1L103S, U14-ch75, adc2

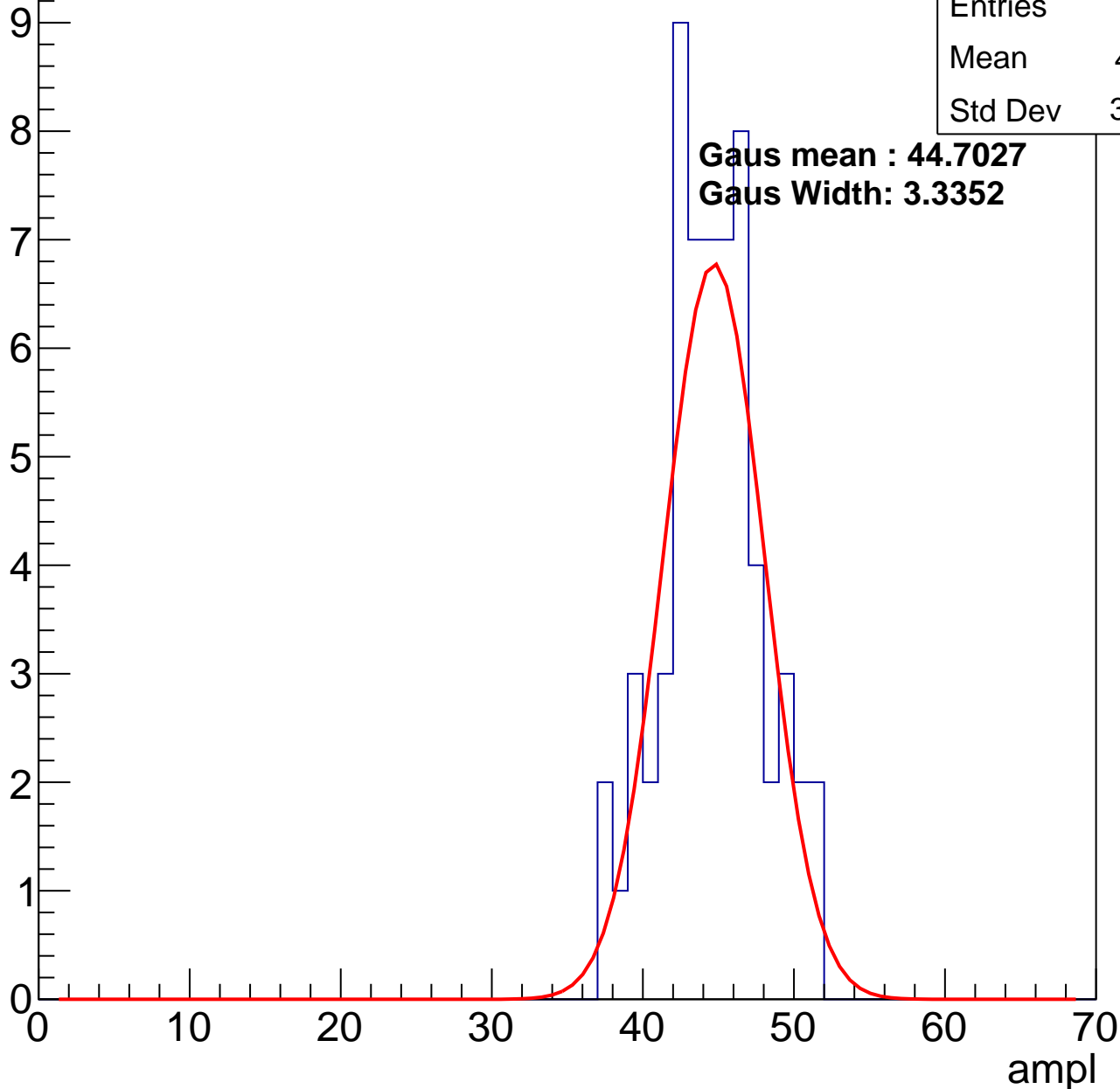
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	44.11
Std Dev	3.268

Gaus mean : 44.7027

Gaus Width: 3.3352

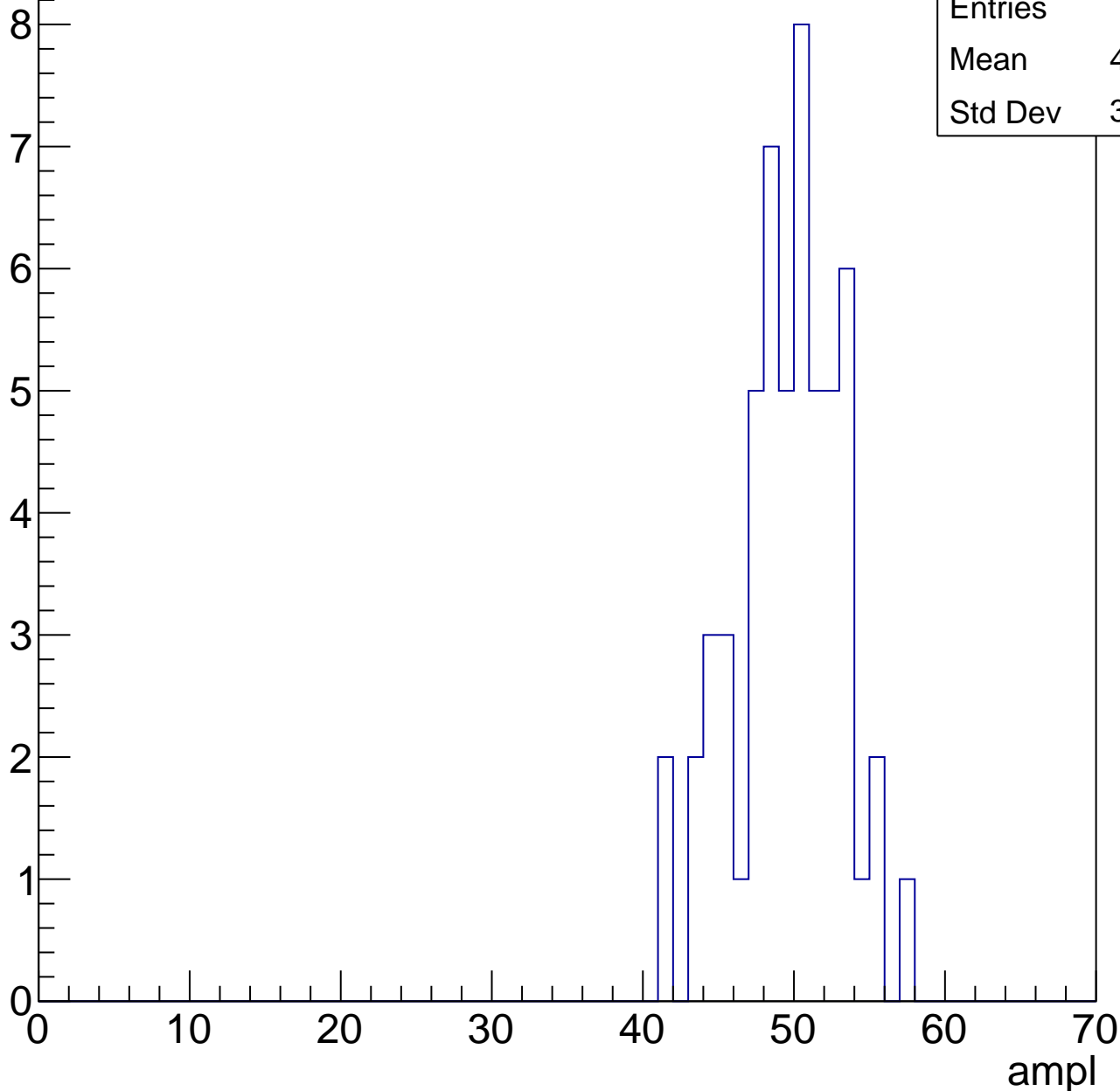


B1L103S, U14-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	49.12
Std Dev	3.505

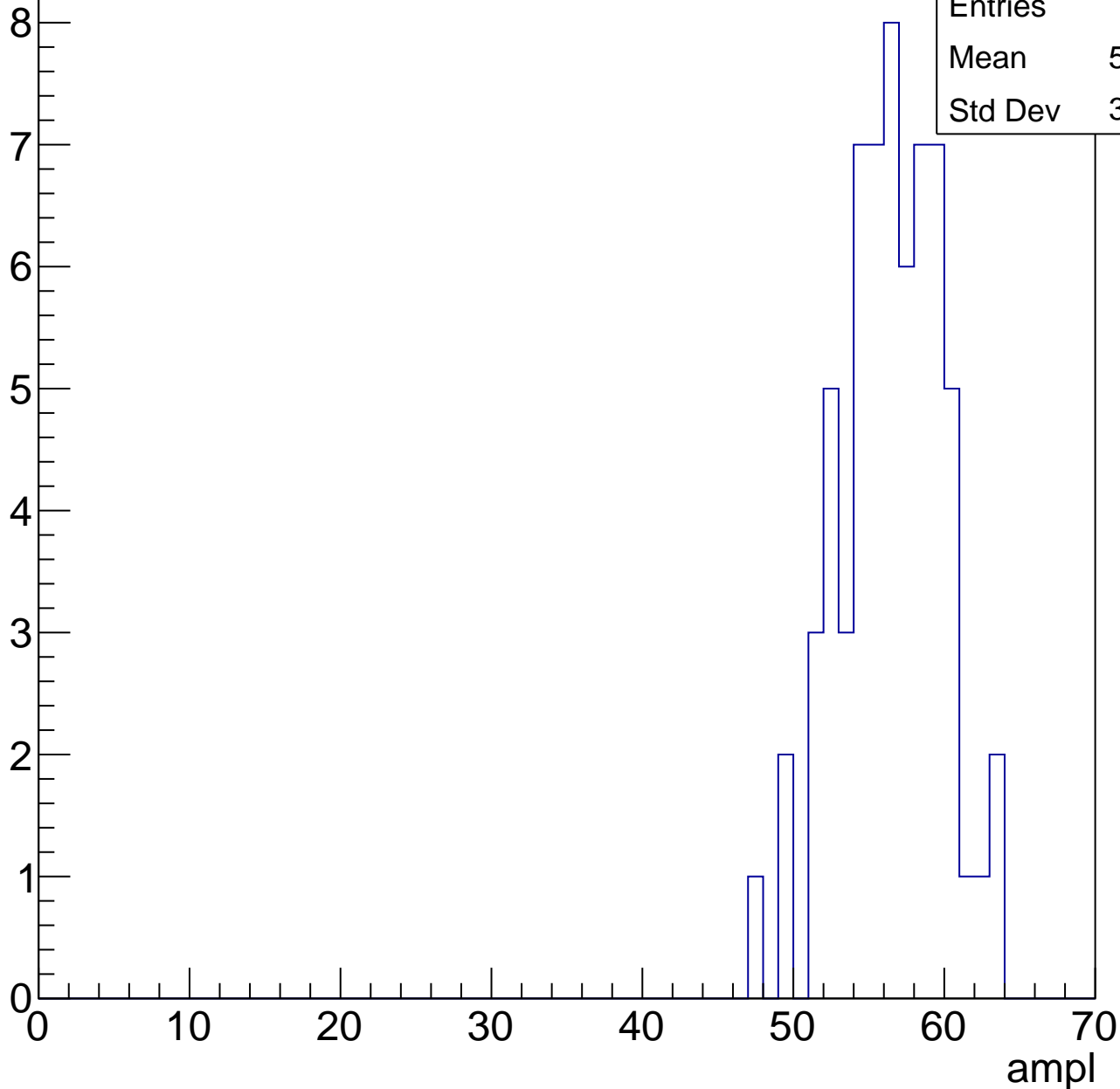


B1L103S, U14-ch75, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

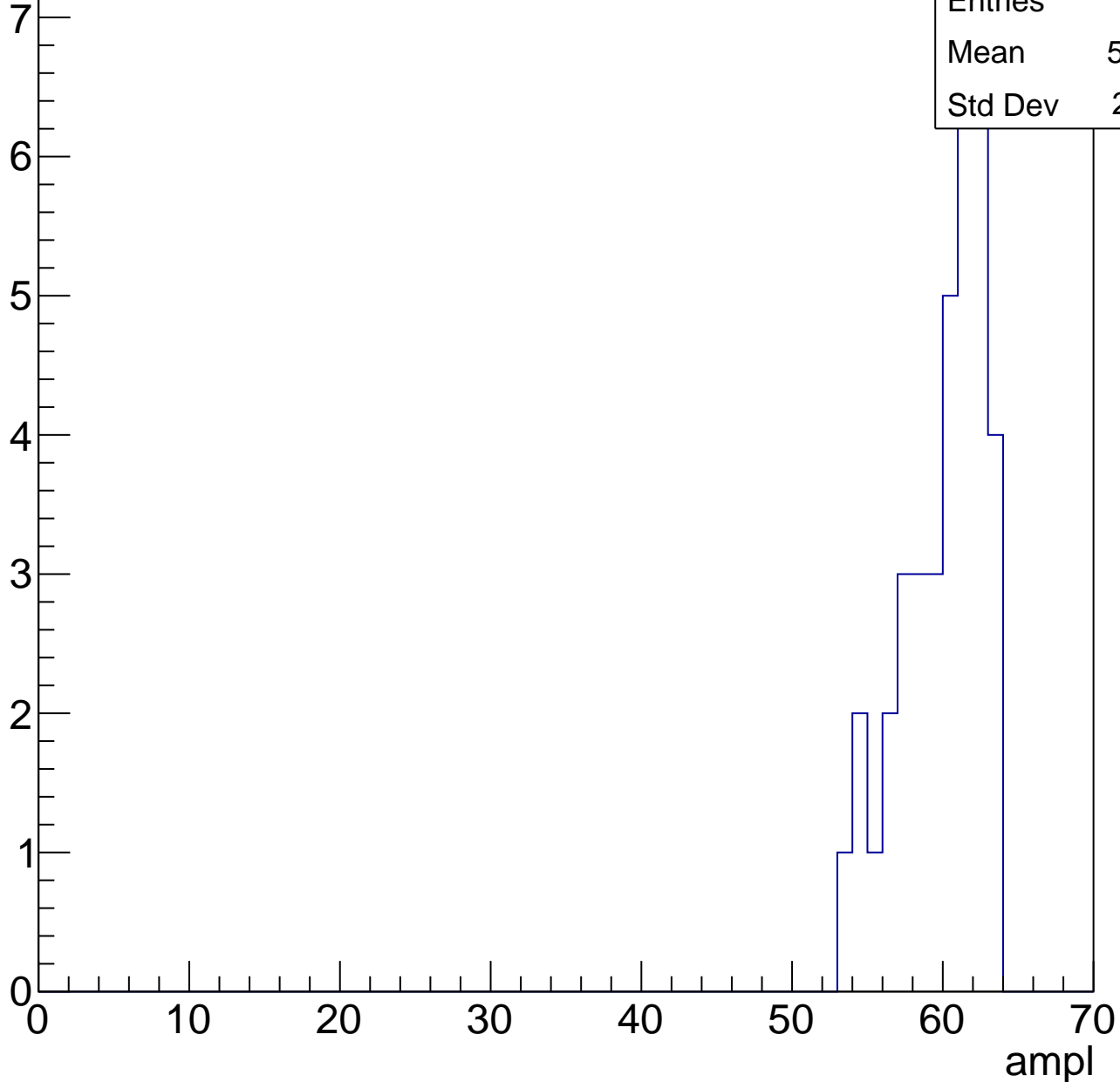
Entries	65
Mean	55.97
Std Dev	3.356



B1L103S, U14-ch75, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

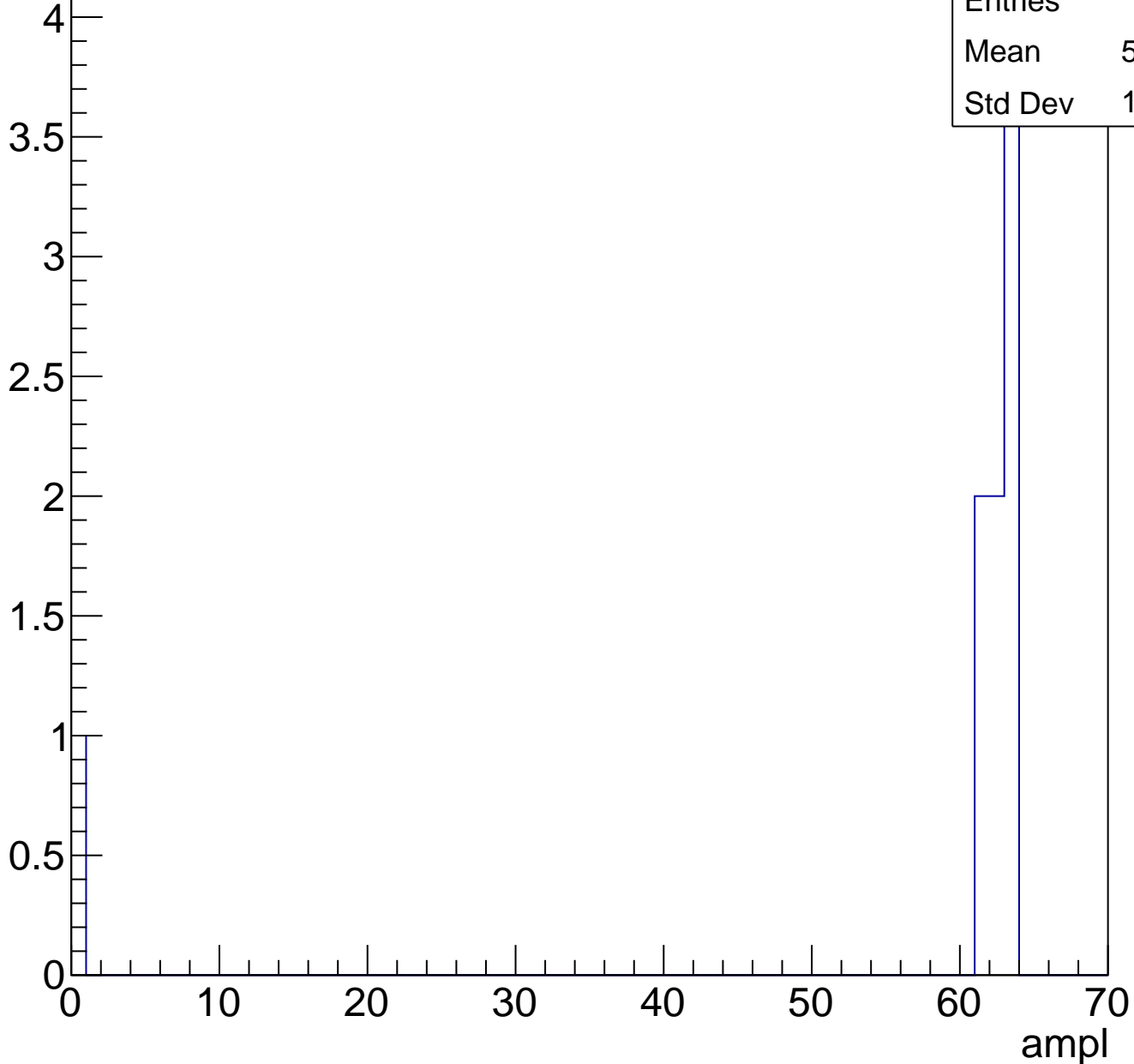


Entries	38
Mean	59.55
Std Dev	2.731

B1L103S, U14-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch76, adc0

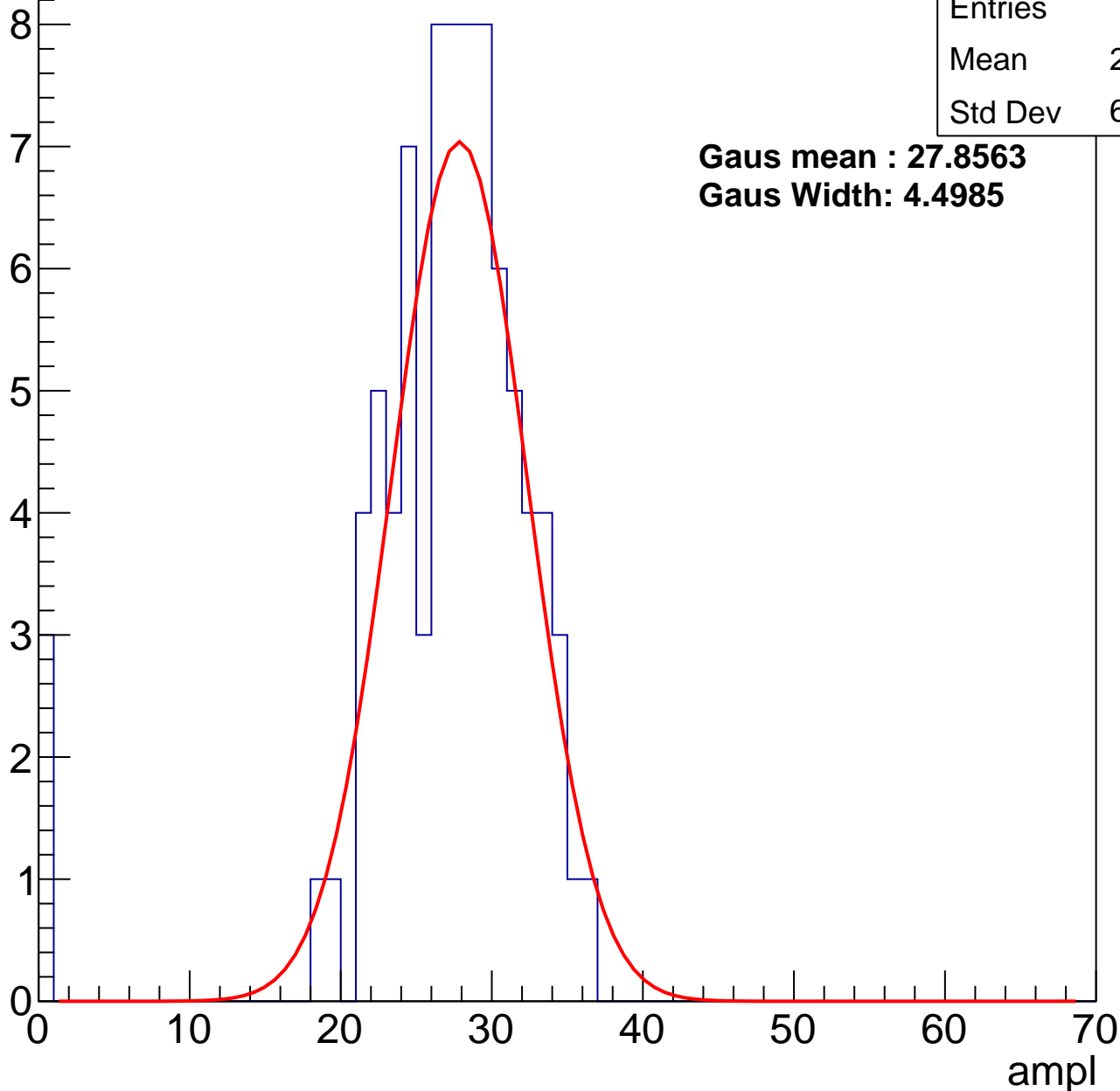
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	26.36
Std Dev	6.384

Gaus mean : 27.8563

Gaus Width: 4.4985



B1L103S, U14-ch76, adc1

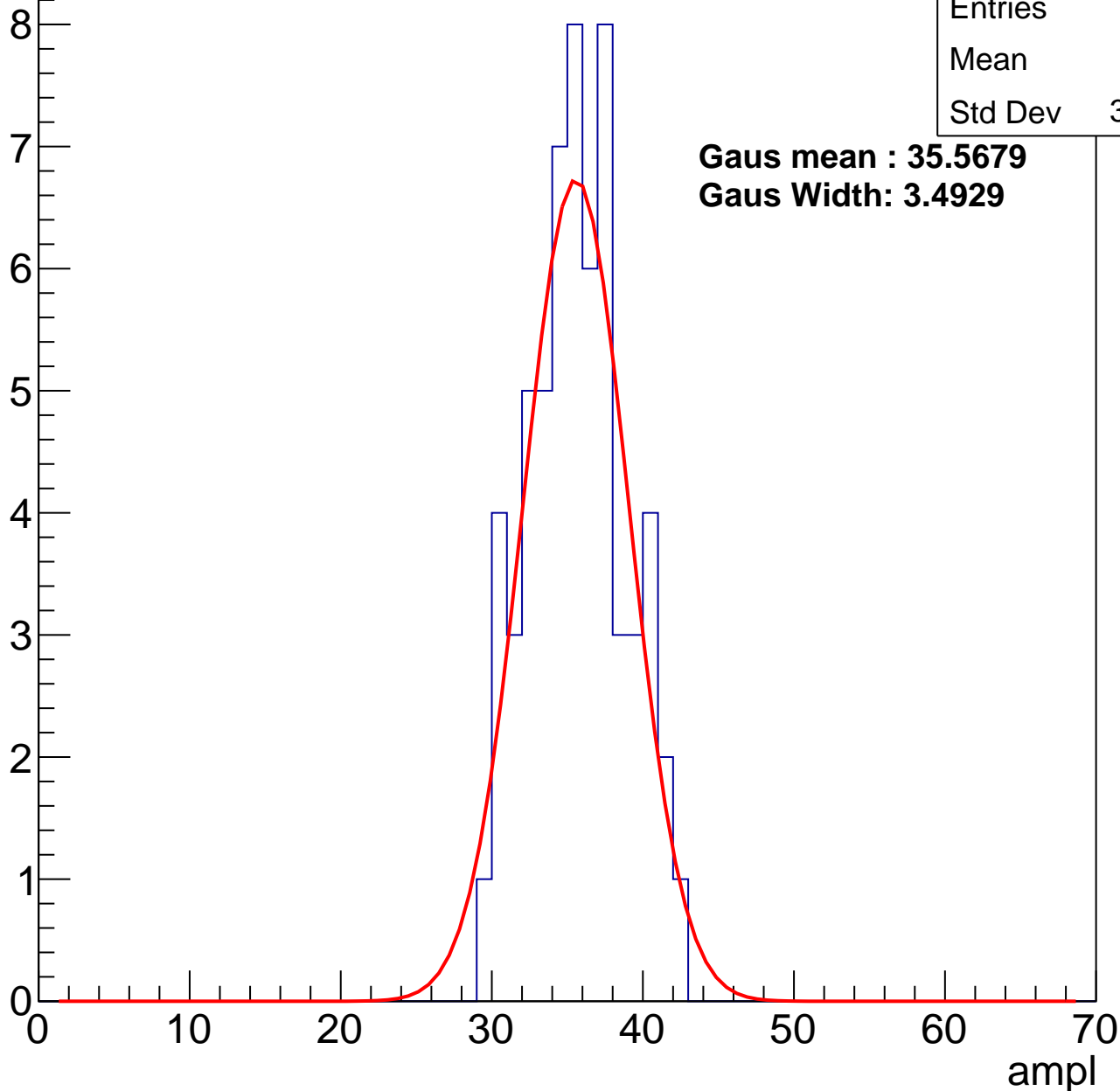
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.2
Std Dev	3.129

Gaus mean : 35.5679

Gaus Width: 3.4929



B1L103S, U14-ch76, adc2

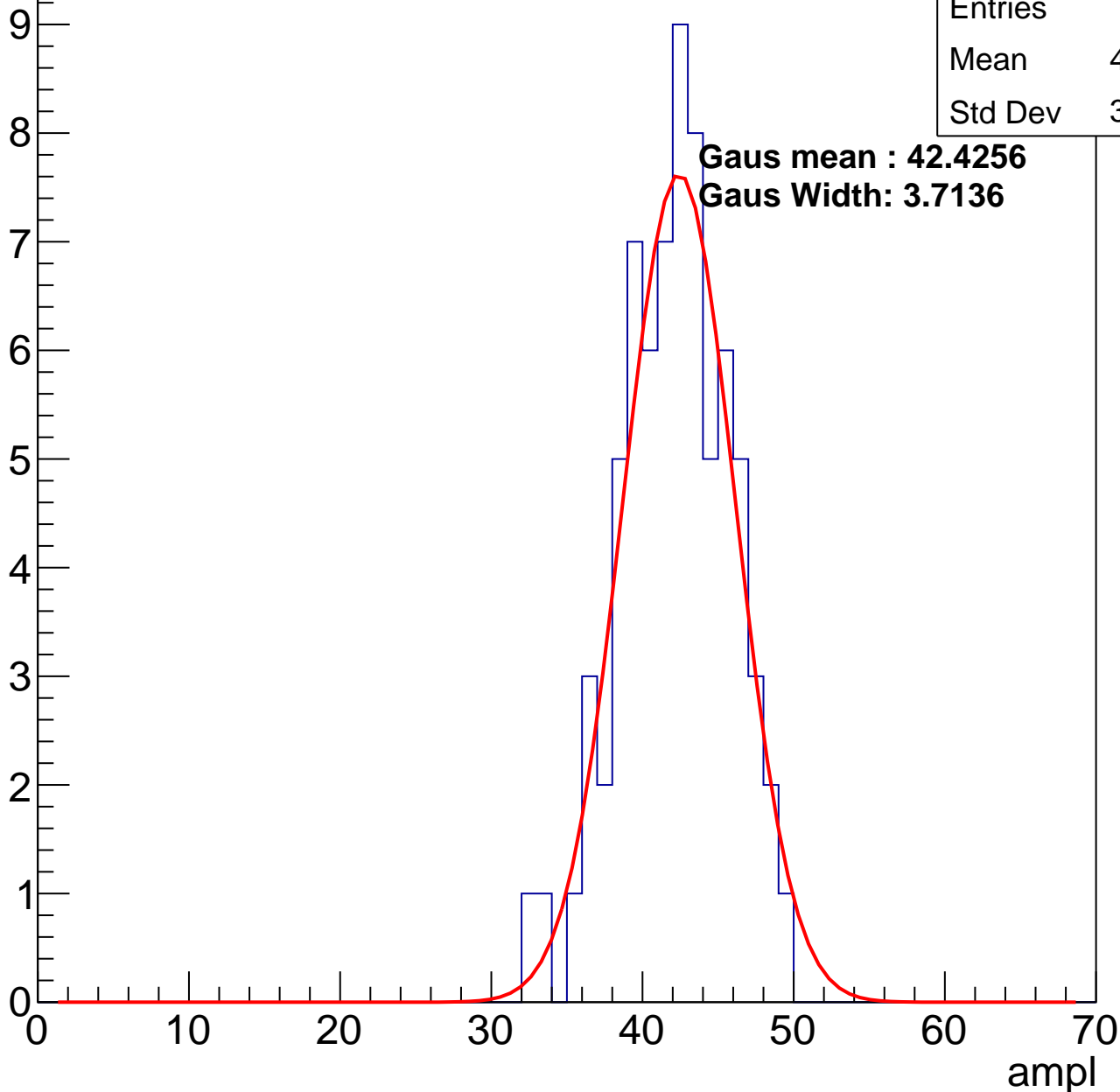
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	41.67
Std Dev	3.559

Gaus mean : 42.4256

Gaus Width: 3.7136

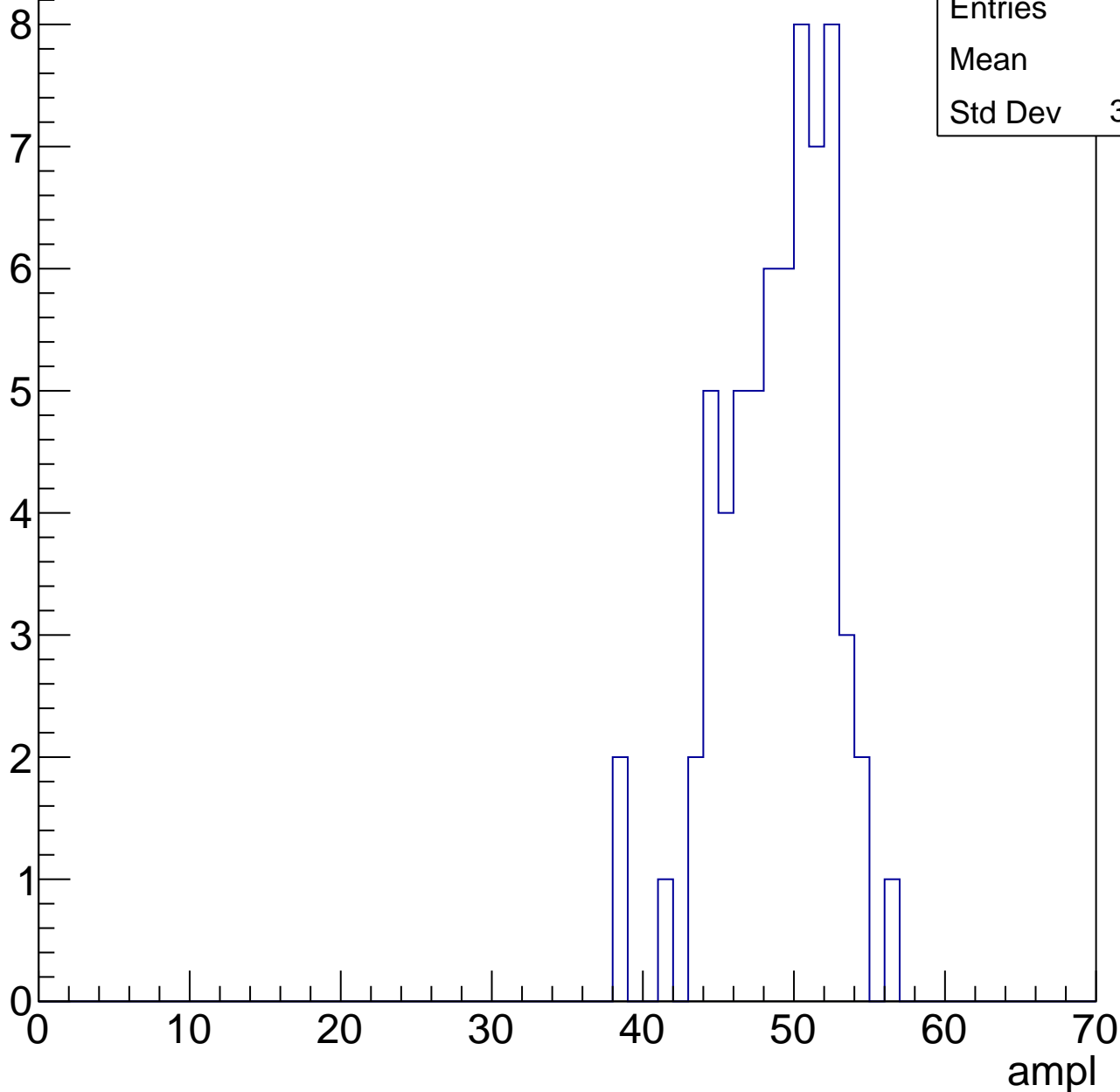


B1L103S, U14-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	48.4
Std Dev	3.658

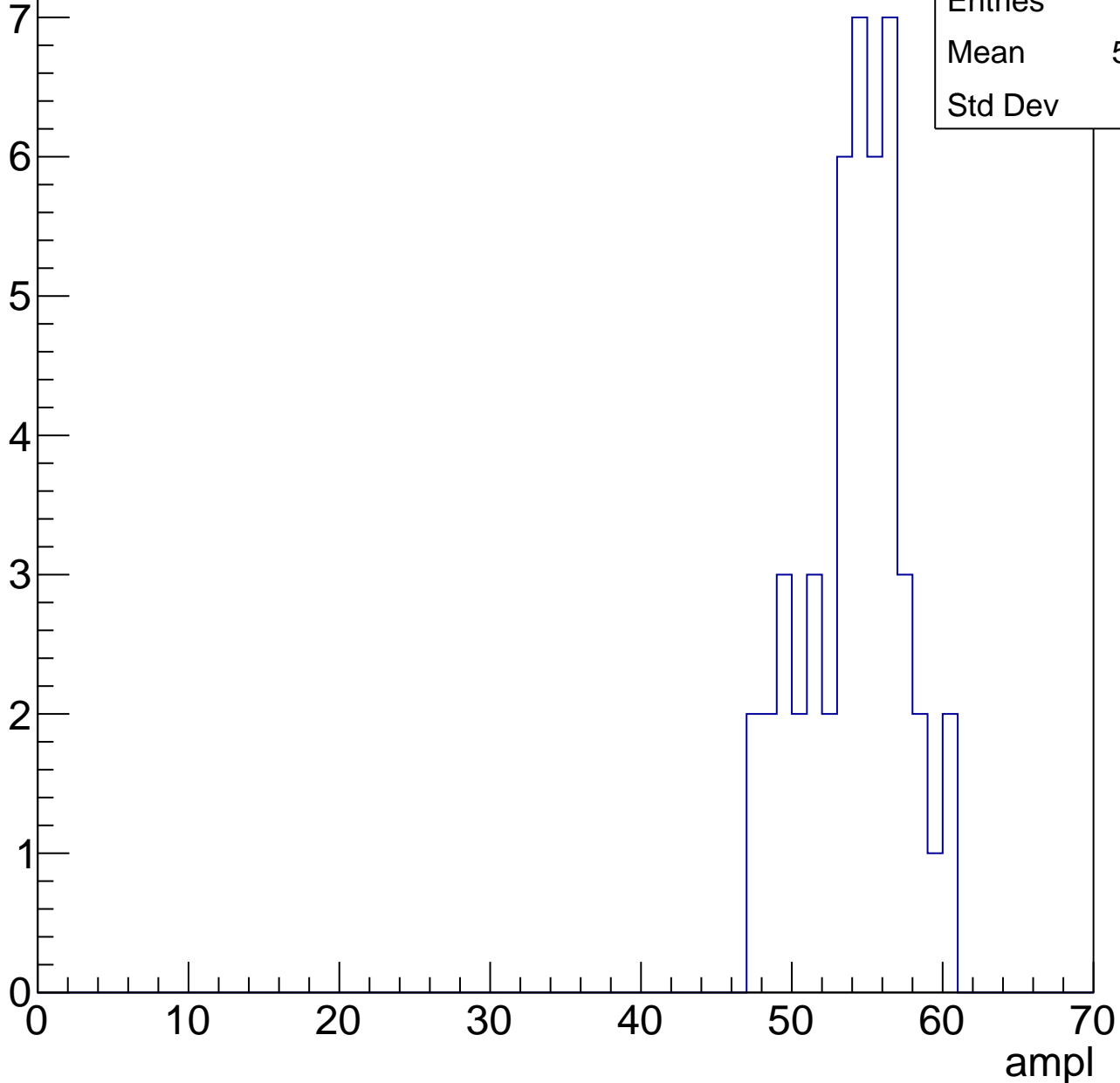


B1L103S, U14-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

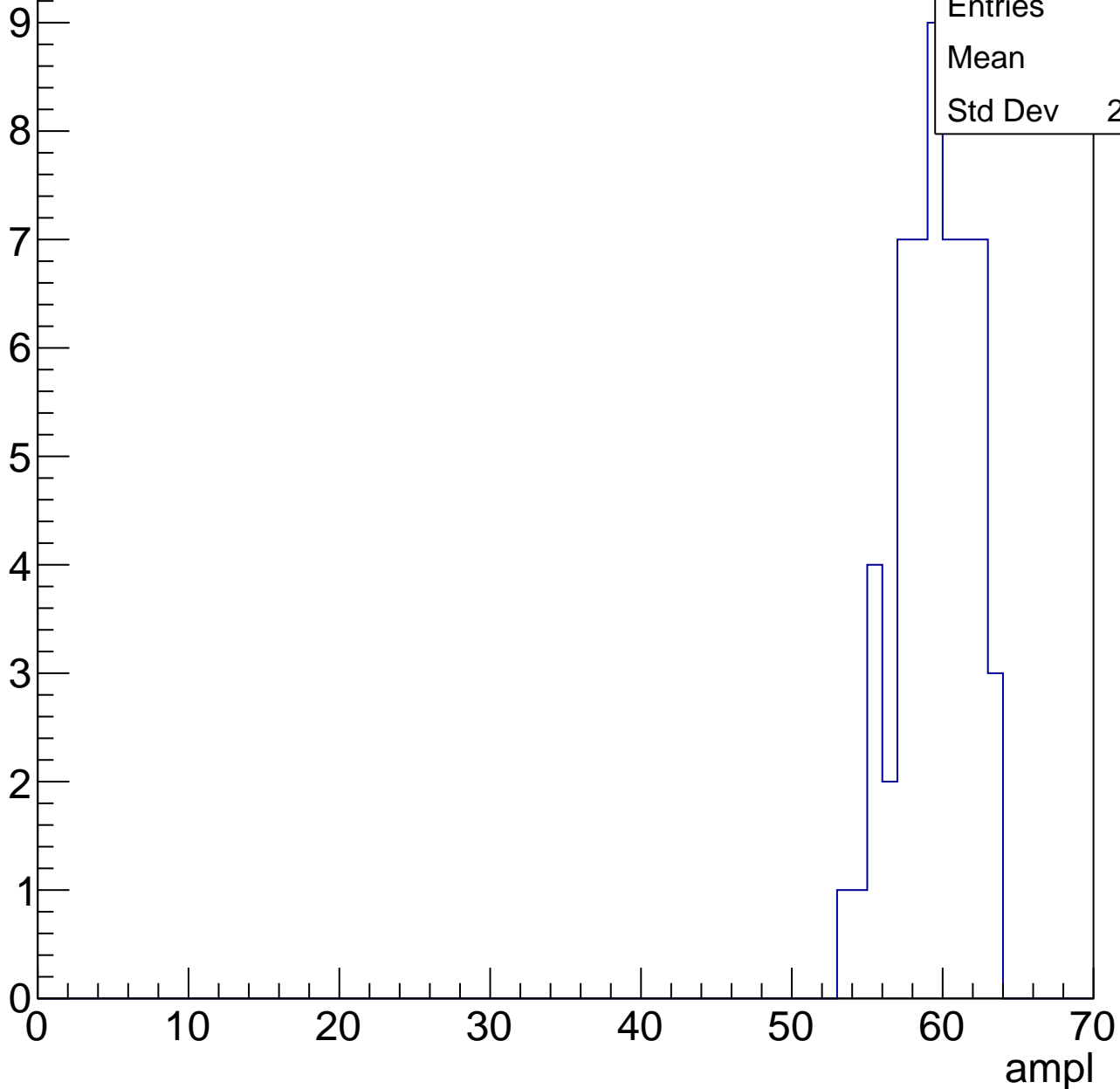
Entries	48
Mean	53.71
Std Dev	3.24



B1L103S, U14-ch76, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

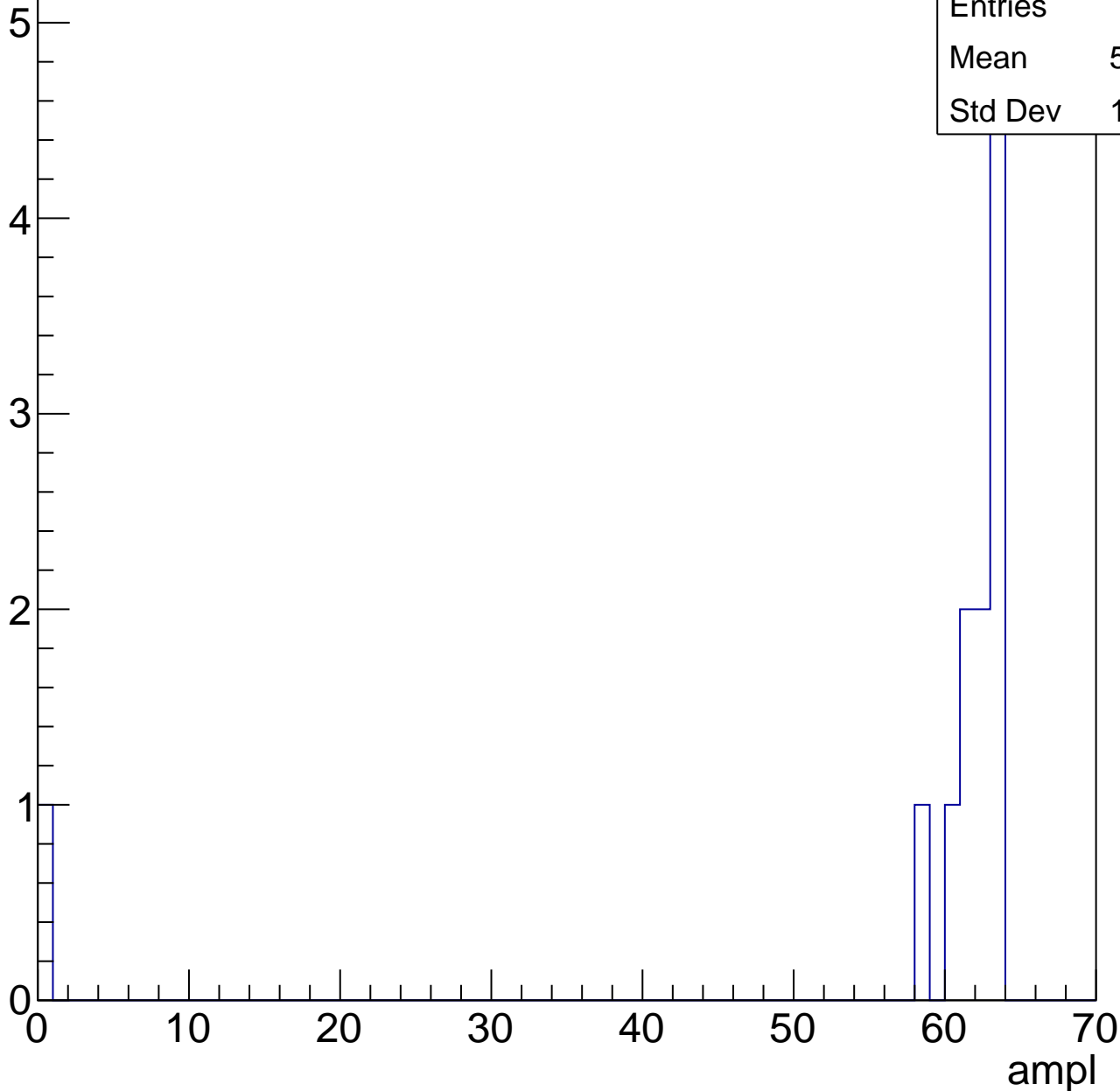


B1L103S, U14-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

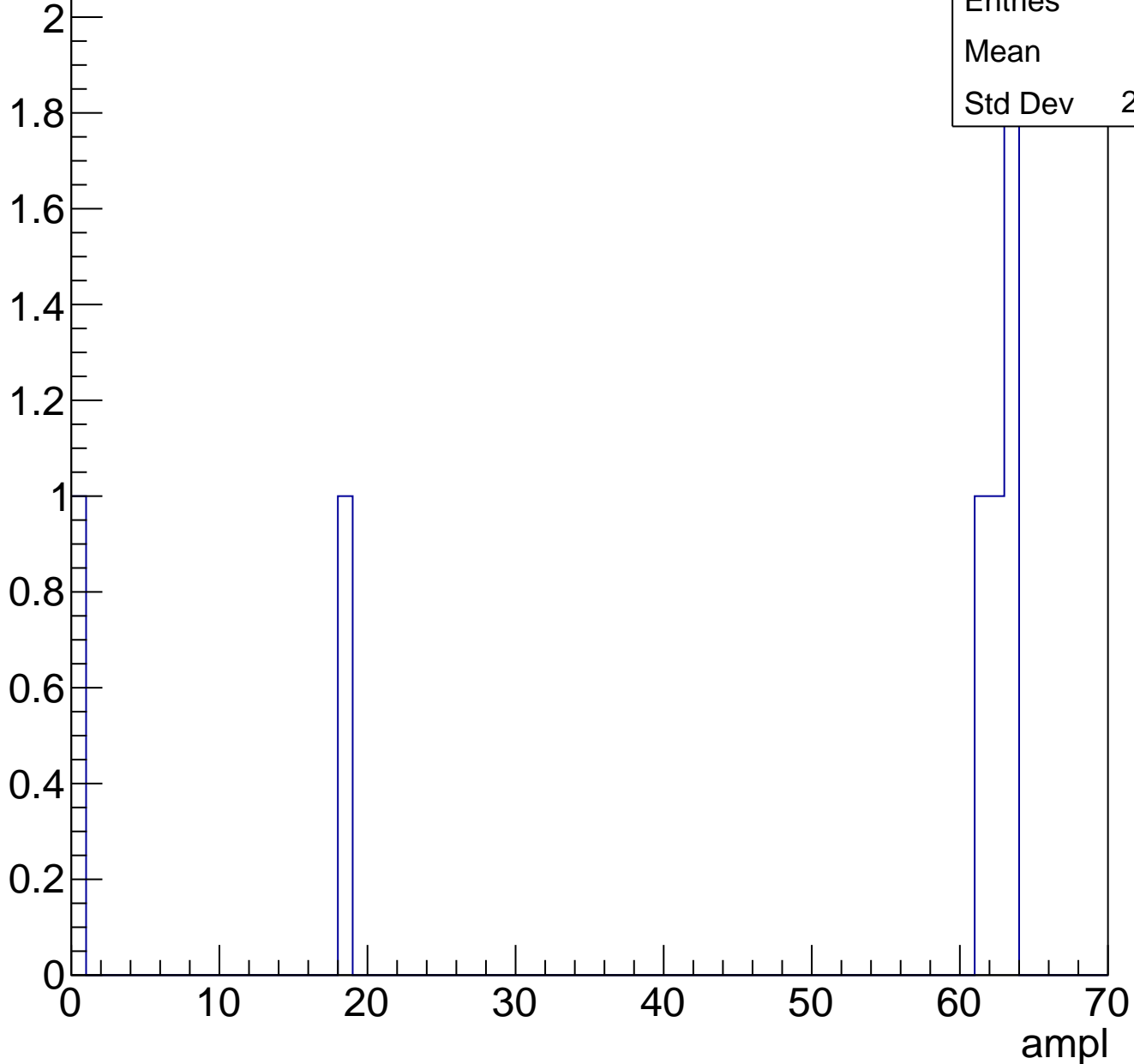
Entries	12
Mean	56.58
Std Dev	17.12



B1L103S, U14-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch77, adc0

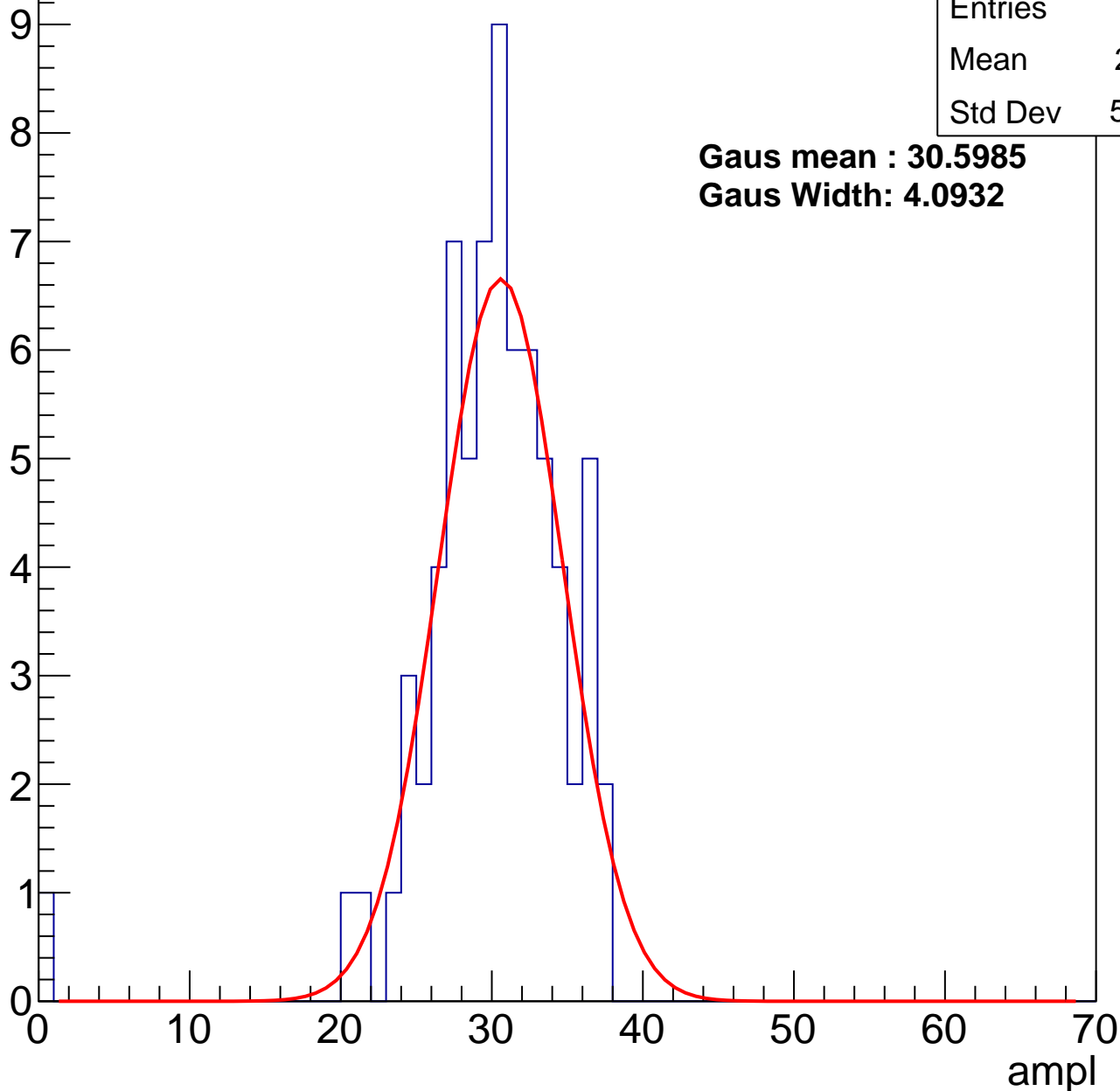
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	29.51
Std Dev	5.162

Gaus mean : 30.5985

Gaus Width: 4.0932



B1L103S, U14-ch77, adc1

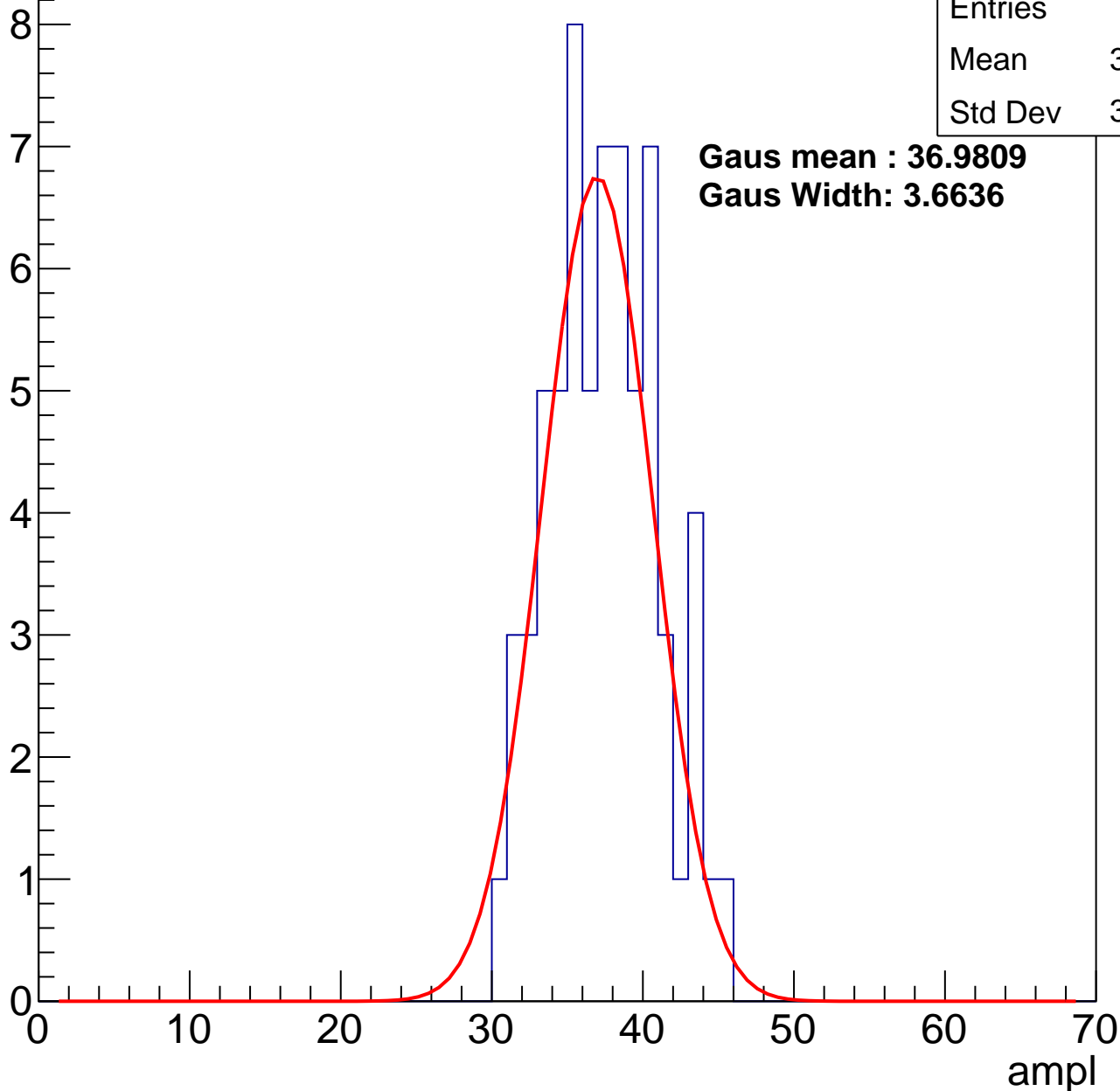
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.97
Std Dev	3.512

Gaus mean : 36.9809

Gaus Width: 3.6636



B1L103S, U14-ch77, adc2

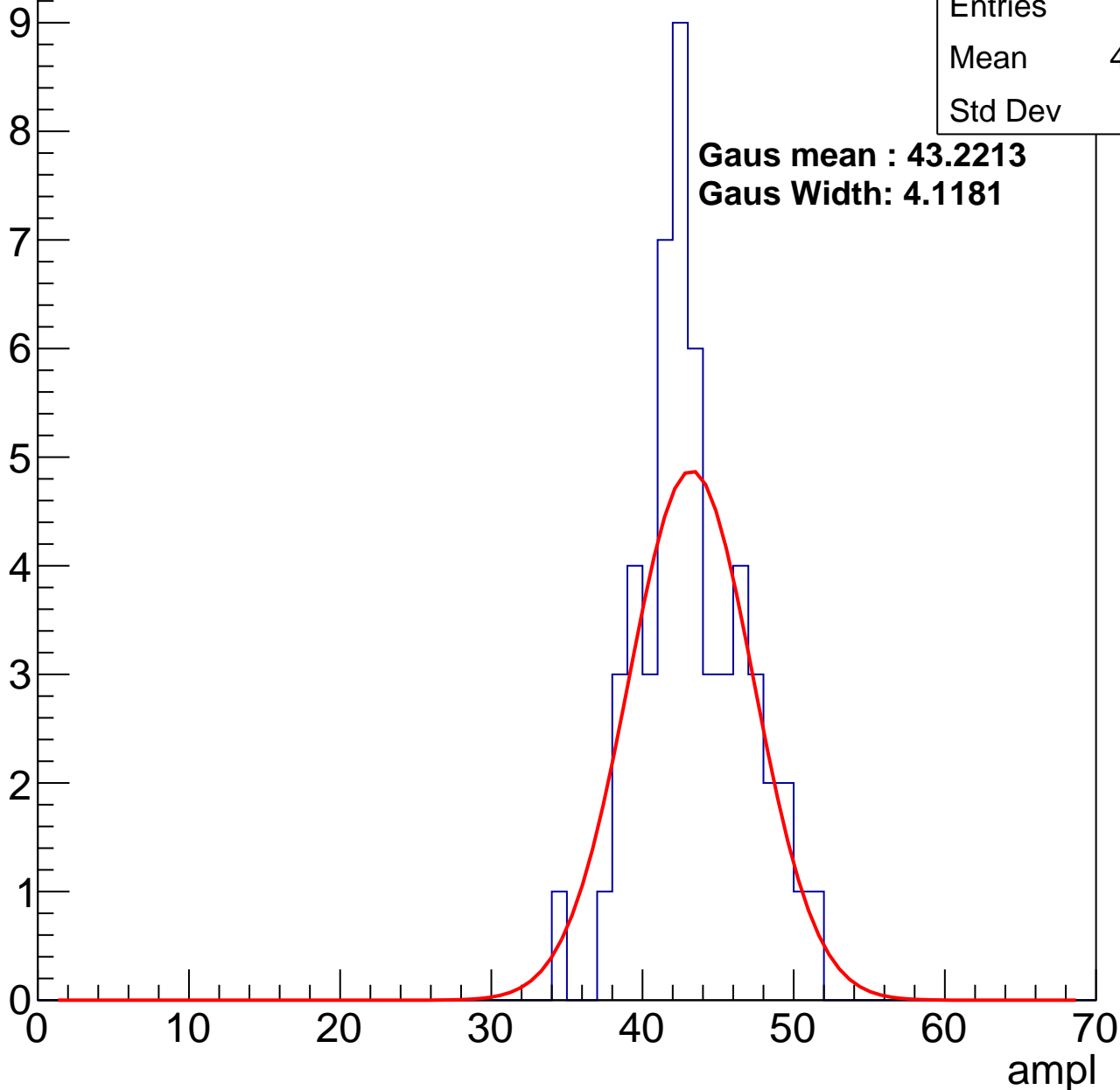
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.85
Std Dev	3.51

Gaus mean : 43.2213

Gaus Width: 4.1181

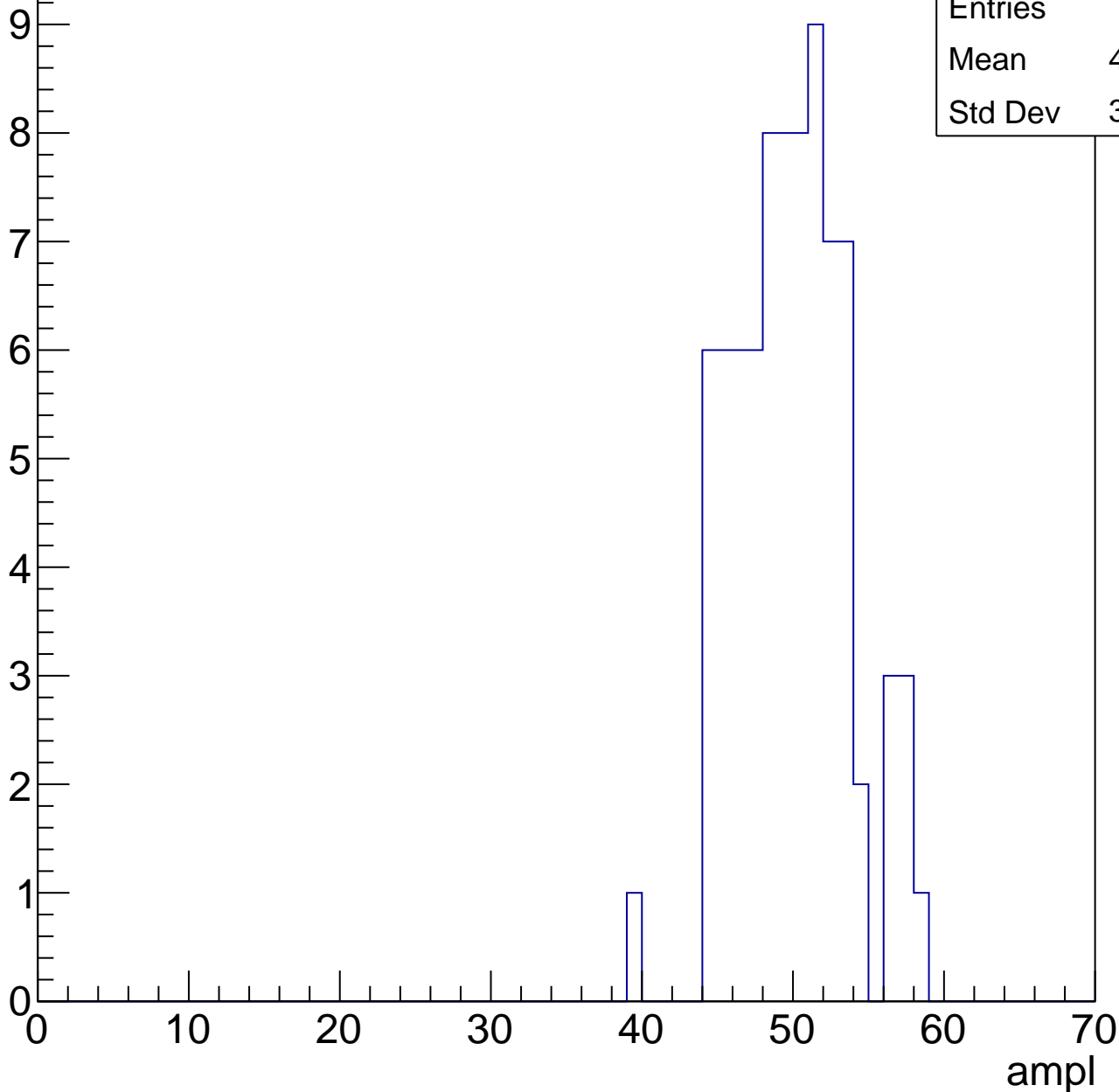


B1L103S, U14-ch77, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	49.46
Std Dev	3.692

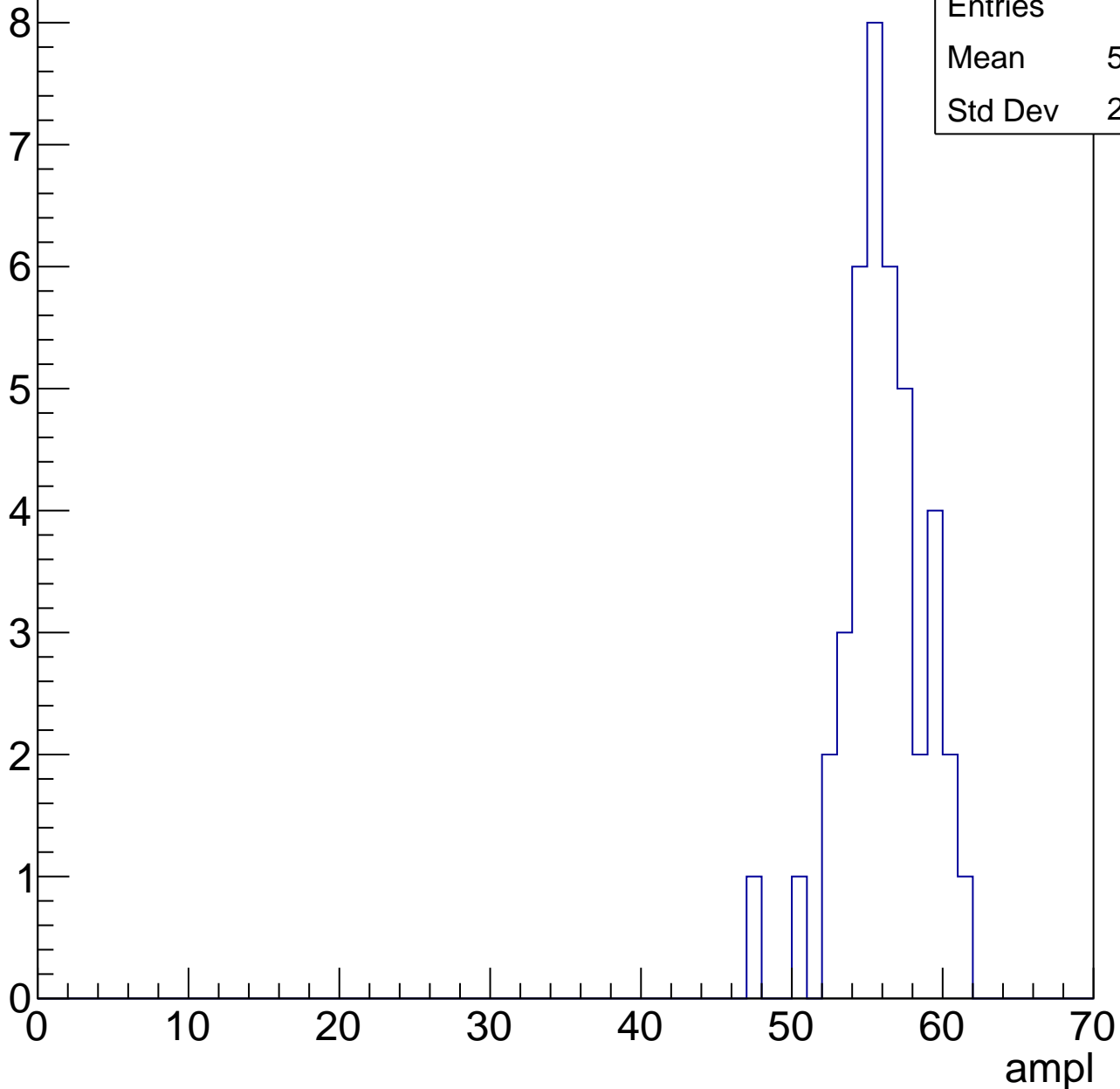


B1L103S, U14-ch77, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	55.56
Std Dev	2.732

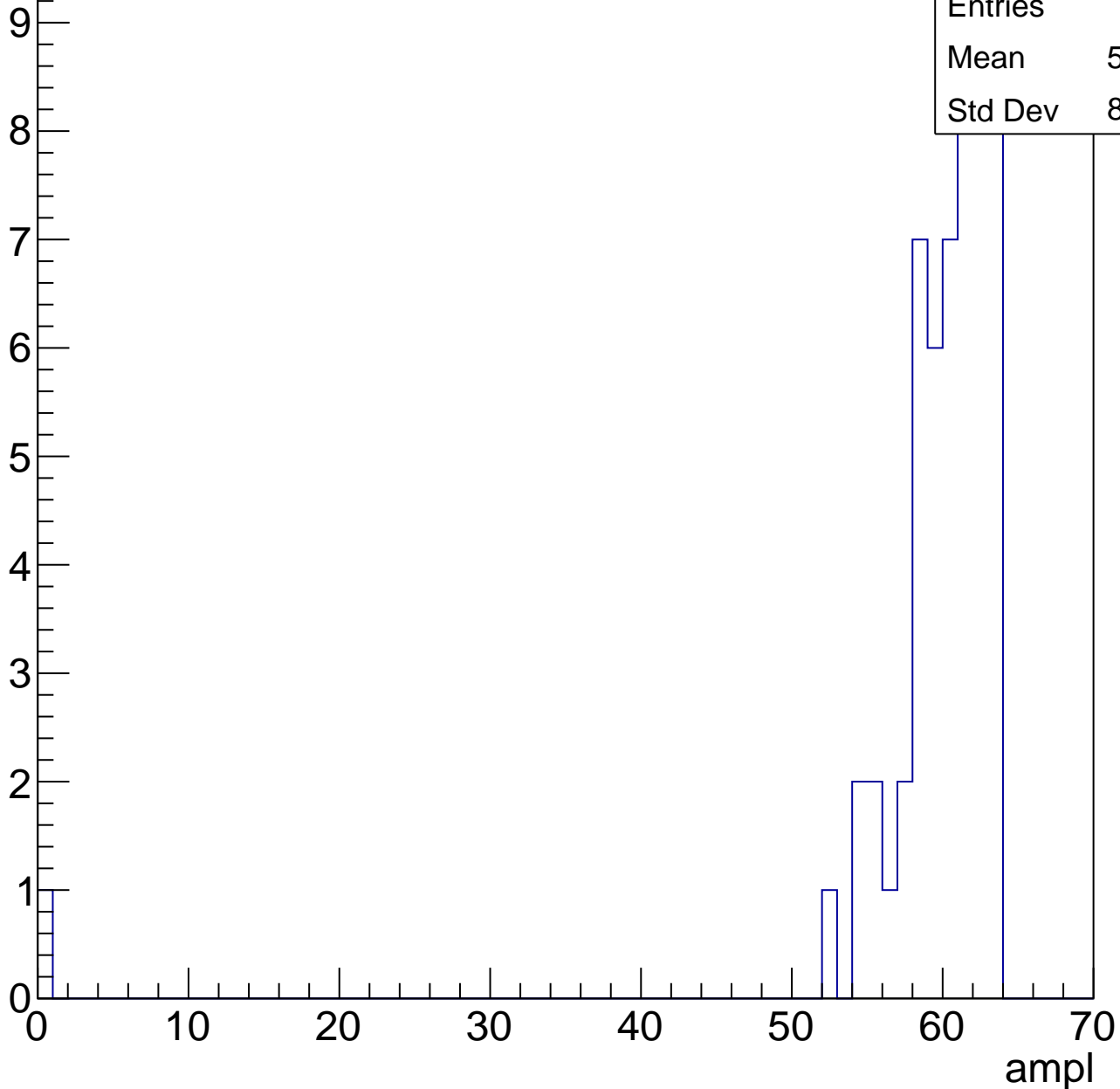


B1L103S, U14-ch77, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

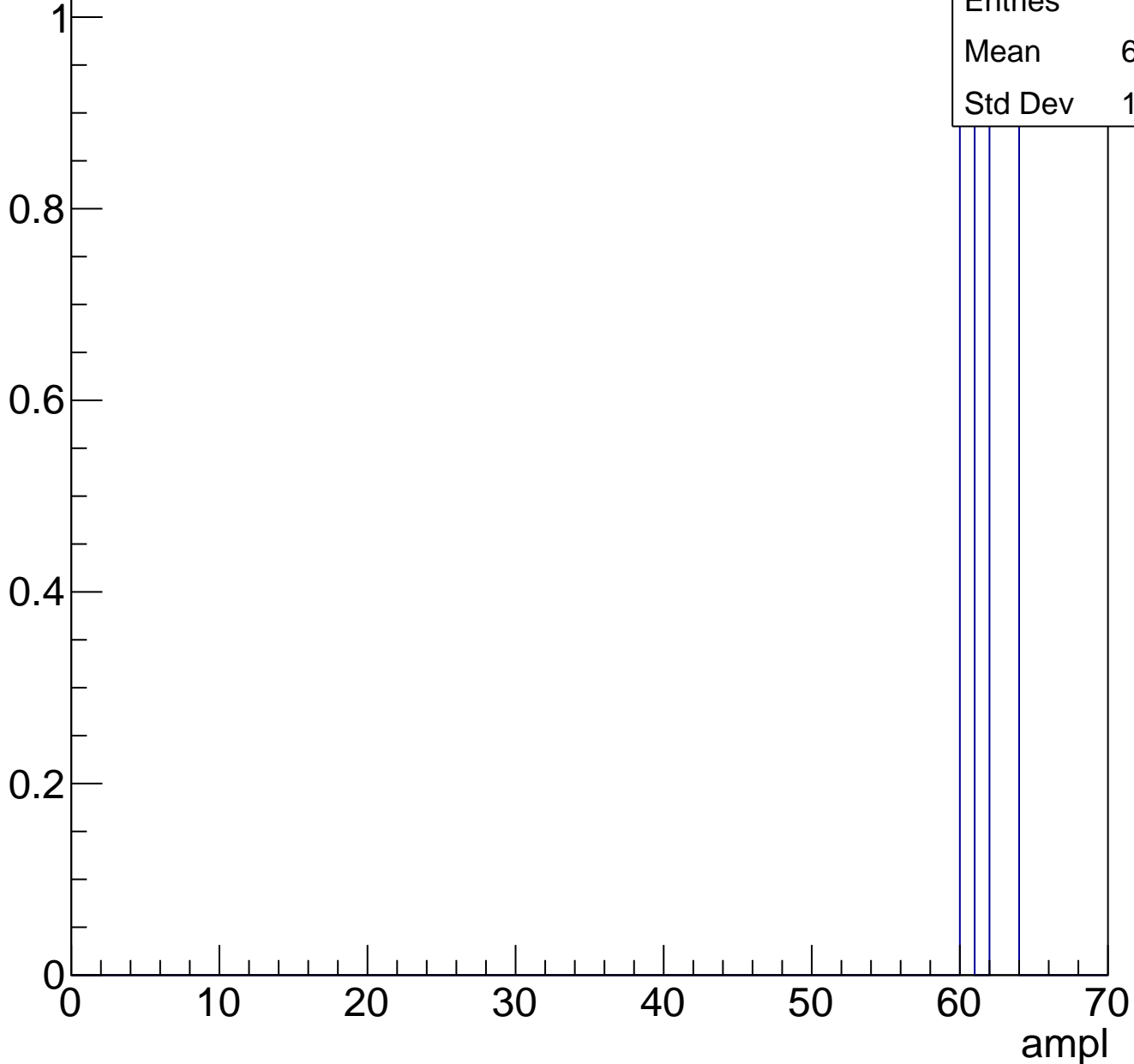
Entries	56
Mean	58.82
Std Dev	8.348



B1L103S, U14-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch78, adc0

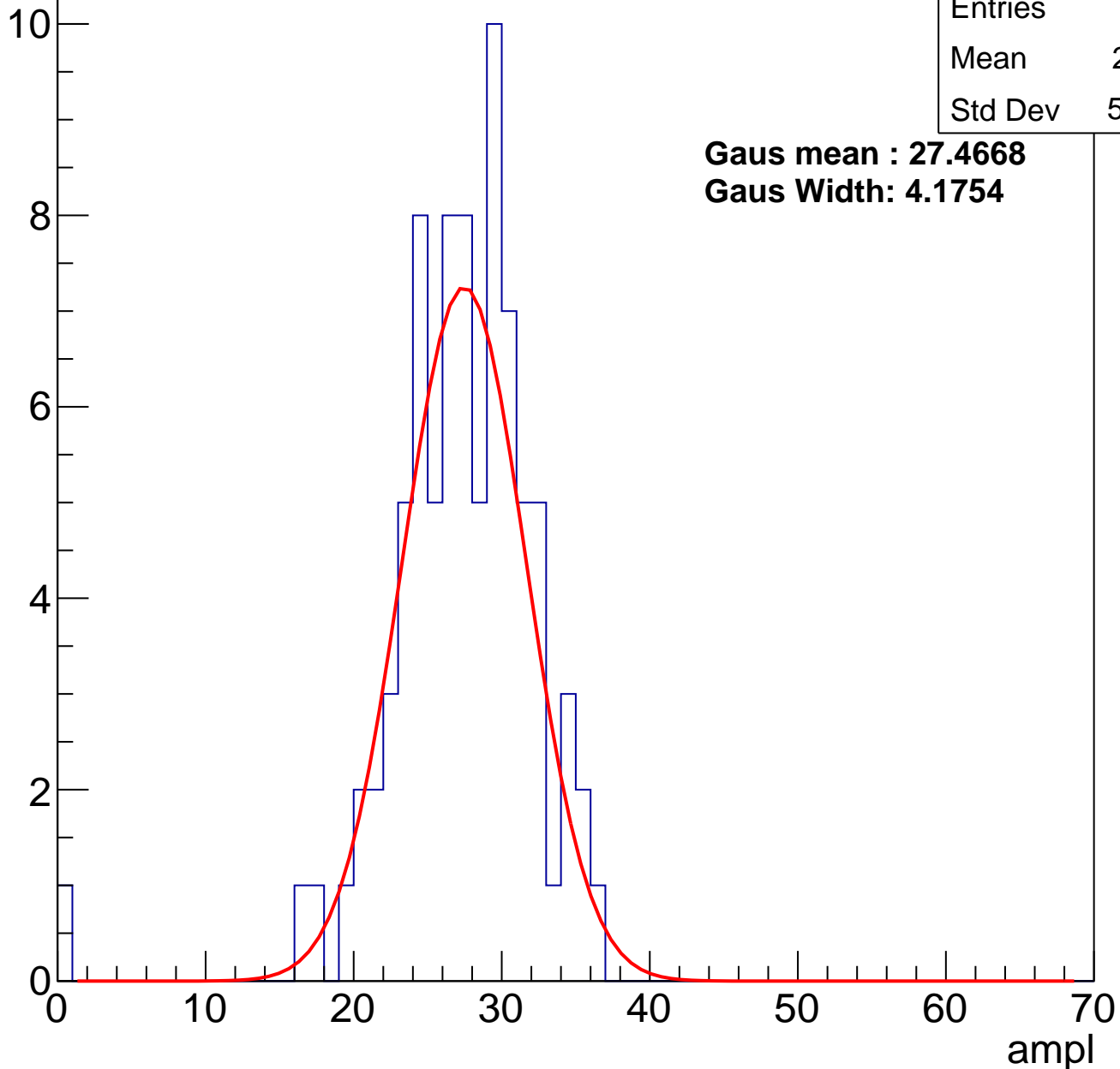
calib_packv5_042523_0143.root, FC#7, port C2

Entries	84
Mean	26.81
Std Dev	5.049

Gaus mean : 27.4668

Gaus Width: 4.1754

Entry



B1L103S, U14-ch78, adc1

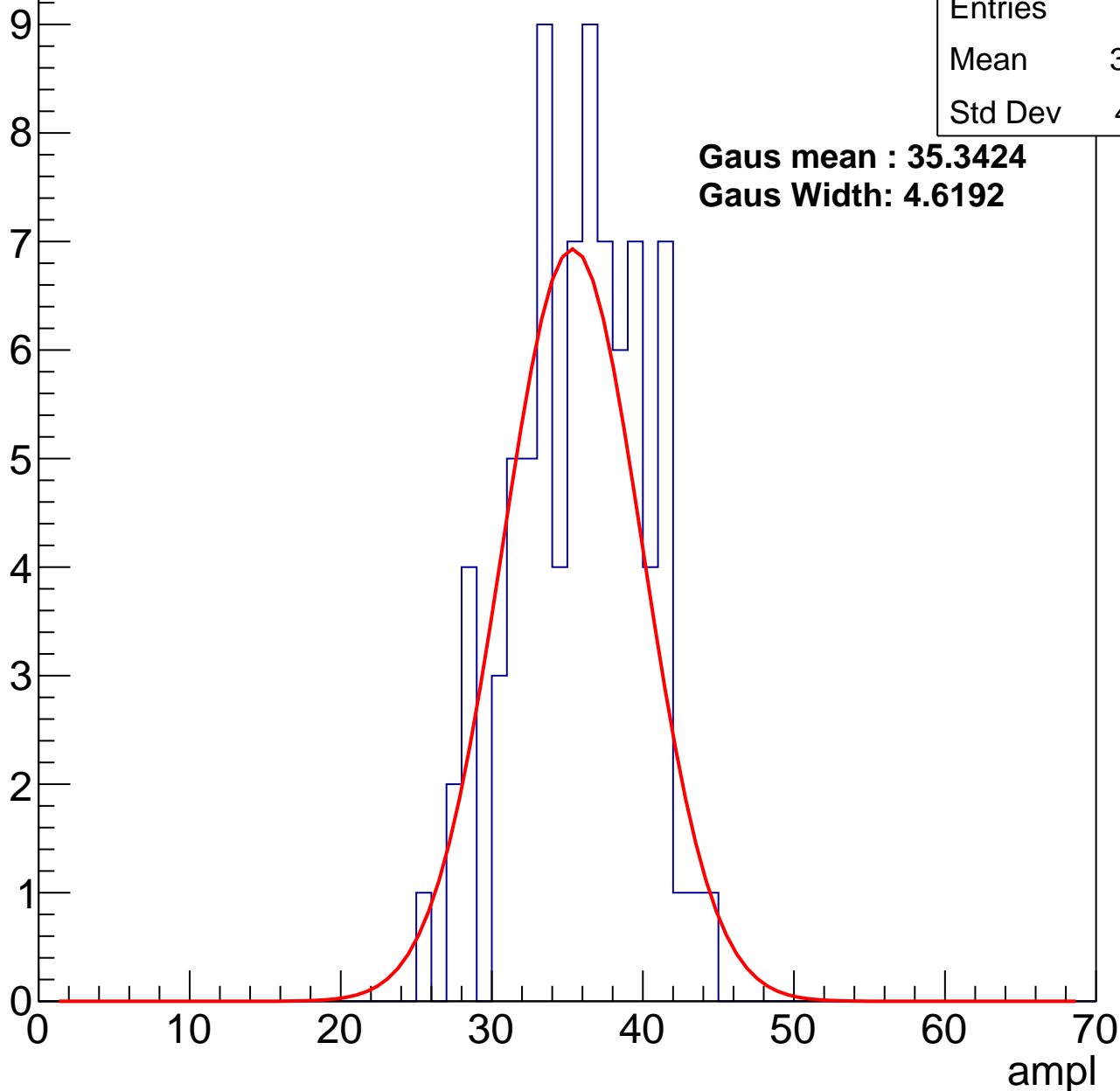
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	35.35
Std Dev	4.111

Gaus mean : 35.3424

Gaus Width: 4.6192



B1L103S, U14-ch78, adc2

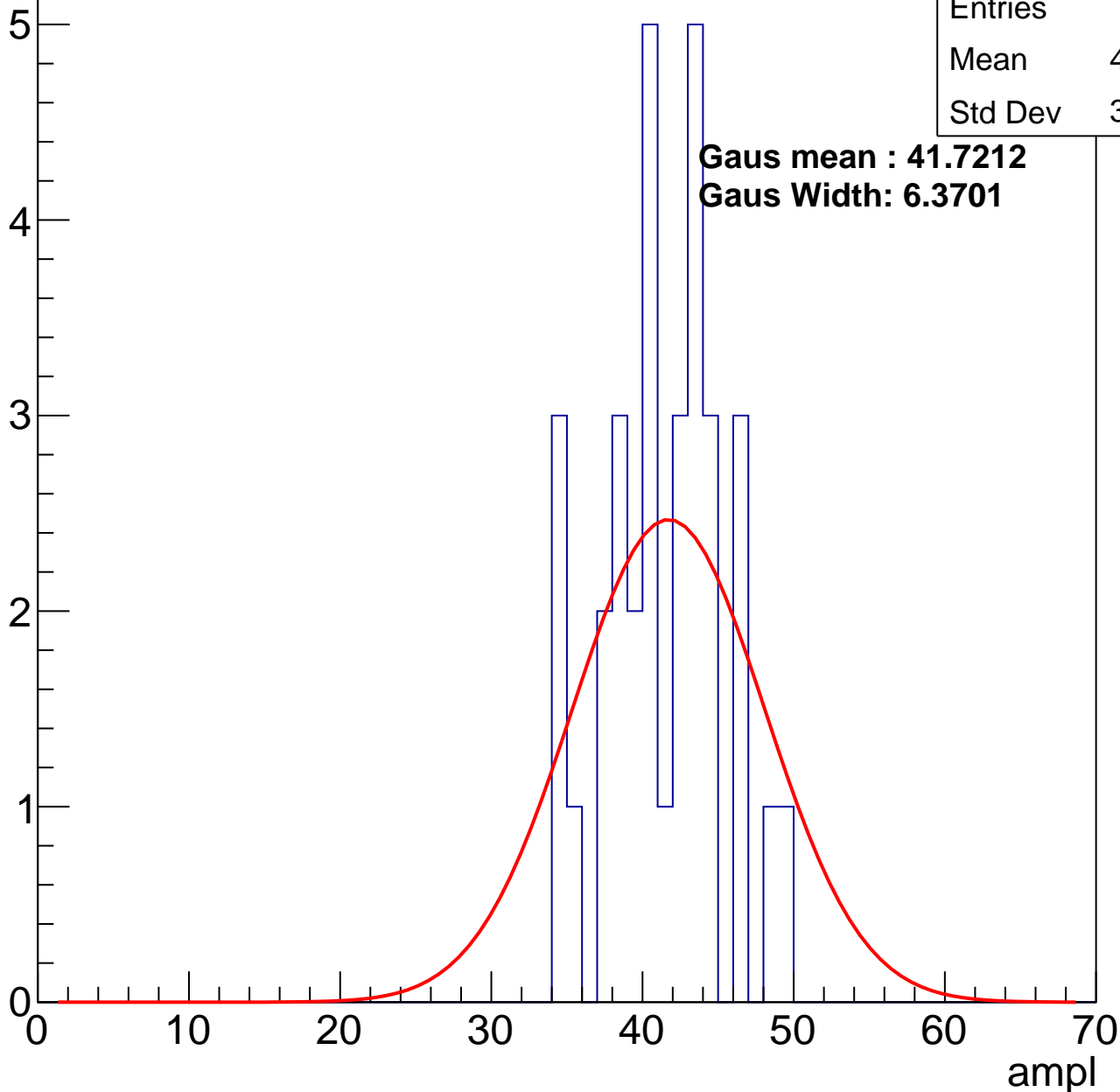
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	40.97
Std Dev	3.865

Gaus mean : 41.7212

Gaus Width: 6.3701



B1L103S, U14-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	46.87
Std Dev	3.939

Entry

10

8

6

4

2

0

0

10

20

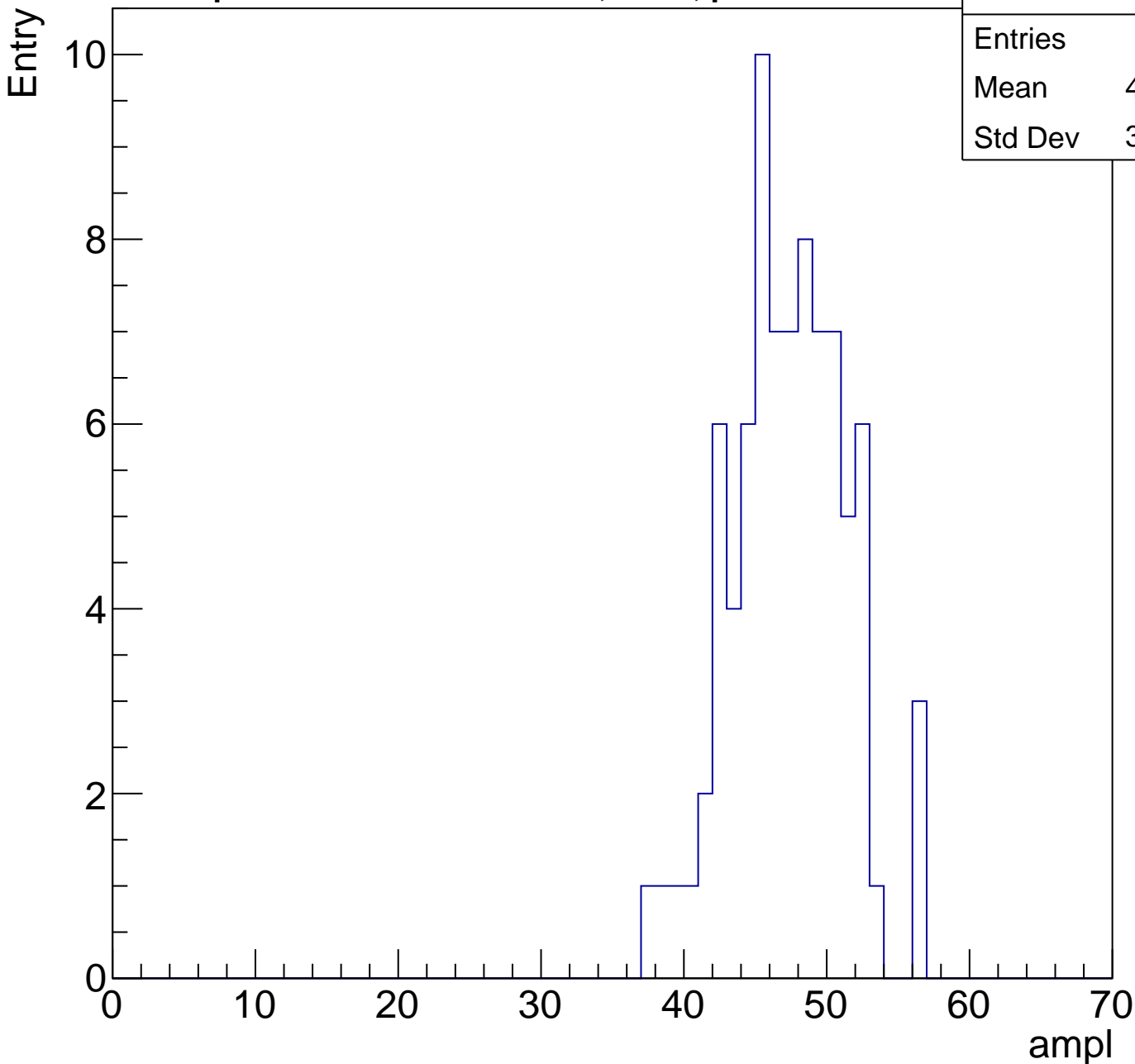
30

40

50

60

ampl

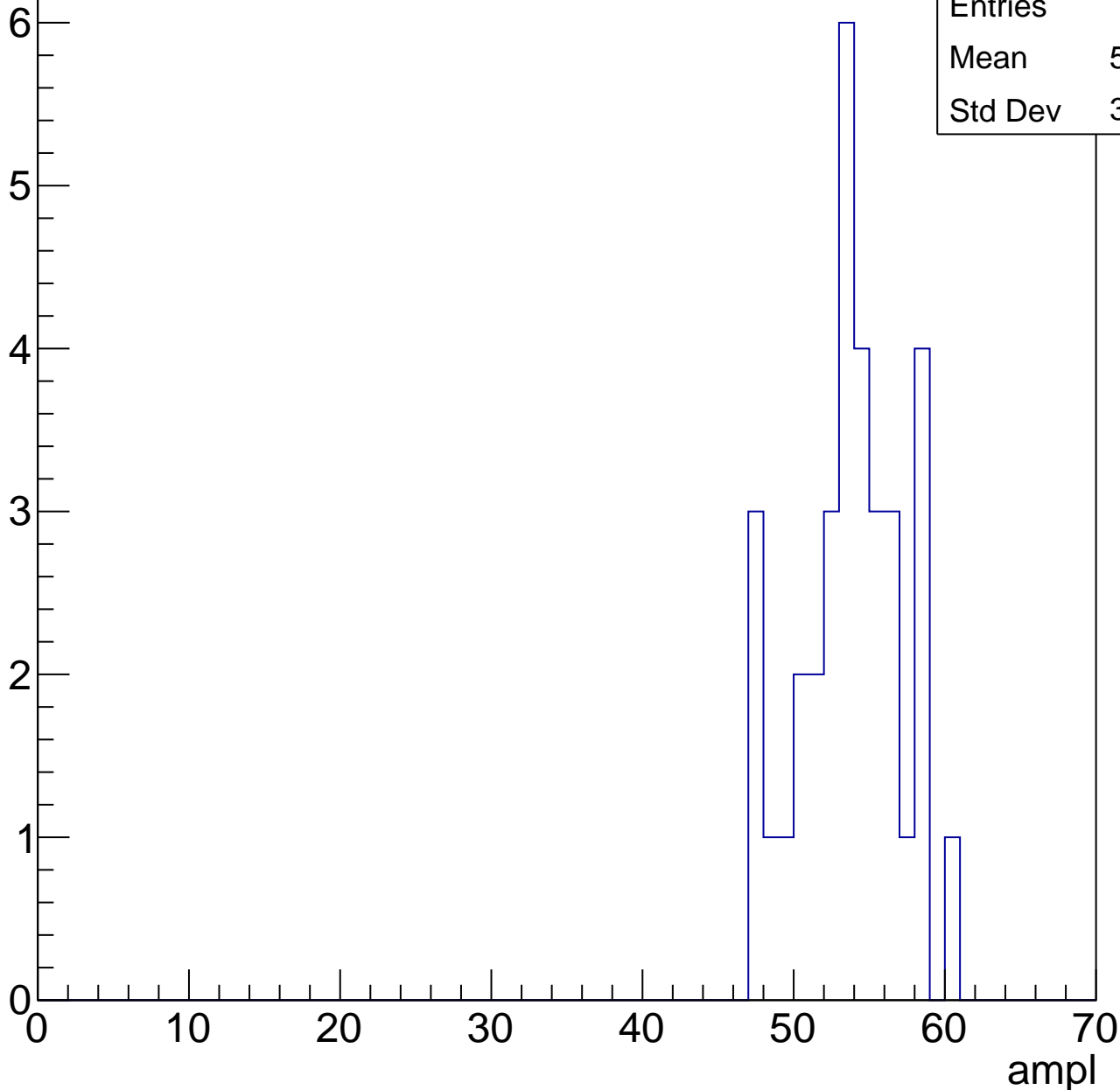


B1L103S, U14-ch78, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

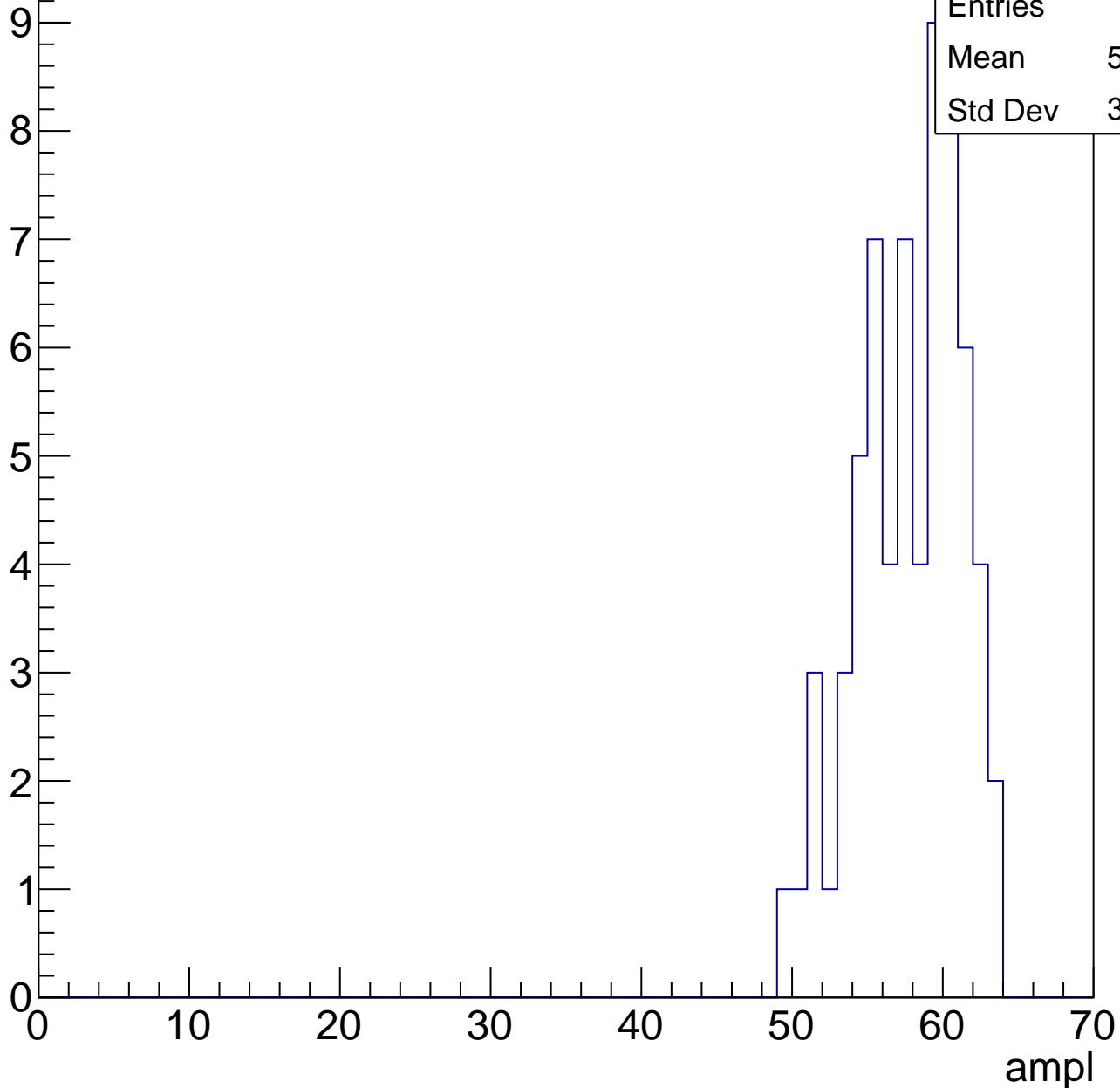
Entries	34
Mean	53.29
Std Dev	3.357



B1L103S, U14-ch78, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

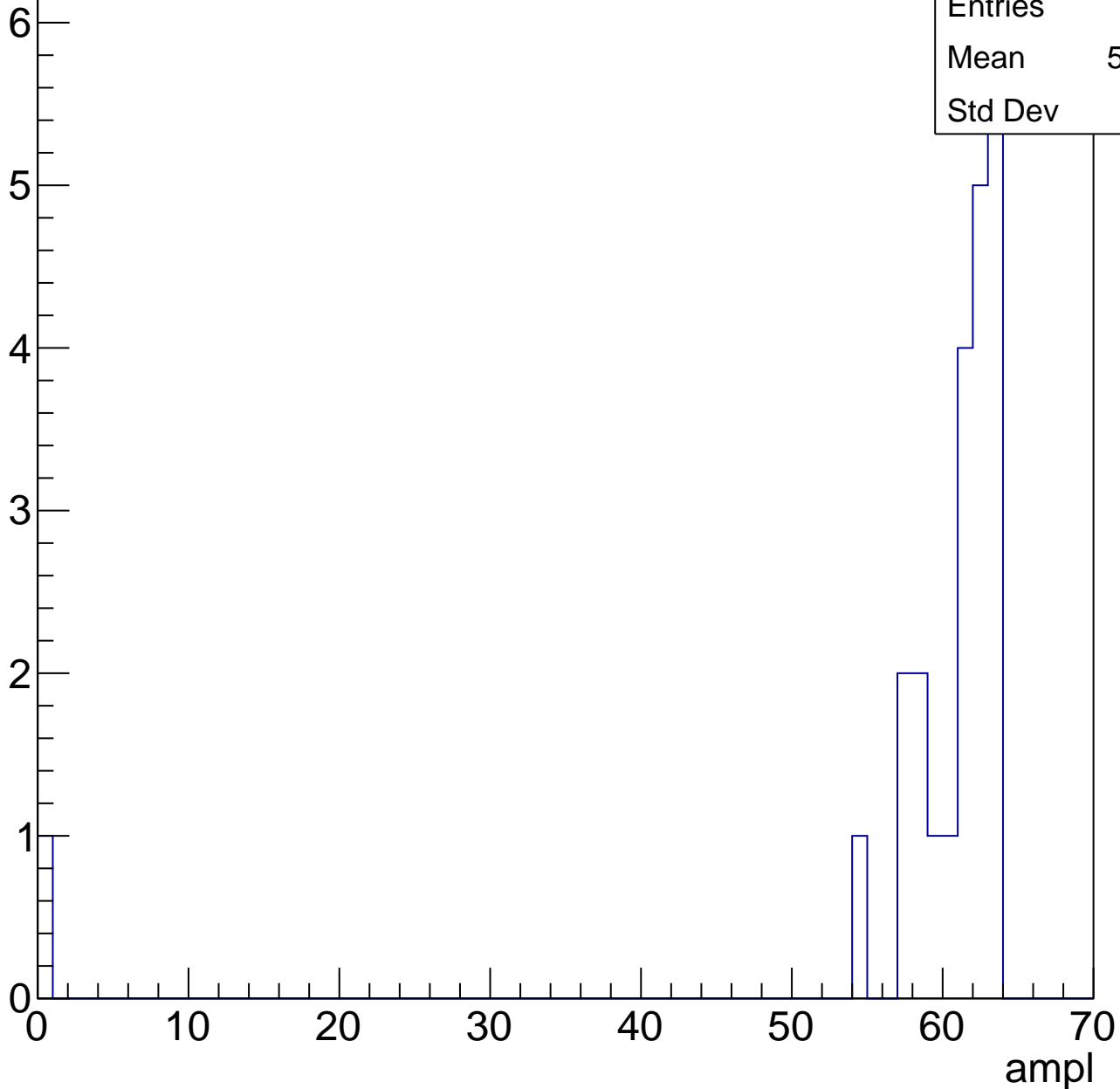


B1L103S, U14-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	23
Mean	58.04
Std Dev	12.6



B1L103S, U14-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch79, adc0

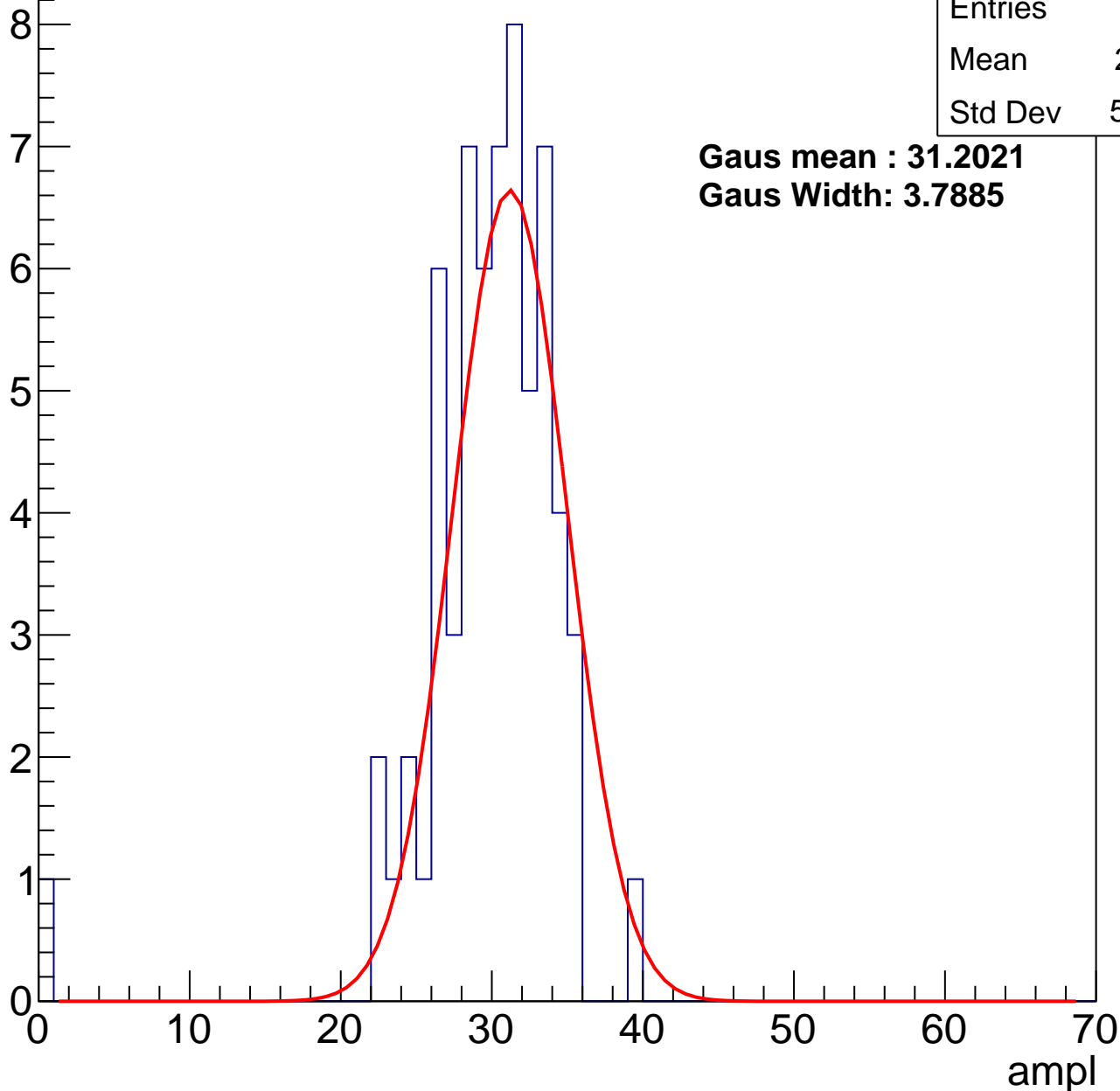
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	29.31
Std Dev	5.018

Gaus mean : 31.2021

Gaus Width: 3.7885



B1L103S, U14-ch79, adc1

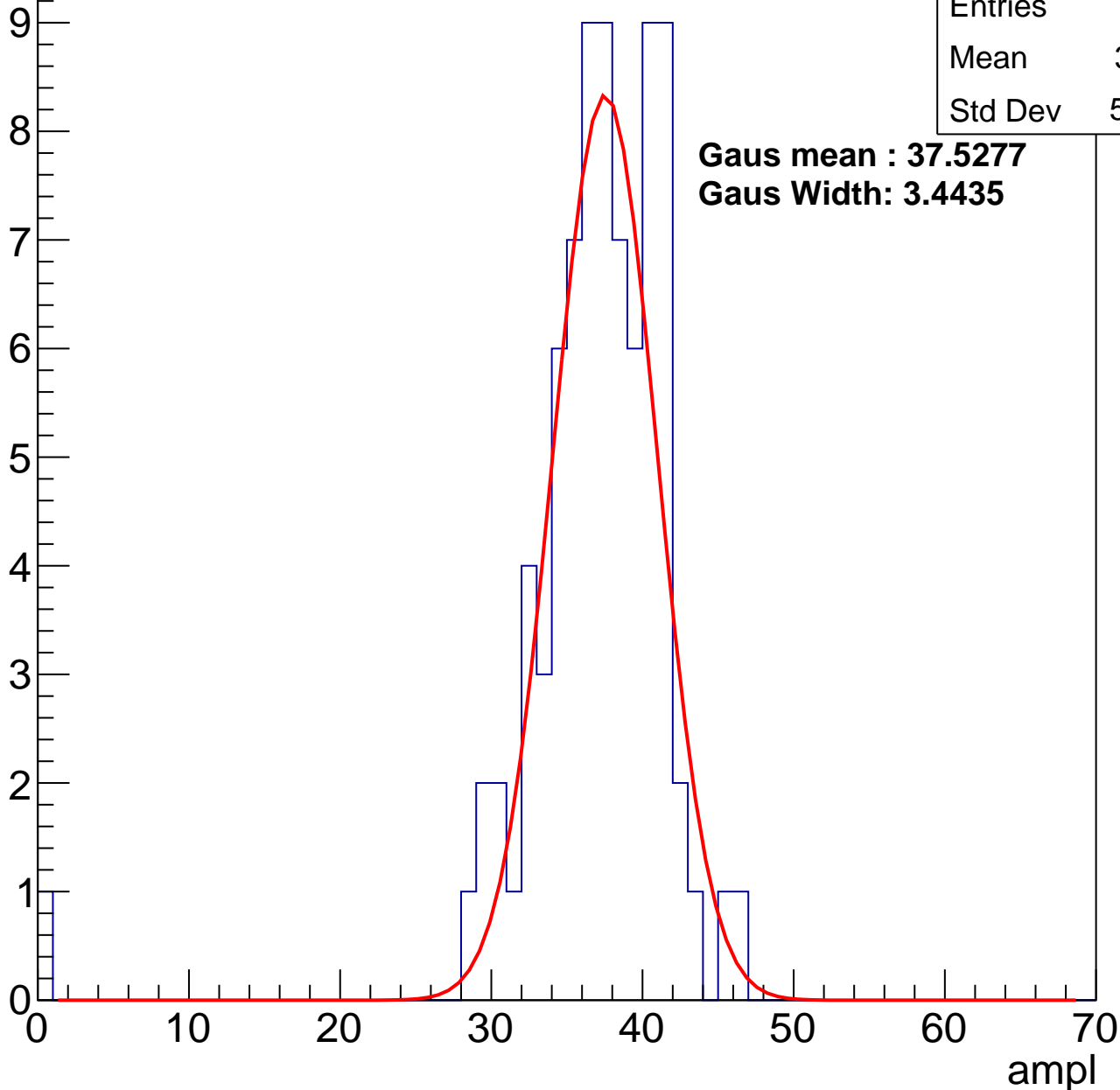
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	36.51
Std Dev	5.464

Gaus mean : 37.5277

Gaus Width: 3.4435



B1L103S, U14-ch79, adc2

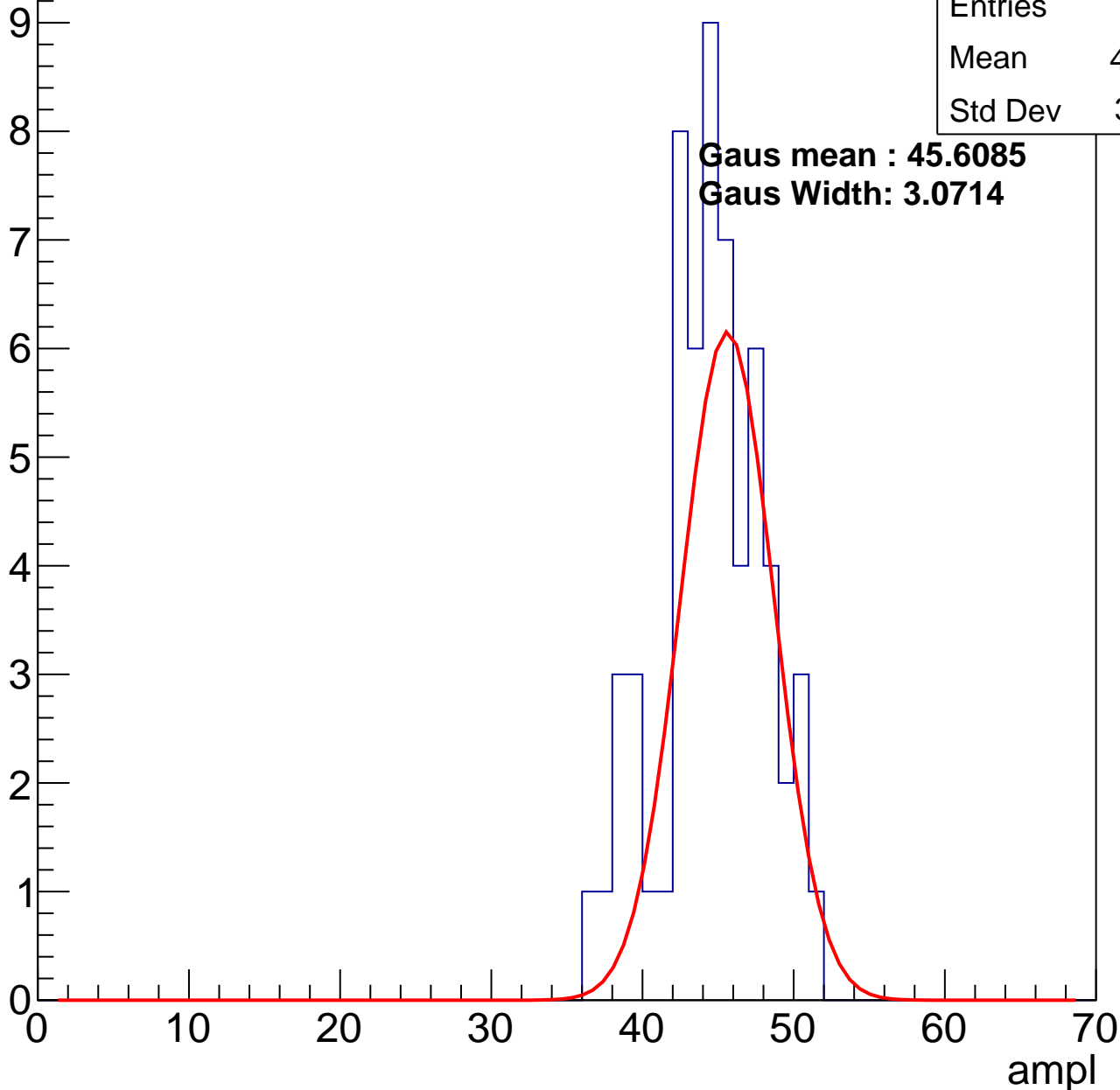
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	44.12
Std Dev	3.431

Gaus mean : 45.6085

Gaus Width: 3.0714

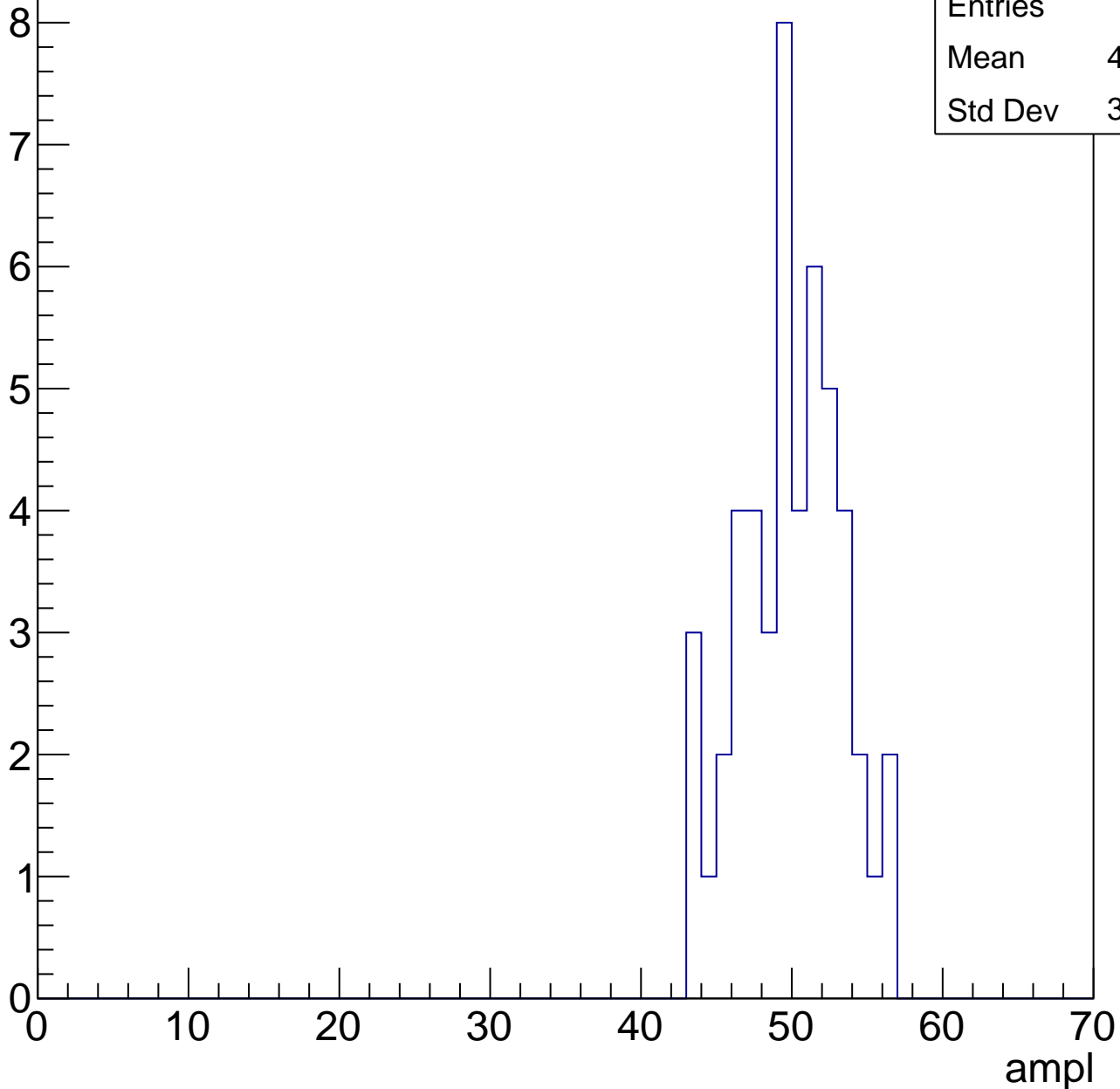


B1L103S, U14-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	49.47
Std Dev	3.289

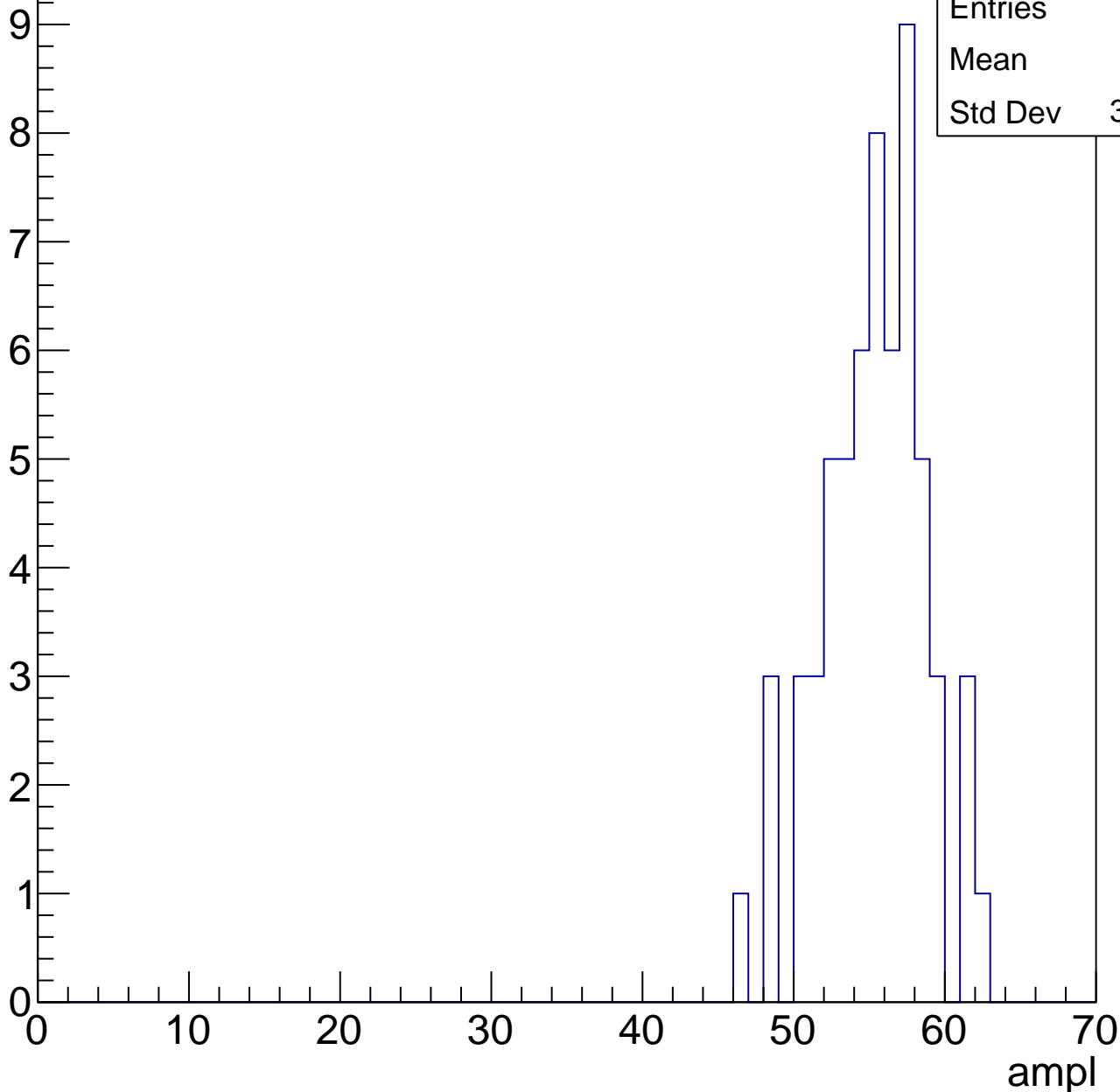


B1L103S, U14-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

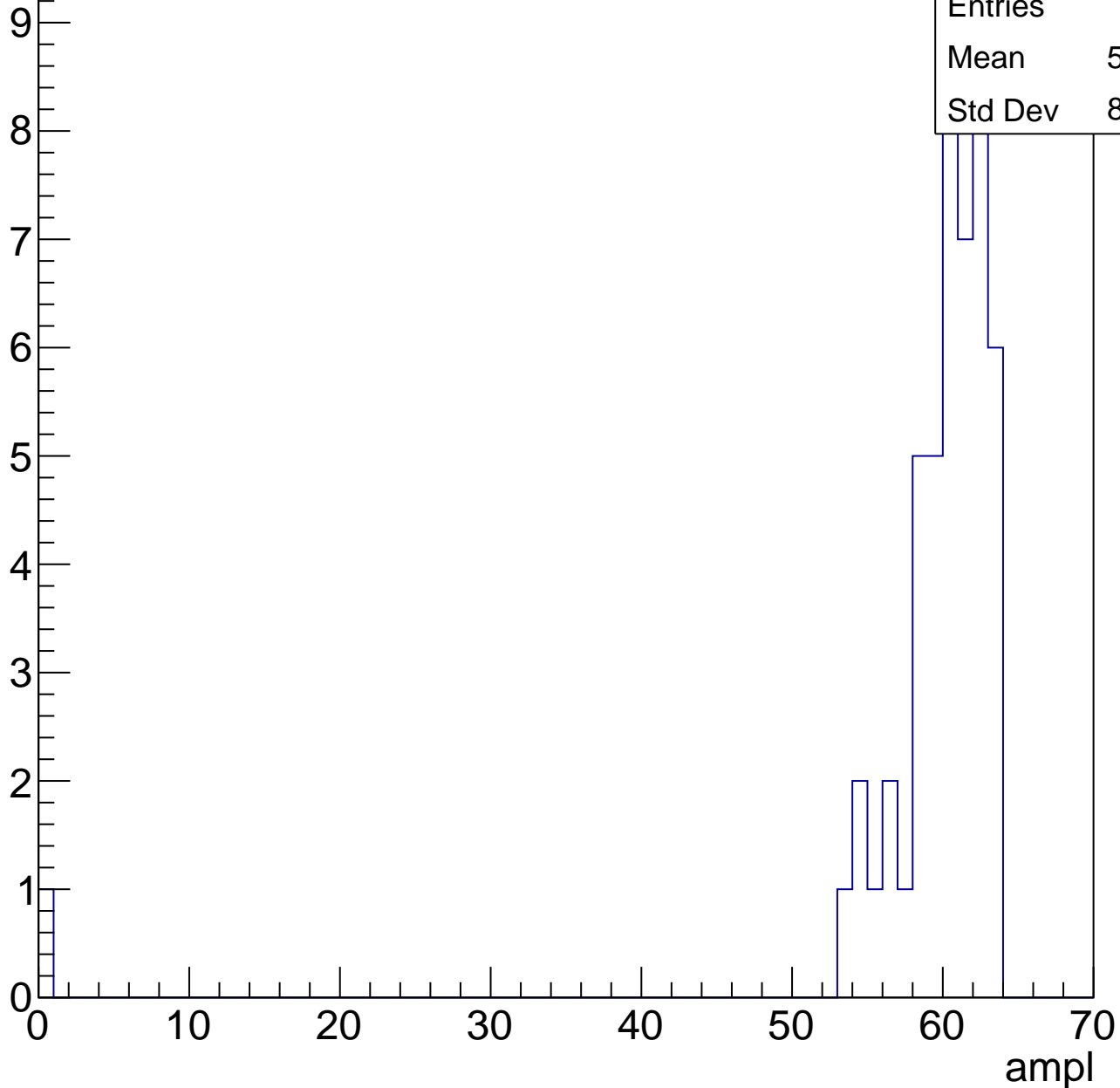
Entries	61
Mean	54.8
Std Dev	3.416



B1L103S, U14-ch79, adc5

calib_packv5_042523_0143.root, FC#7, port C2

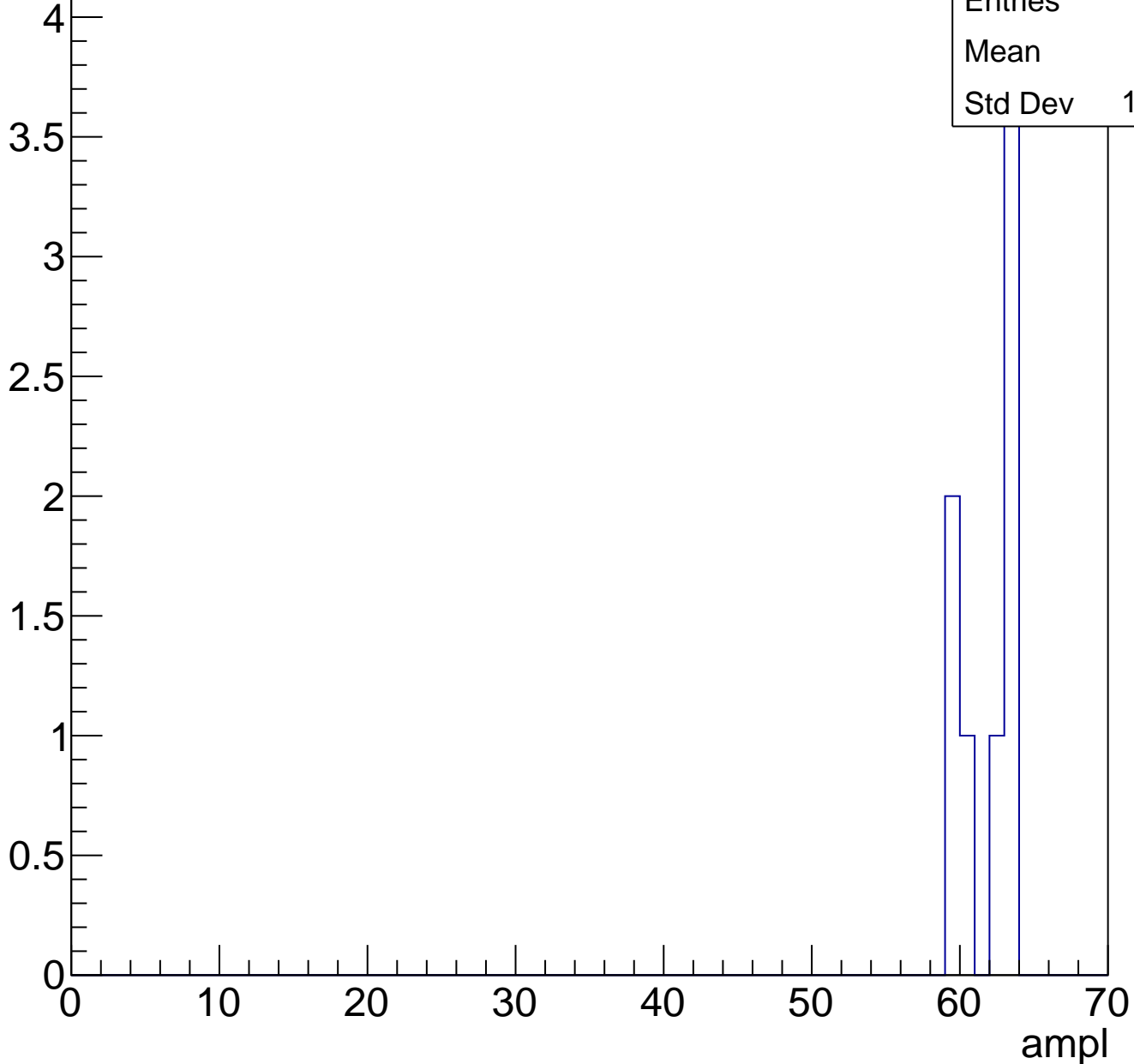
Entry



B1L103S, U14-ch79, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U14-ch80, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	28.15
Std Dev	3.487

Gaus mean : 28.7329

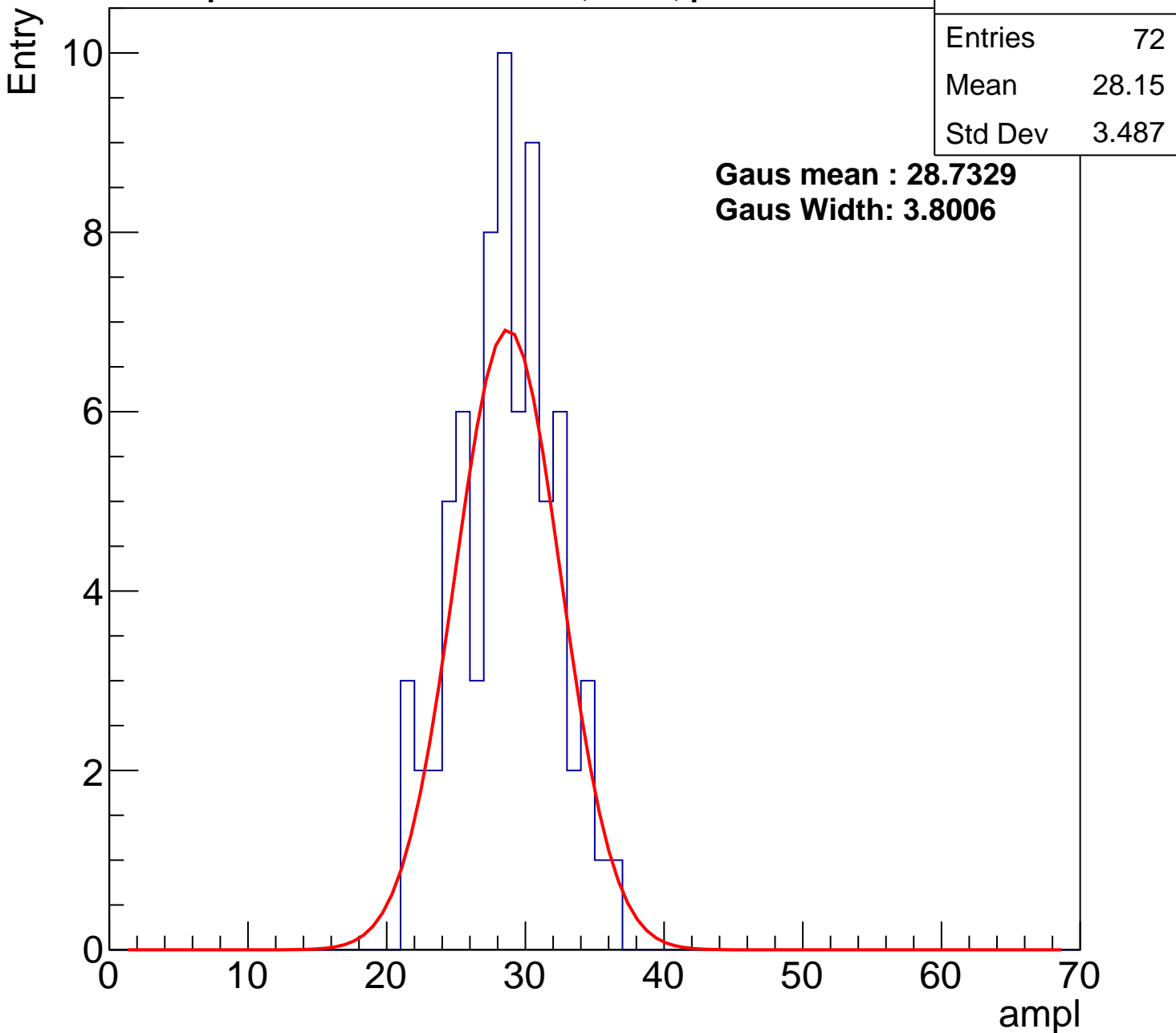
Gaus Width: 3.8006

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch80, adc1

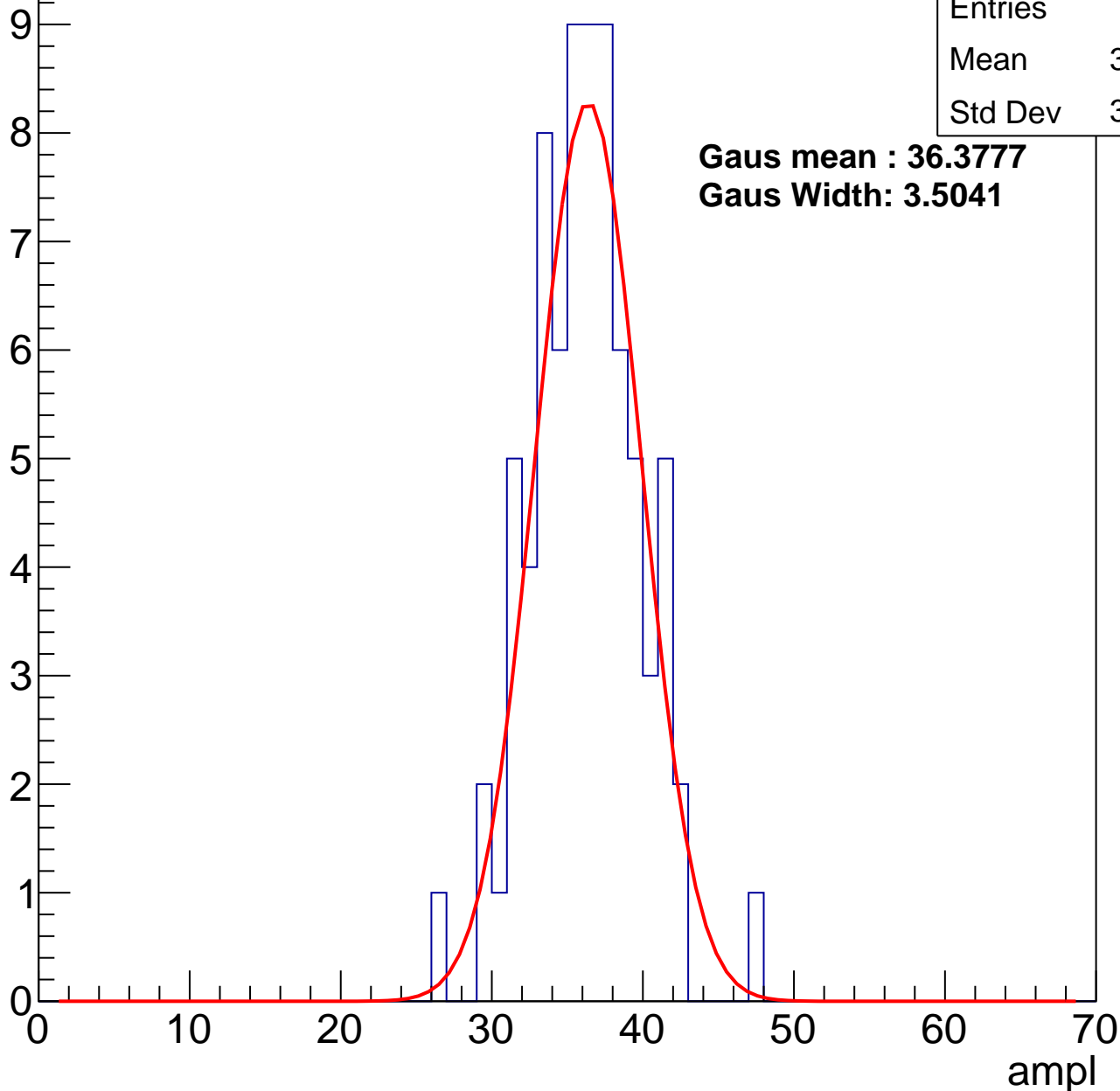
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.74
Std Dev	3.567

Gaus mean : 36.3777

Gaus Width: 3.5041

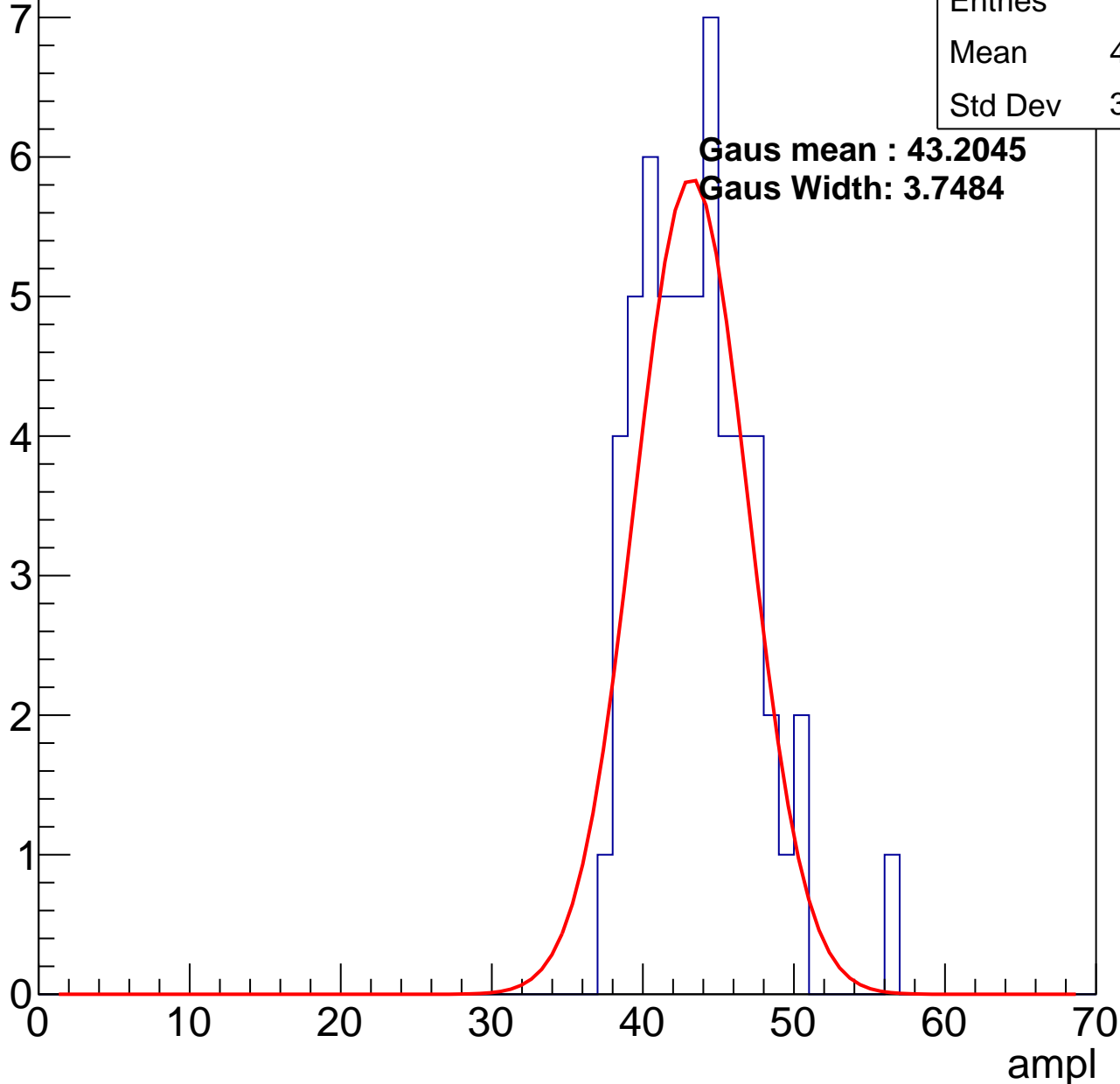


B1L103S, U14-ch80, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.12
Std Dev	3.708

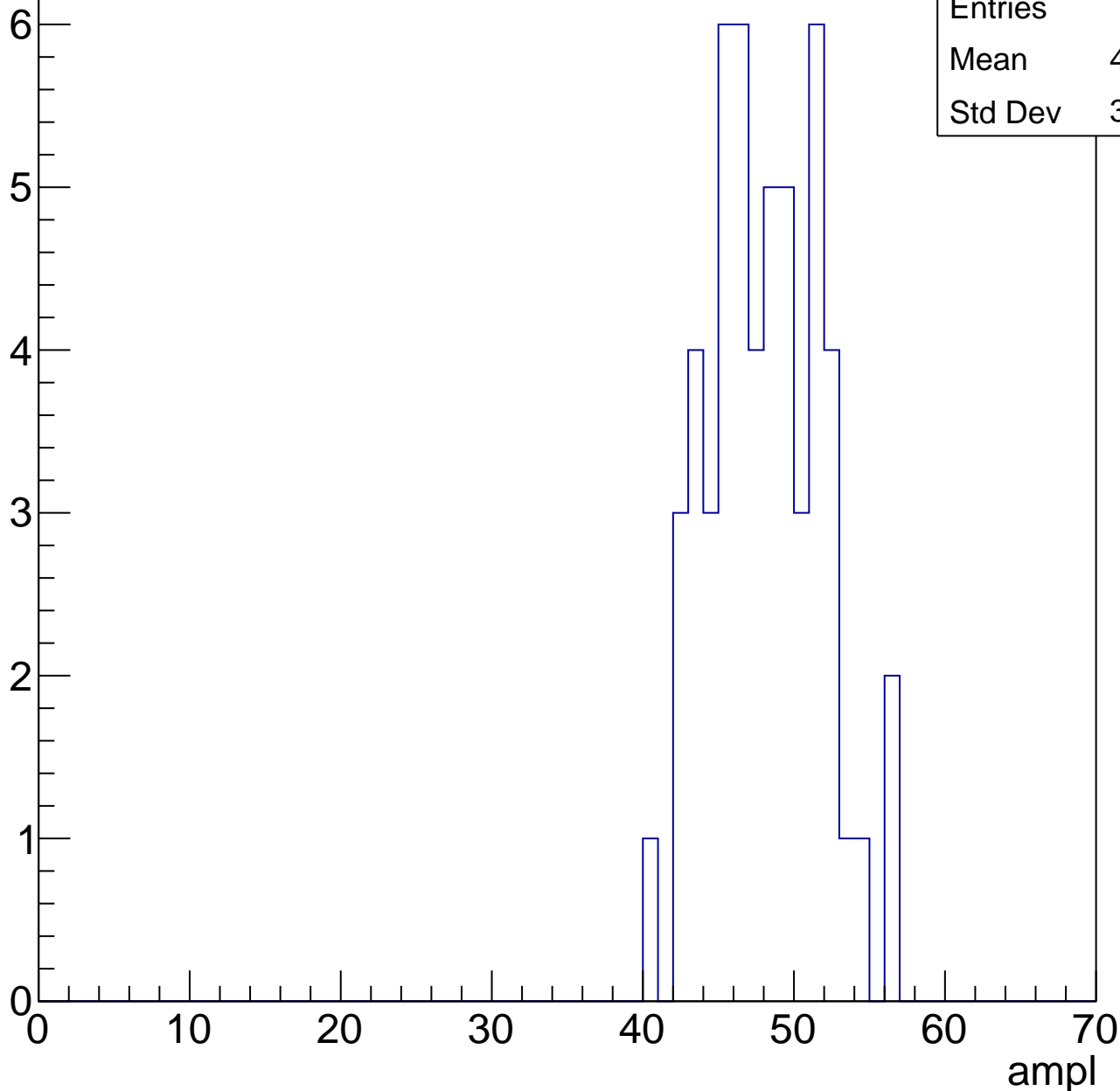


B1L103S, U14-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	47.63
Std Dev	3.648

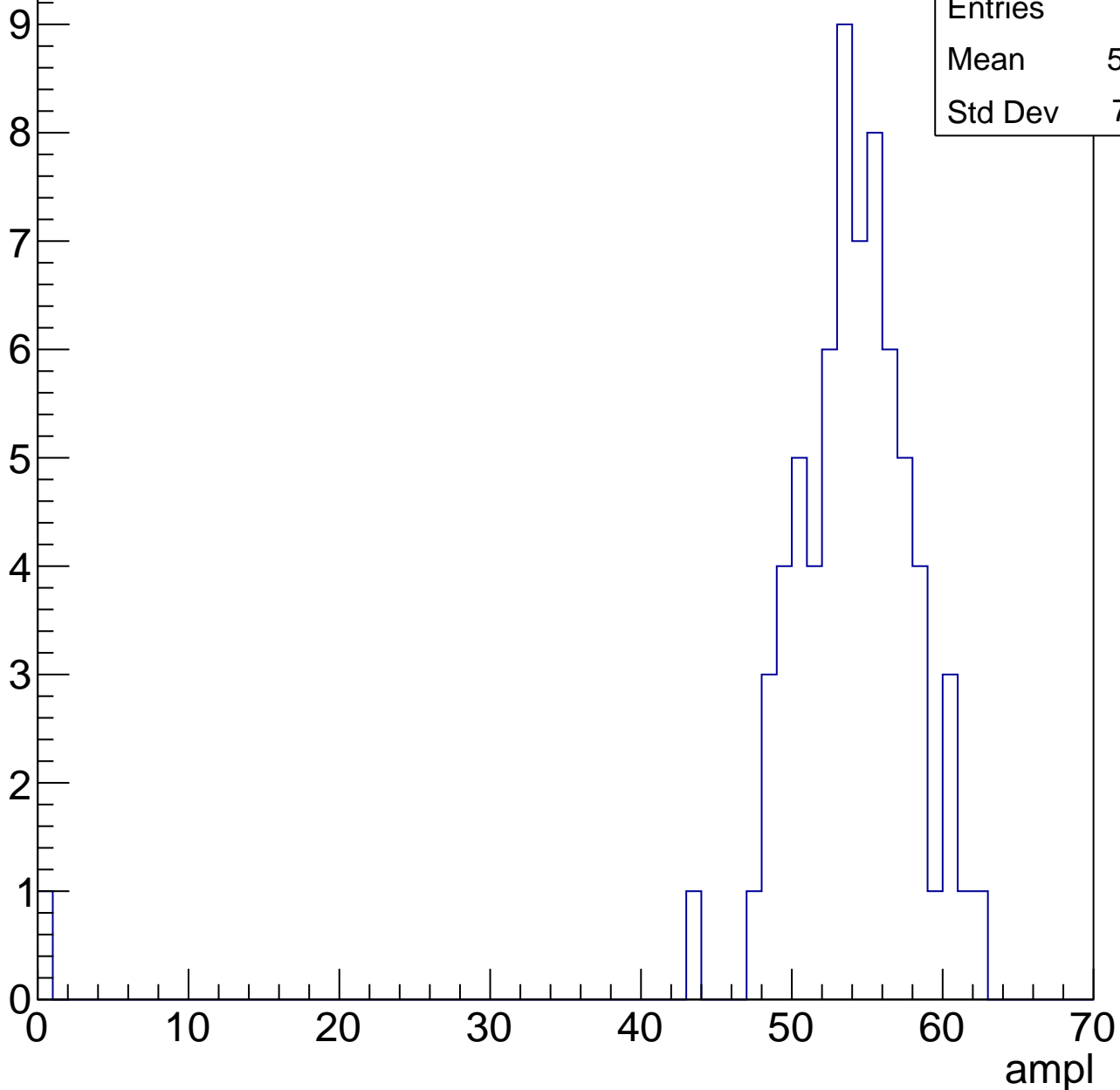


B1L103S, U14-ch80, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

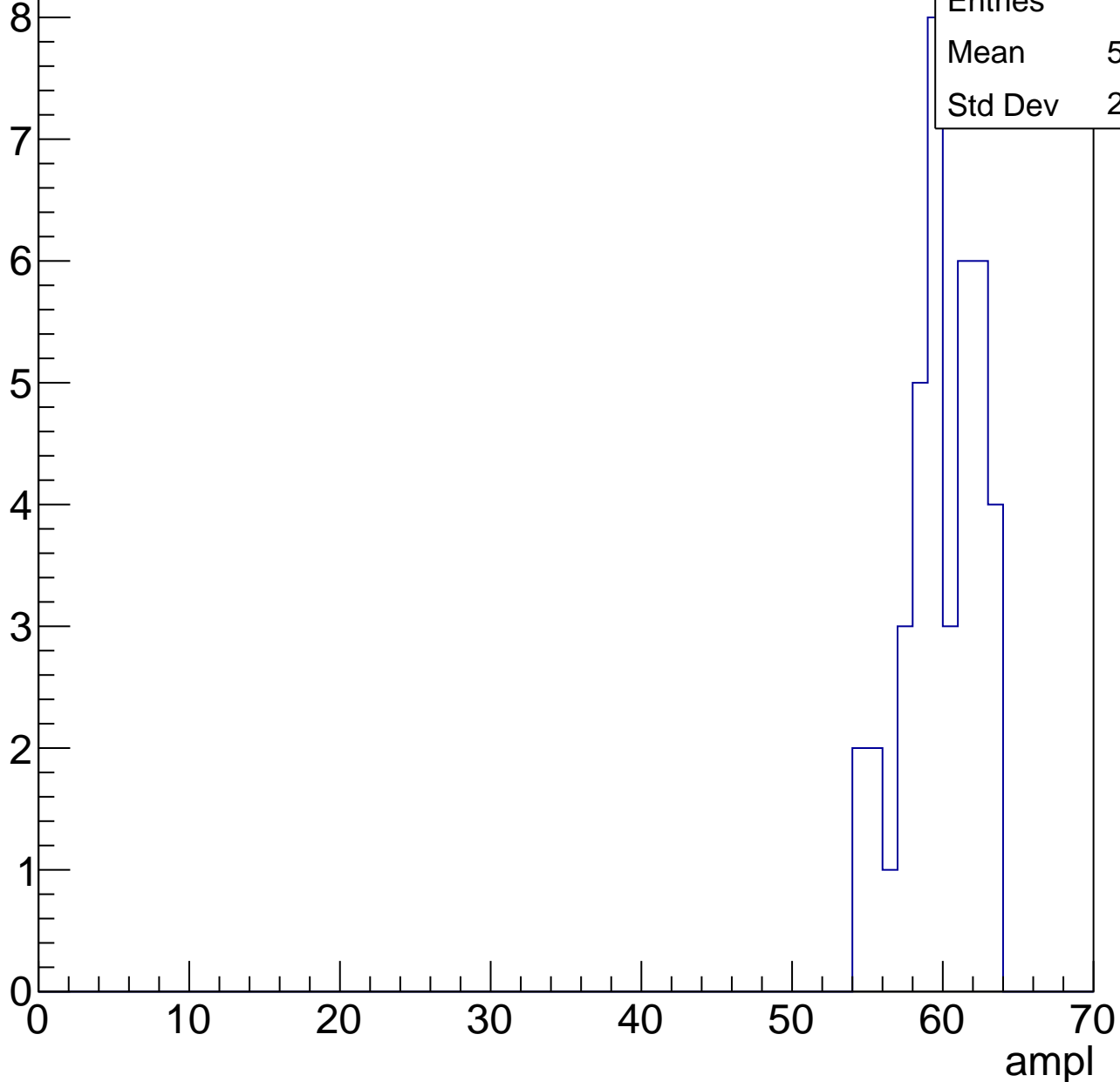
Entries	70
Mean	52.94
Std Dev	7.321



B1L103S, U14-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

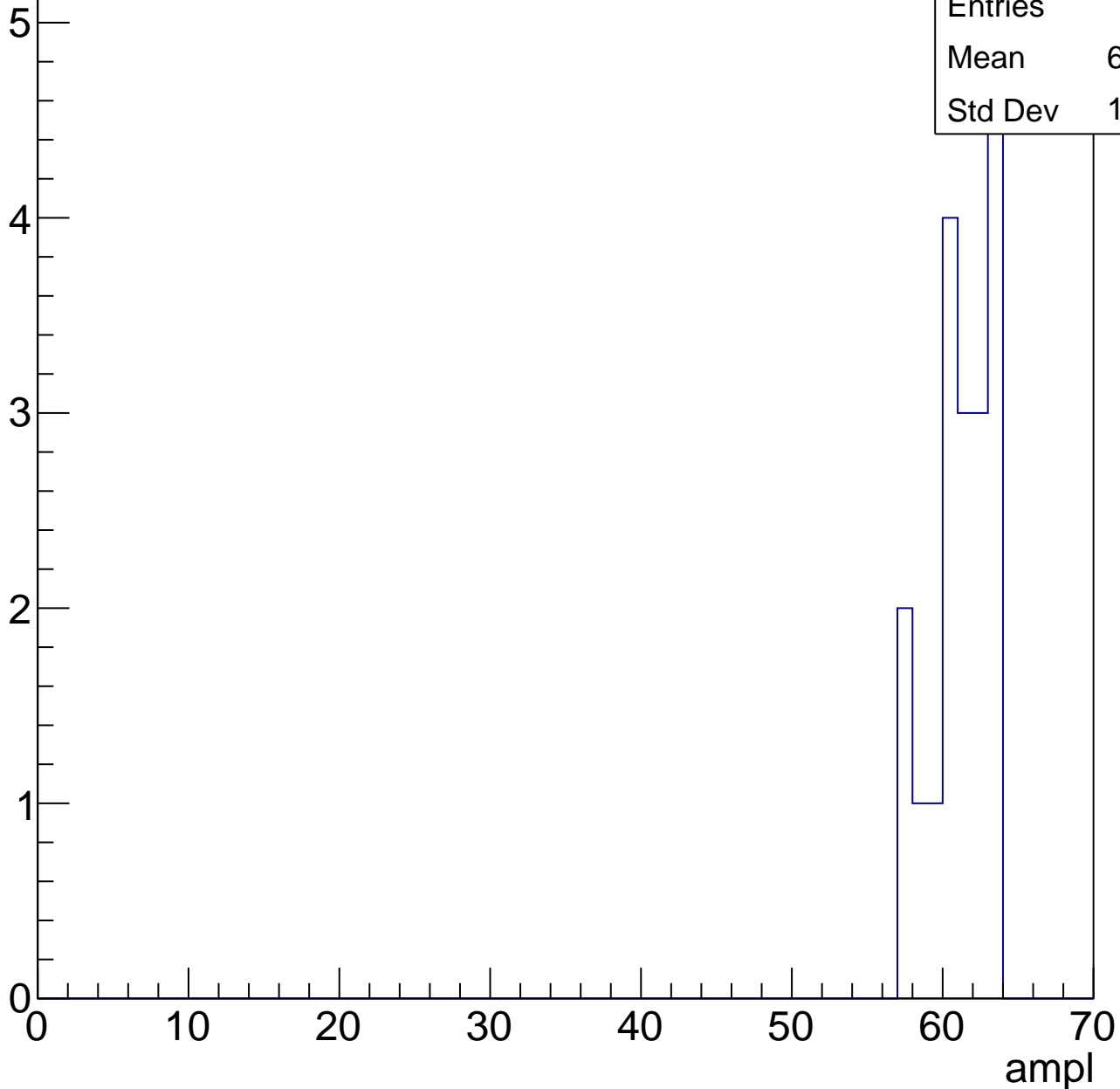


B1L103S, U14-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	60.79
Std Dev	1.935



B1L103S, U14-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch81, adc0

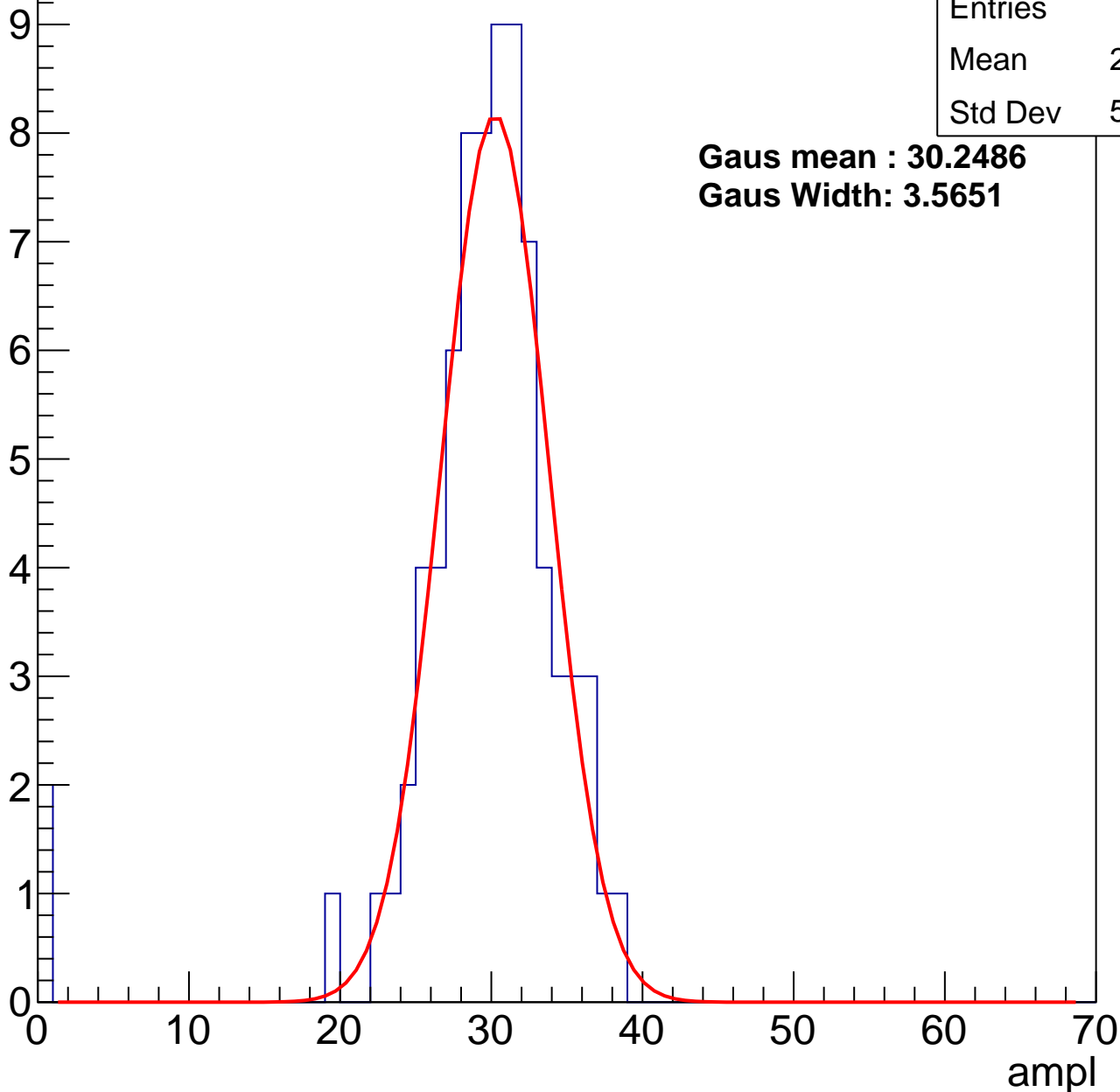
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	28.95
Std Dev	5.917

Gaus mean : 30.2486

Gaus Width: 3.5651



B1L103S, U14-ch81, adc1

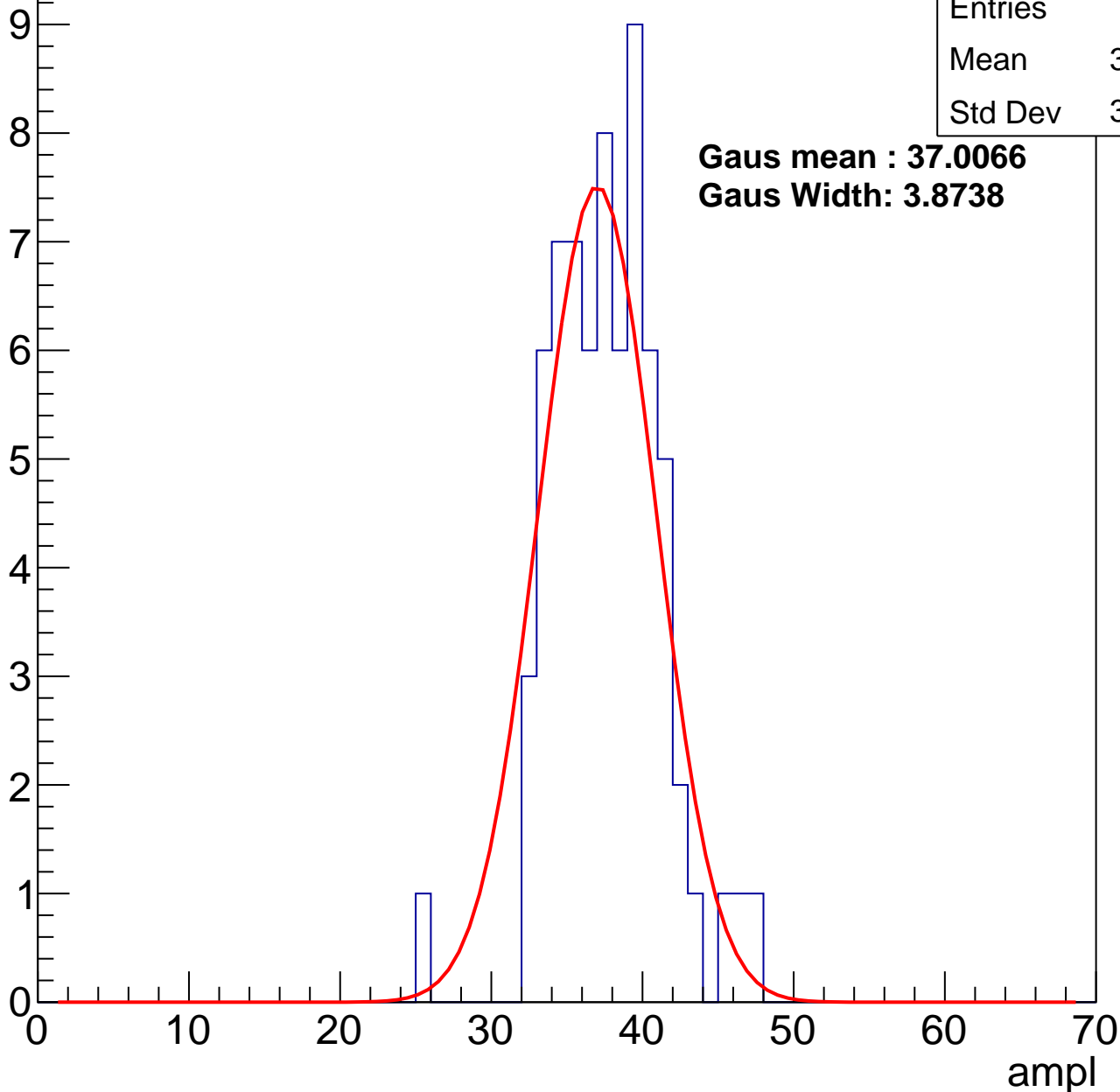
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	37.19
Std Dev	3.619

Gaus mean : 37.0066

Gaus Width: 3.8738



B1L103S, U14-ch81, adc2

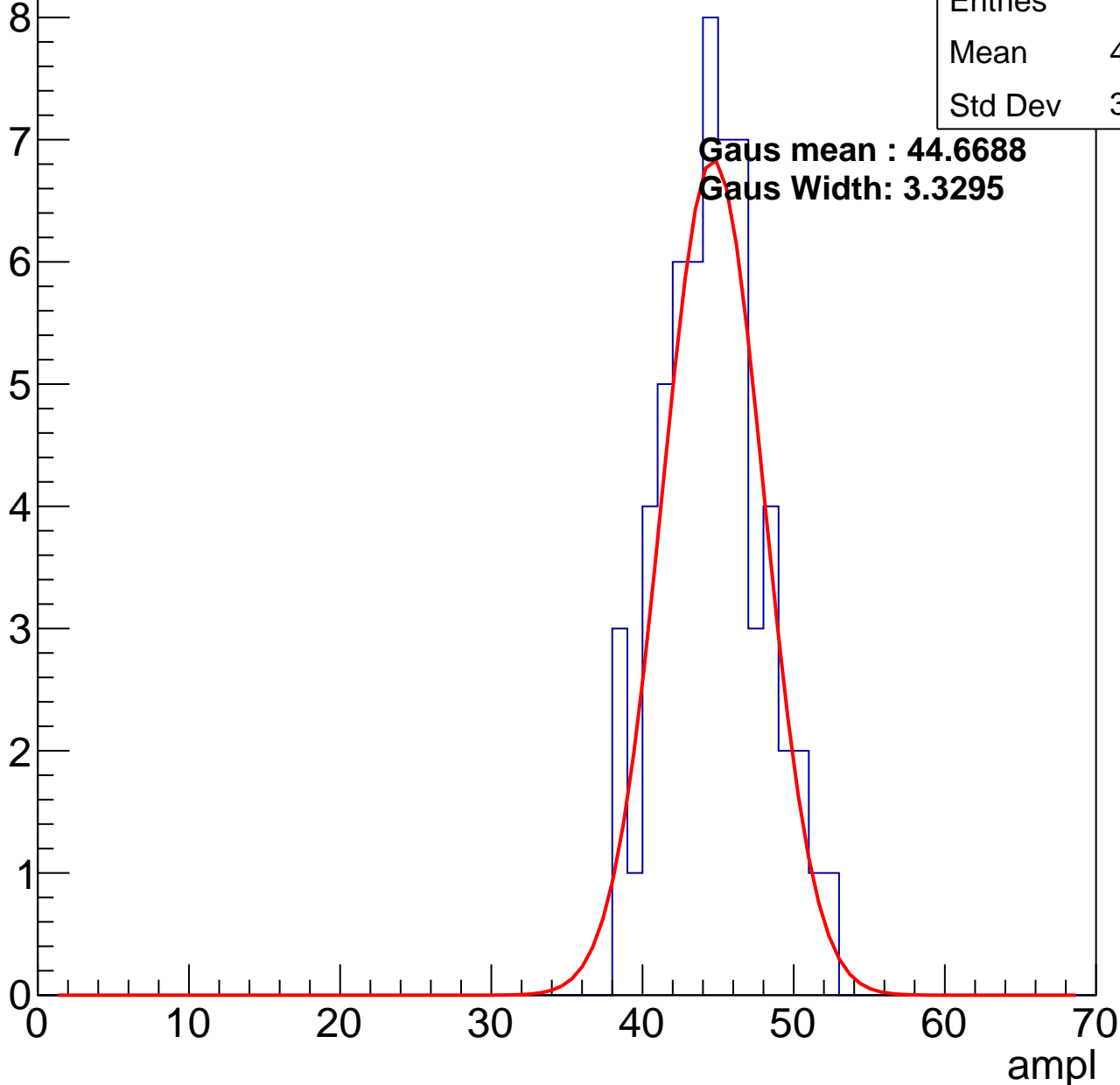
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	44.18
Std Dev	3.243

Gaus mean : 44.6688

Gaus Width: 3.3295

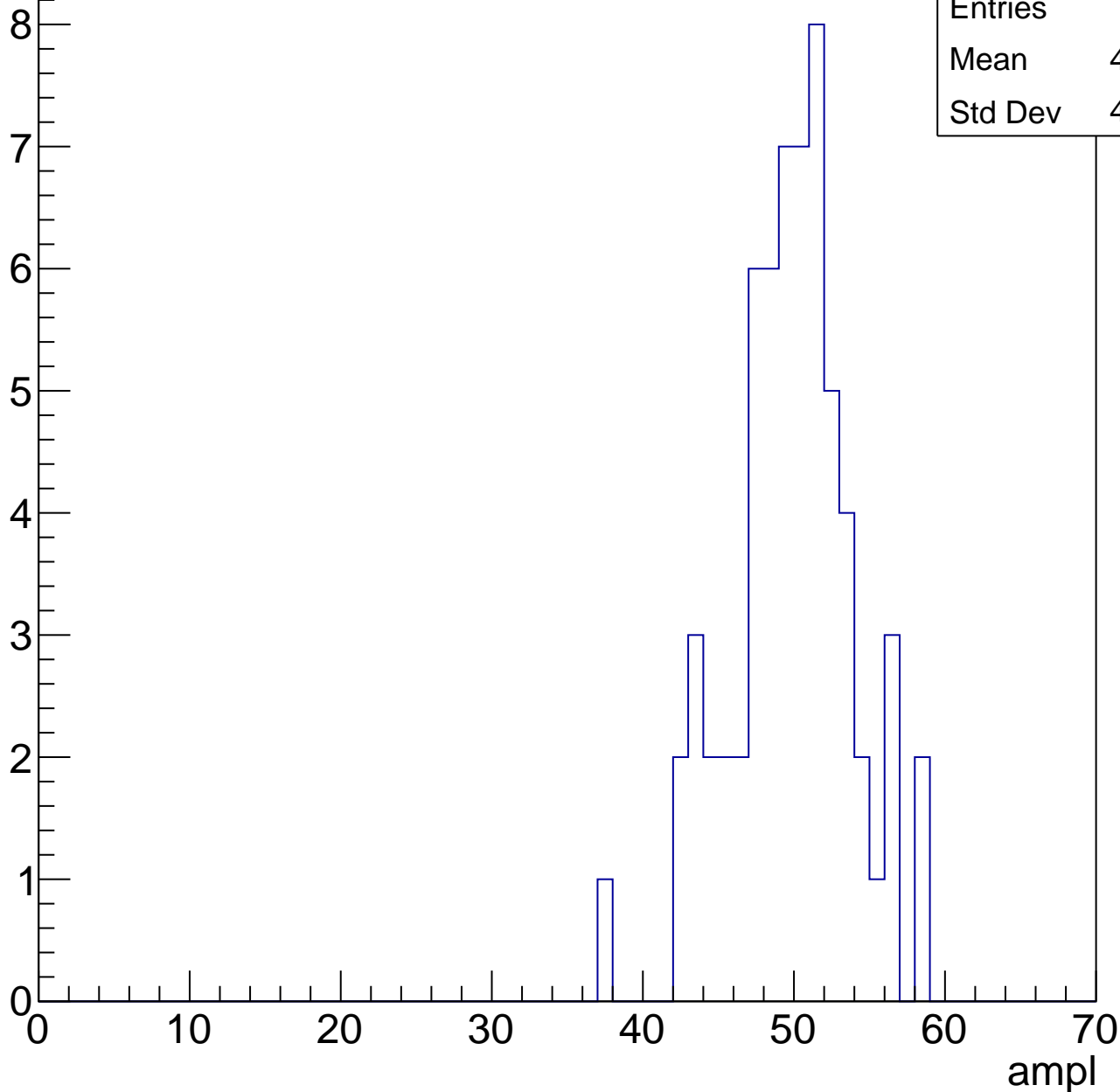


B1L103S, U14-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

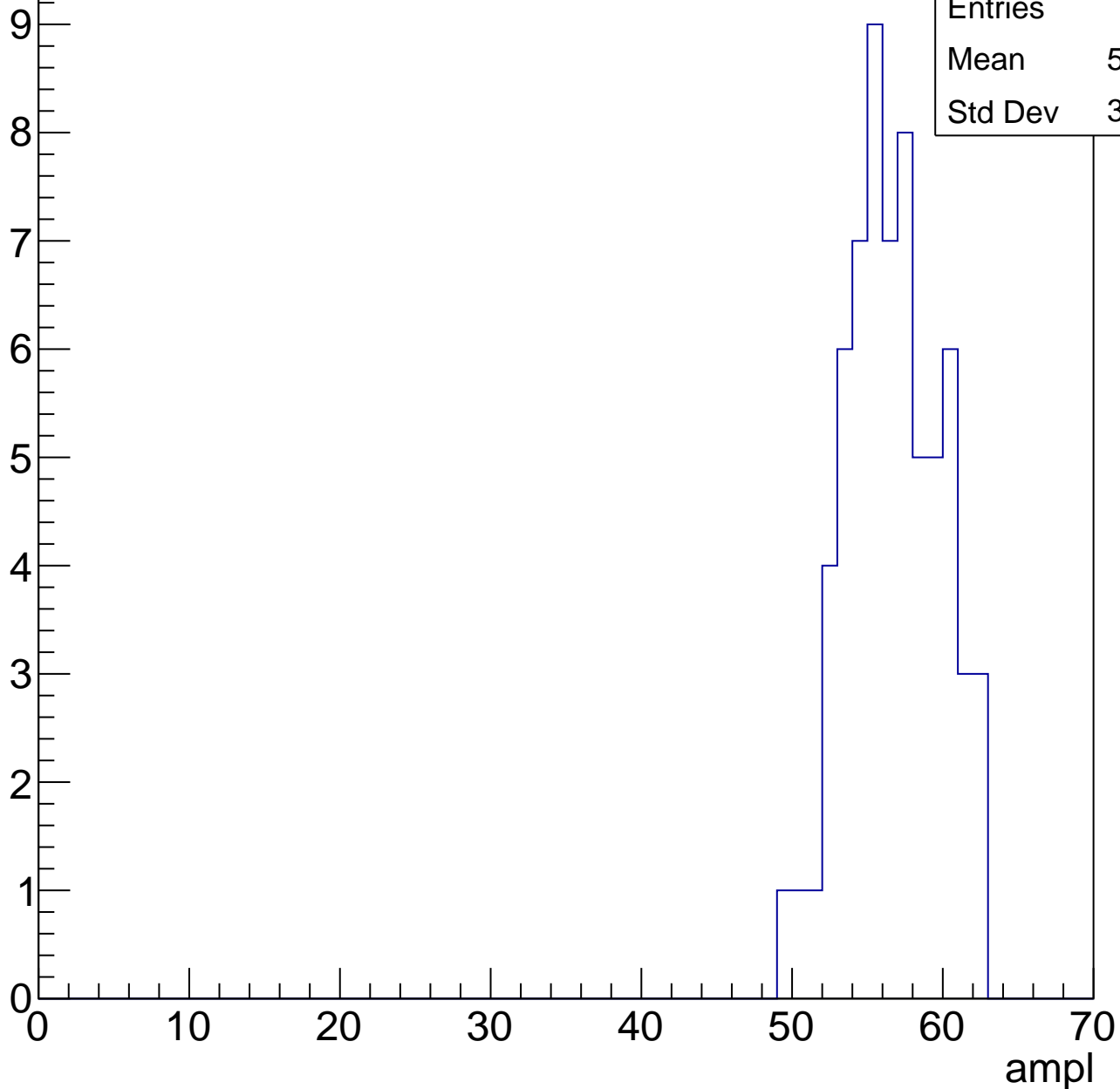
Entries	63
Mean	49.37
Std Dev	4.013



B1L103S, U14-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

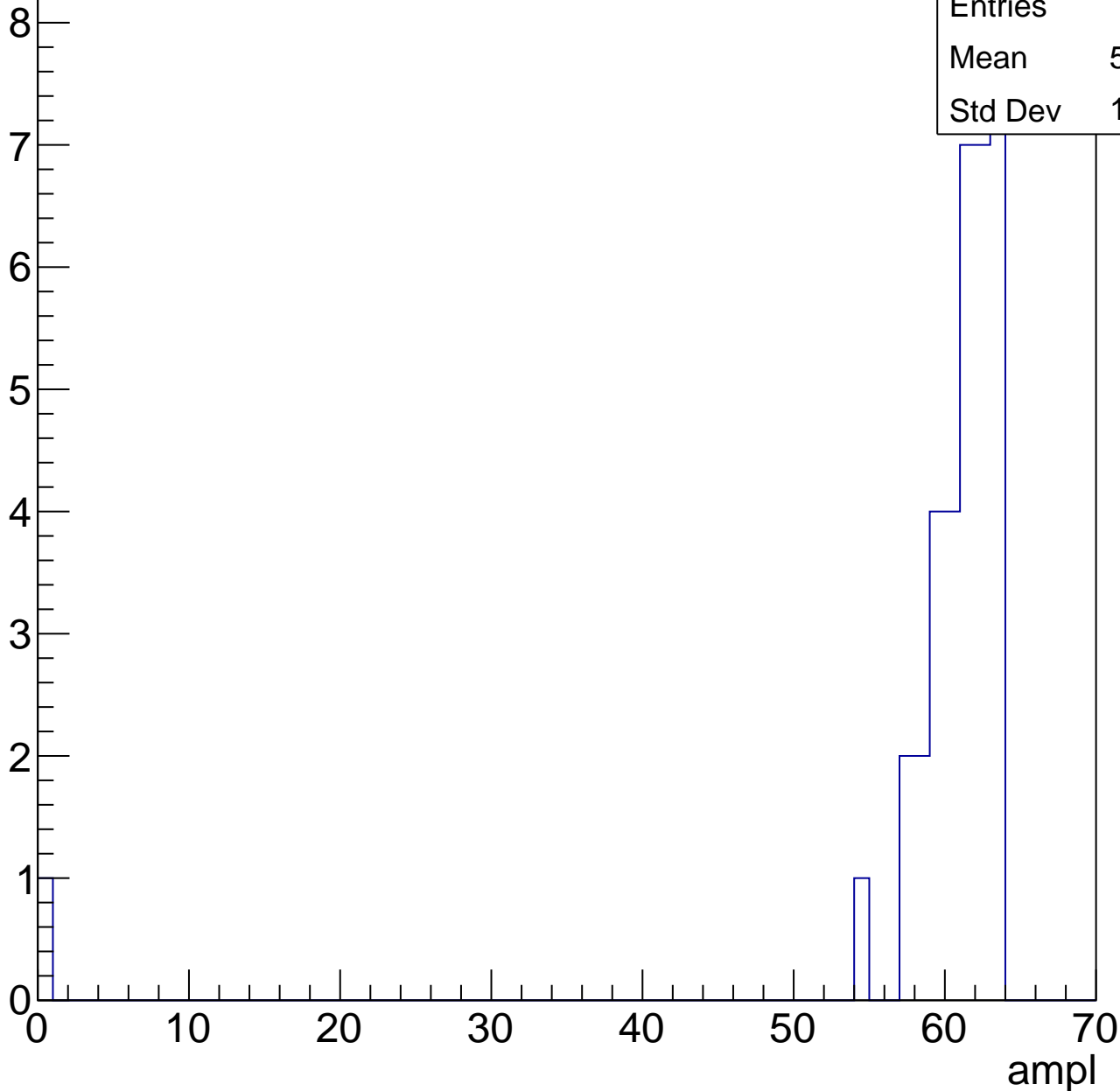


B1L103S, U14-ch81, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

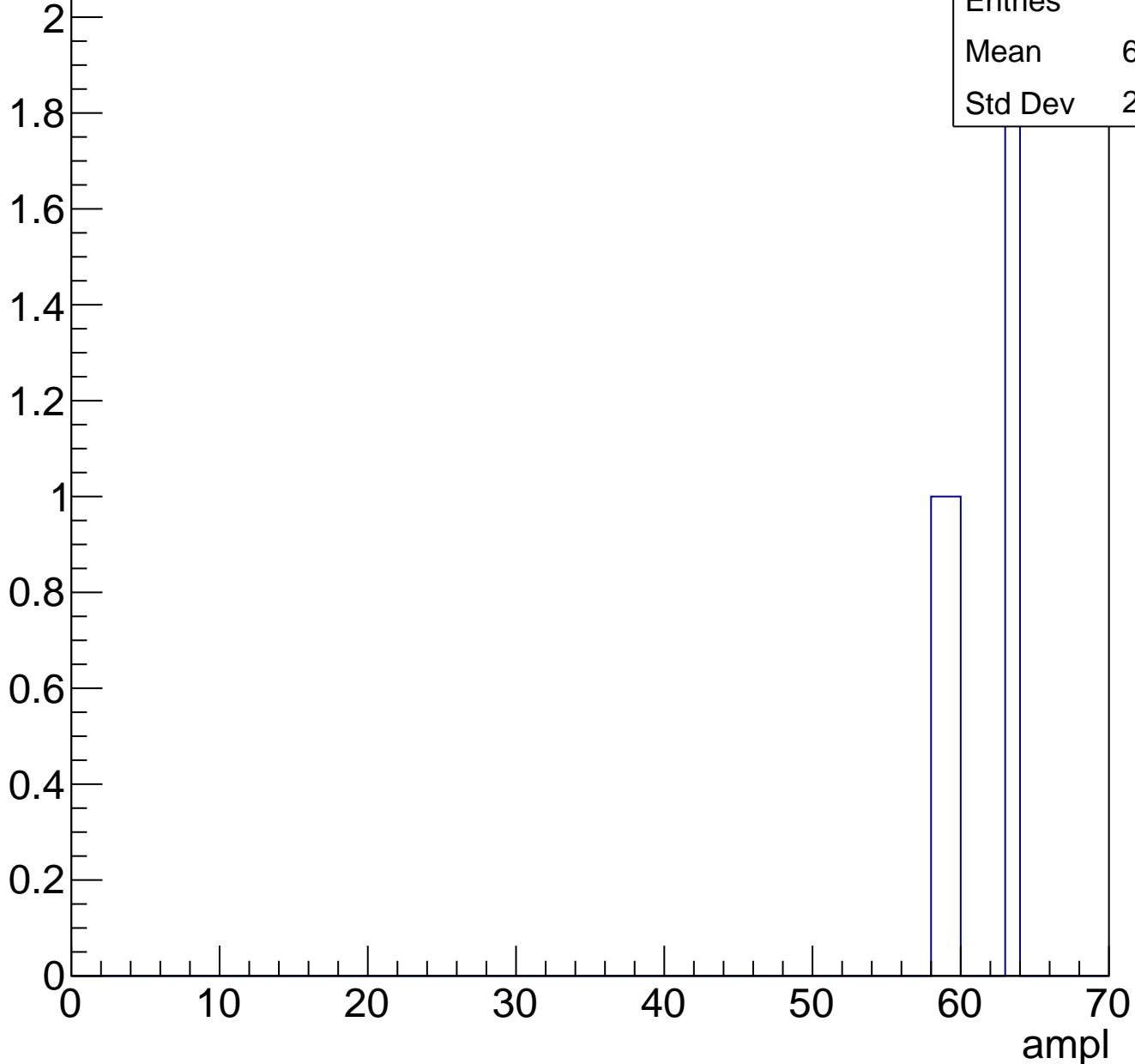
Entries	36
Mean	59.03
Std Dev	10.19



B1L103S, U14-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U14-ch82, adc0

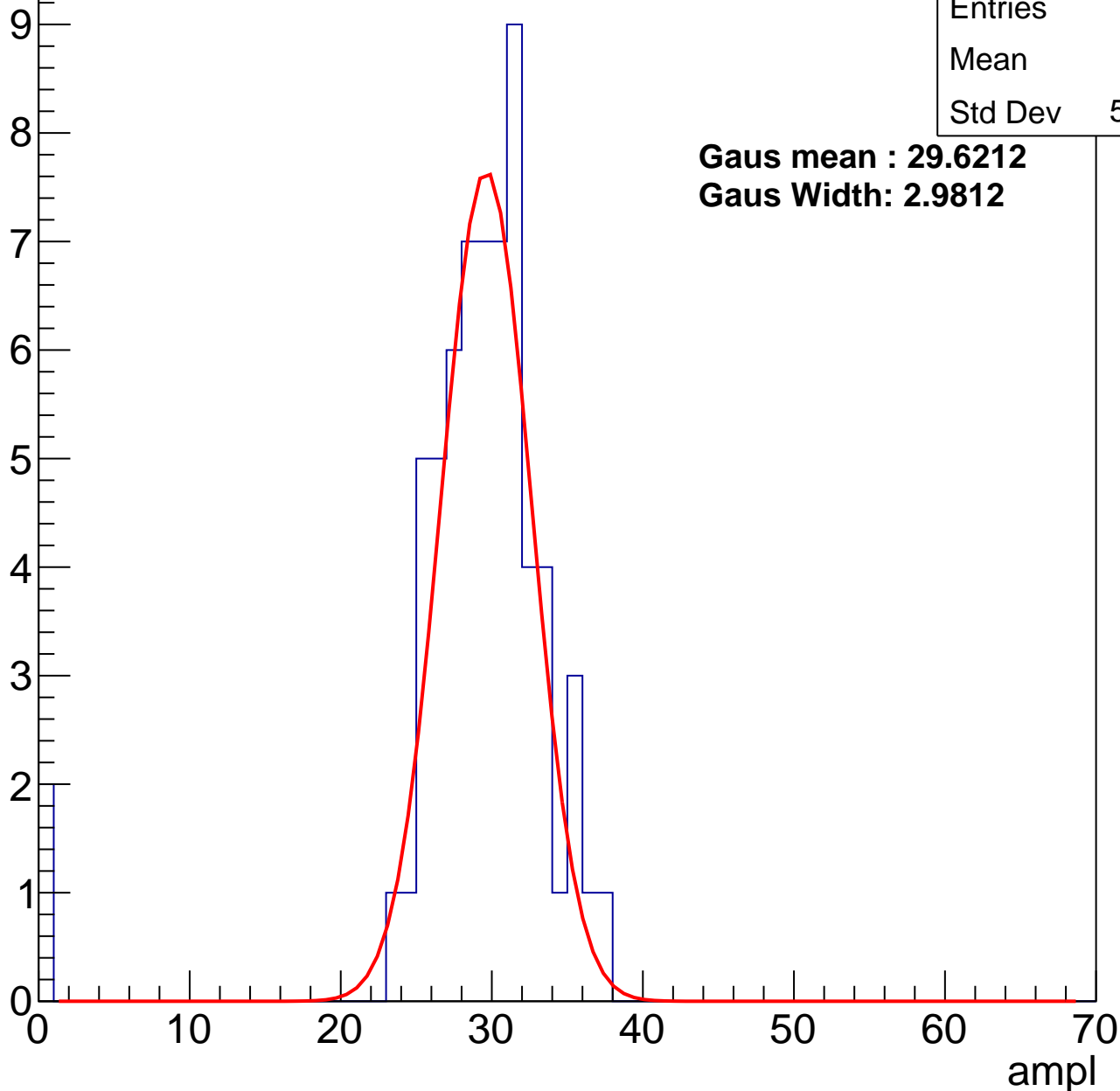
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.5
Std Dev	5.958

Gaus mean : 29.6212

Gaus Width: 2.9812



B1L103S, U14-ch82, adc1

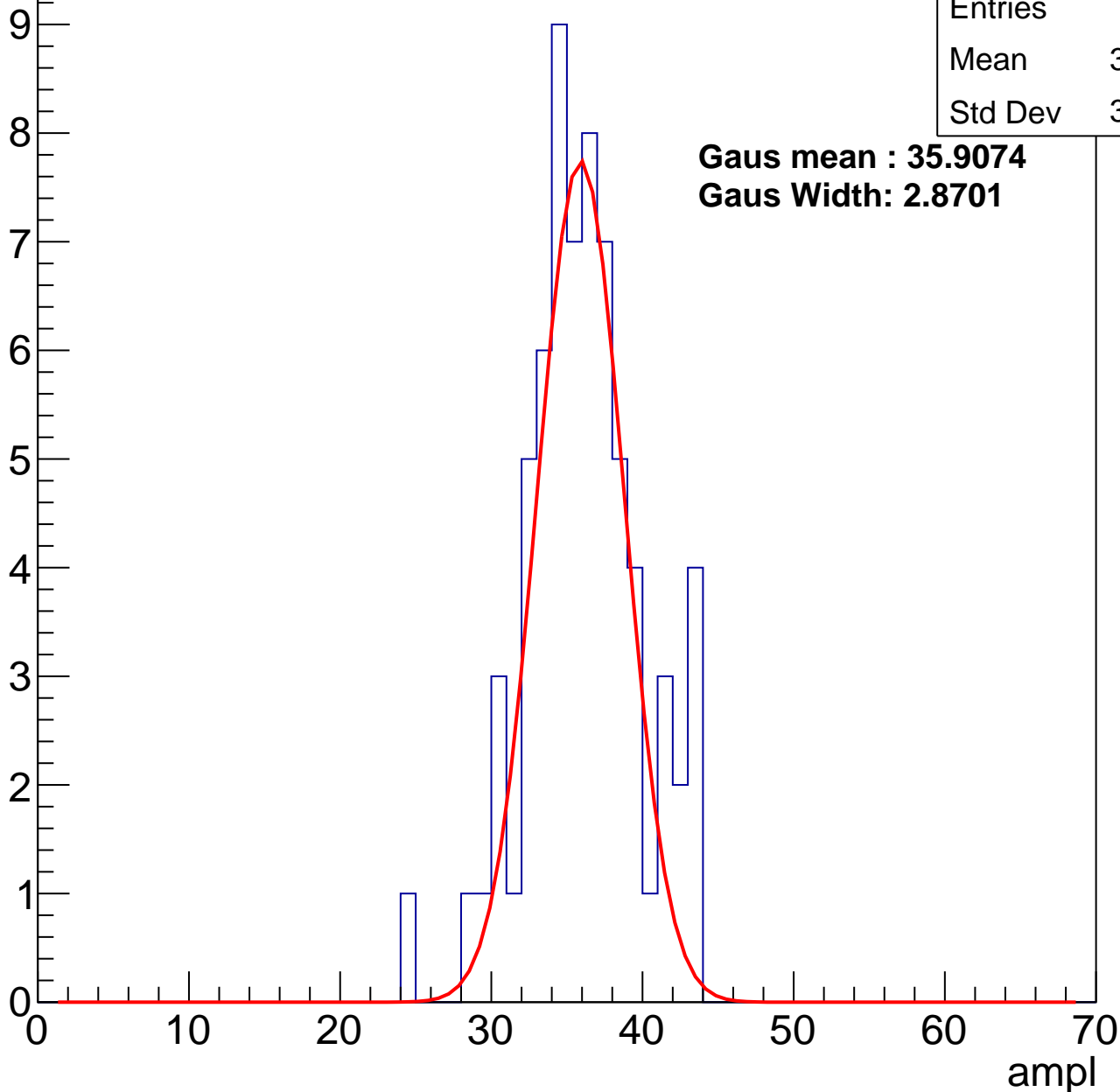
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	35.63
Std Dev	3.796

Gaus mean : 35.9074

Gaus Width: 2.8701



B1L103S, U14-ch82, adc2

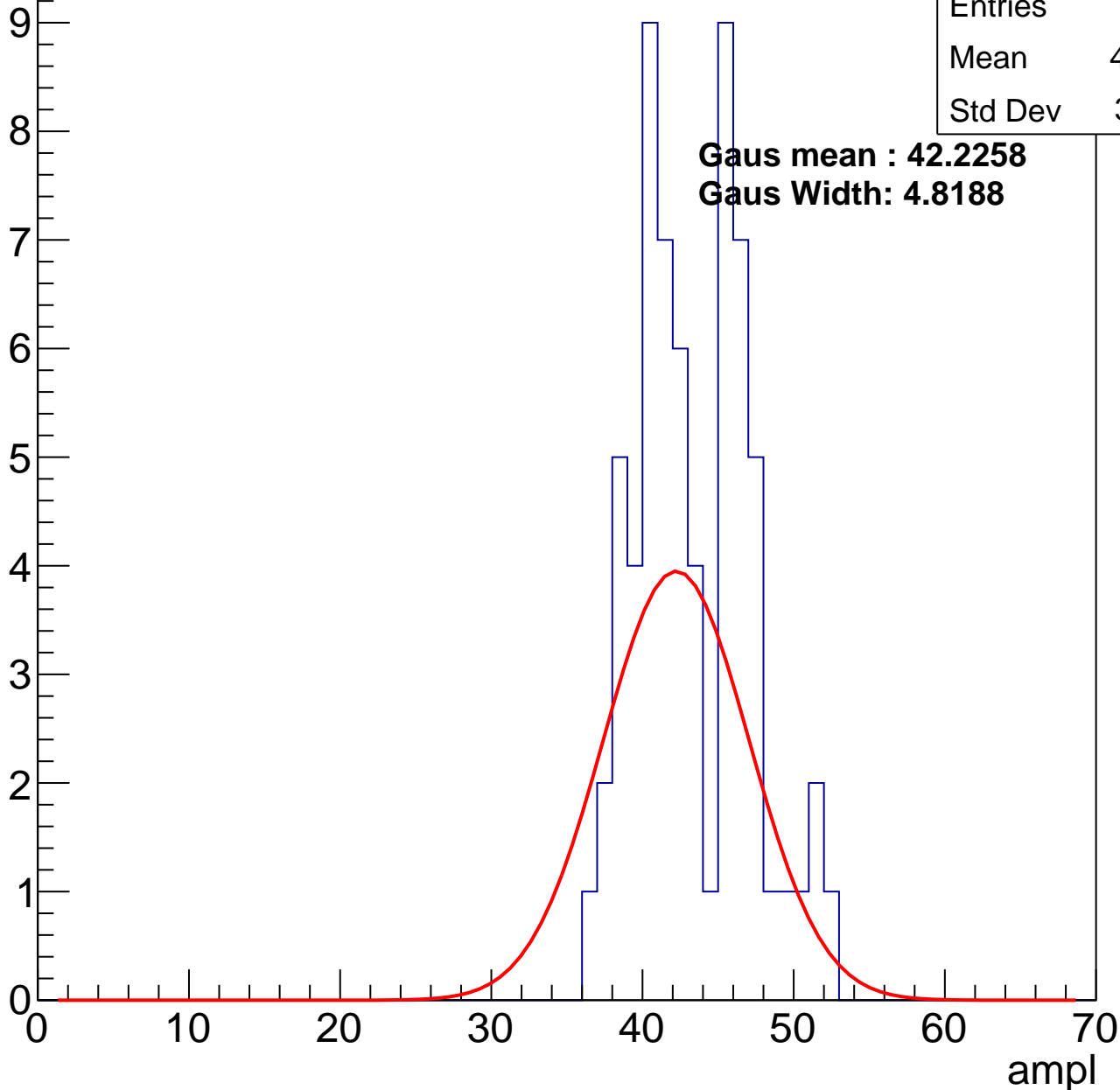
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.94
Std Dev	3.741

Gaus mean : 42.2258

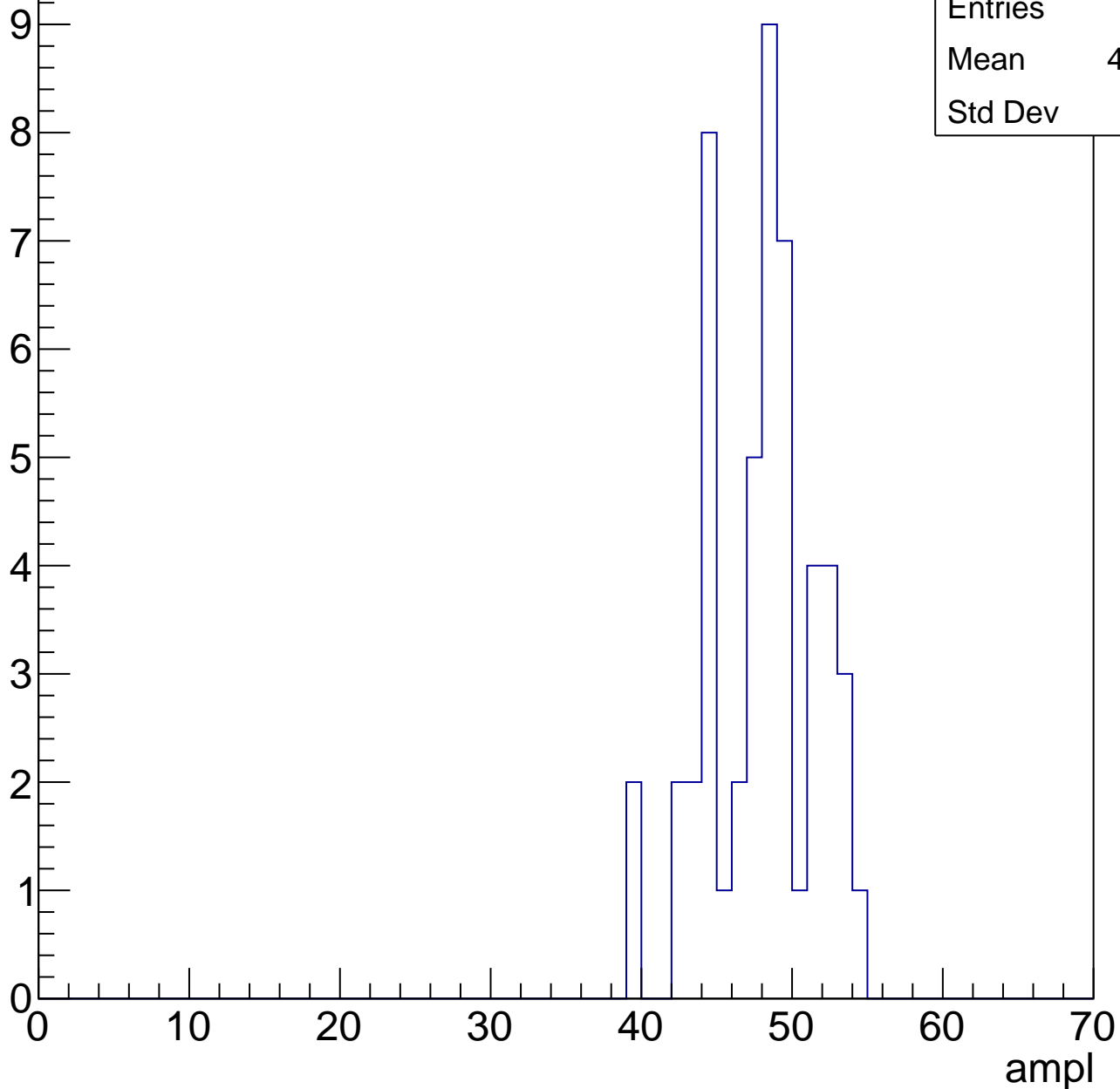
Gaus Width: 4.8188



B1L103S, U14-ch82, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

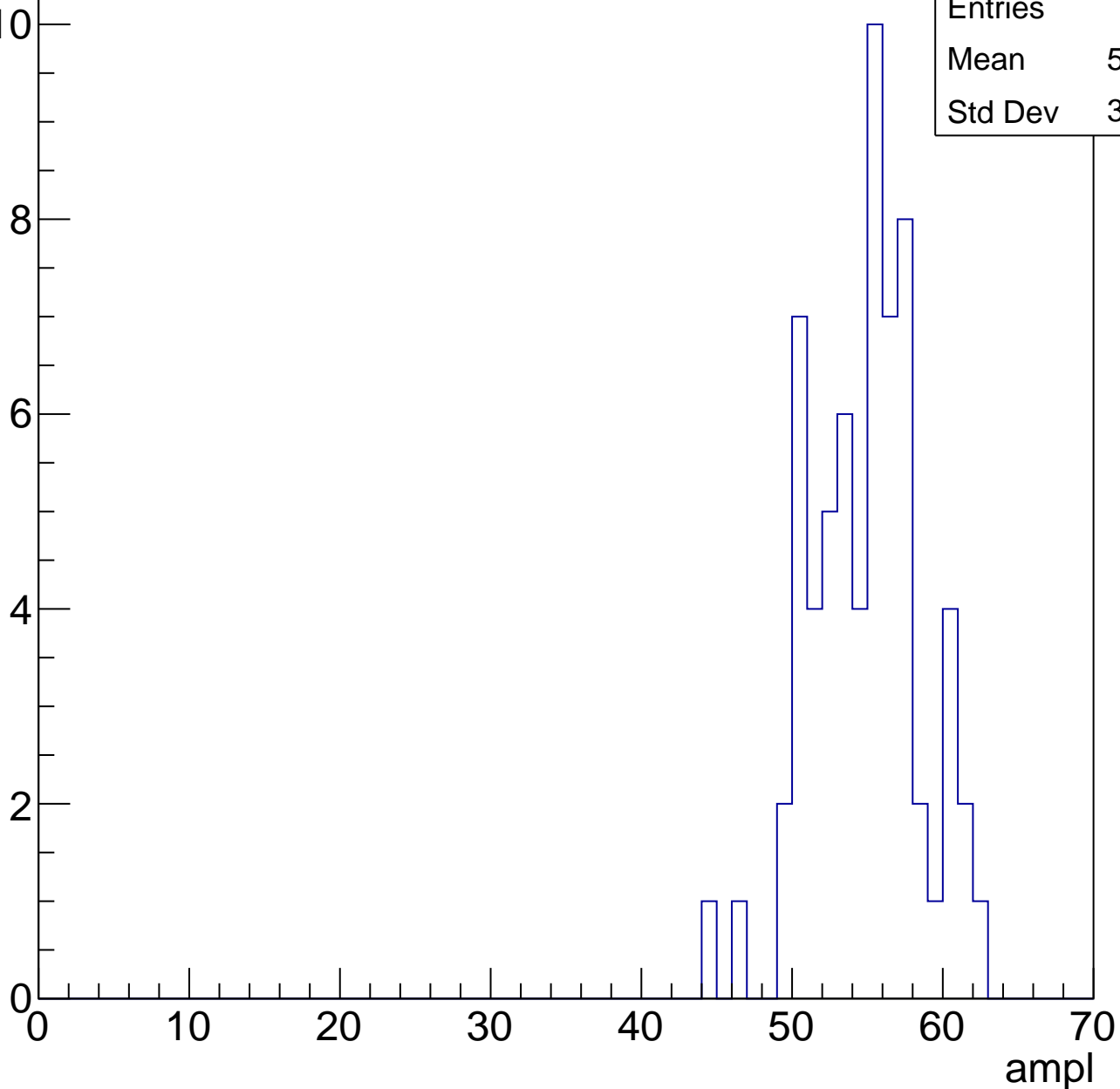


B1L103S, U14-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

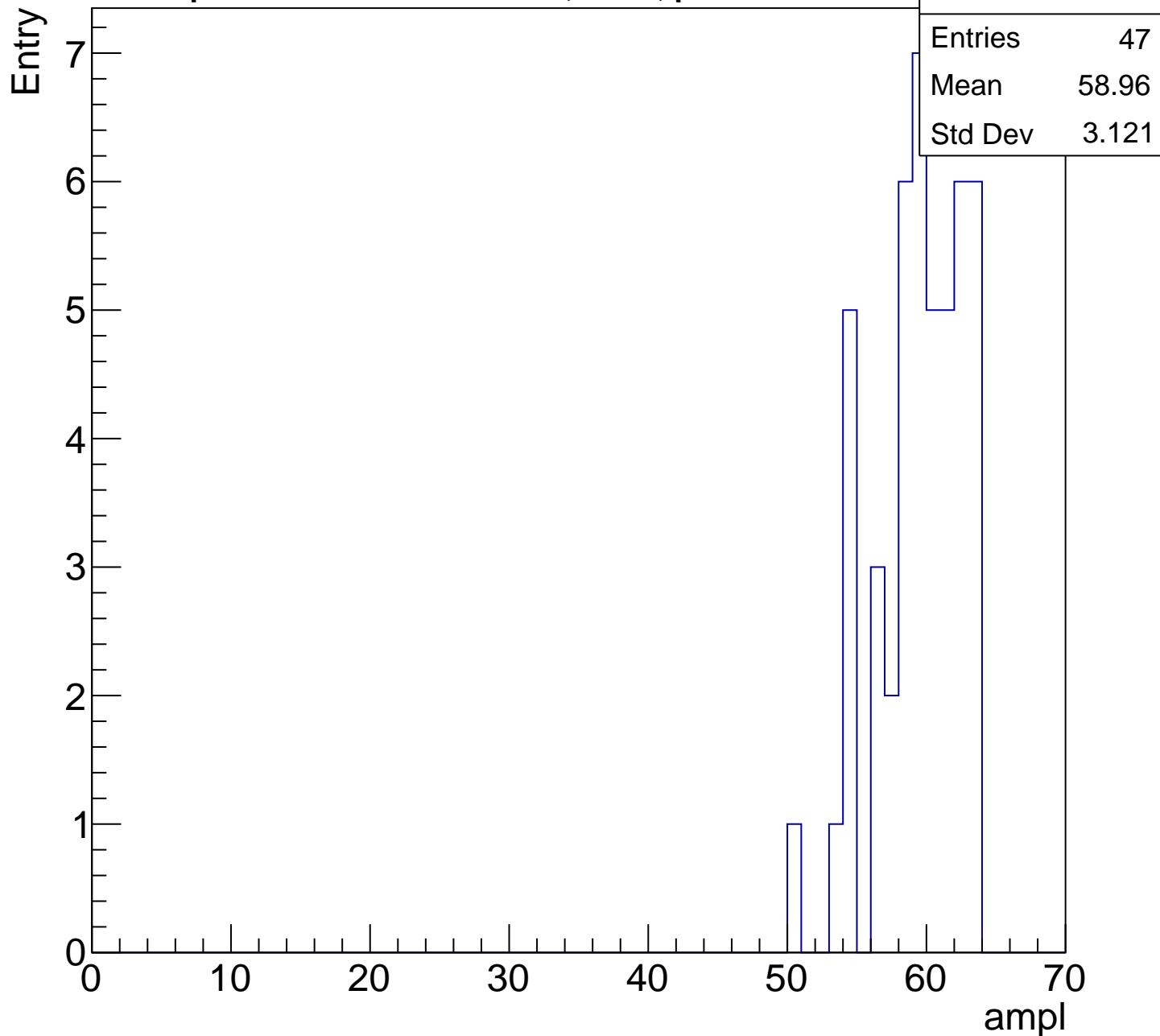
Entry

Entries	65
Mean	54.35
Std Dev	3.622



B1L103S, U14-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U14-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

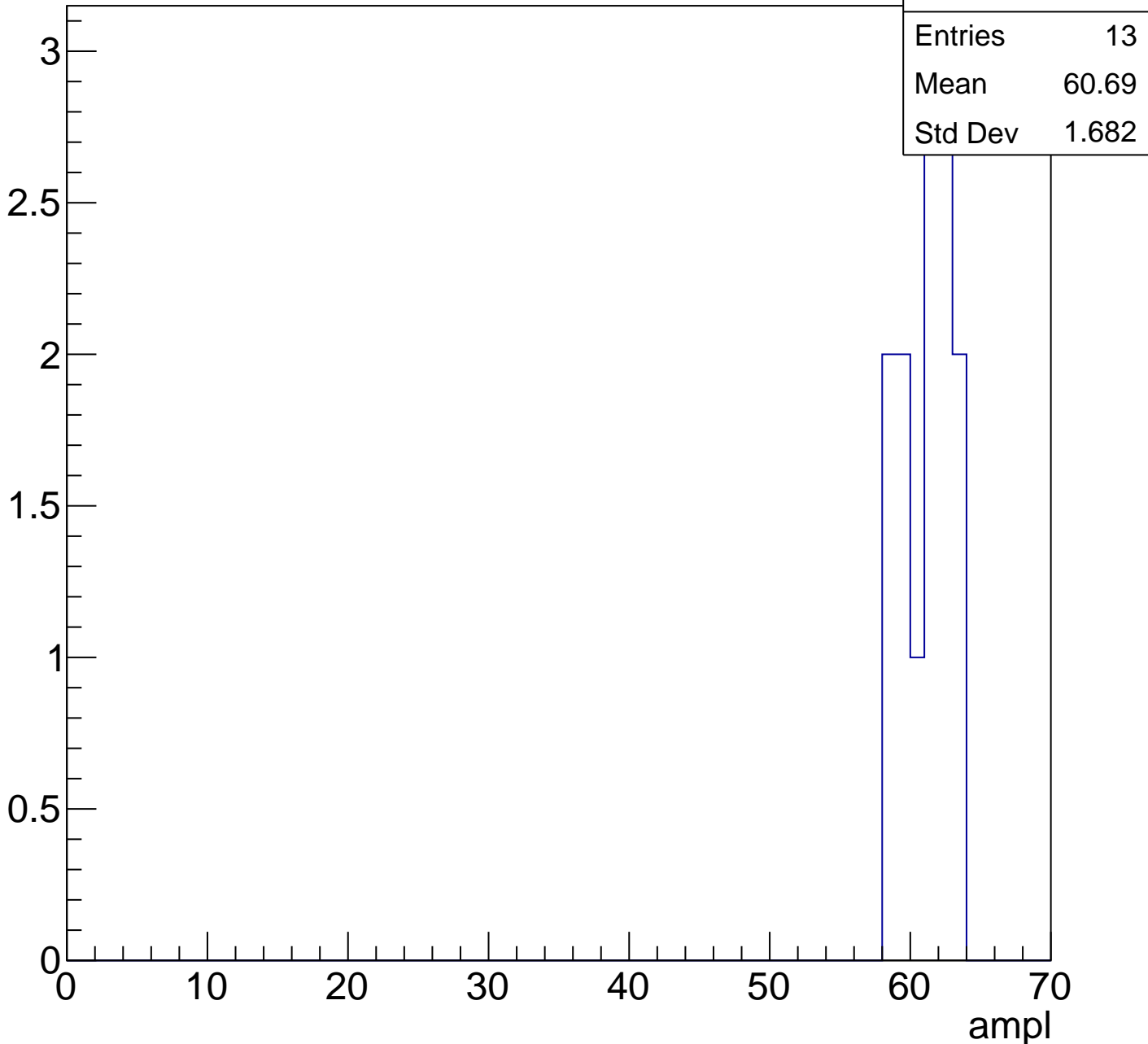
50

60

70

ampl

Entries	13
Mean	60.69
Std Dev	1.682



B1L103S, U14-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch83, adc0

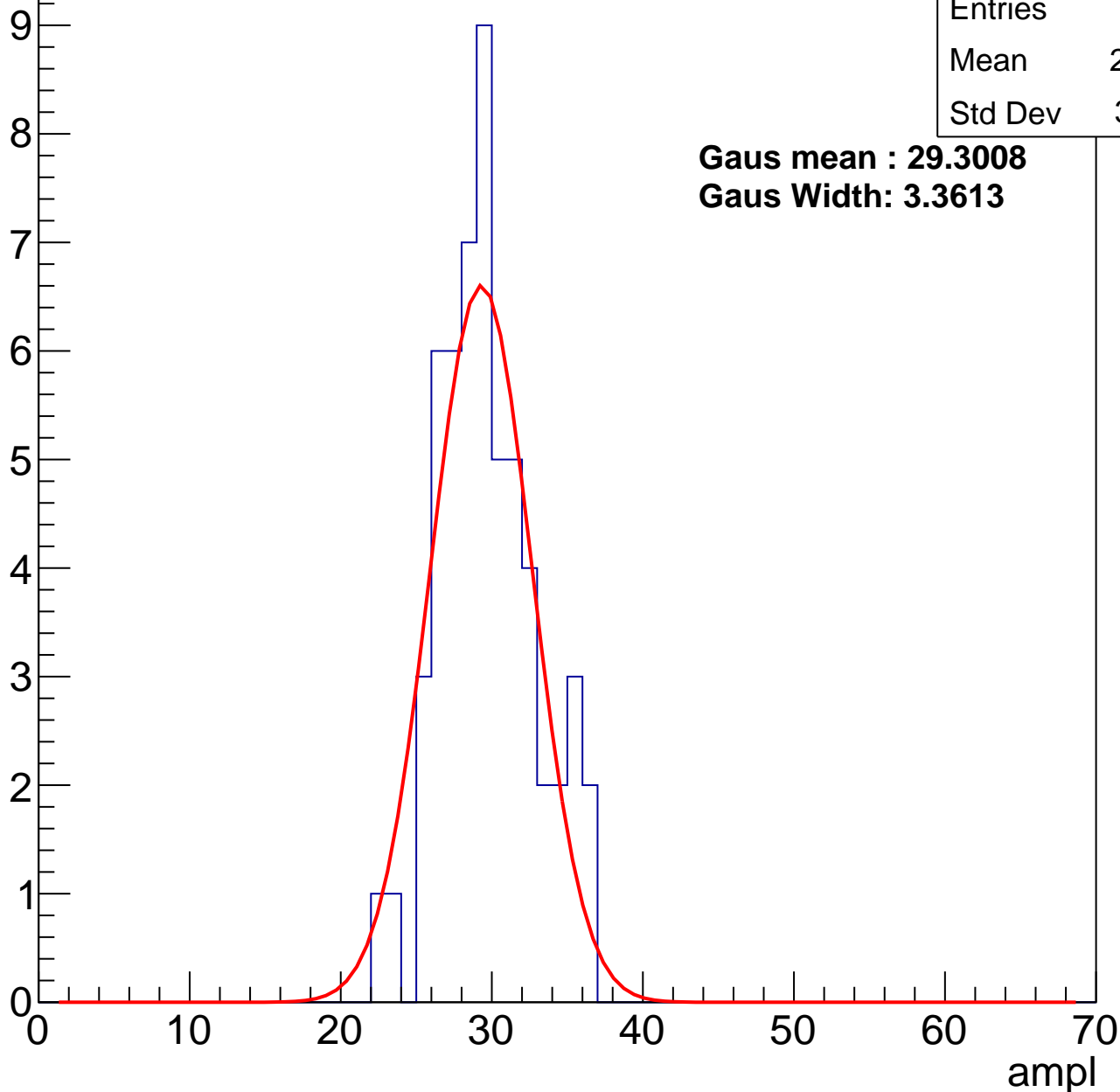
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	29.27
Std Dev	3.171

Gaus mean : 29.3008

Gaus Width: 3.3613



B1L103S, U14-ch83, adc1

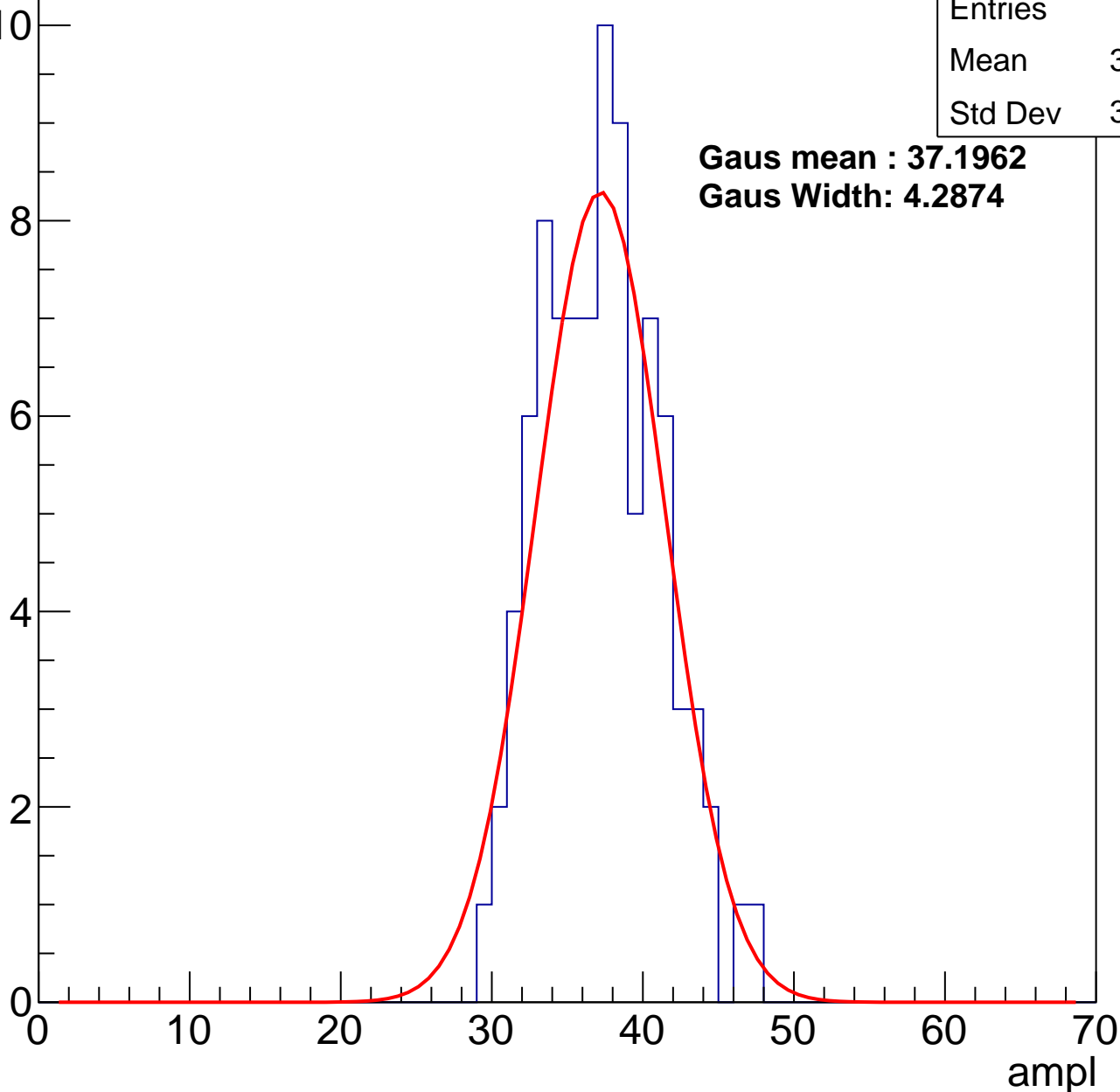
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	89
Mean	36.78
Std Dev	3.862

Gaus mean : 37.1962

Gaus Width: 4.2874

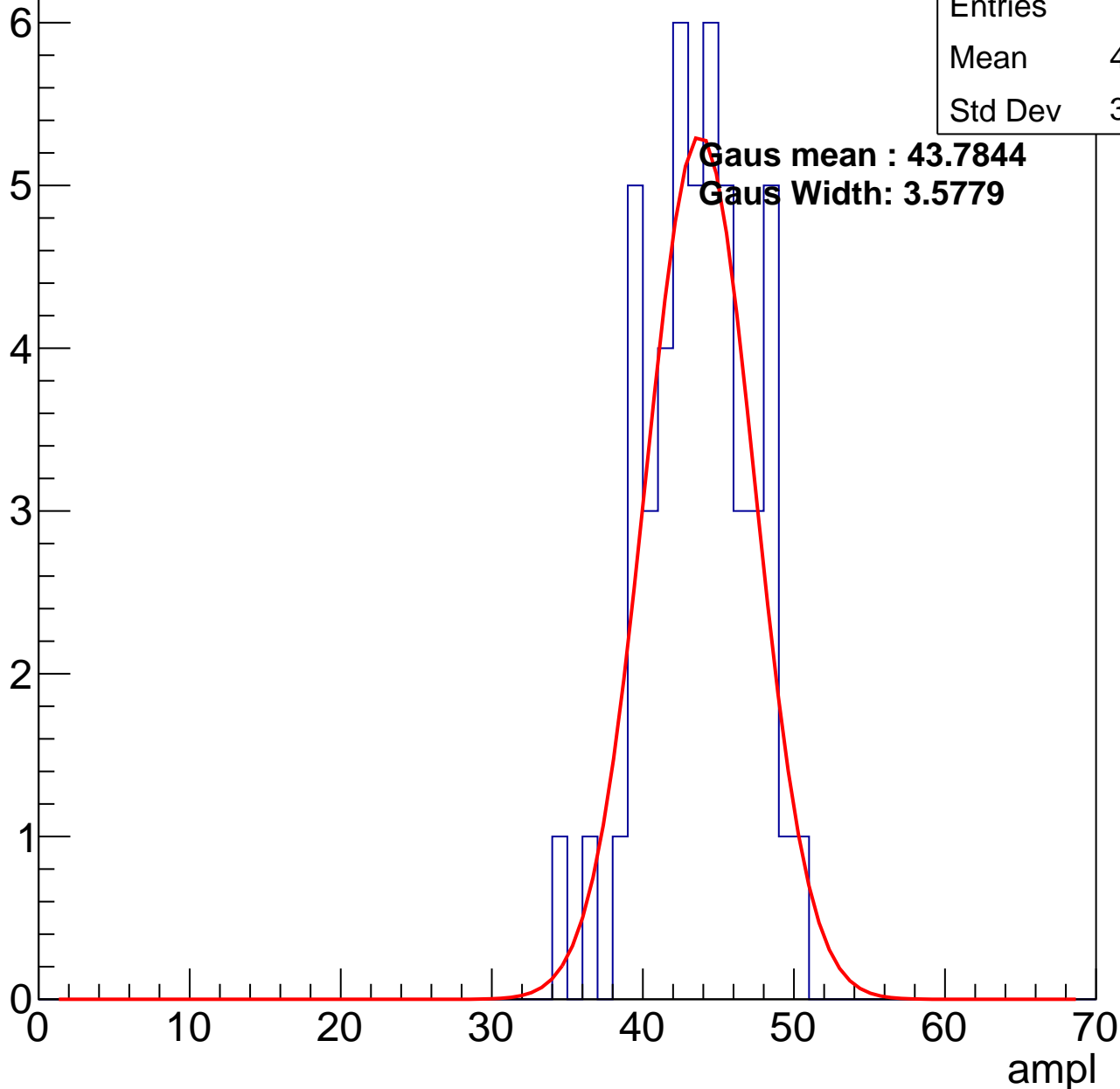


B1L103S, U14-ch83, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	43.22
Std Dev	3.443

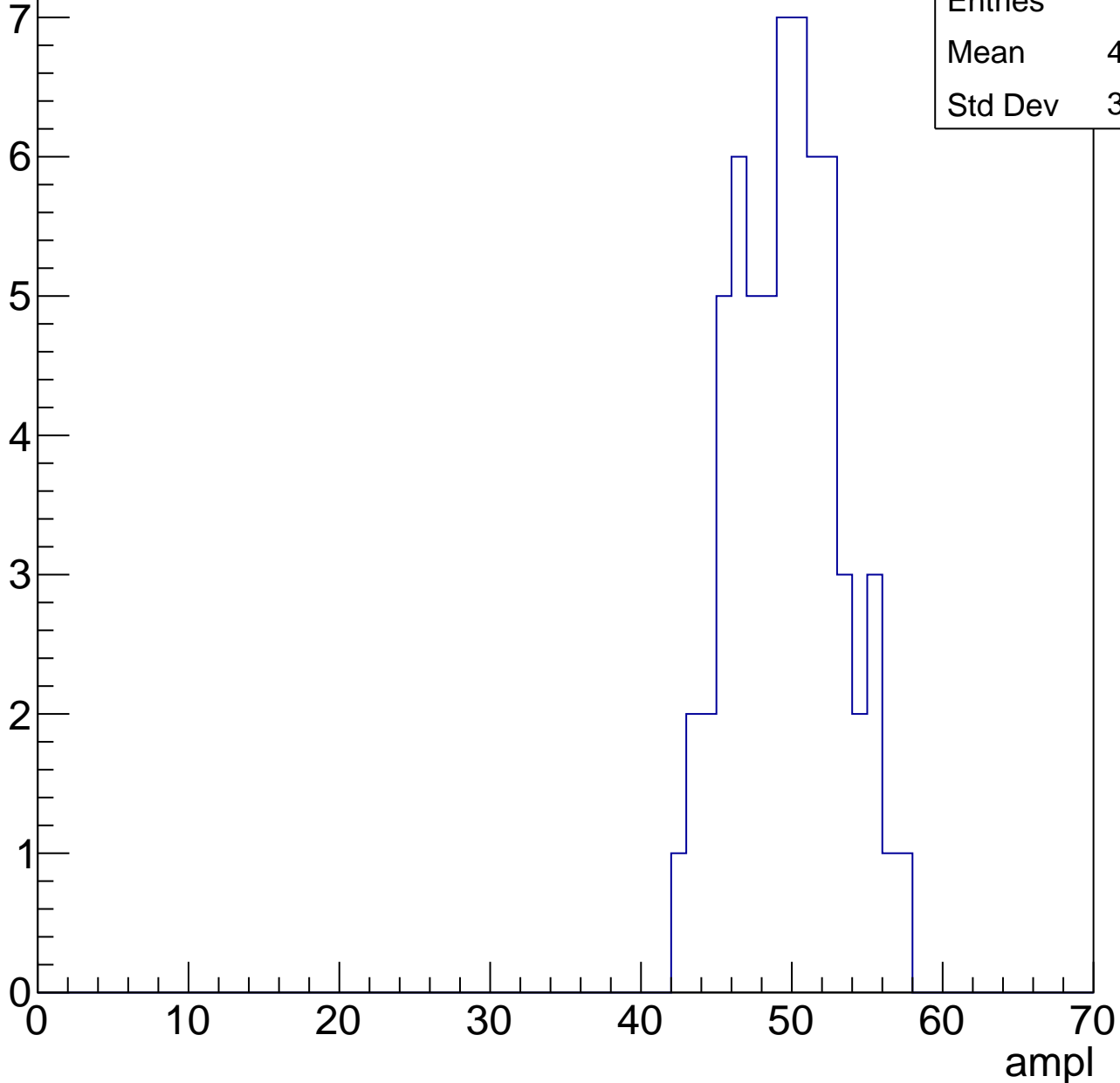


B1L103S, U14-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	49.16
Std Dev	3.437

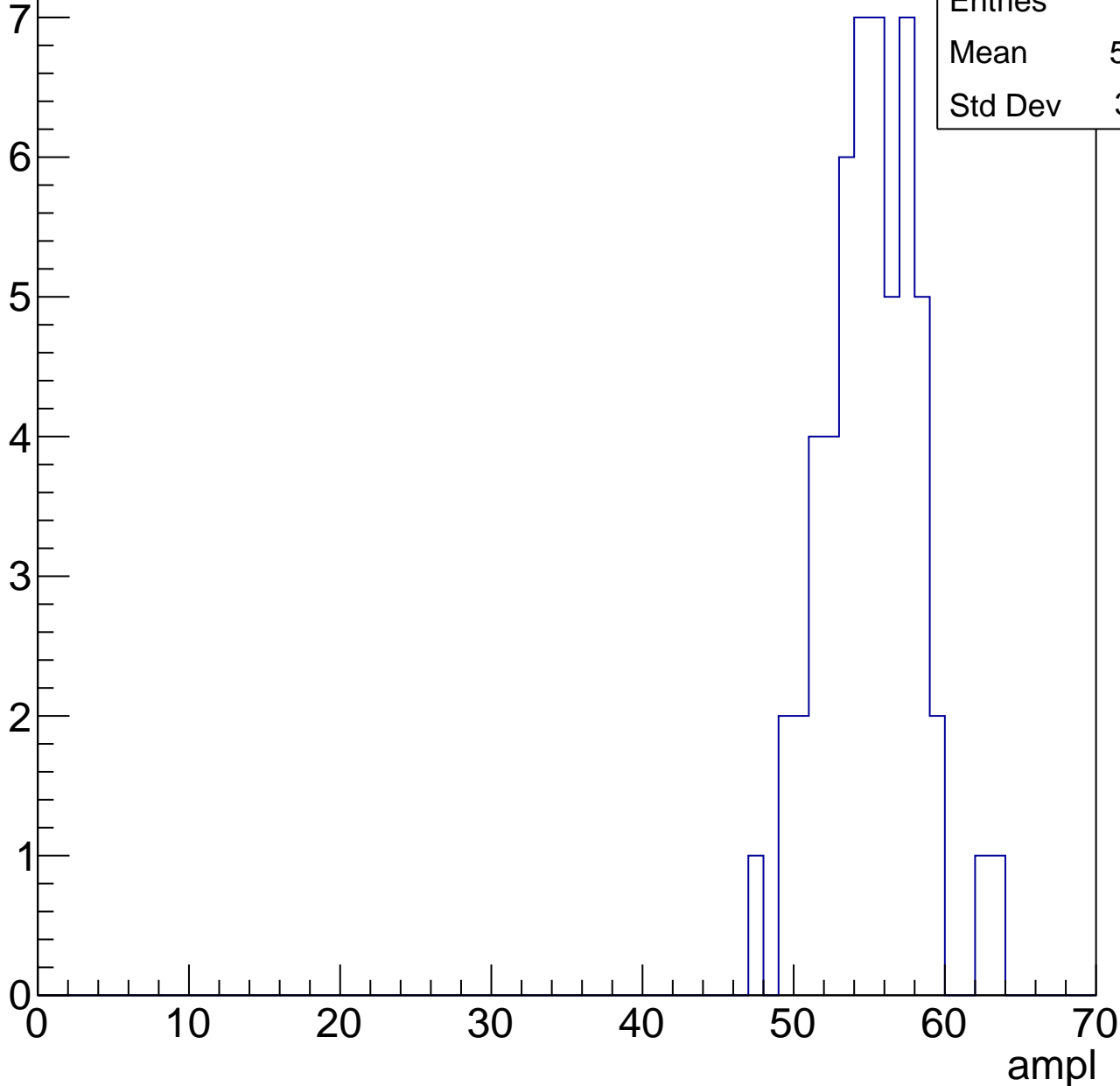


B1L103S, U14-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	54.63
Std Dev	3.141

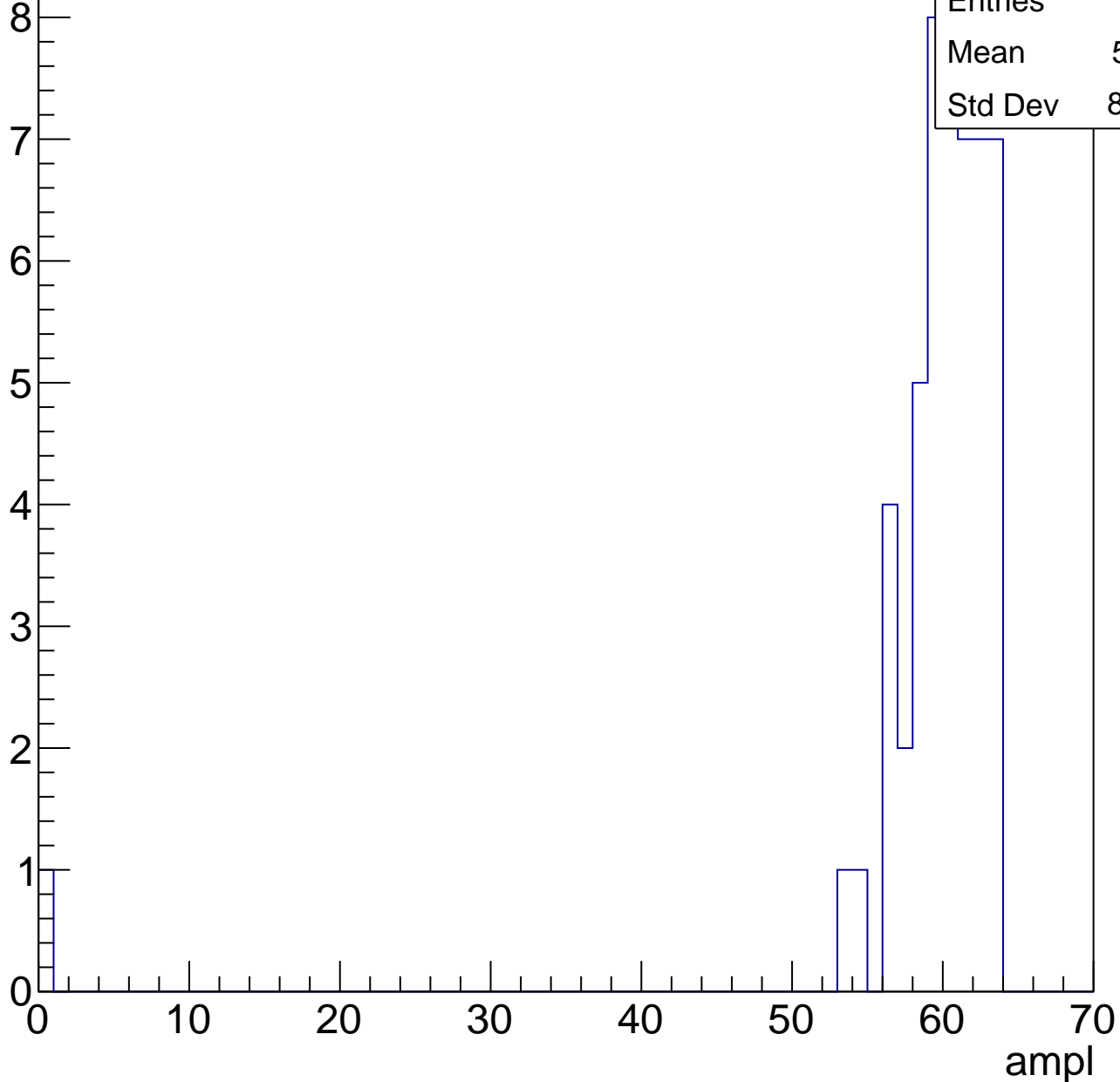


B1L103S, U14-ch83, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.61
Std Dev	8.625



B1L103S, U14-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

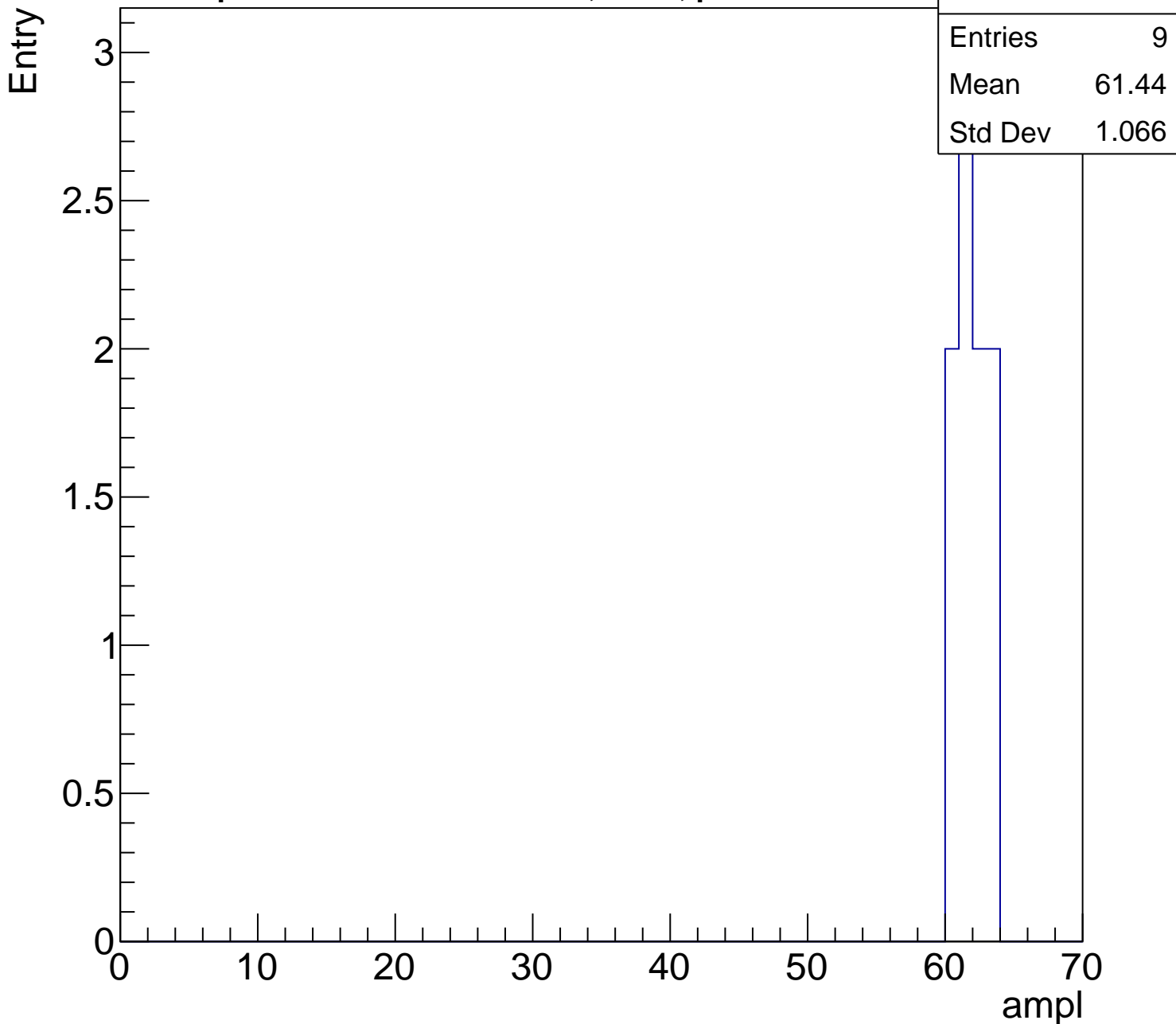
9

Mean

61.44

Std Dev

1.066



B1L103S, U14-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U14-ch84, adc0

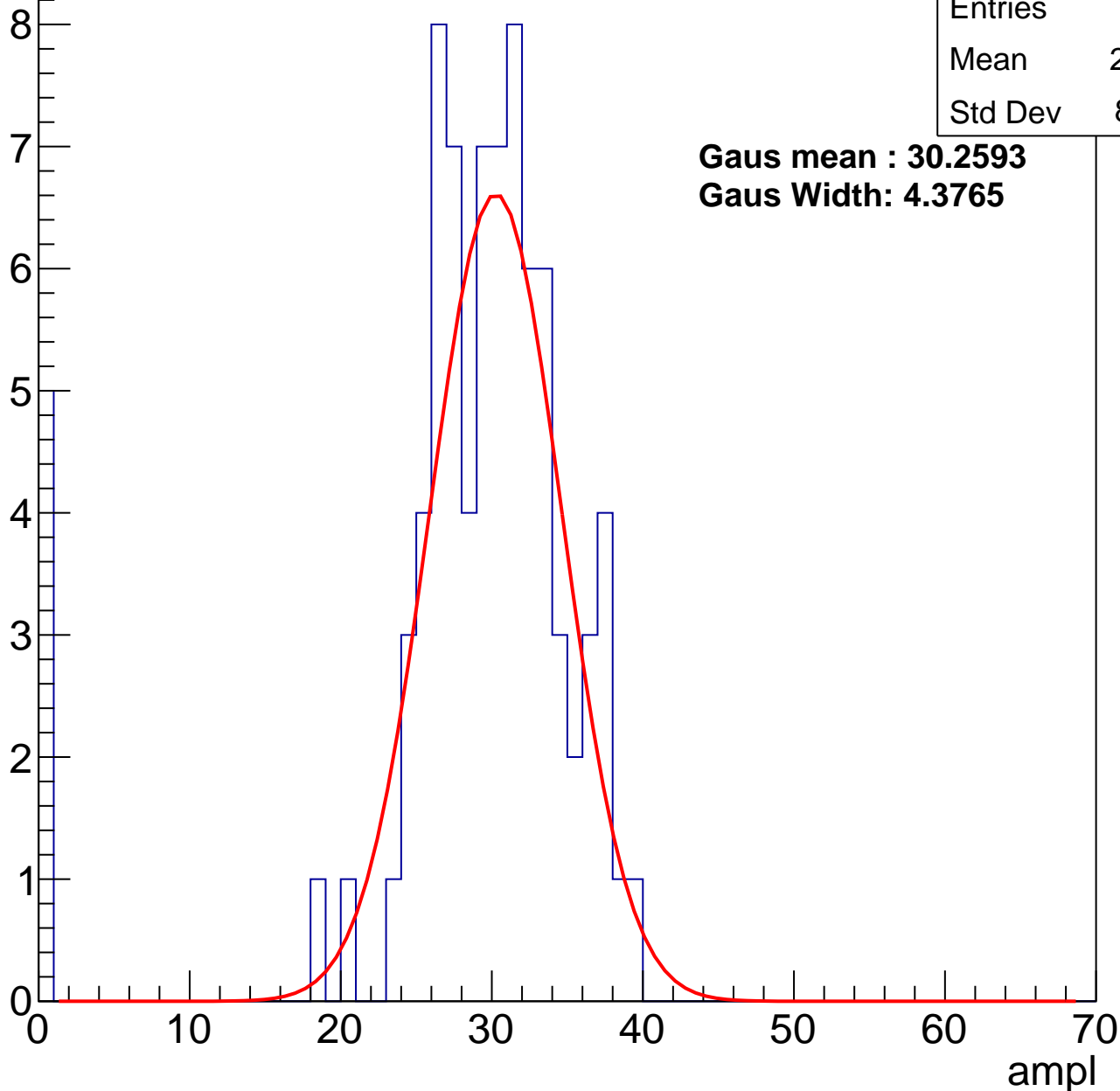
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	28.02
Std Dev	8.211

Gaus mean : 30.2593

Gaus Width: 4.3765



B1L103S, U14-ch84, adc1

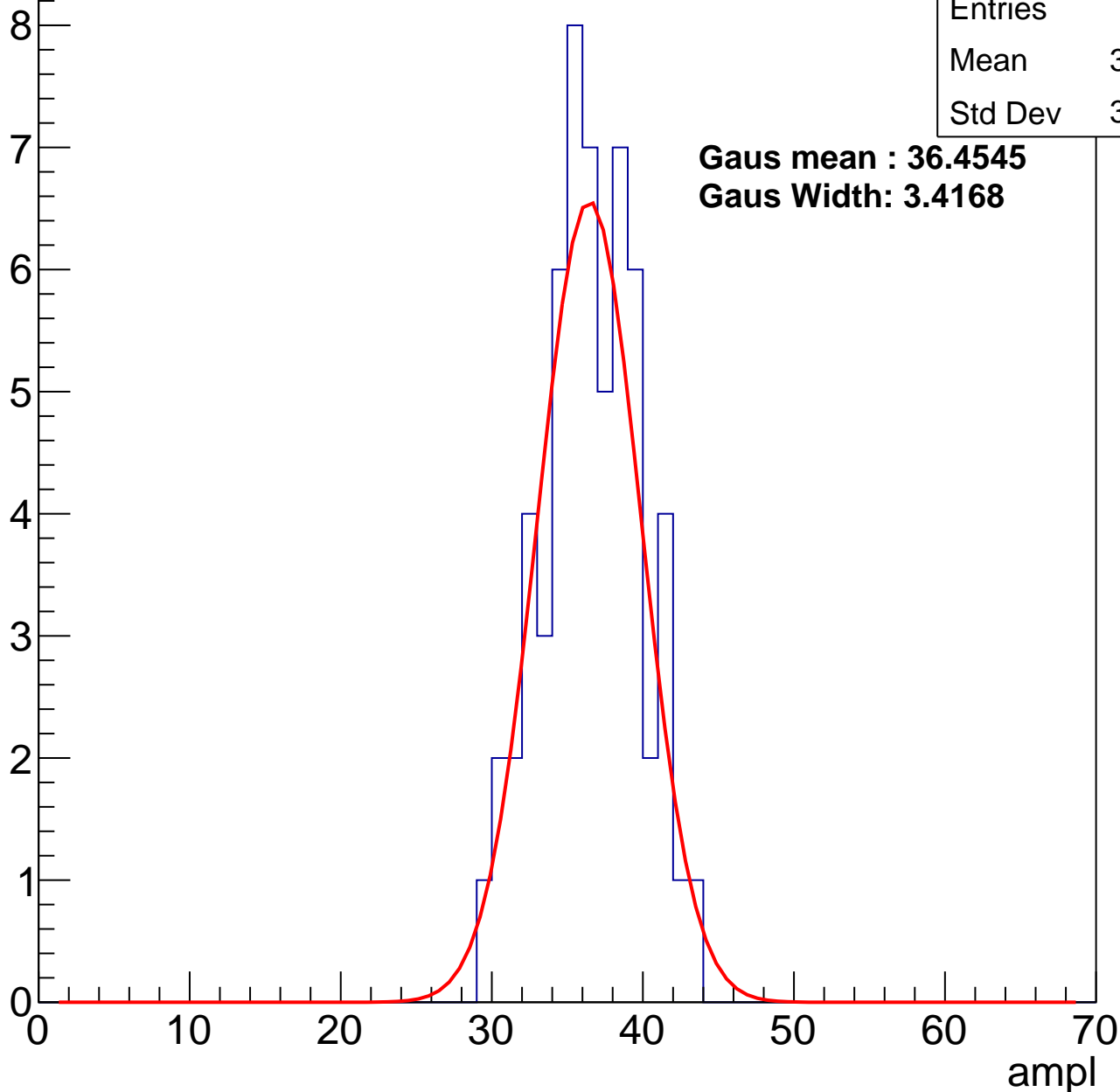
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	36.07
Std Dev	3.183

Gaus mean : 36.4545

Gaus Width: 3.4168

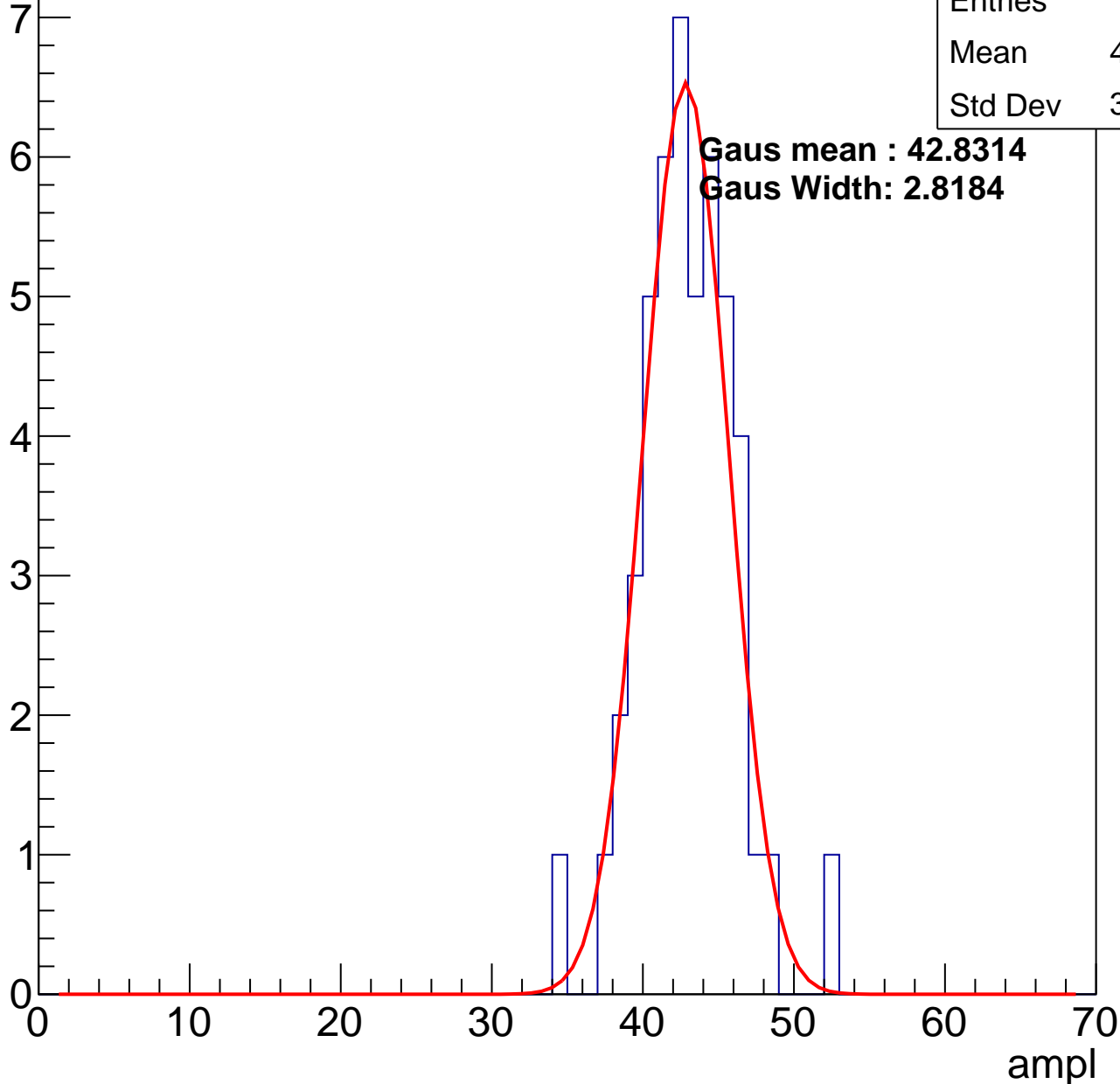


B1L103S, U14-ch84, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

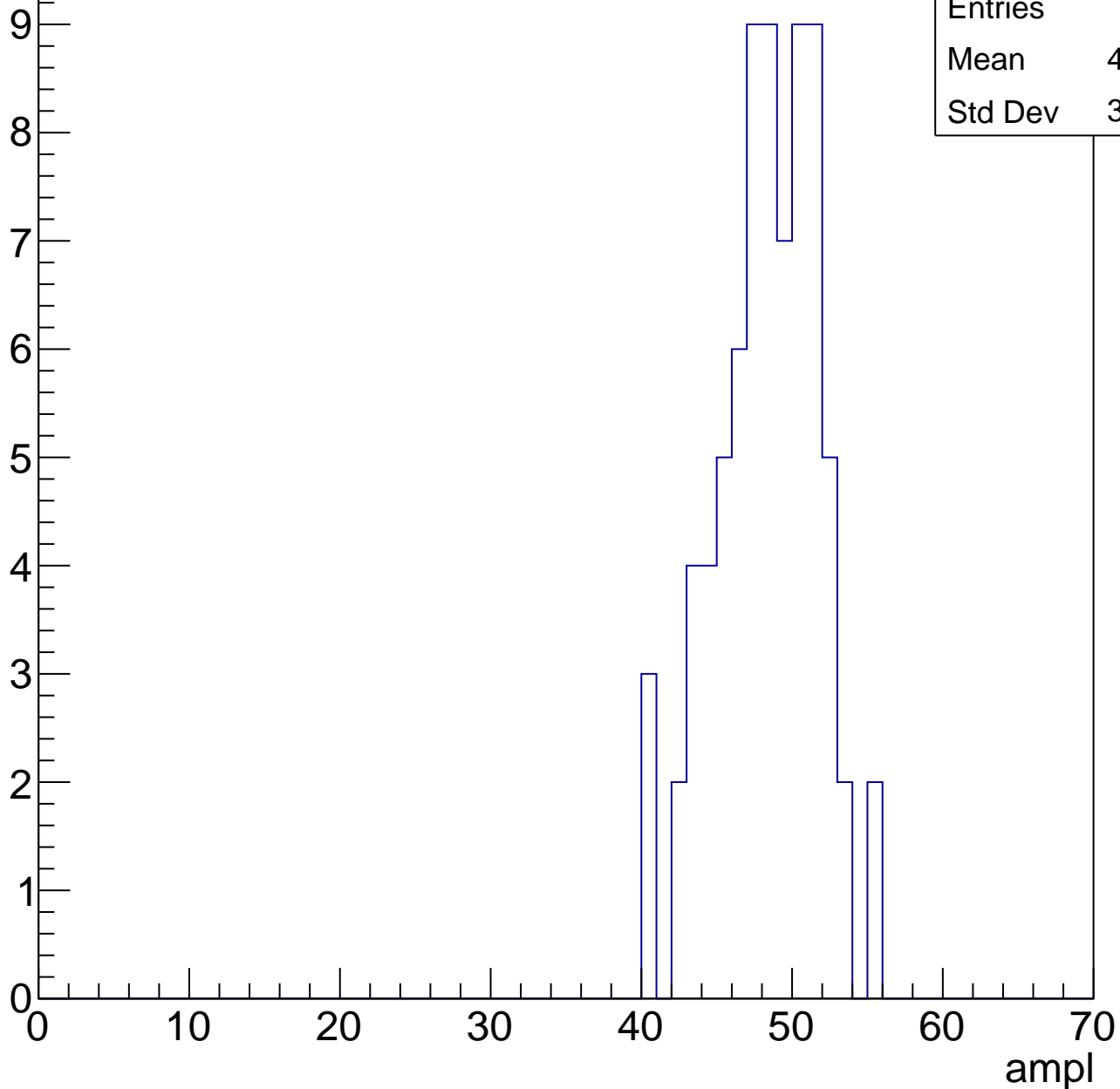
Entries	48
Mean	42.48
Std Dev	3.102



B1L103S, U14-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



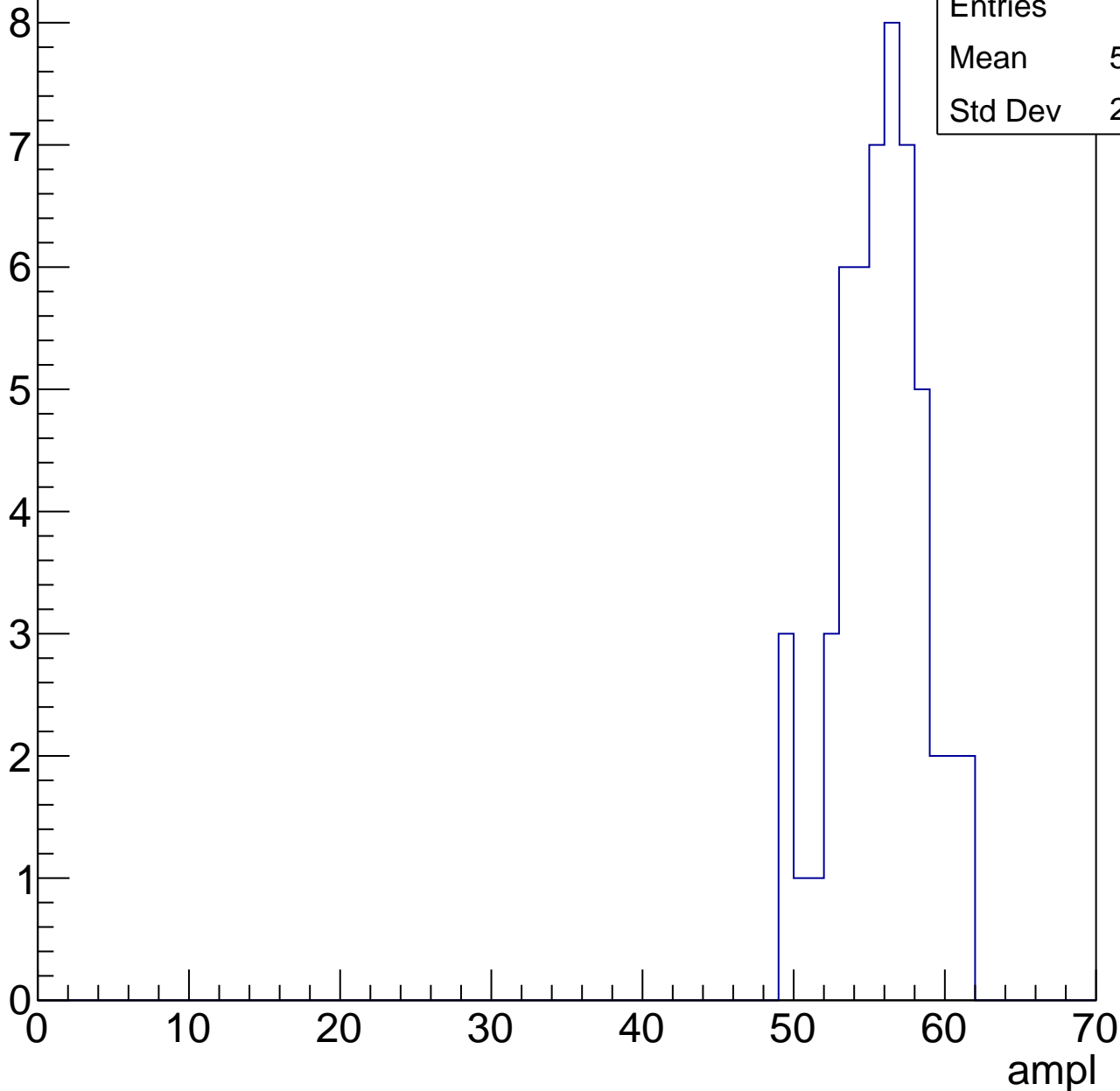
Entries	76
Mean	47.84
Std Dev	3.356

B1L103S, U14-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.25
Std Dev	2.874

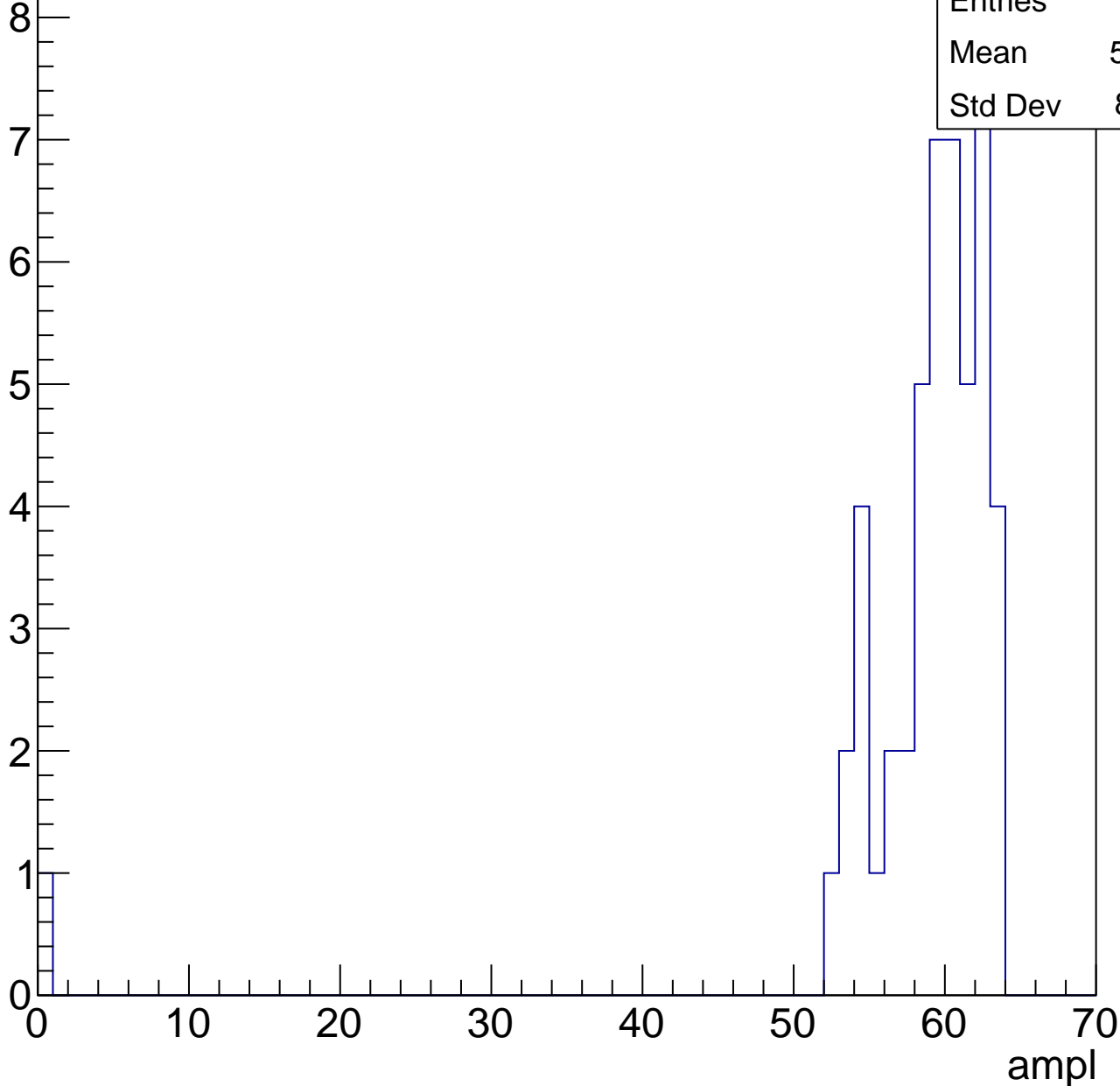


B1L103S, U14-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	57.78
Std Dev	8.851

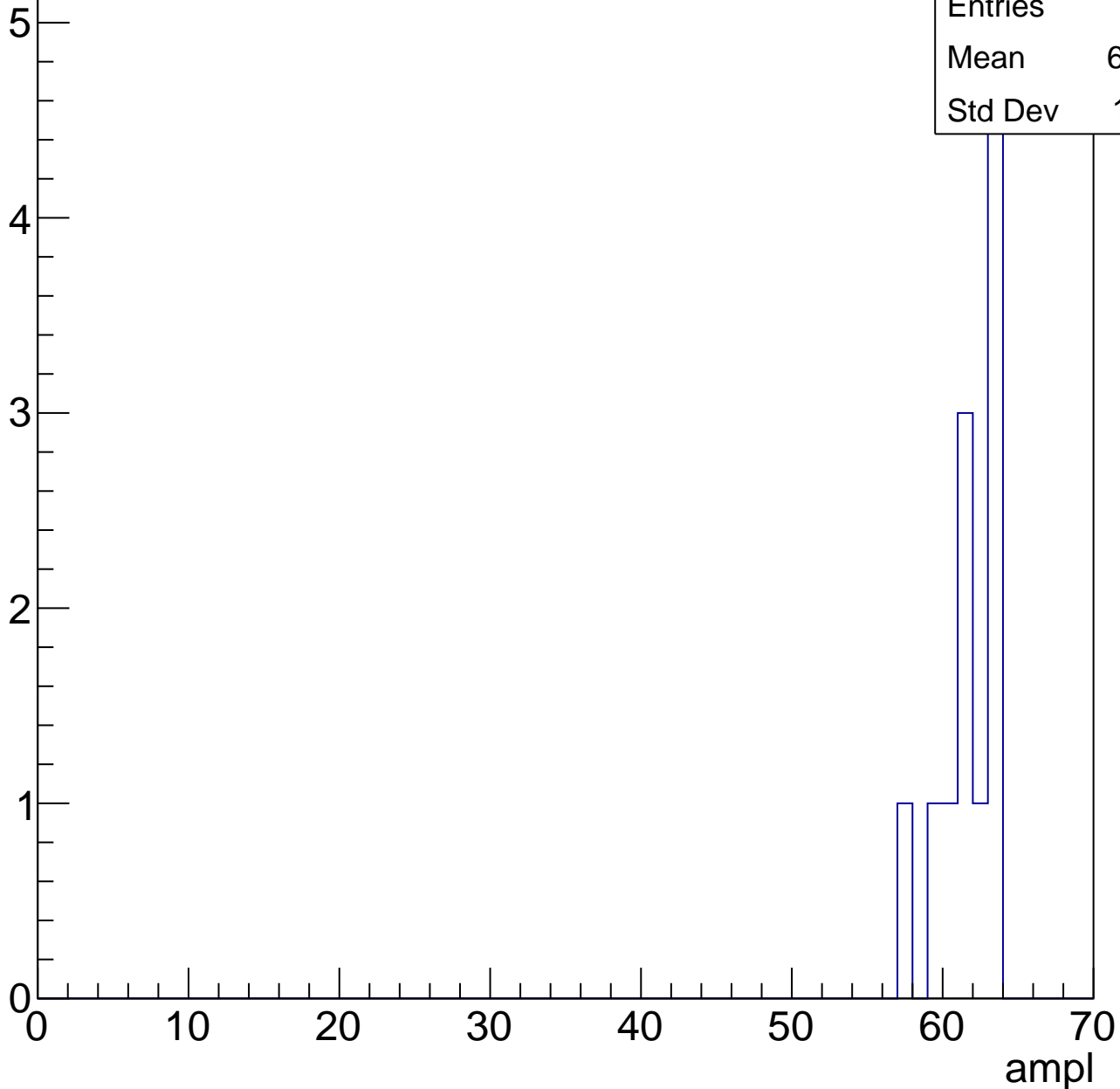


B1L103S, U14-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.33
Std Dev	1.841



B1L103S, U14-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch85, adc0

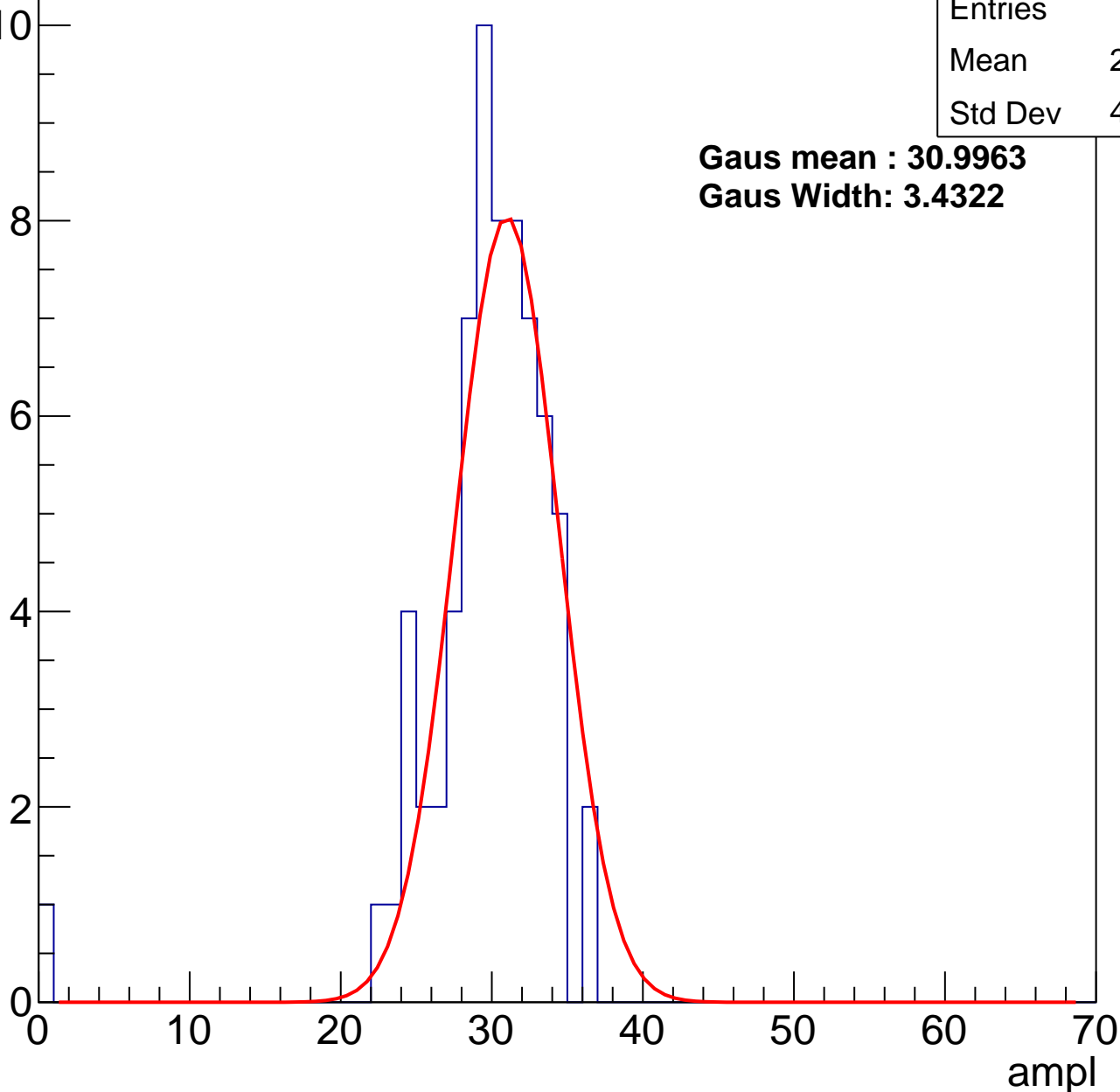
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	29.25
Std Dev	4.723

Gaus mean : 30.9963

Gaus Width: 3.4322



B1L103S, U14-ch85, adc1

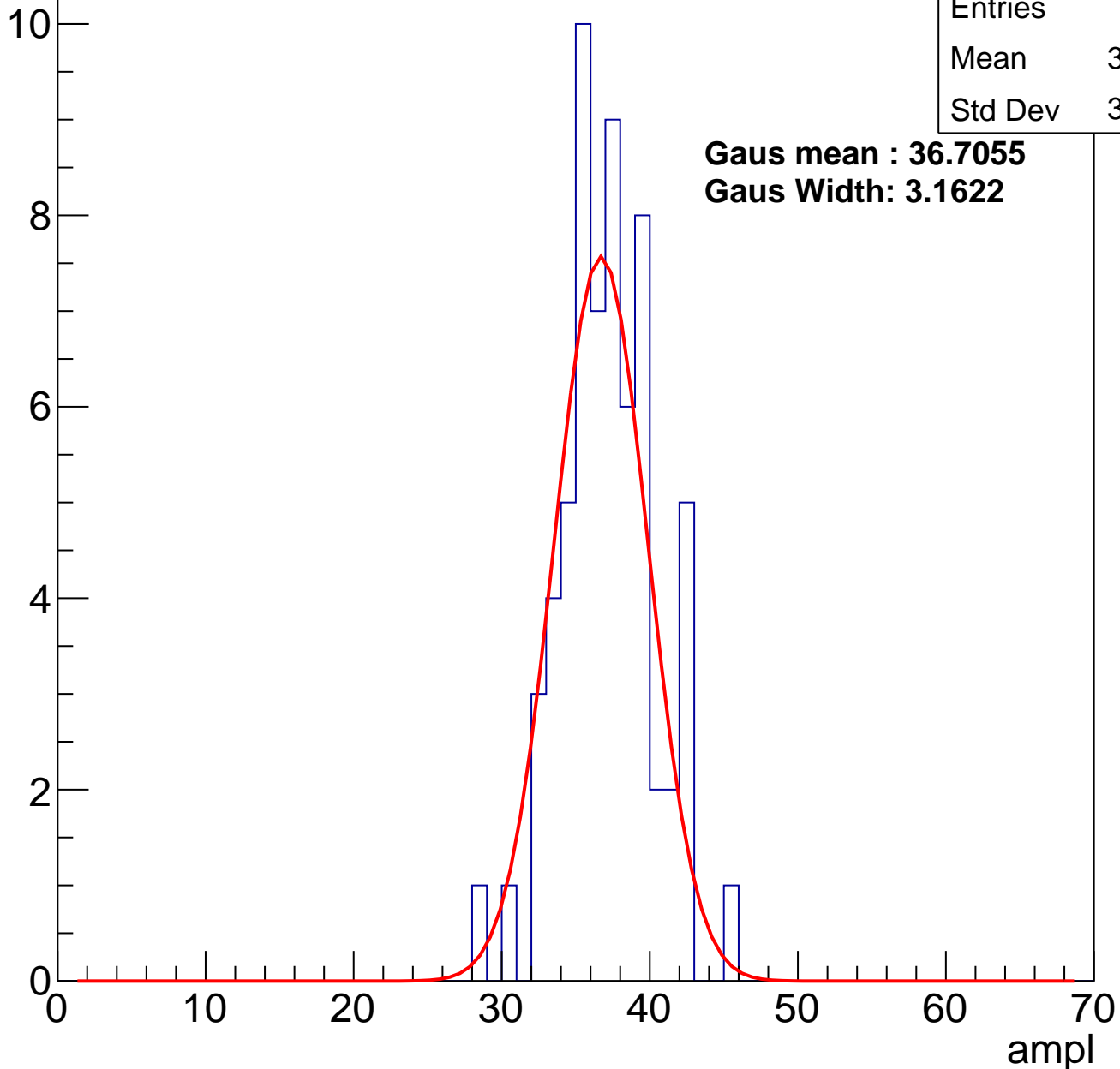
calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	36.69
Std Dev	3.142

Gaus mean : 36.7055

Gaus Width: 3.1622

Entry



B1L103S, U14-ch85, adc2

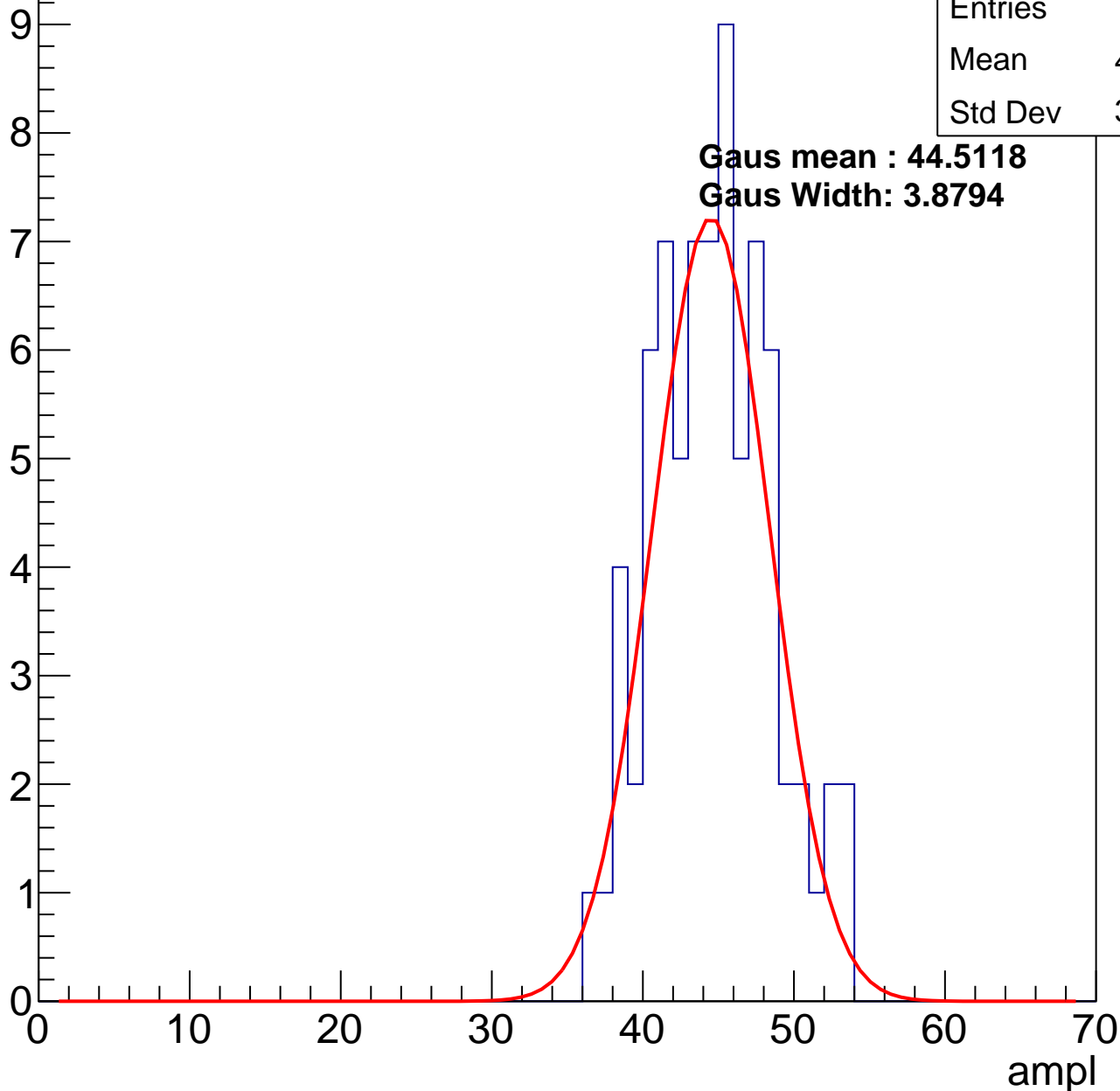
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	44.21
Std Dev	3.871

Gaus mean : 44.5118

Gaus Width: 3.8794

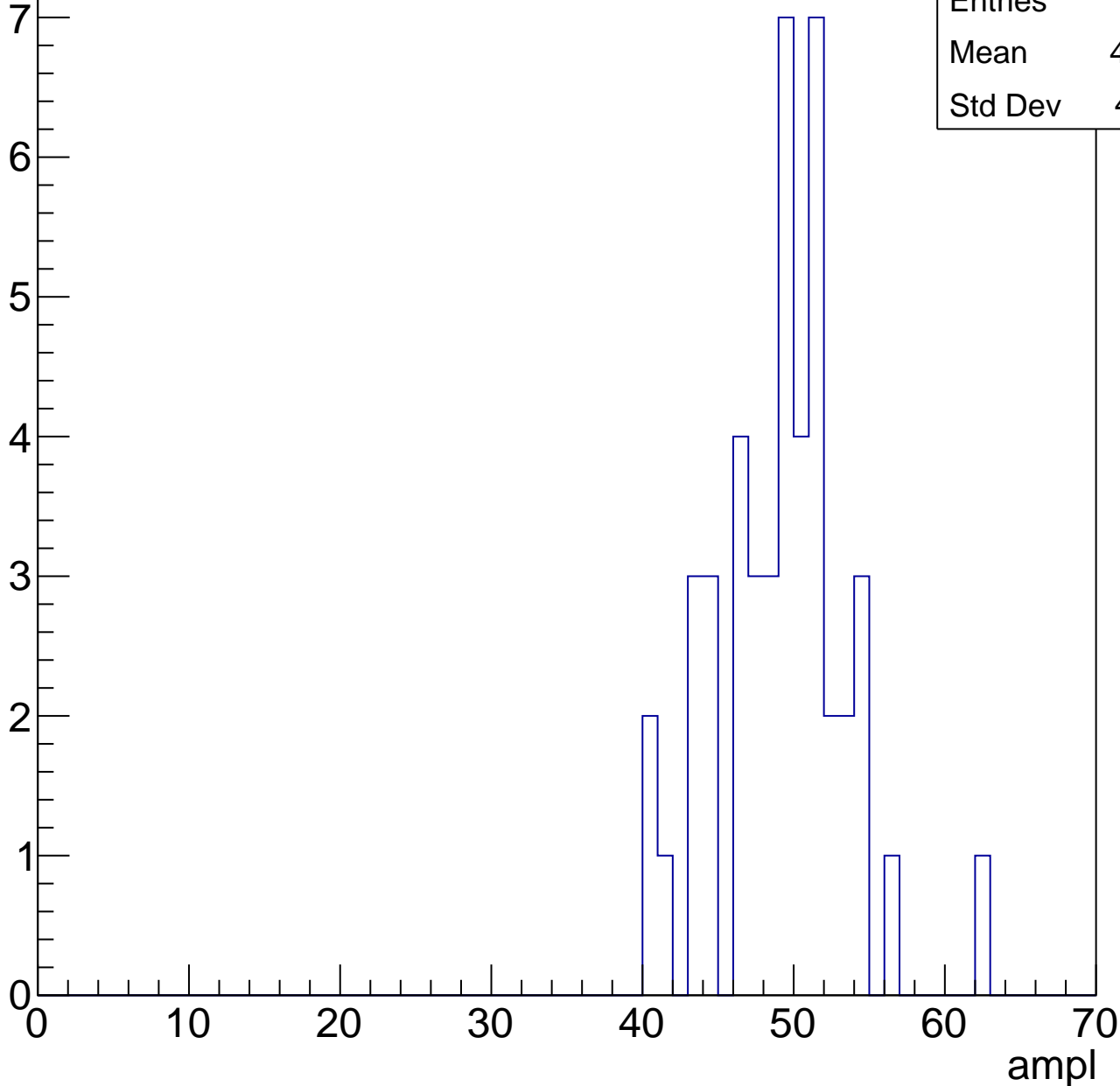


B1L103S, U14-ch85, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

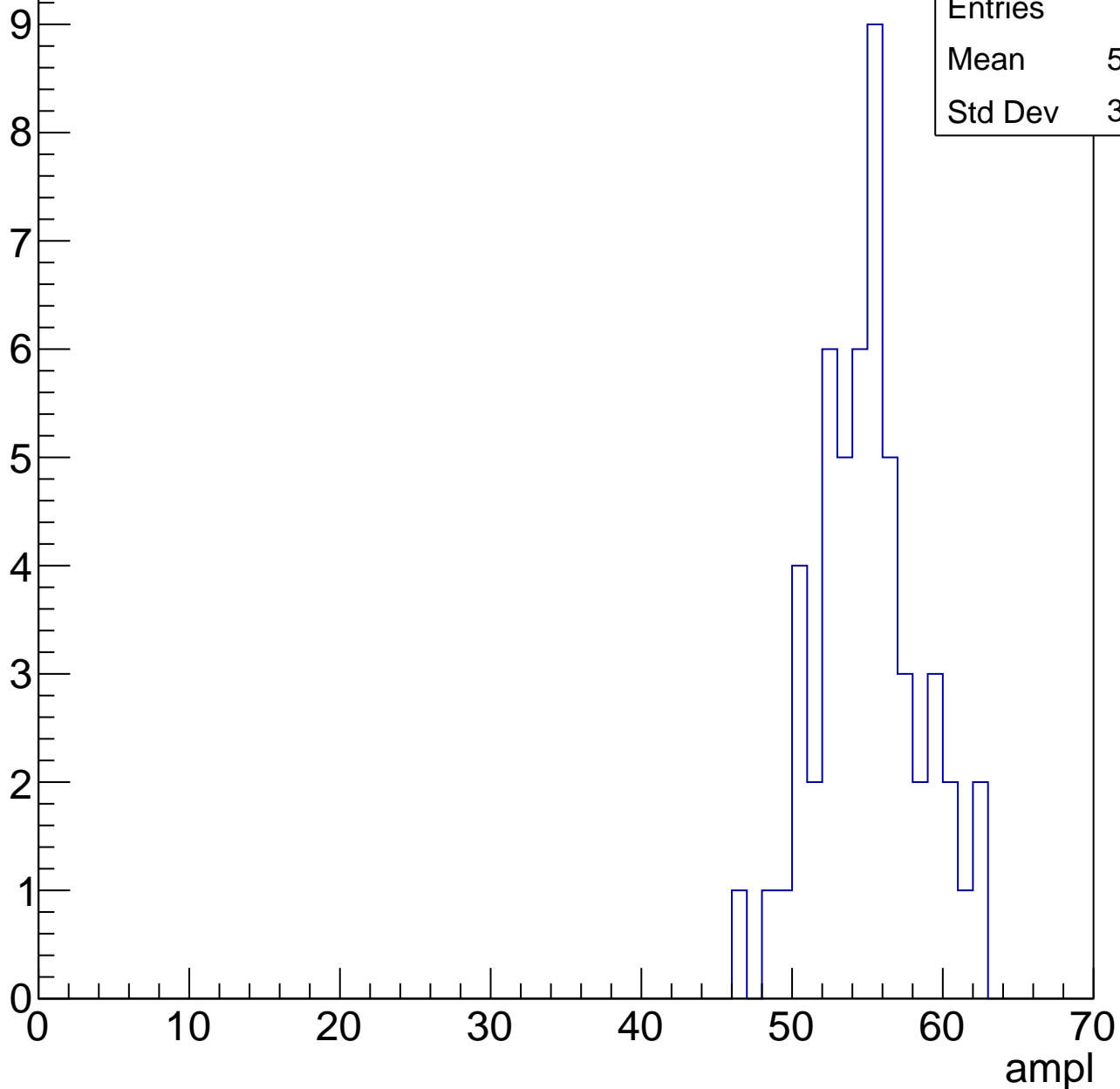
Entries	46
Mean	48.72
Std Dev	4.251



B1L103S, U14-ch85, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch85, adc5

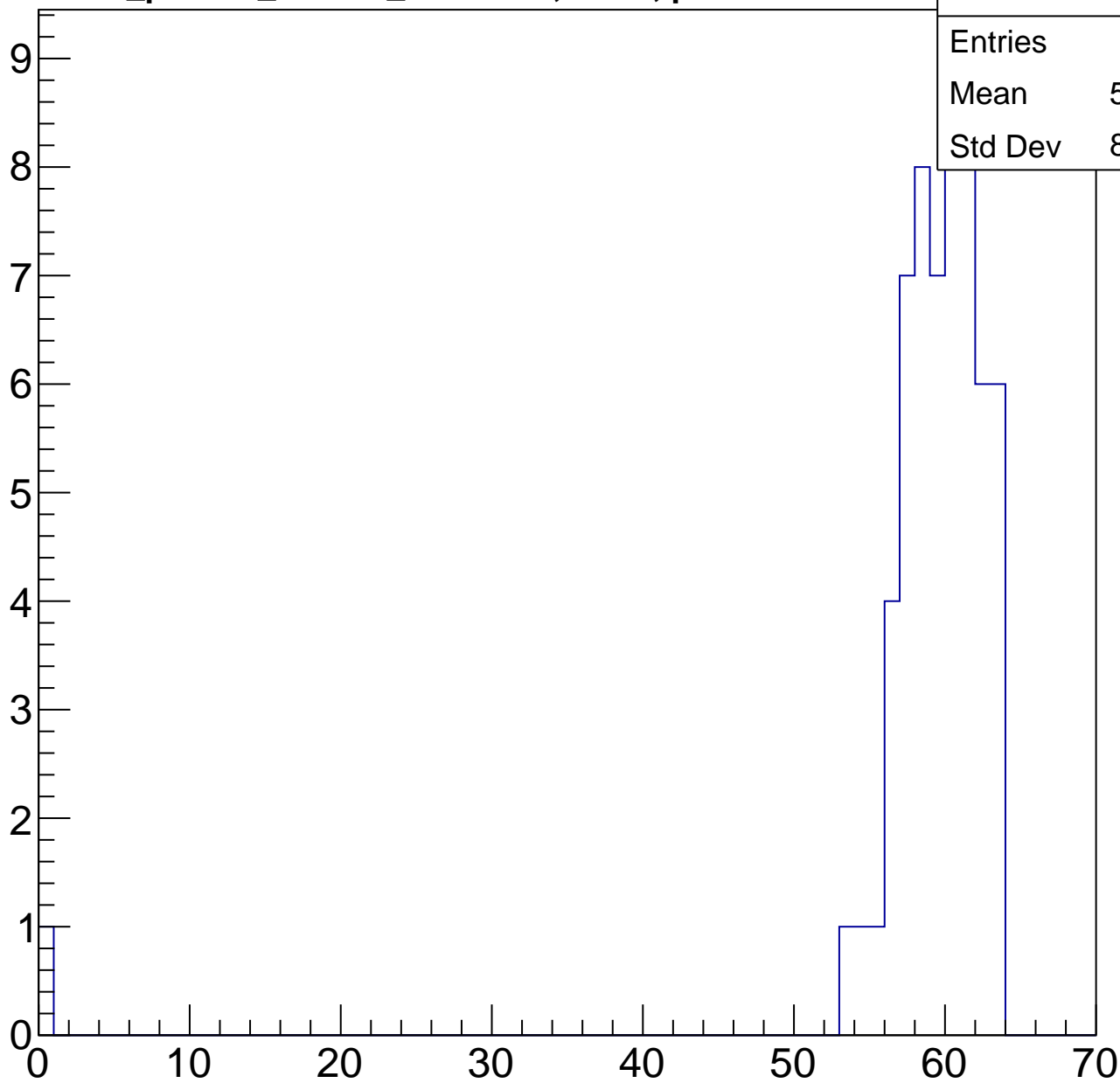
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	58.32
Std Dev	8.022

ampl



B1L103S, U14-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	62.8
Std Dev	0.4

B1L103S, U14-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U14-ch86, adc0

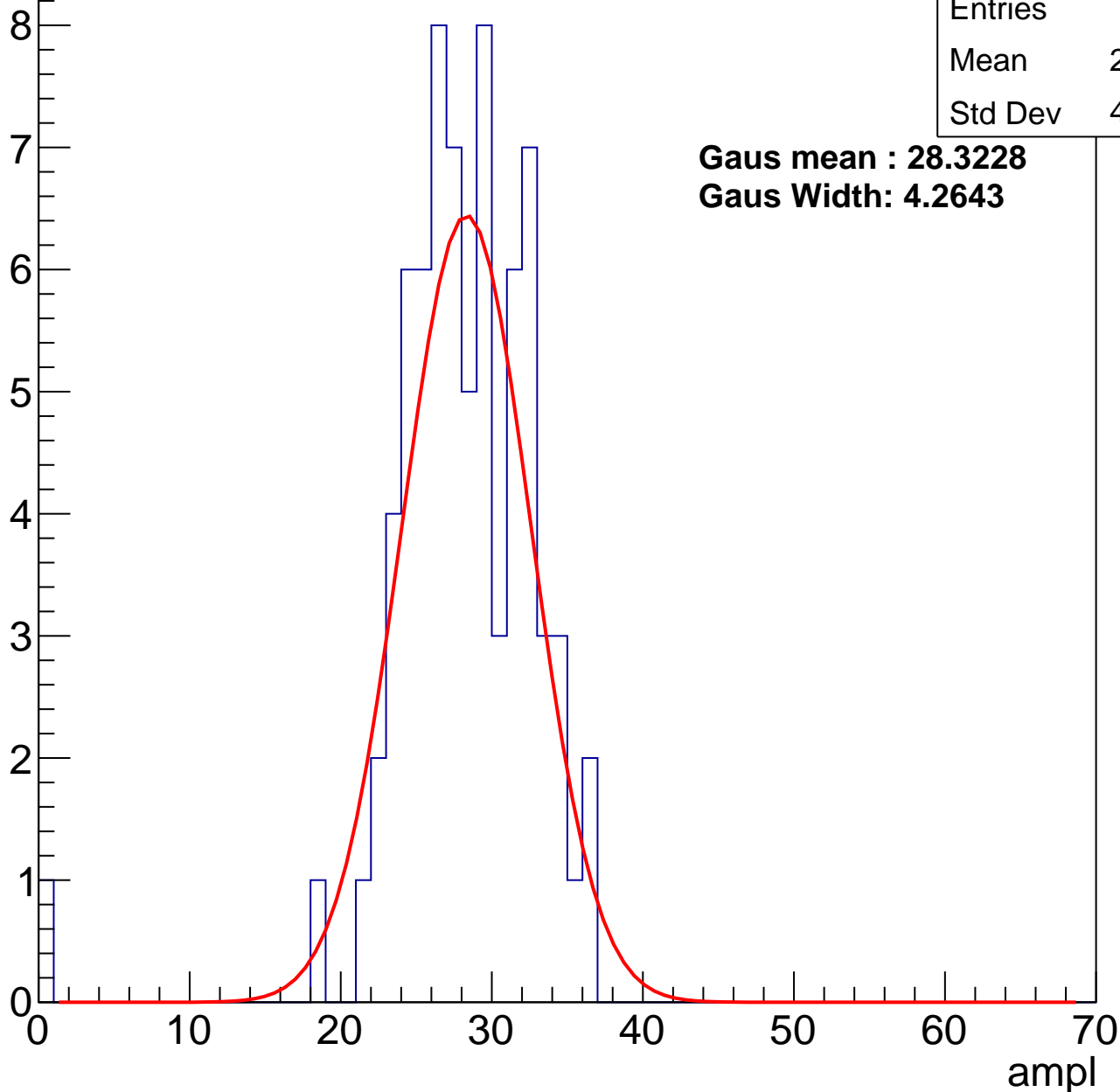
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	27.65
Std Dev	4.974

Gaus mean : 28.3228

Gaus Width: 4.2643



B1L103S, U14-ch86, adc1

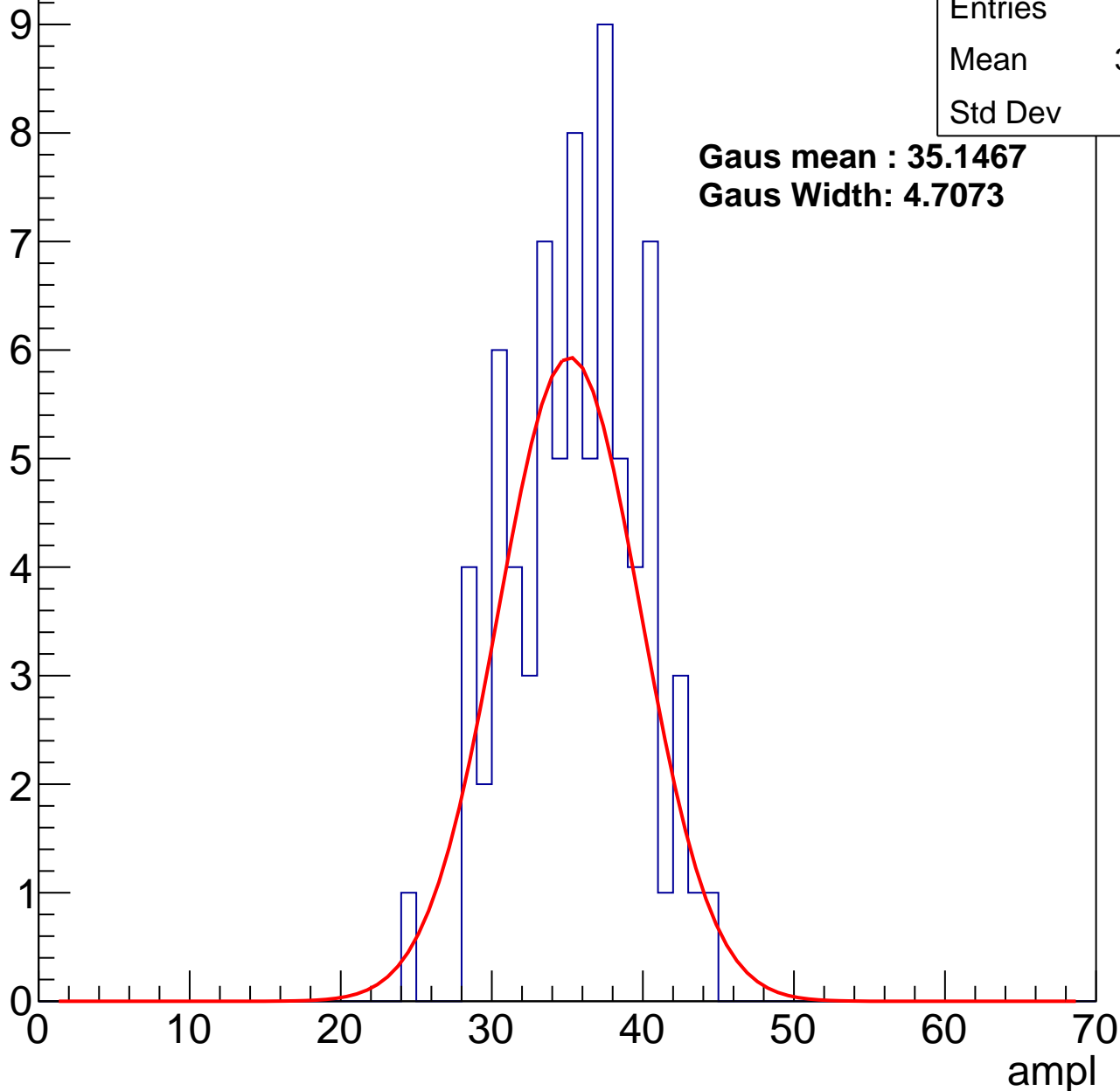
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.11
Std Dev	4.16

Gaus mean : 35.1467

Gaus Width: 4.7073



B1L103S, U14-ch86, adc2

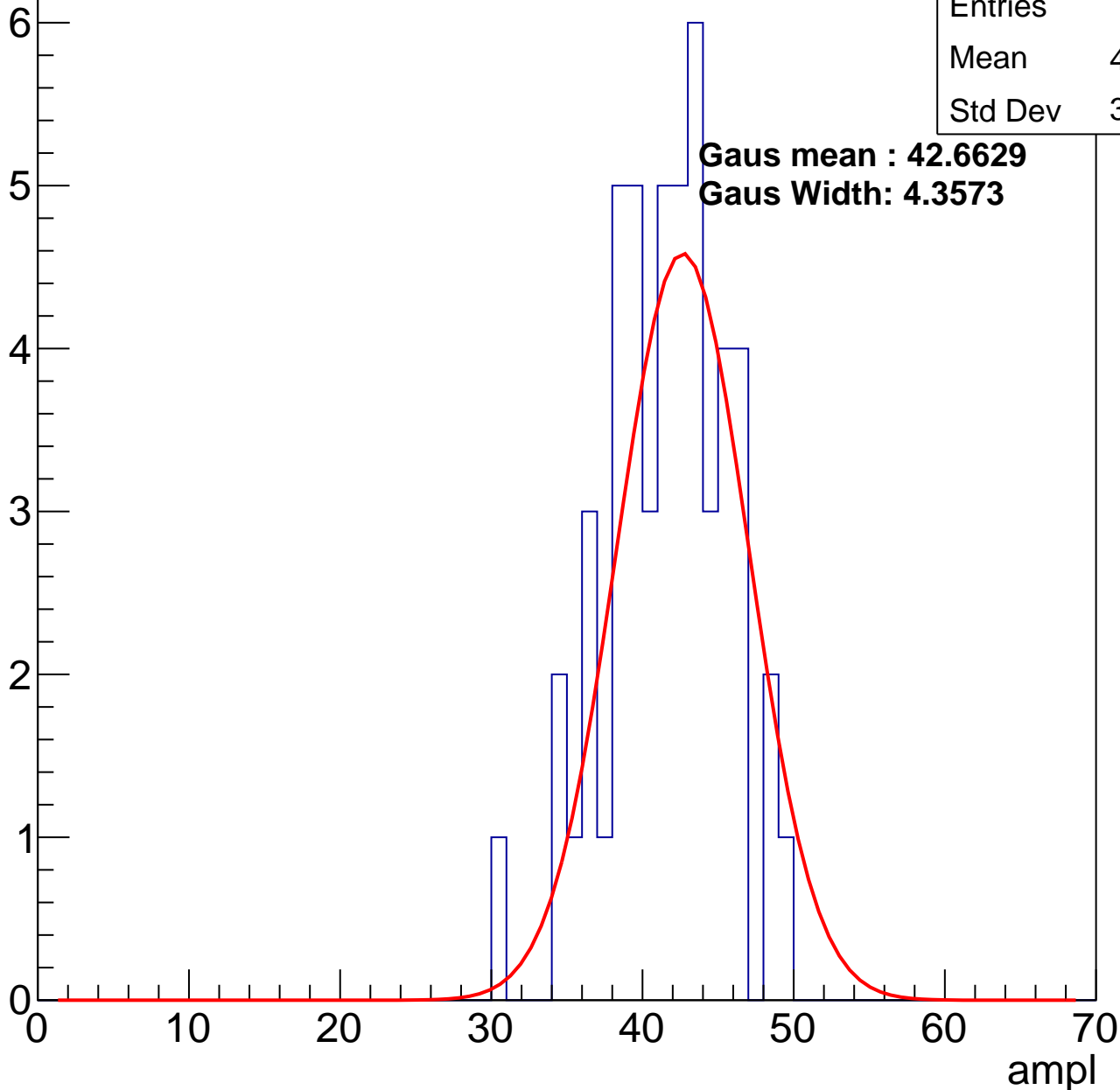
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	41.12
Std Dev	3.949

Gaus mean : 42.6629

Gaus Width: 4.3573

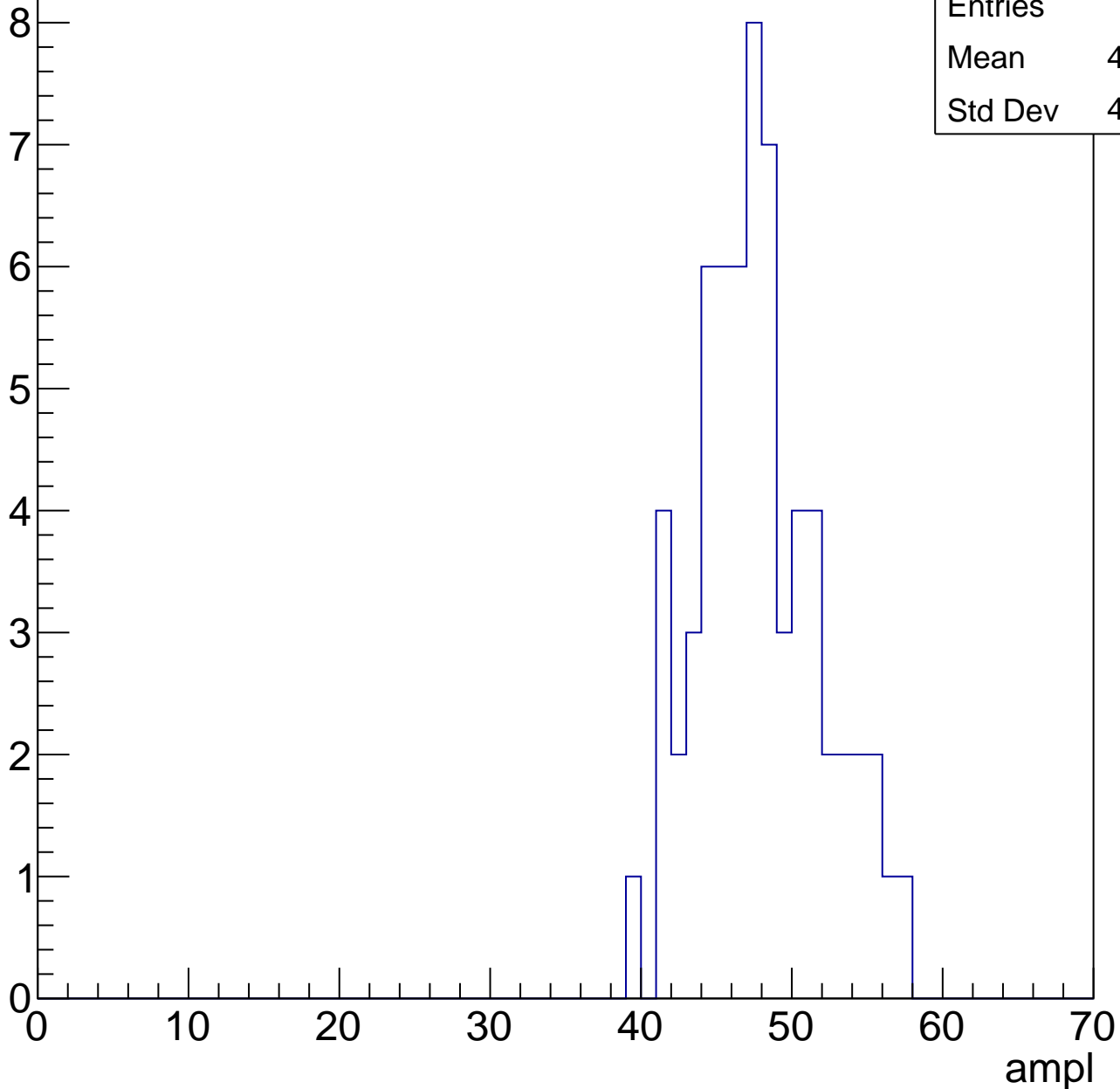


B1L103S, U14-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

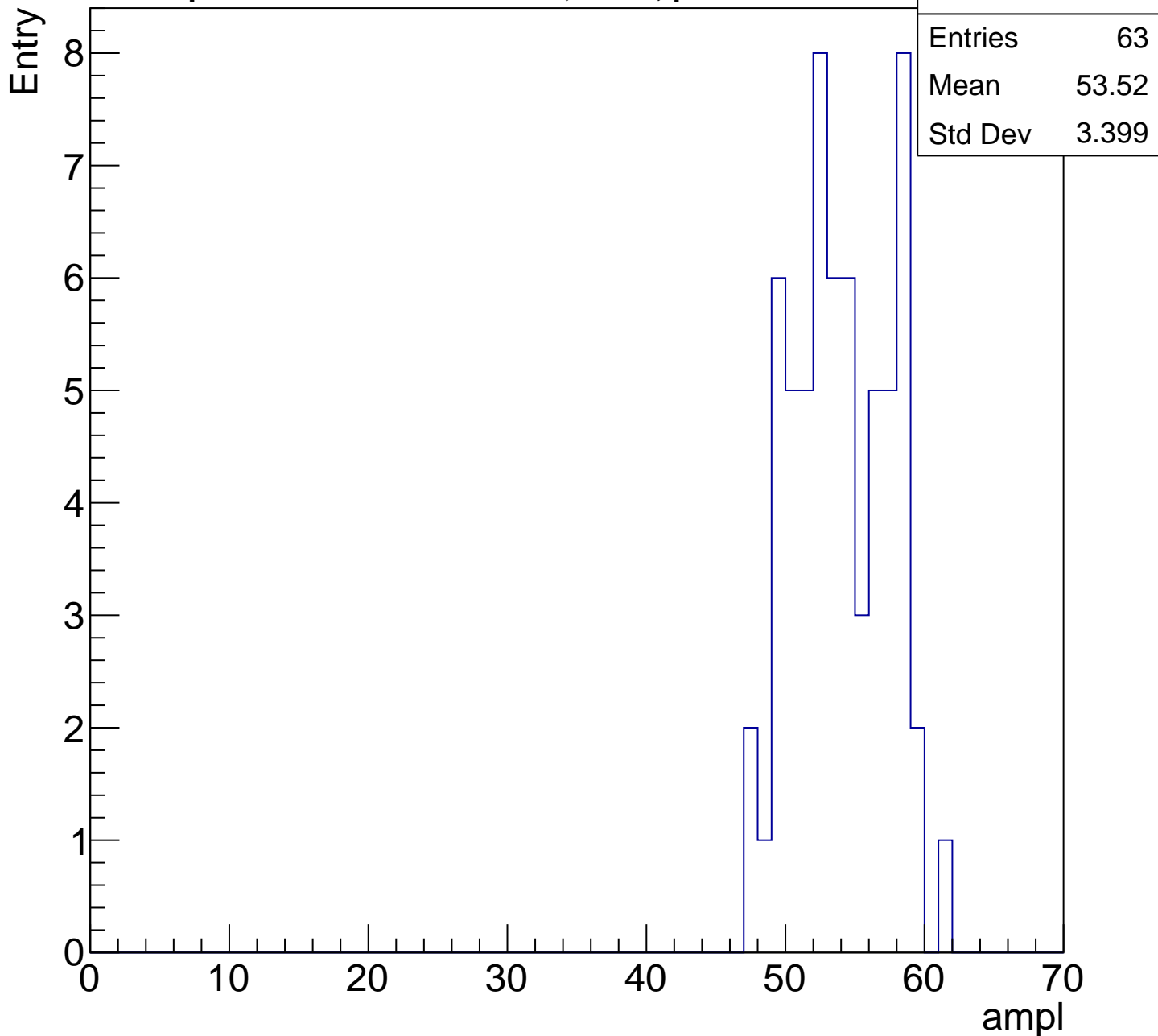
Entry

Entries	64
Mean	47.34
Std Dev	4.013



B1L103S, U14-ch86, adc4

calib_packv5_042523_0143.root, FC#7, port C2

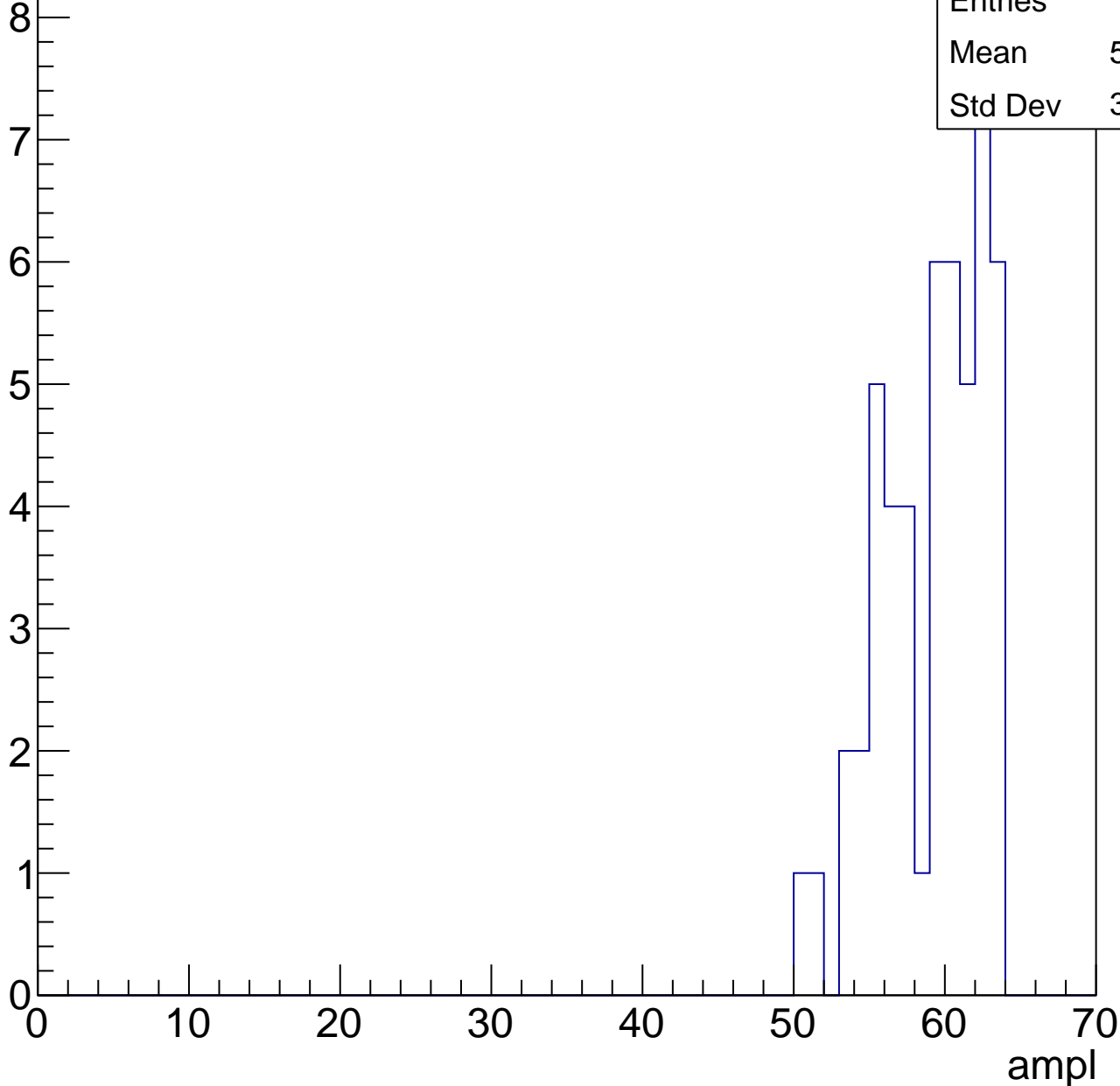


B1L103S, U14-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

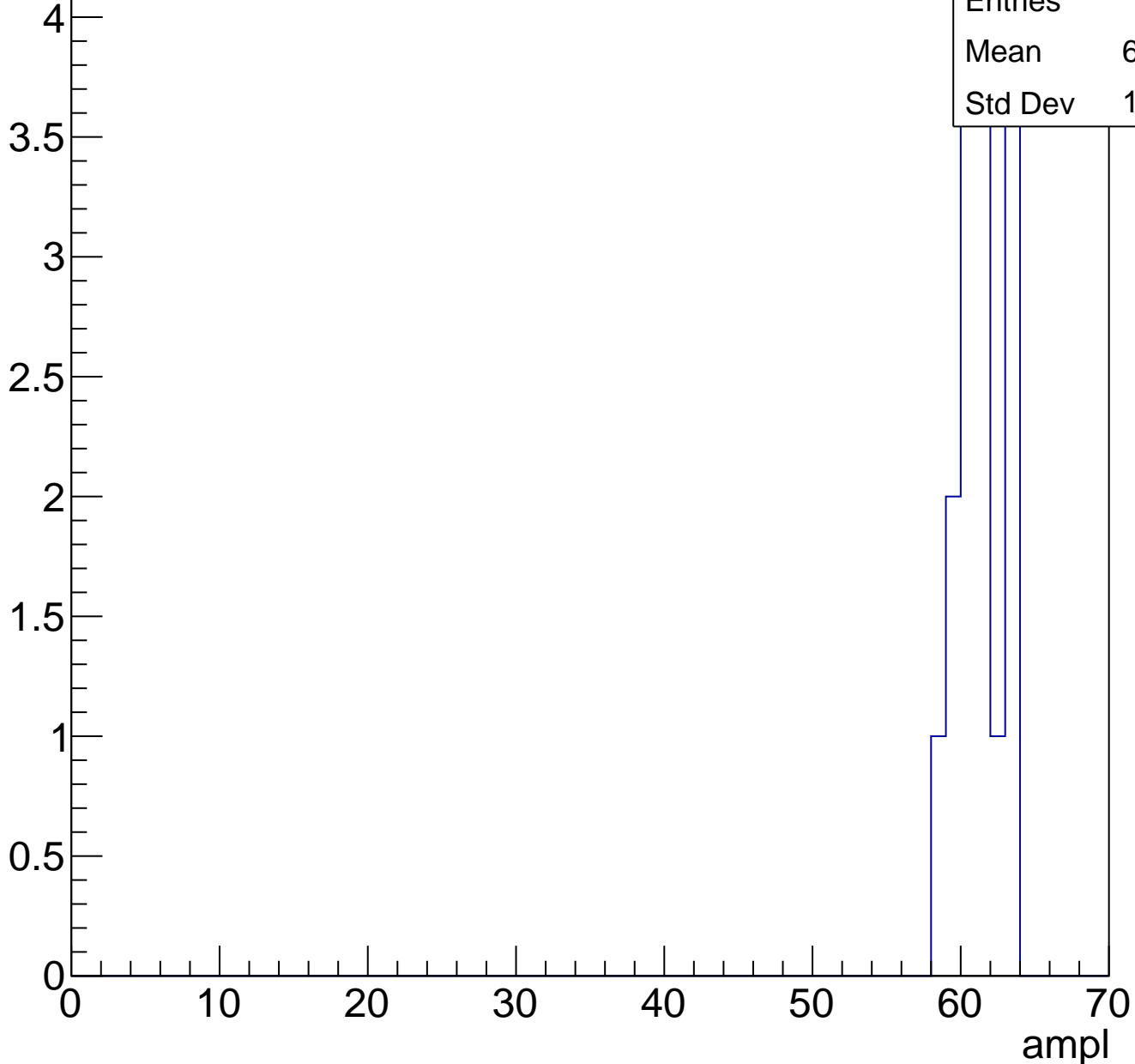
Entries	51
Mean	58.69
Std Dev	3.398



B1L103S, U14-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	16
Mean	60.88
Std Dev	1.536

B1L103S, U14-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch87, adc0

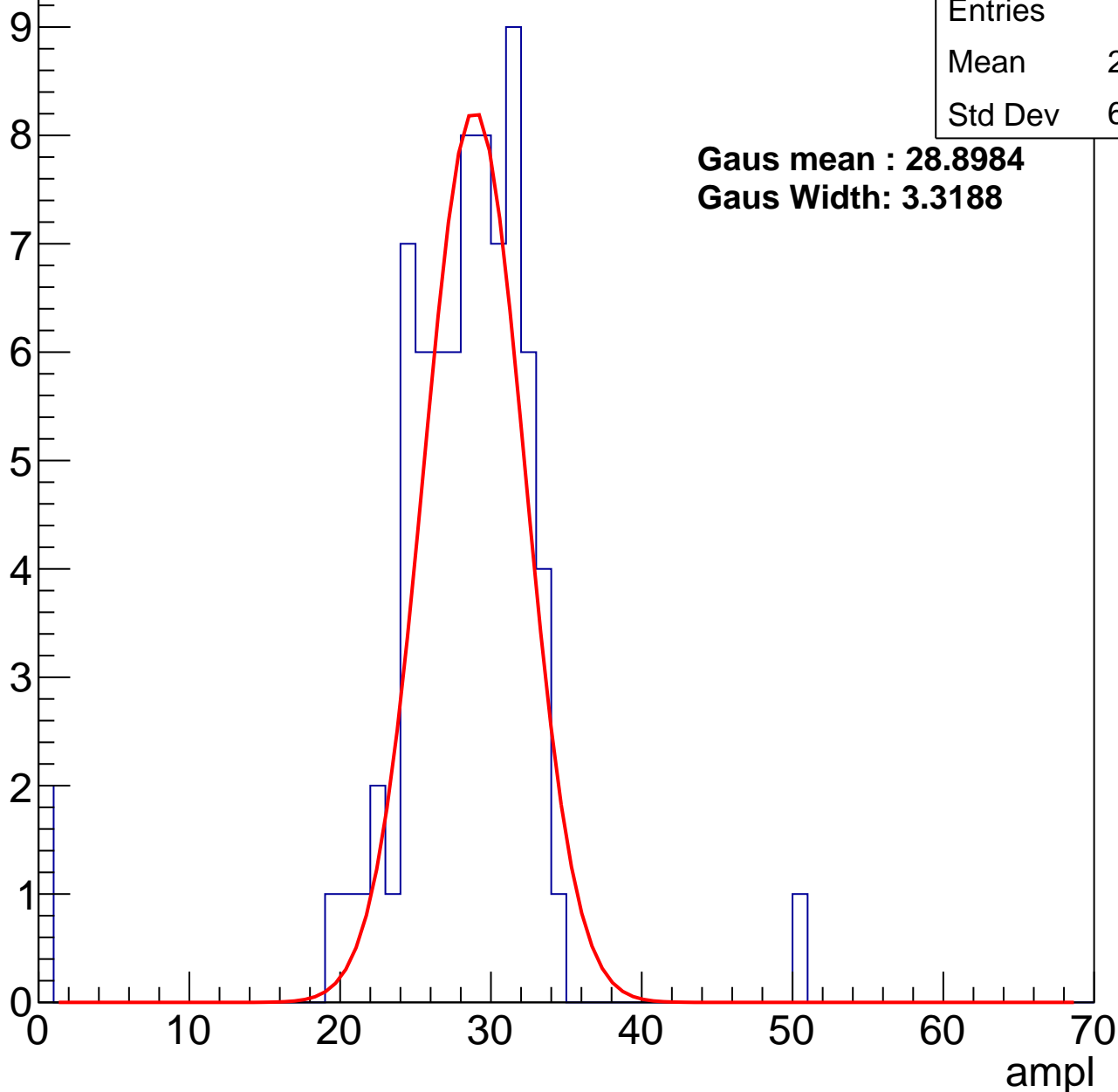
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	27.48
Std Dev	6.102

Gaus mean : 28.8984

Gaus Width: 3.3188



B1L103S, U14-ch87, adc1

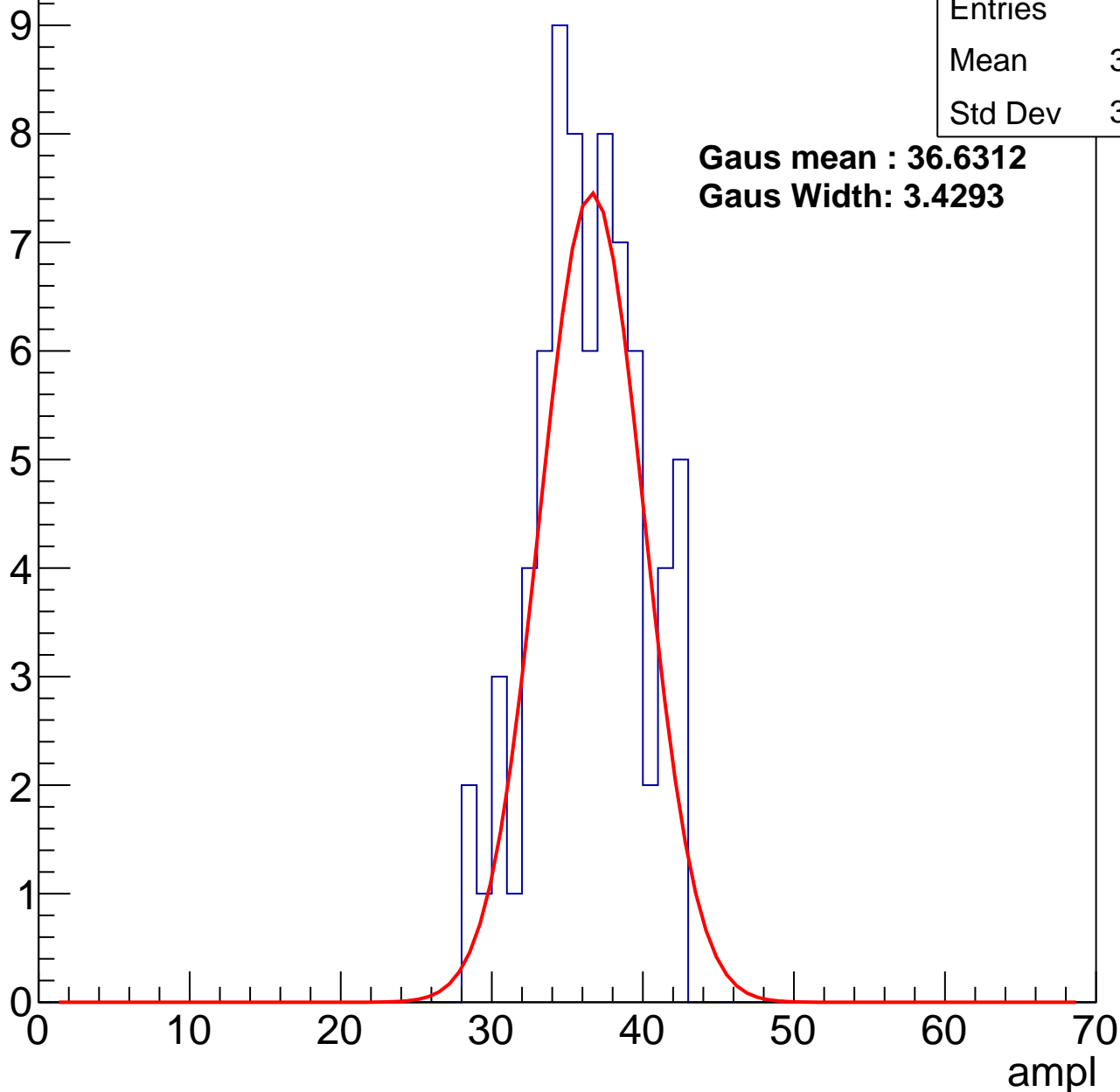
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	35.89
Std Dev	3.482

Gaus mean : 36.6312

Gaus Width: 3.4293



B1L103S, U14-ch87, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.37
Std Dev	3.839

Gaus mean : 42.2680

Gaus Width: 4.6858

10

8

6

4

2

0

0

10

20

30

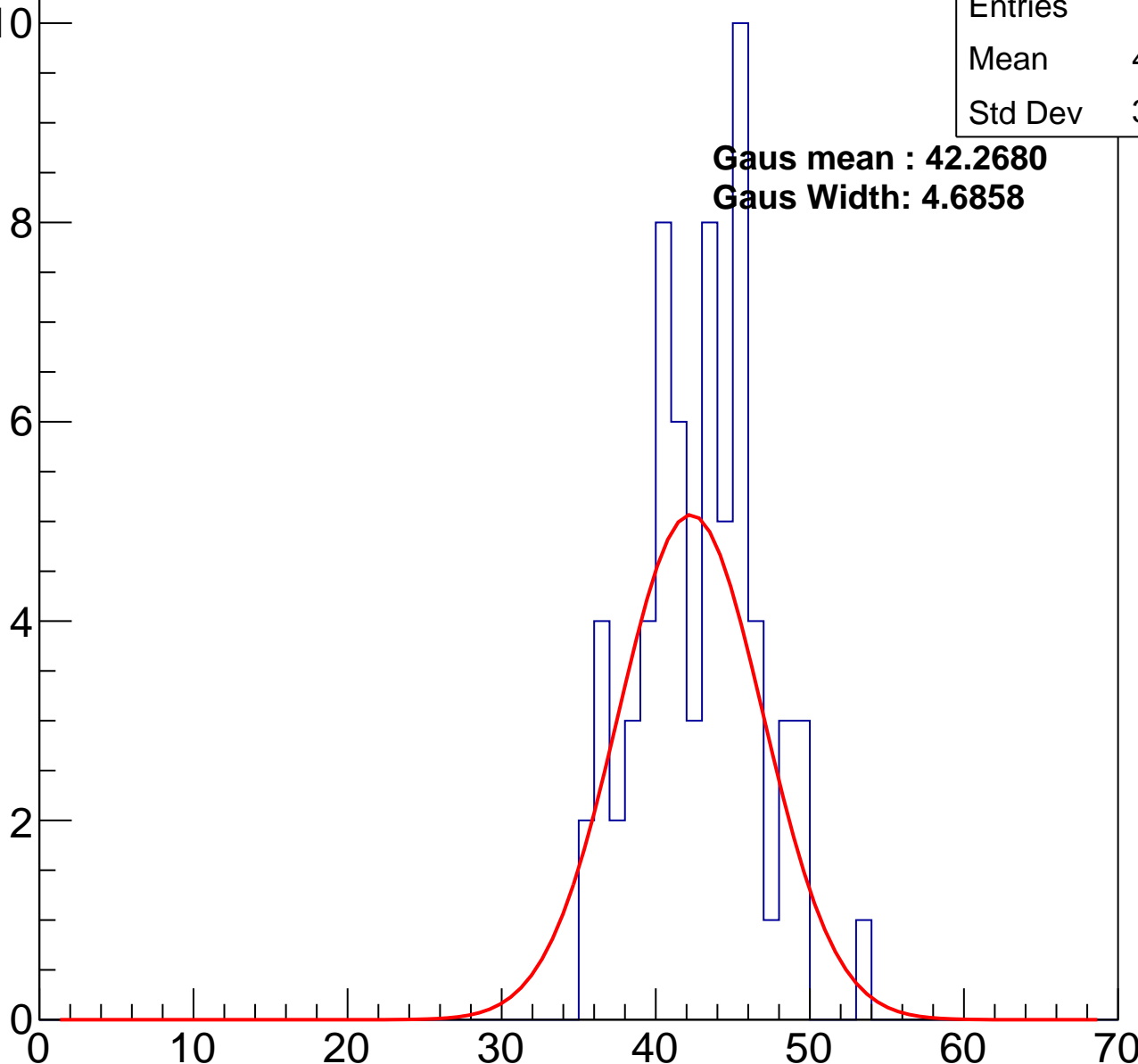
40

50

60

70

ampl

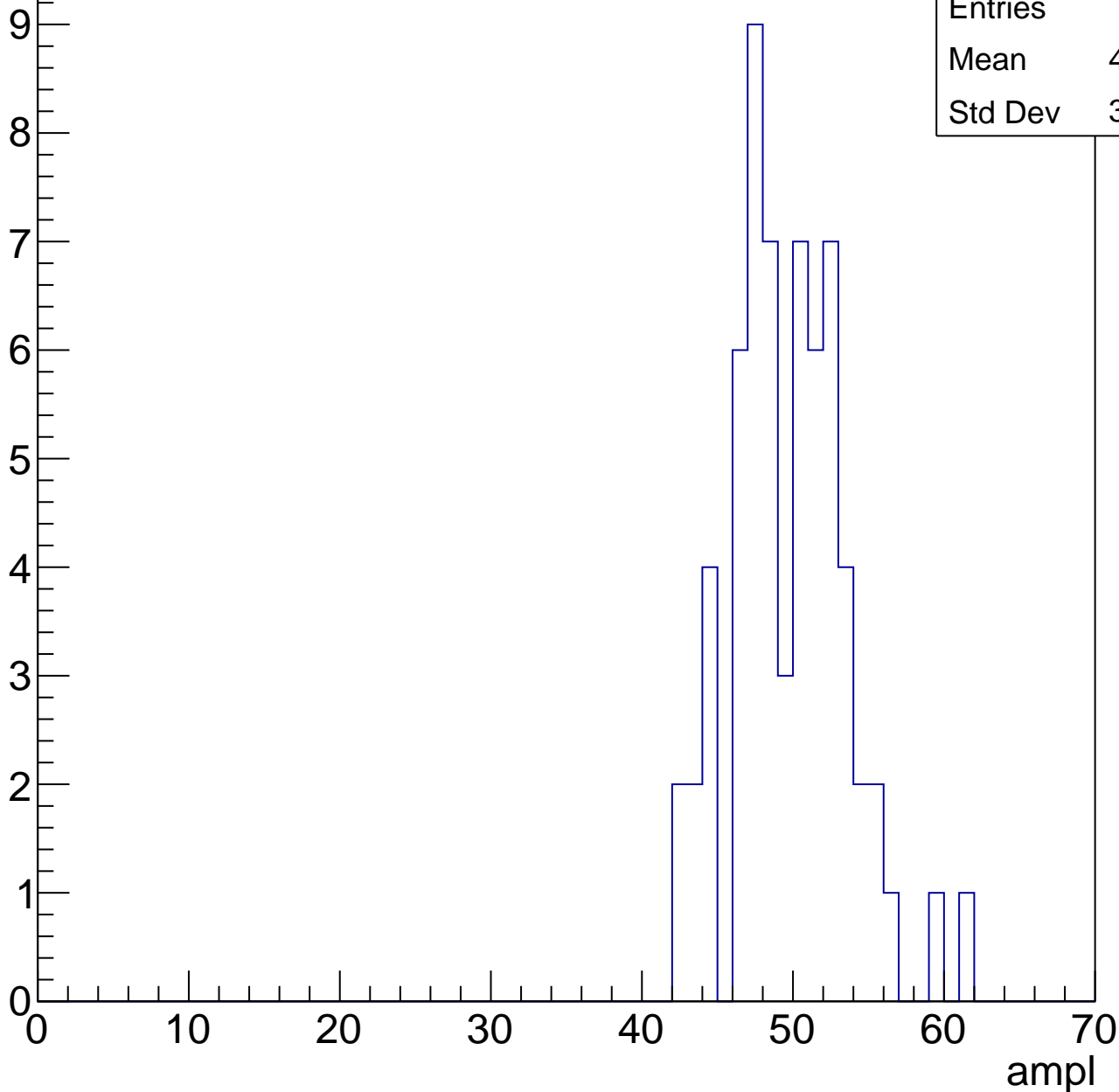


B1L103S, U14-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	49.28
Std Dev	3.822

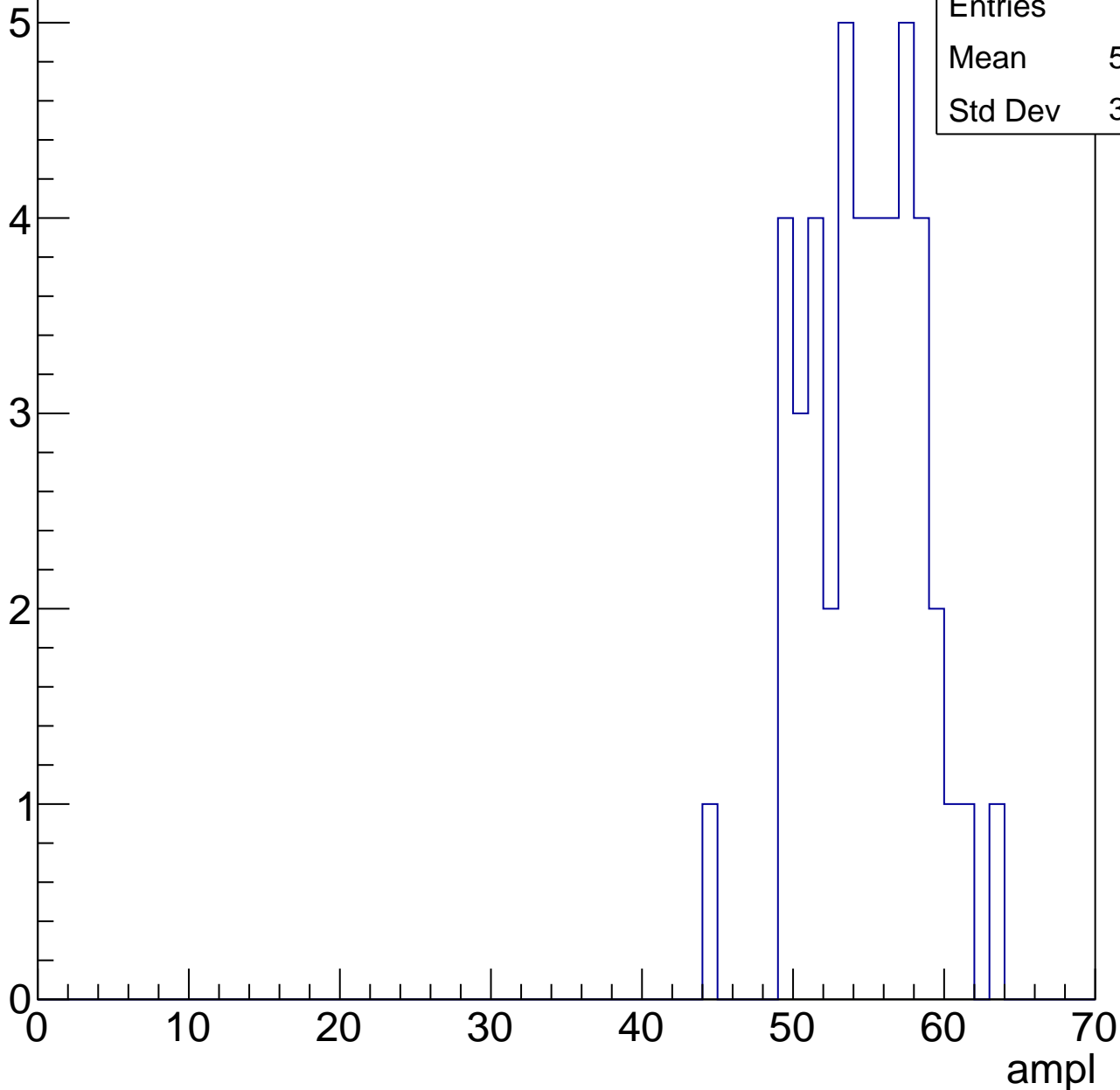


B1L103S, U14-ch87, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	54.27
Std Dev	3.768

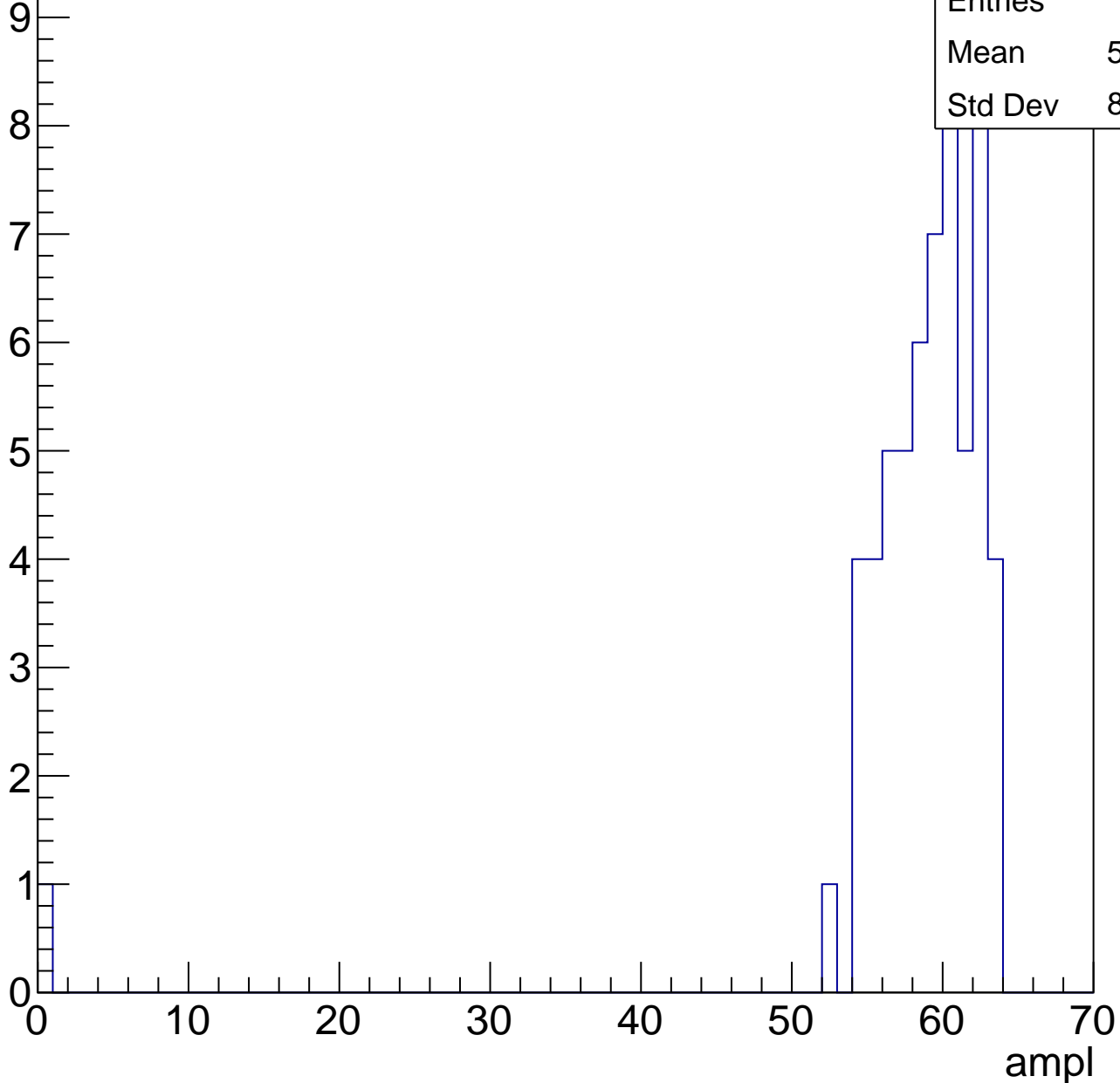


B1L103S, U14-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	57.75
Std Dev	8.065



B1L103S, U14-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

62

Std Dev

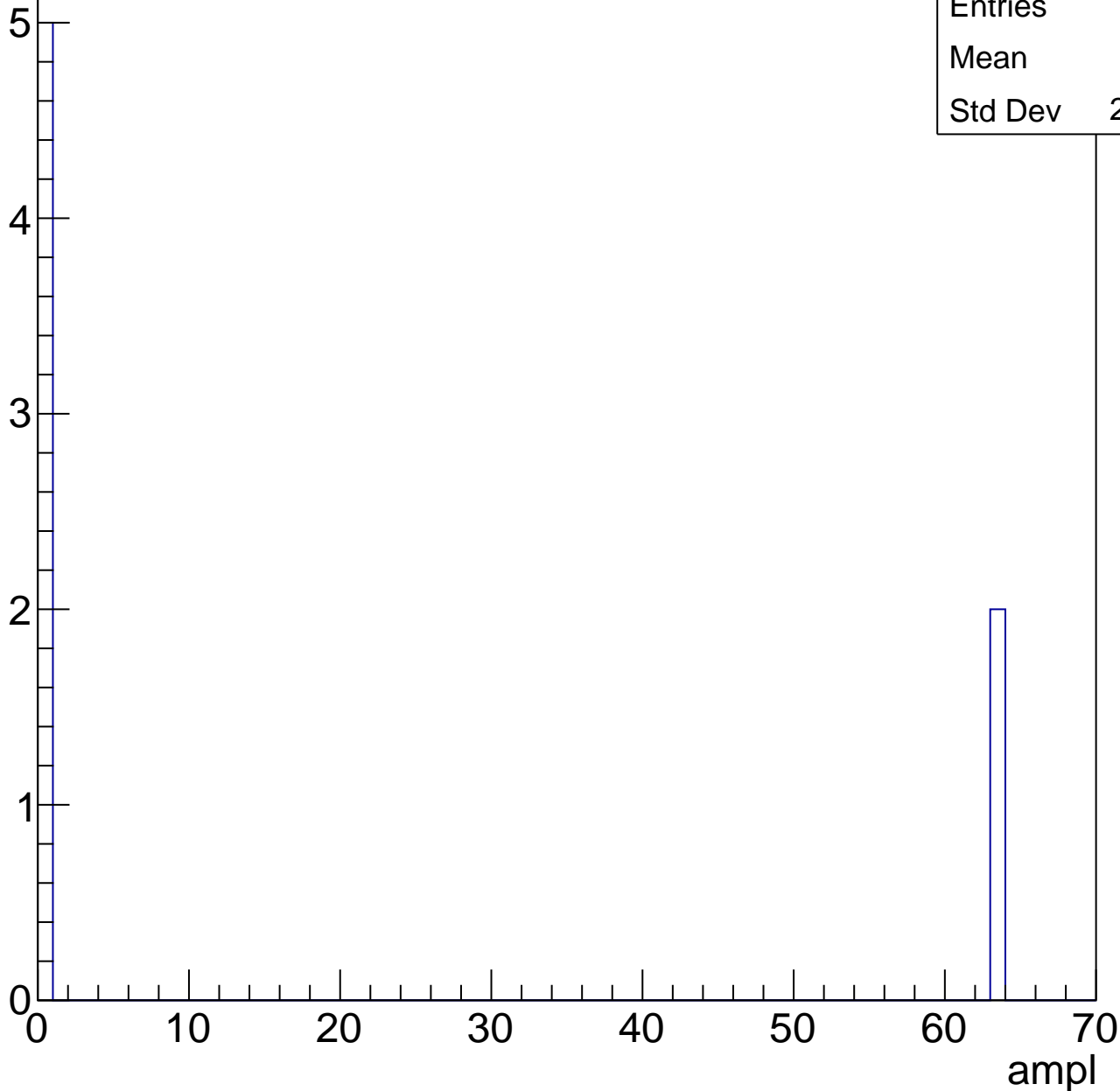
0.866

B1L103S, U14-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	7
Mean	18
Std Dev	28.46



B1L103S, U14-ch88, adc0

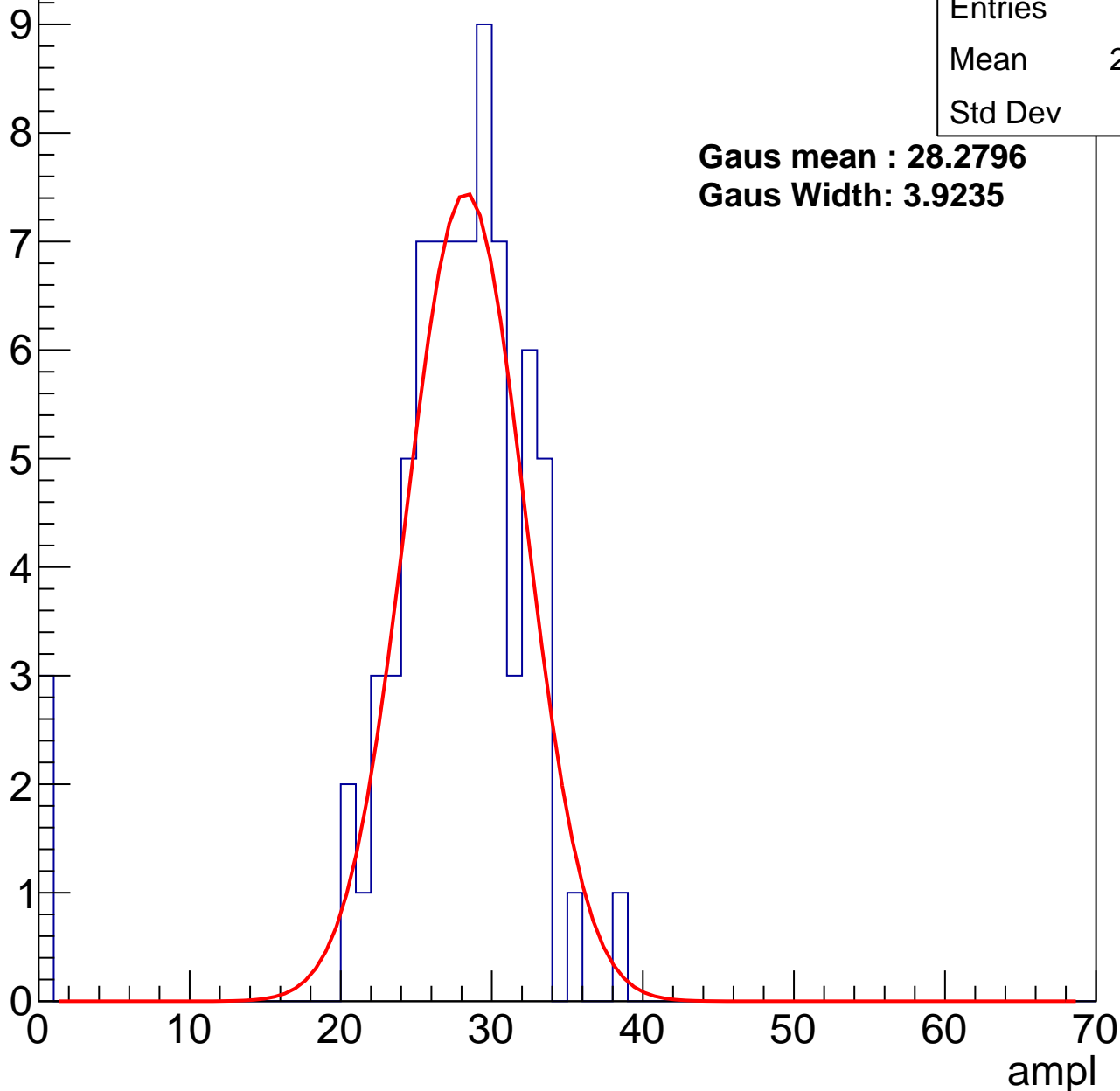
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	26.65
Std Dev	6.43

Gaus mean : 28.2796

Gaus Width: 3.9235



B1L103S, U14-ch88, adc1

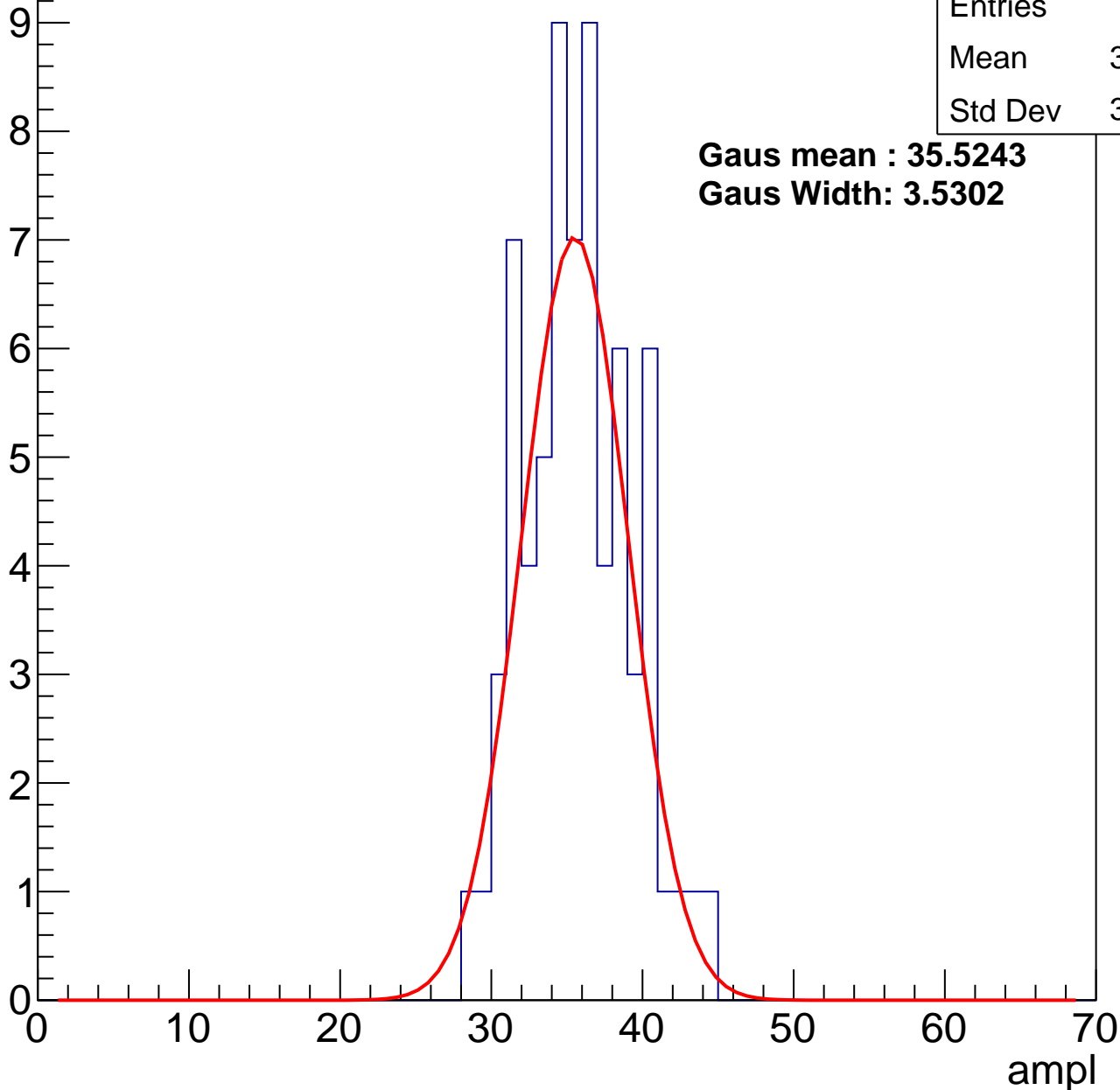
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.29
Std Dev	3.485

Gaus mean : 35.5243

Gaus Width: 3.5302



B1L103S, U14-ch88, adc2

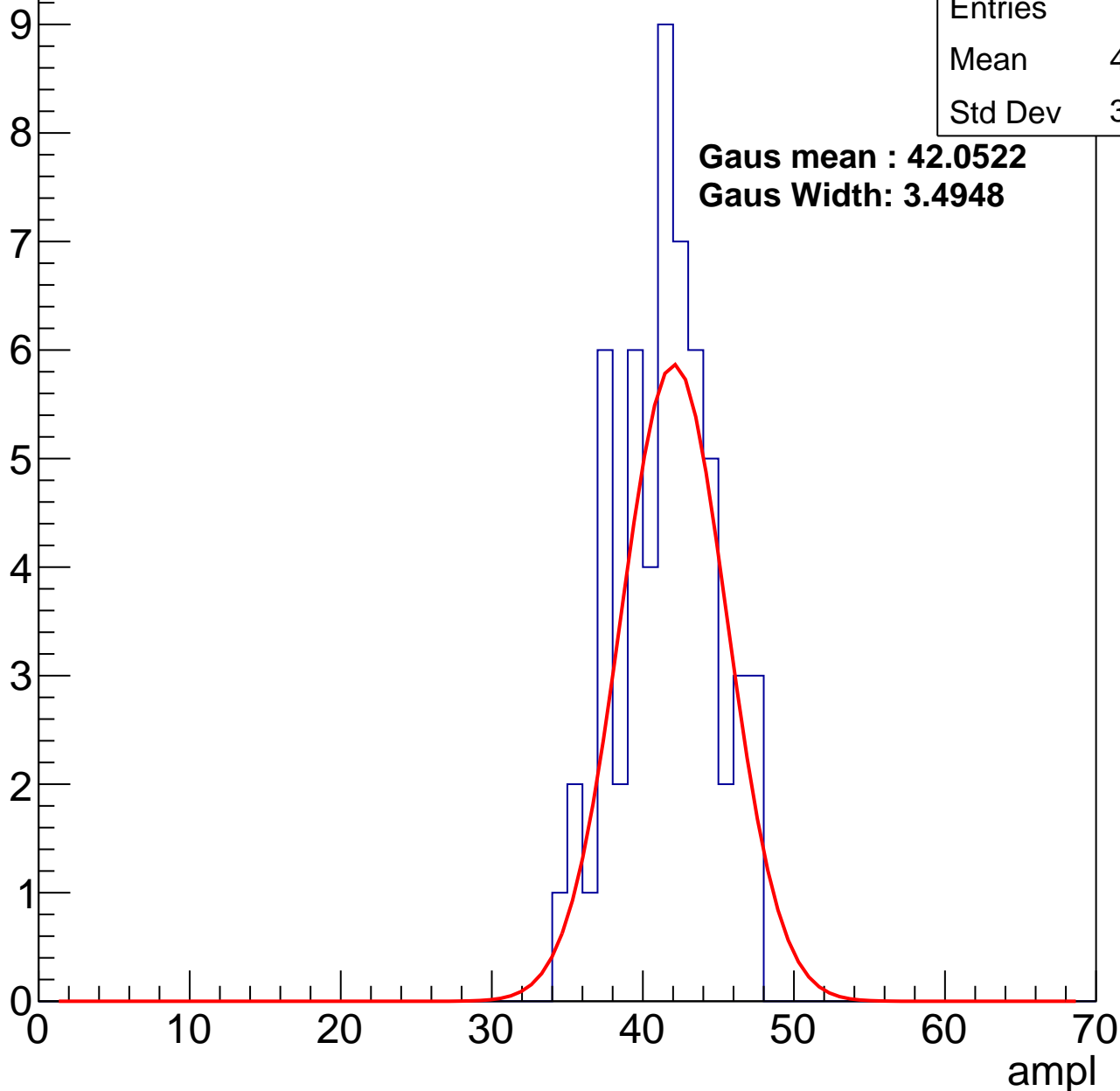
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41.09
Std Dev	3.186

Gaus mean : 42.0522

Gaus Width: 3.4948

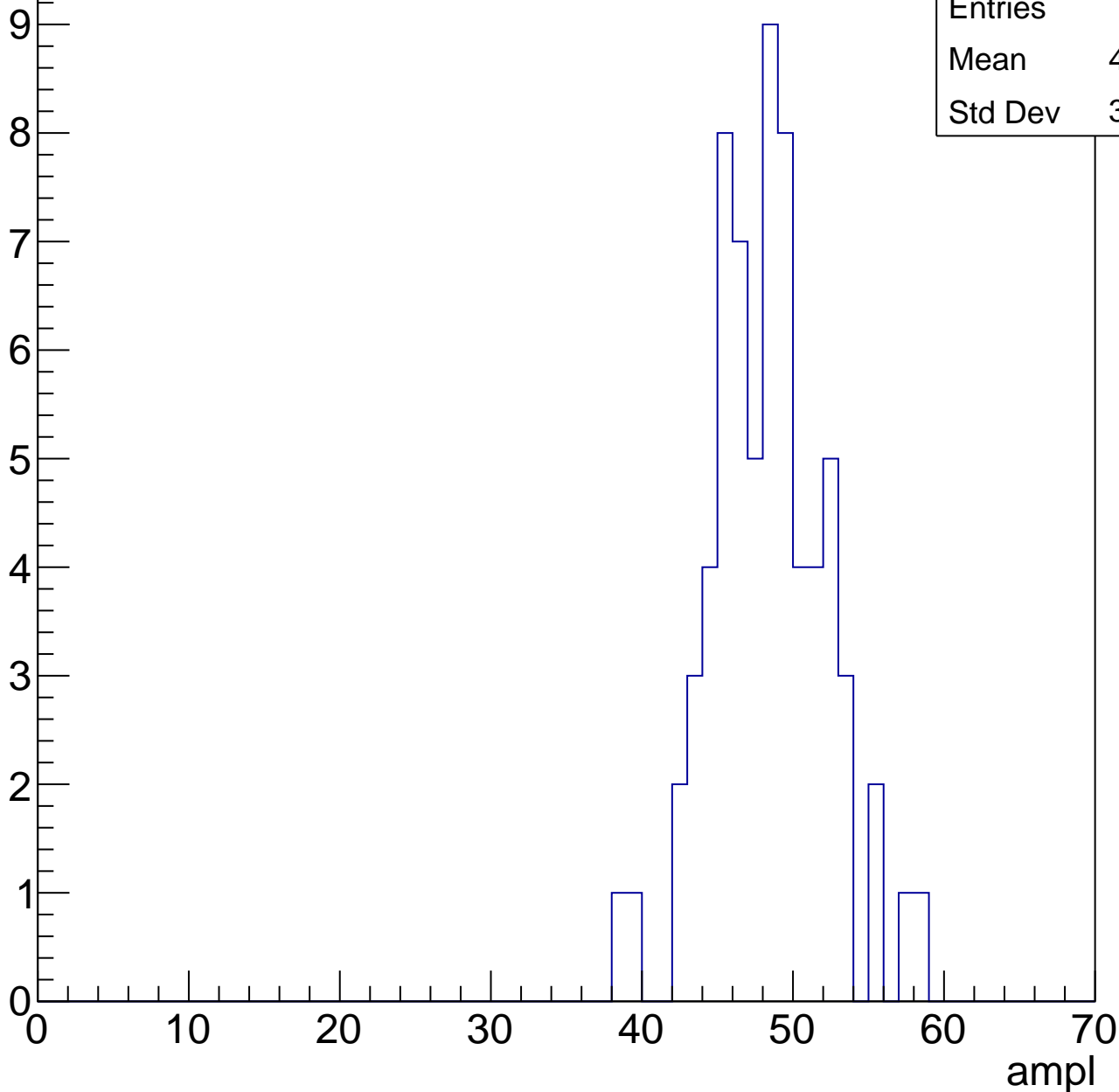


B1L103S, U14-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

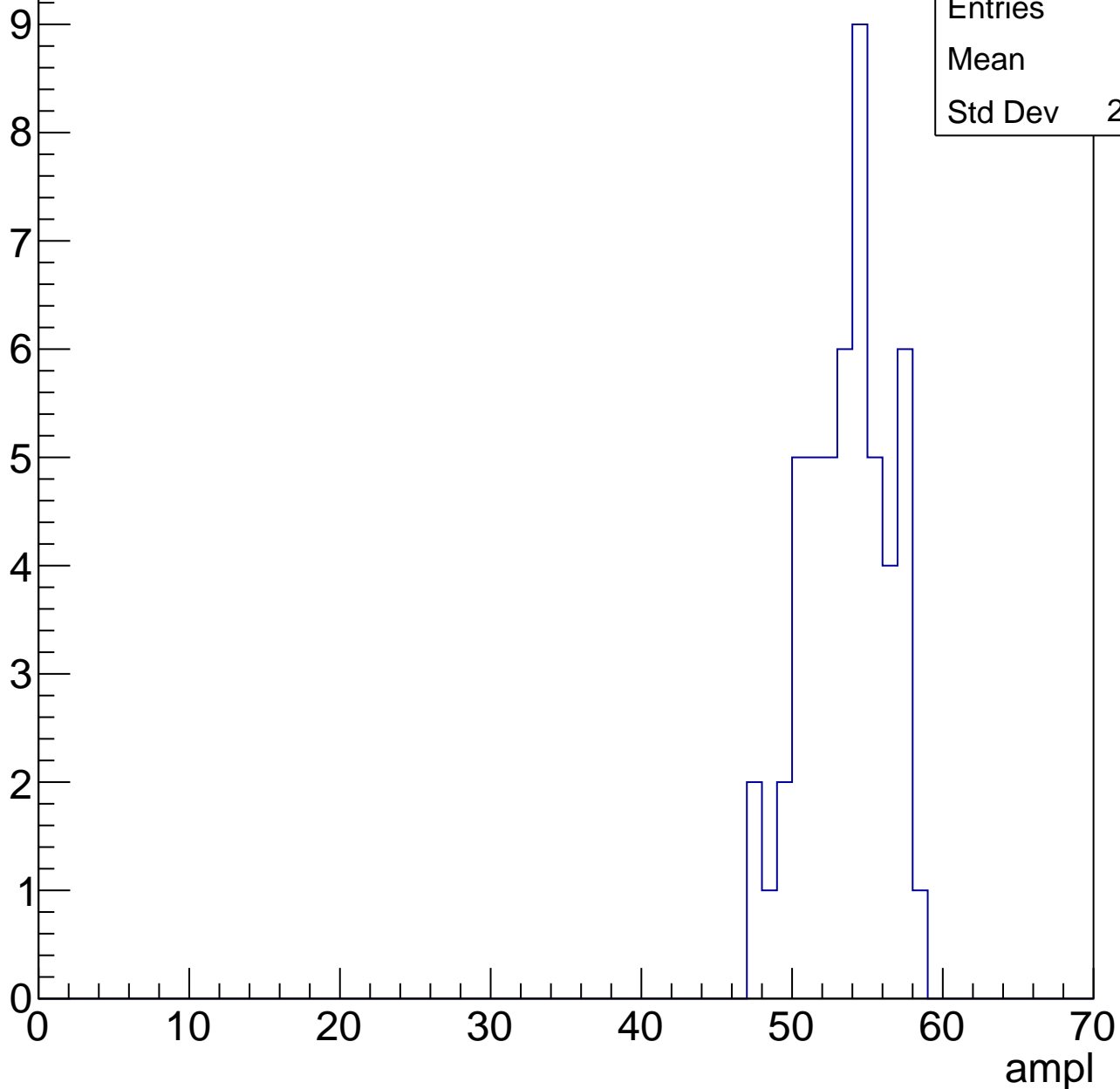
Entries	68
Mean	47.87
Std Dev	3.815



B1L103S, U14-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2

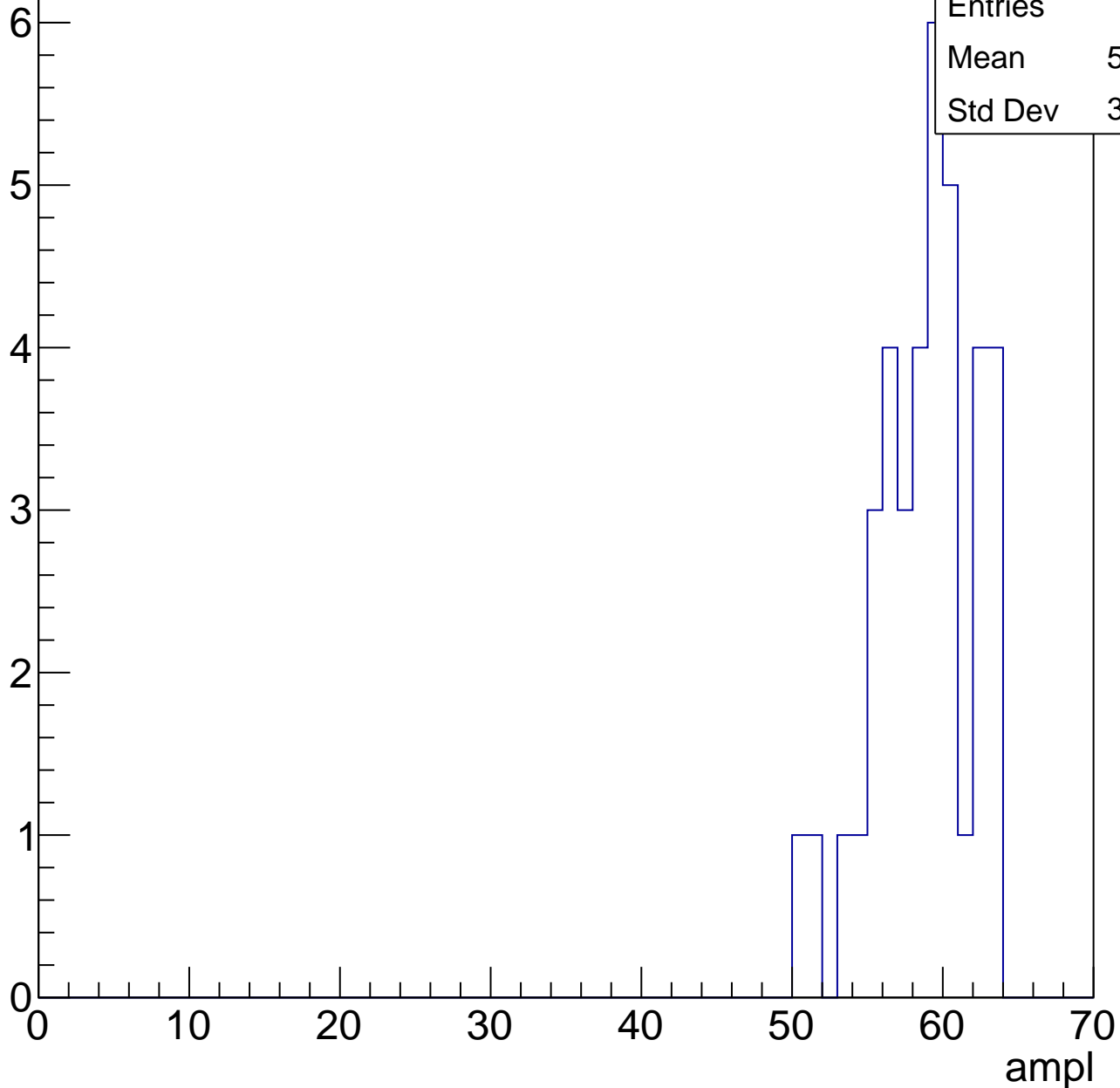
Entry



B1L103S, U14-ch88, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



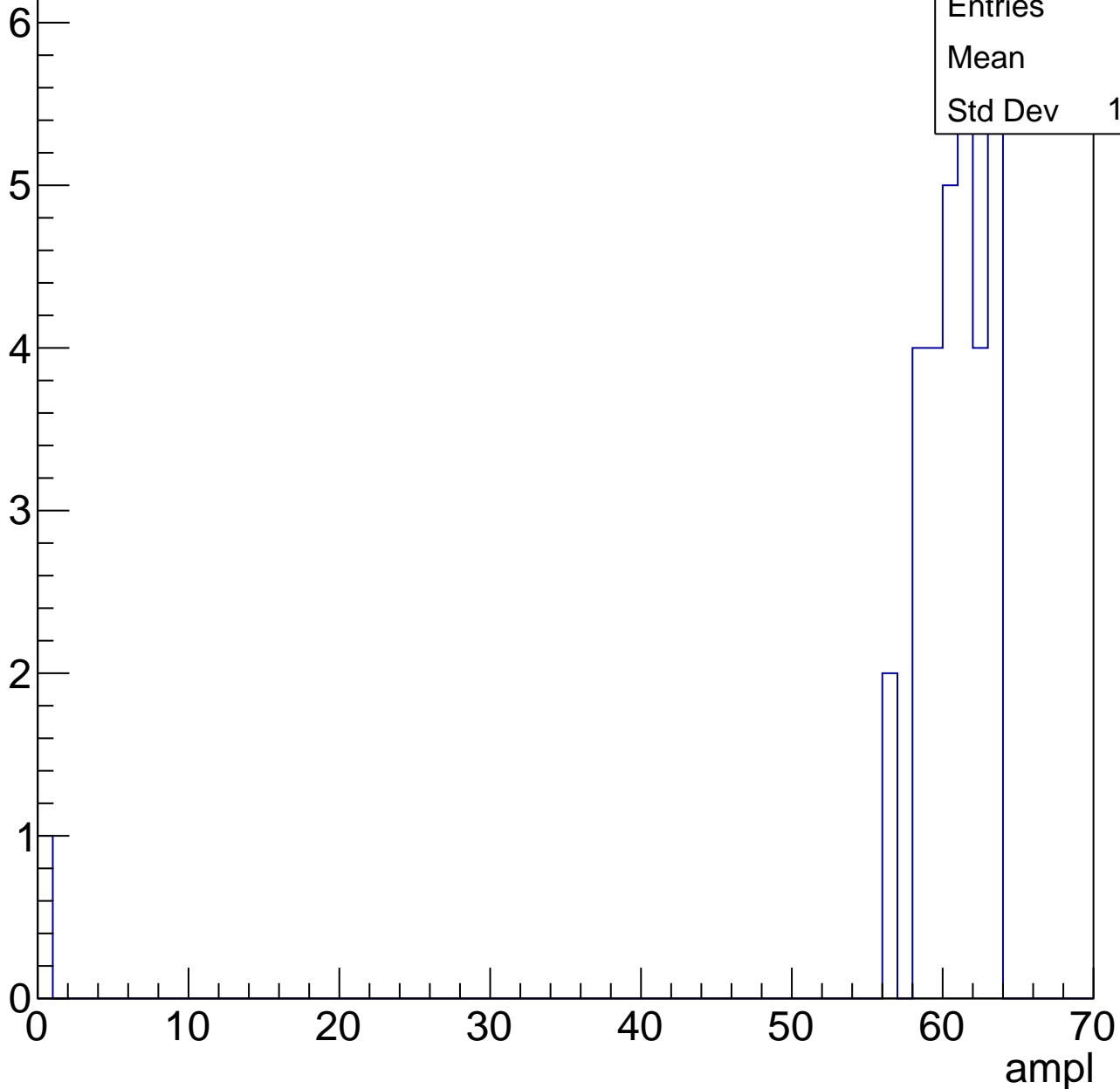
Entries	38
Mean	58.29
Std Dev	3.227

B1L103S, U14-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

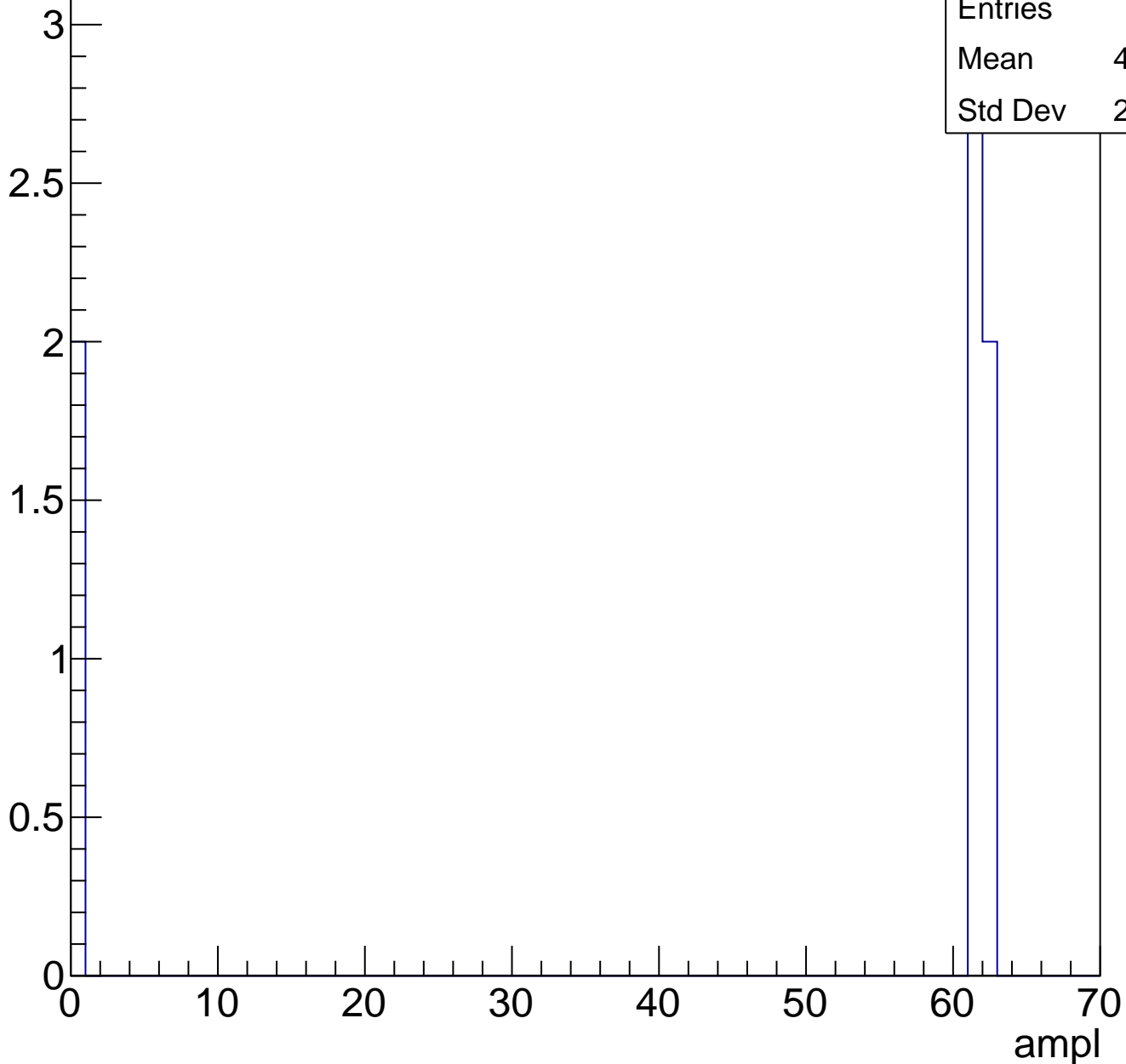
Entries	32
Mean	58.5
Std Dev	10.69



B1L103S, U14-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch89, adc0

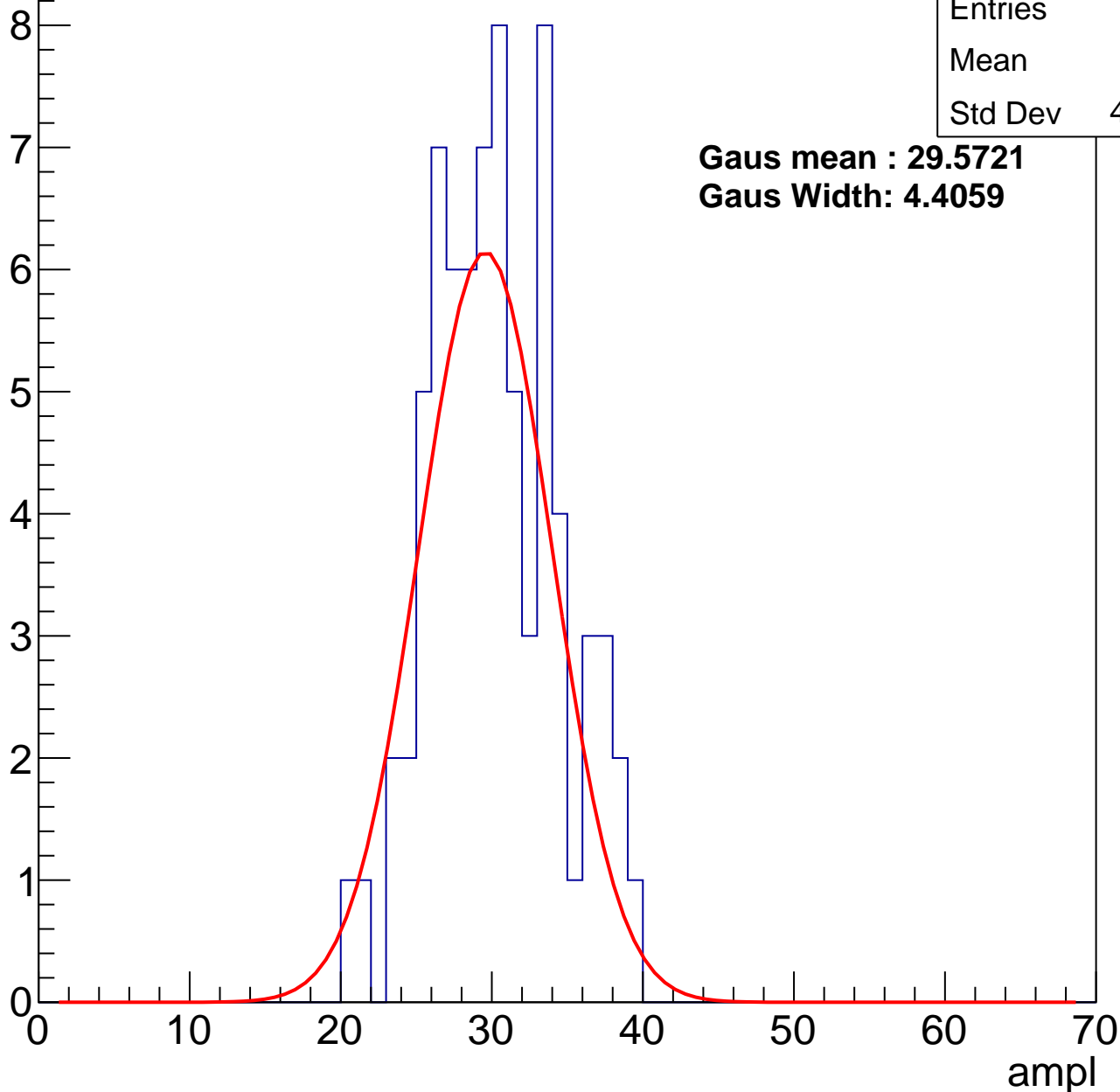
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	29.8
Std Dev	4.183

Gaus mean : 29.5721

Gaus Width: 4.4059



B1L103S, U14-ch89, adc1

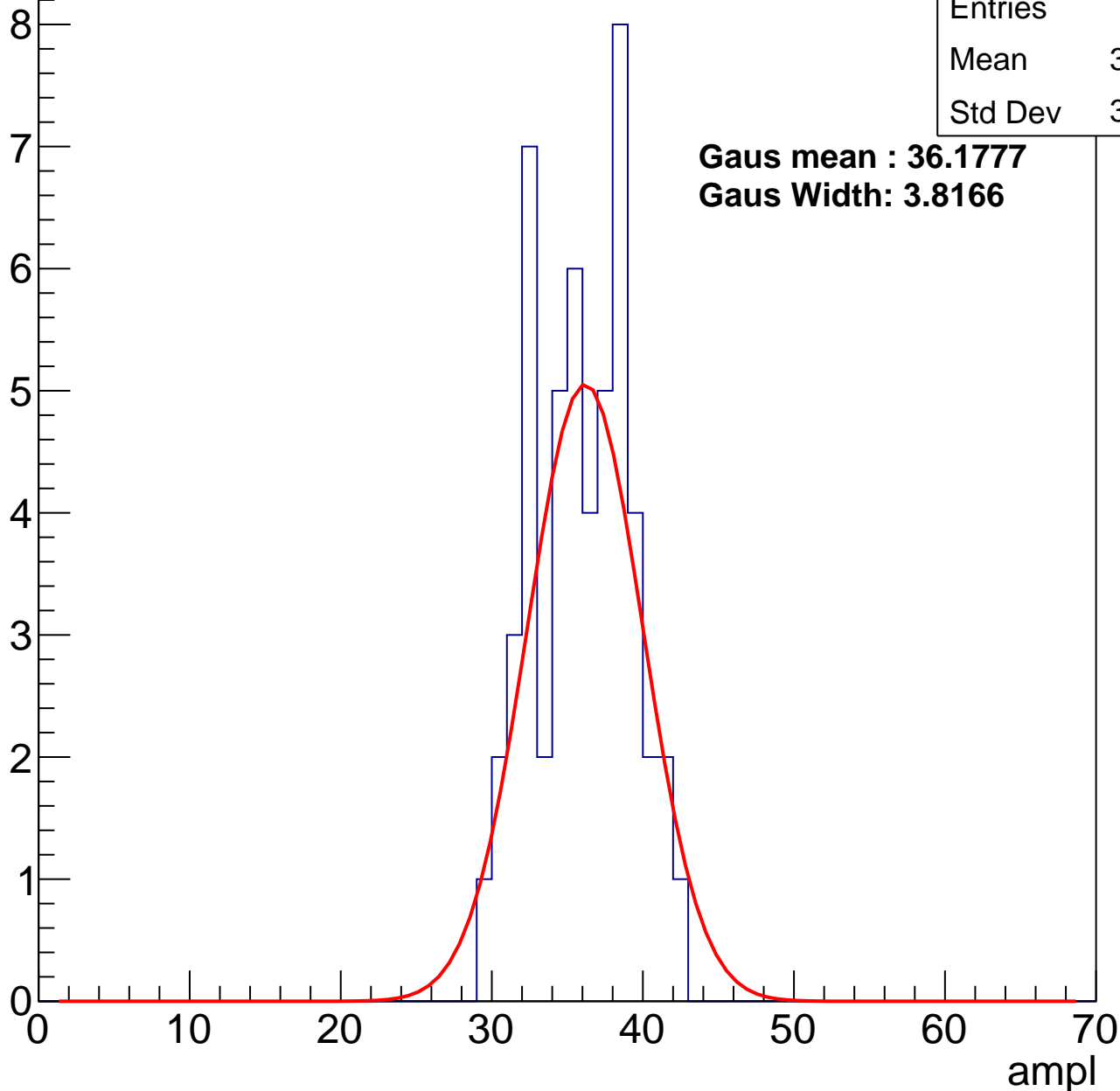
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	35.48
Std Dev	3.189

Gaus mean : 36.1777

Gaus Width: 3.8166



B1L103S, U14-ch89, adc2

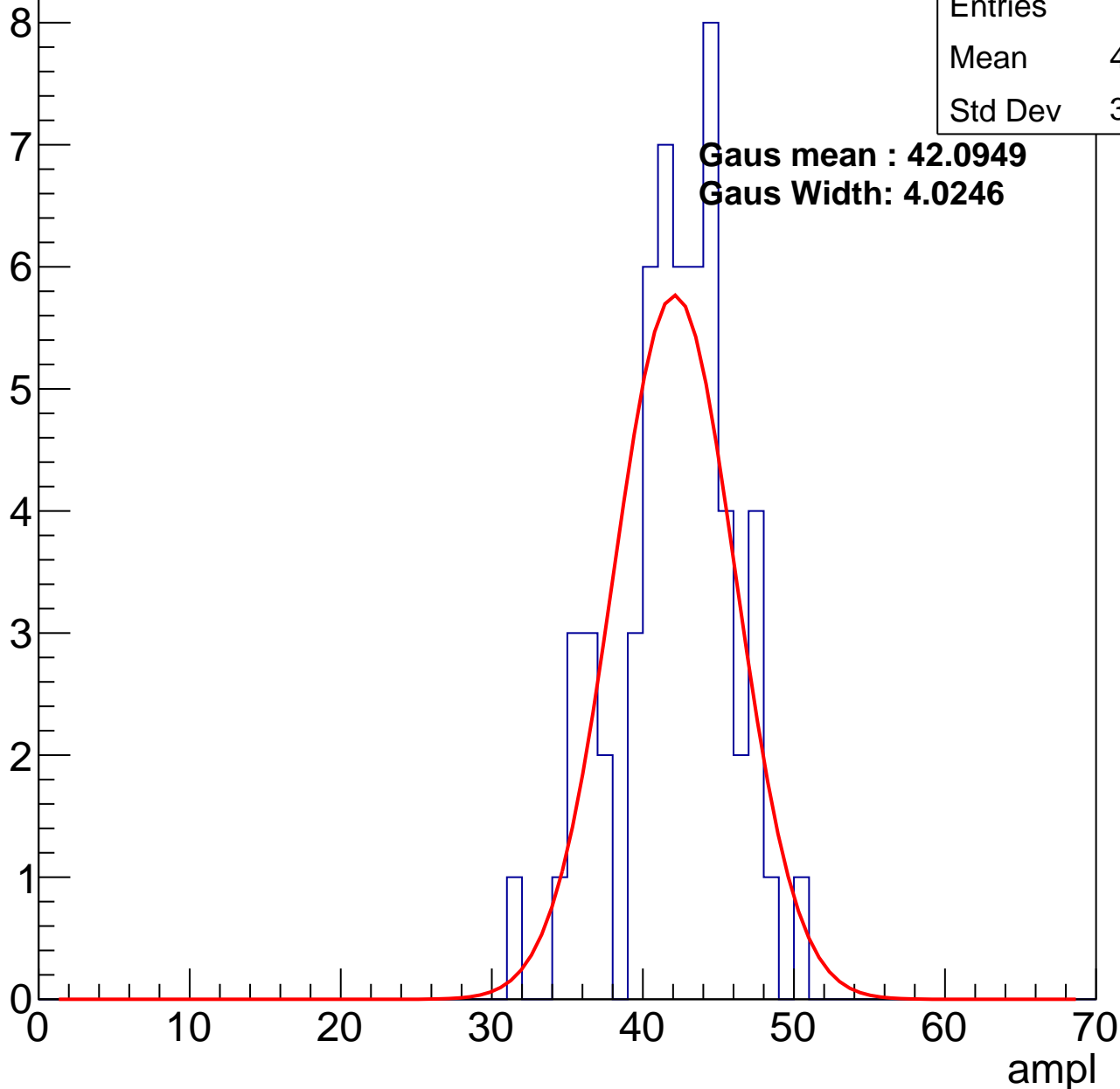
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	41.66
Std Dev	3.822

Gaus mean : 42.0949

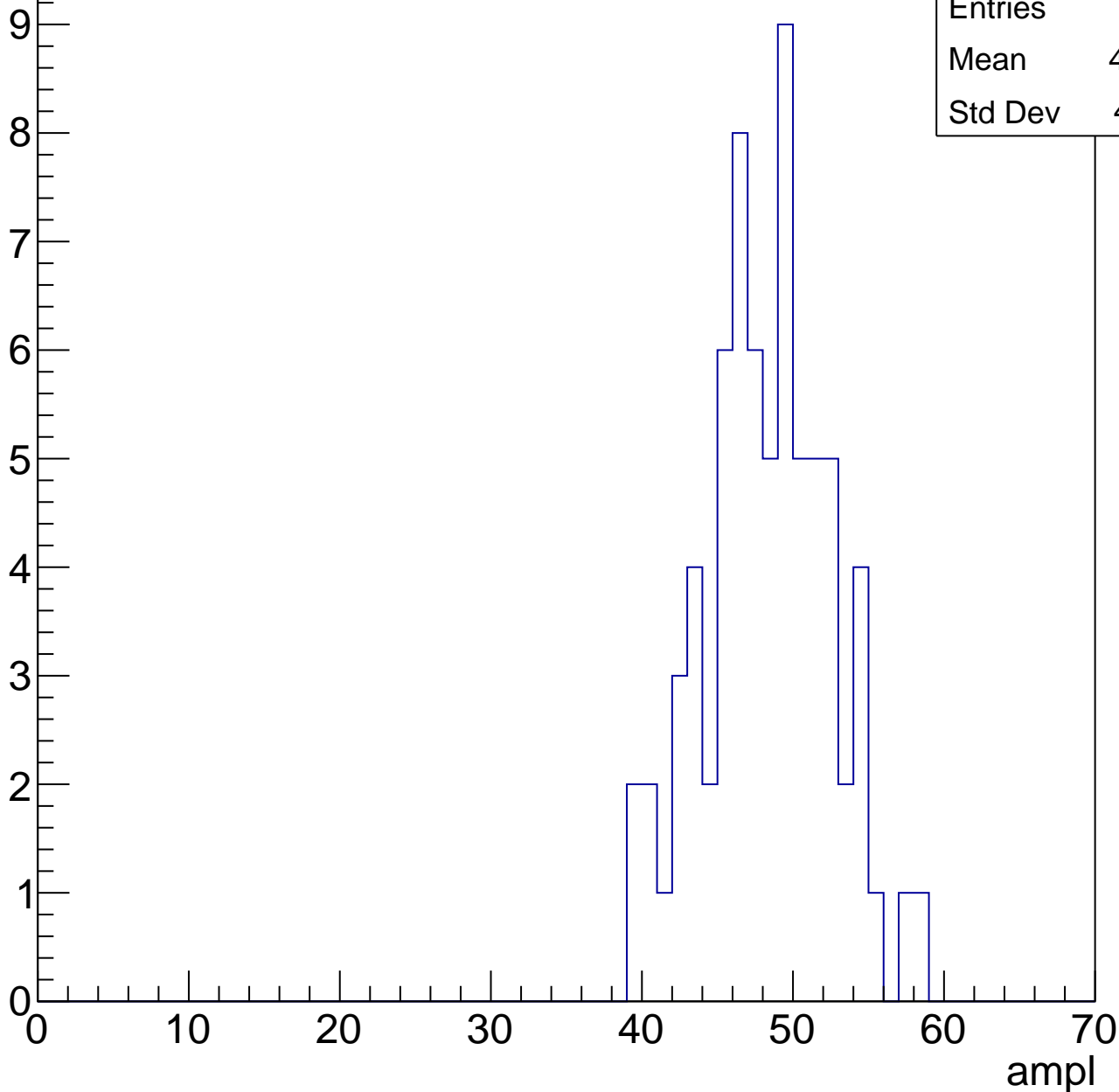
Gaus Width: 4.0246



B1L103S, U14-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

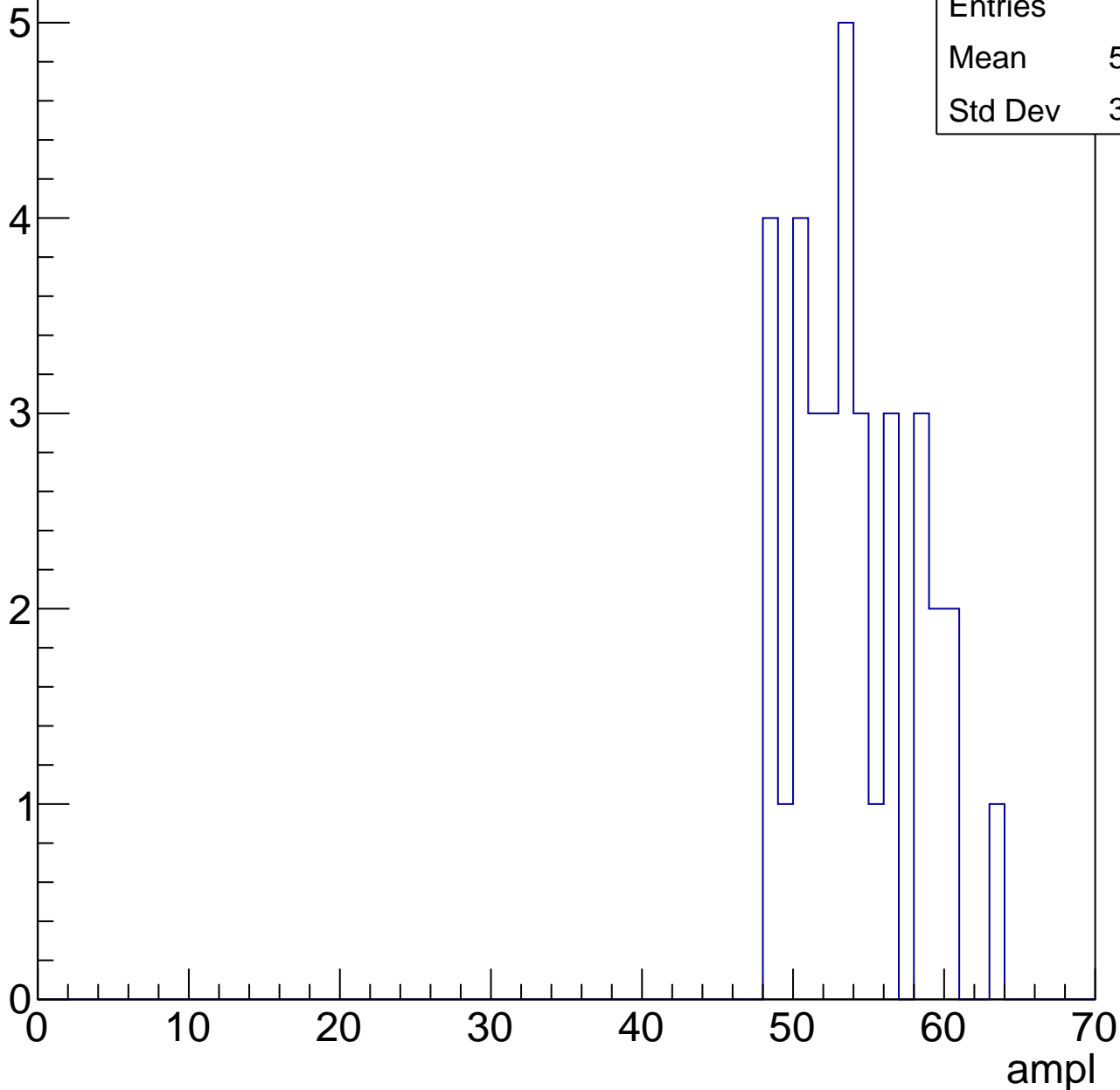


B1L103S, U14-ch89, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	53.57
Std Dev	3.908



B1L103S, U14-ch89, adc5

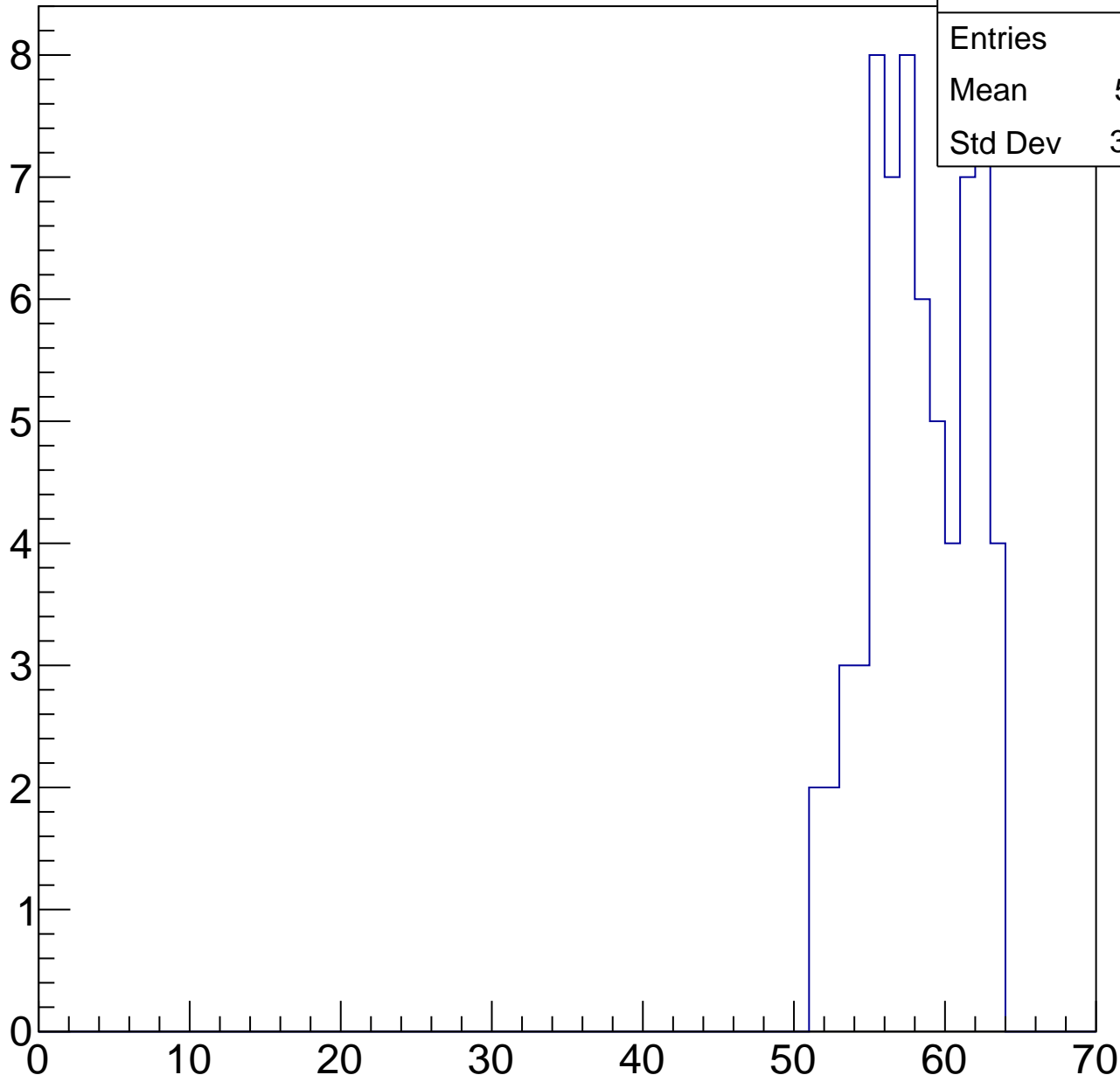
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	67
Mean	57.81
Std Dev	3.256

ampl

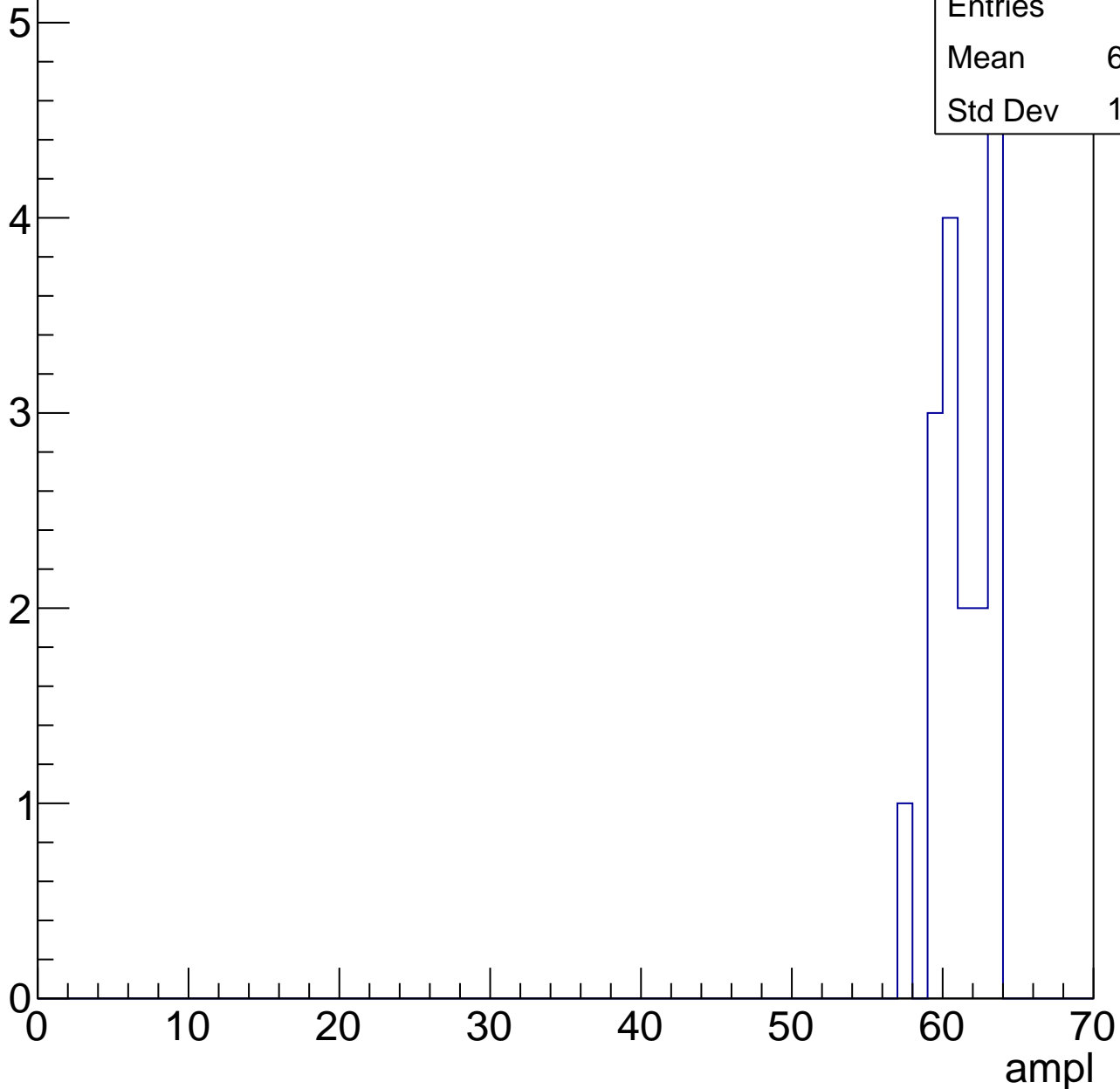


B1L103S, U14-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

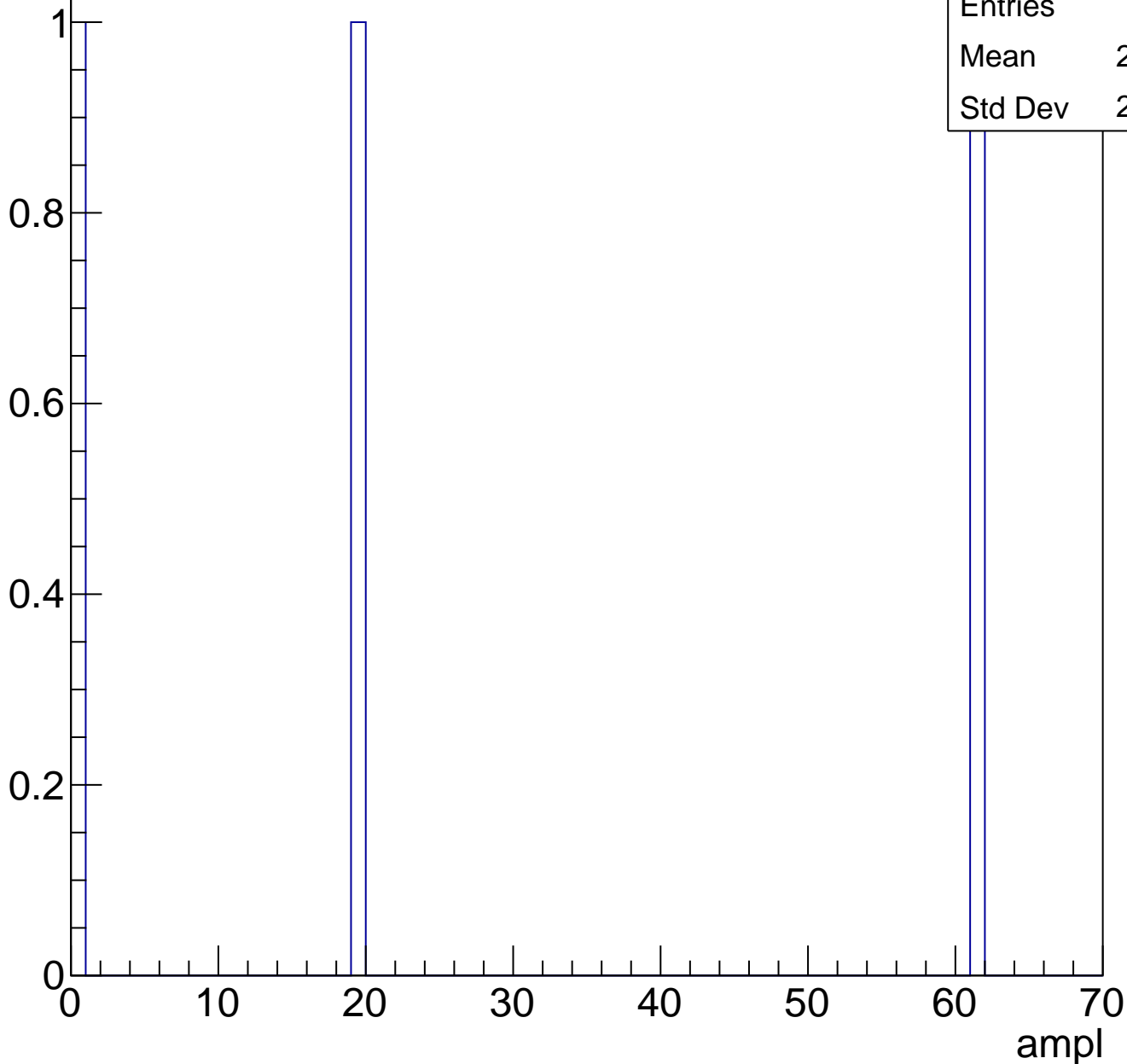
Entries	17
Mean	60.88
Std Dev	1.778



B1L103S, U14-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	26.67
Std Dev	25.49

B1L103S, U14-ch90, adc0

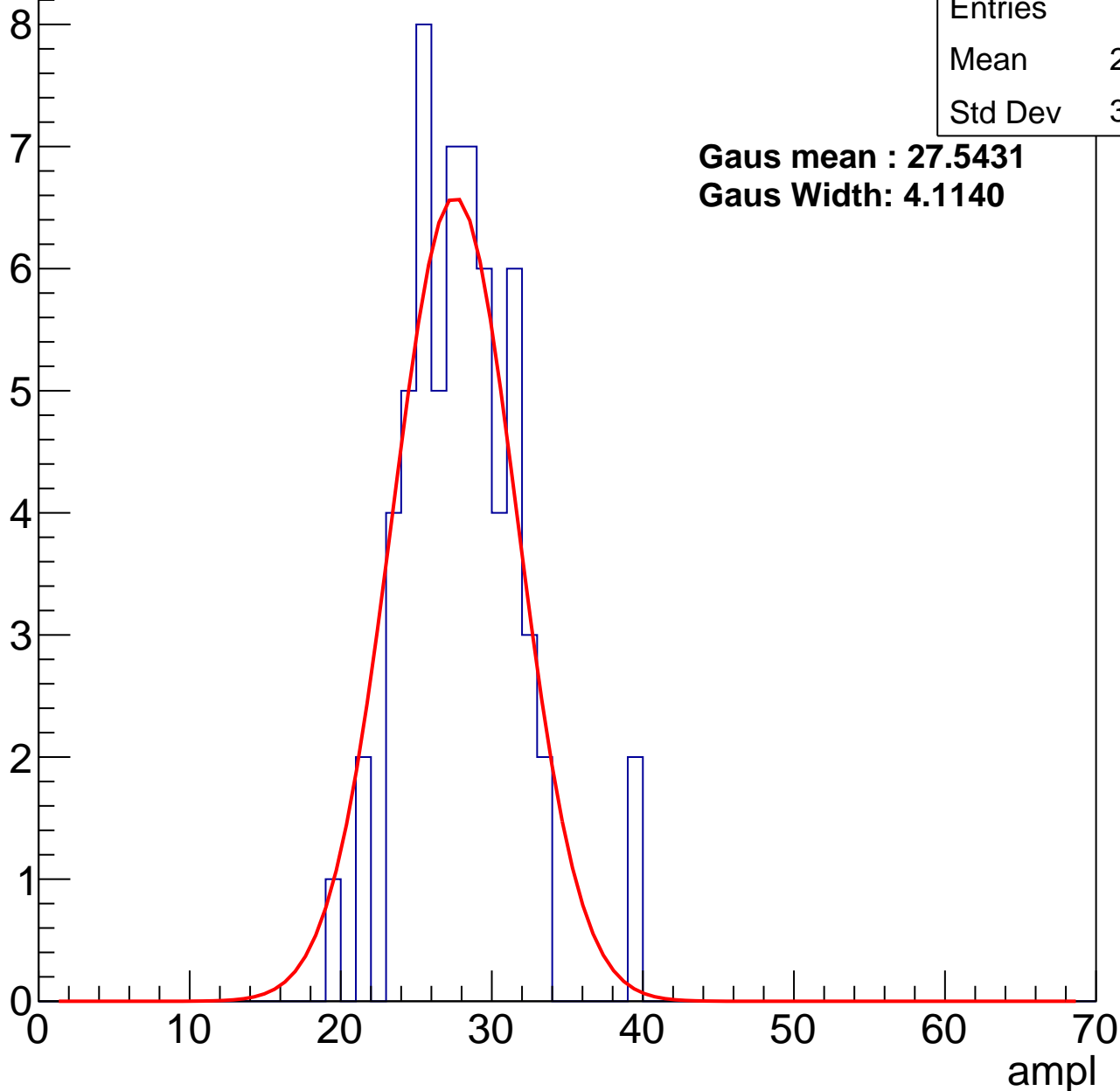
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	27.55
Std Dev	3.727

Gaus mean : 27.5431

Gaus Width: 4.1140



B1L103S, U14-ch90, adc1

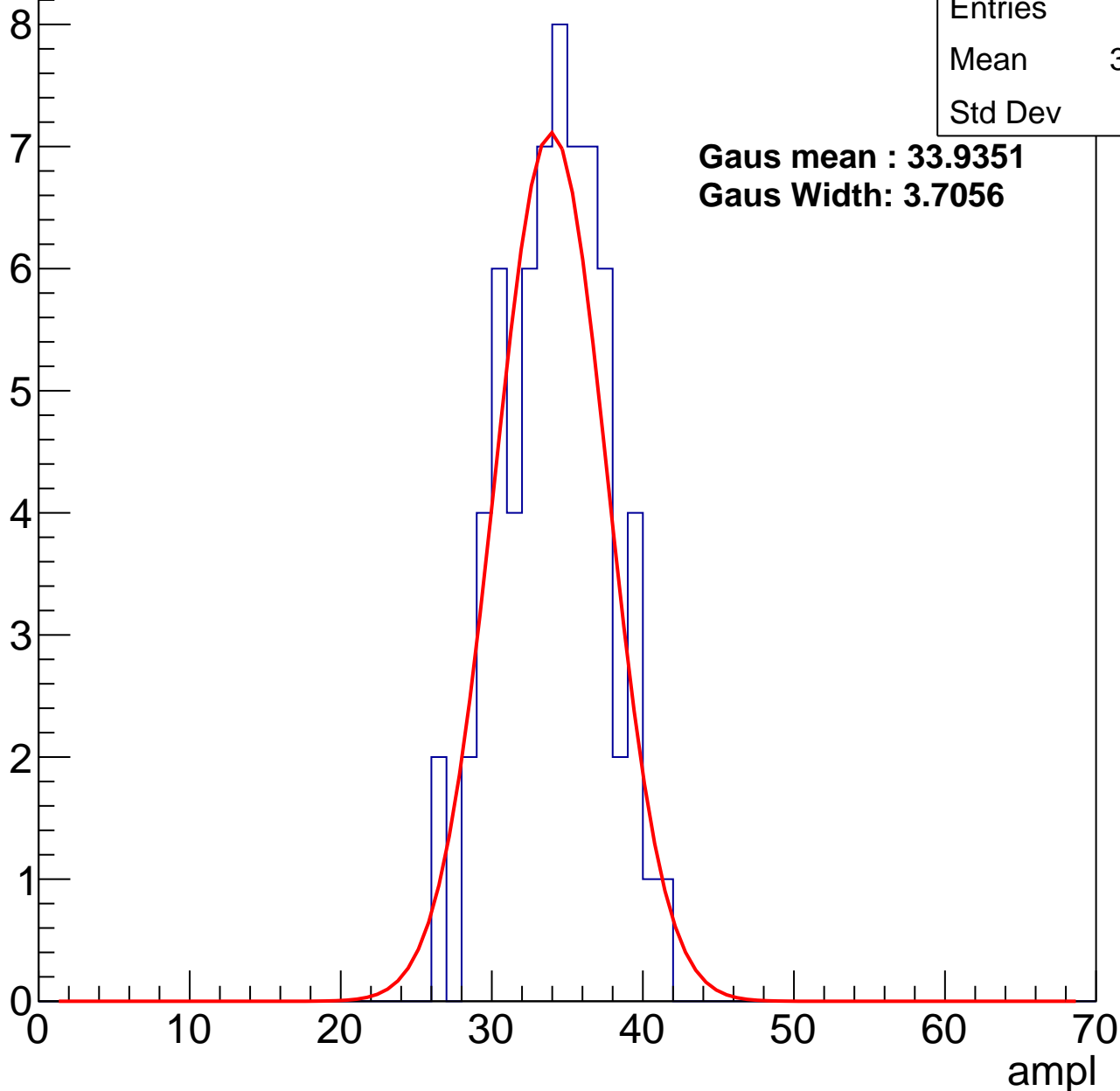
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	33.66
Std Dev	3.37

Gaus mean : 33.9351

Gaus Width: 3.7056



B1L103S, U14-ch90, adc2

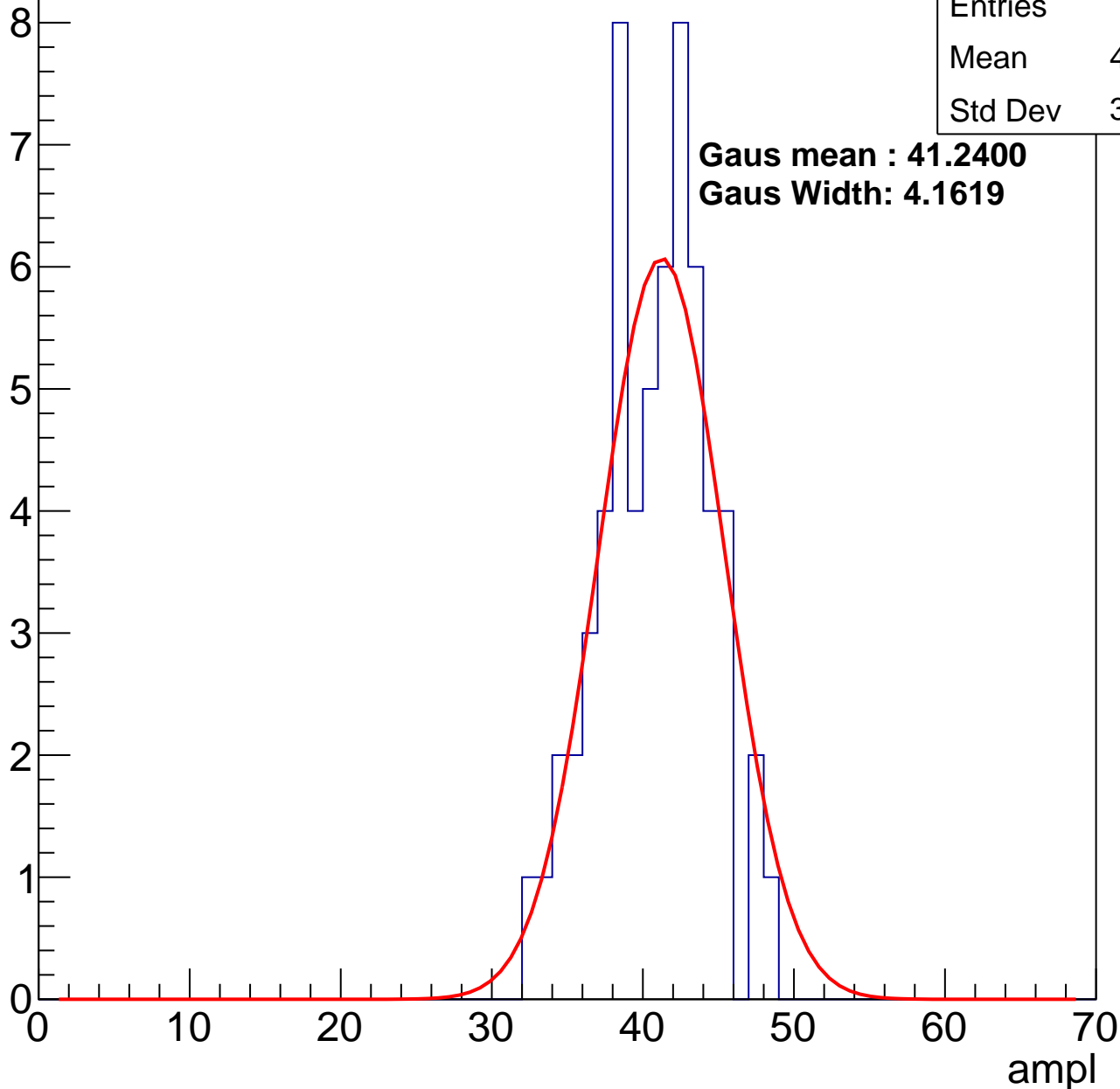
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	40.28
Std Dev	3.549

Gaus mean : 41.2400

Gaus Width: 4.1619

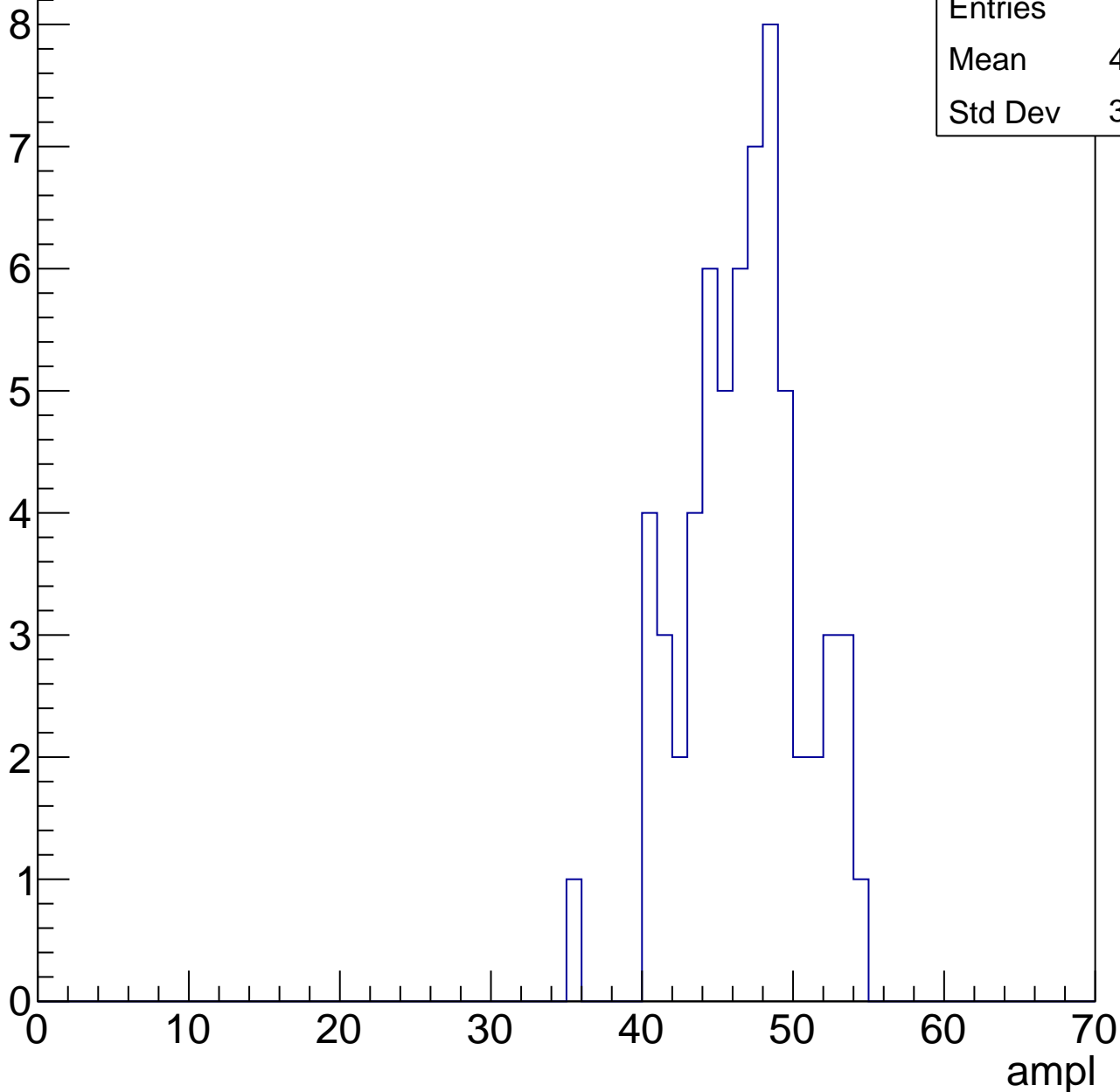


B1L103S, U14-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	46.26
Std Dev	3.848

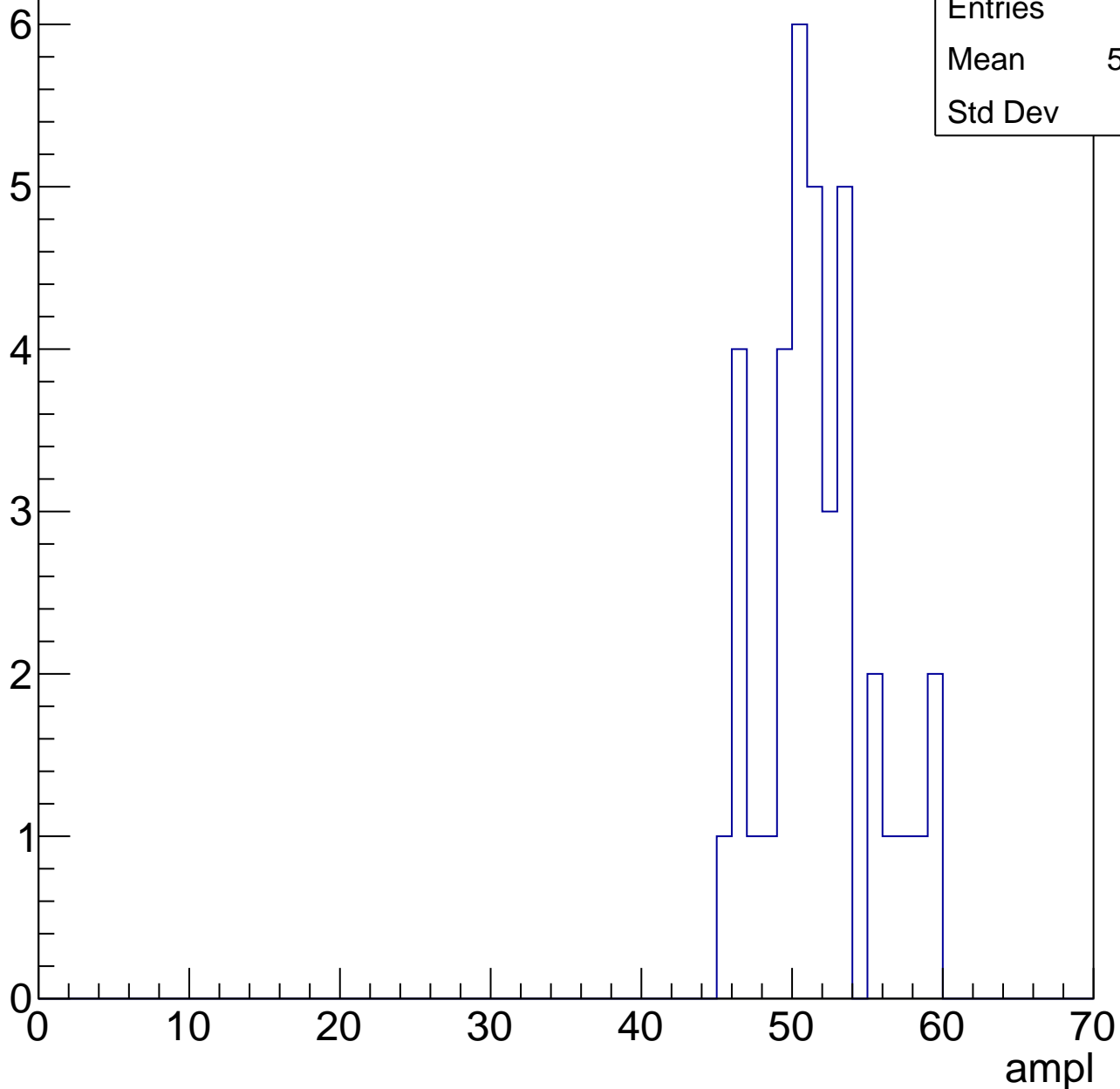


B1L103S, U14-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	51.22
Std Dev	3.58

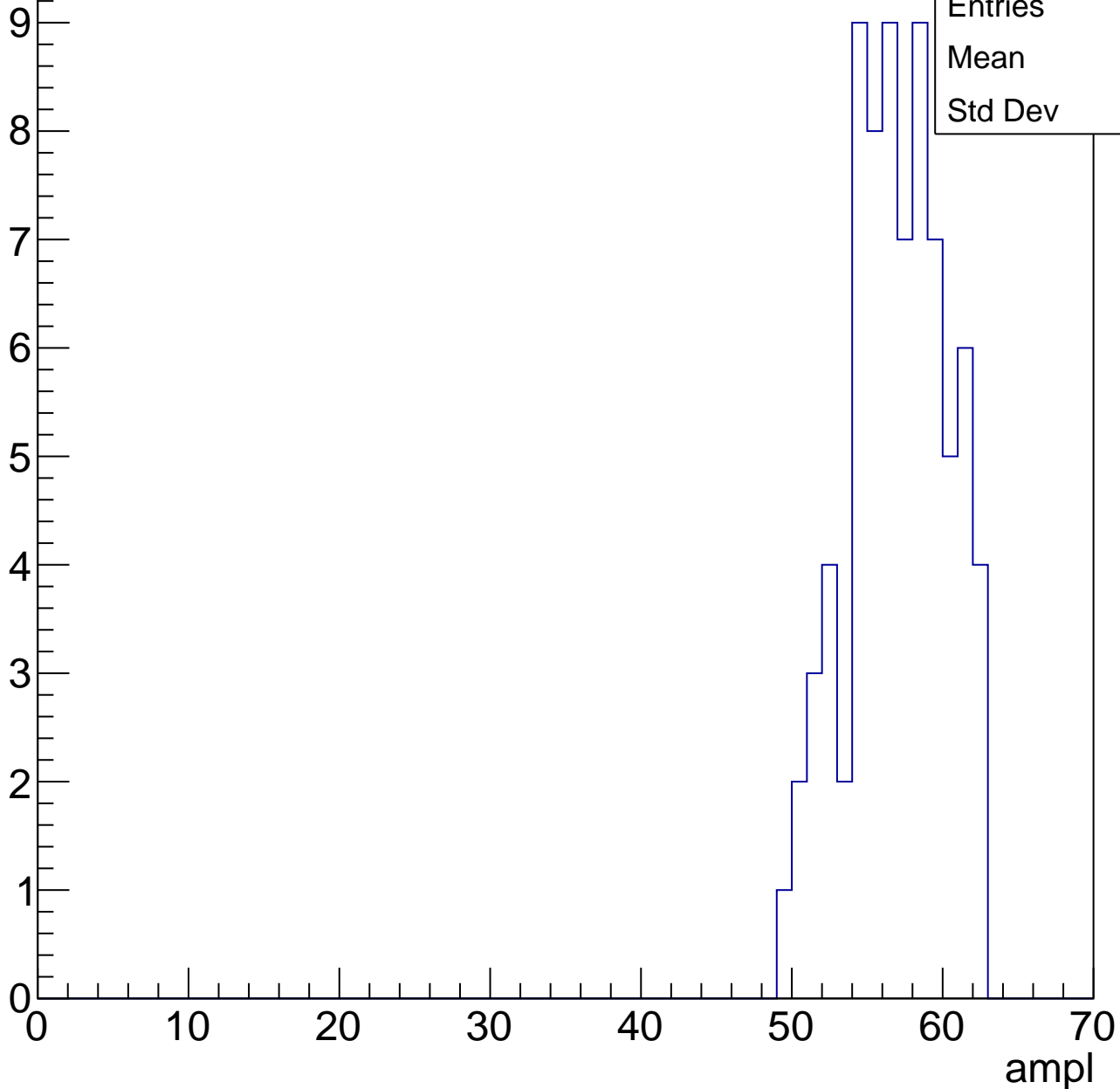


B1L103S, U14-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	56.5
Std Dev	3.21

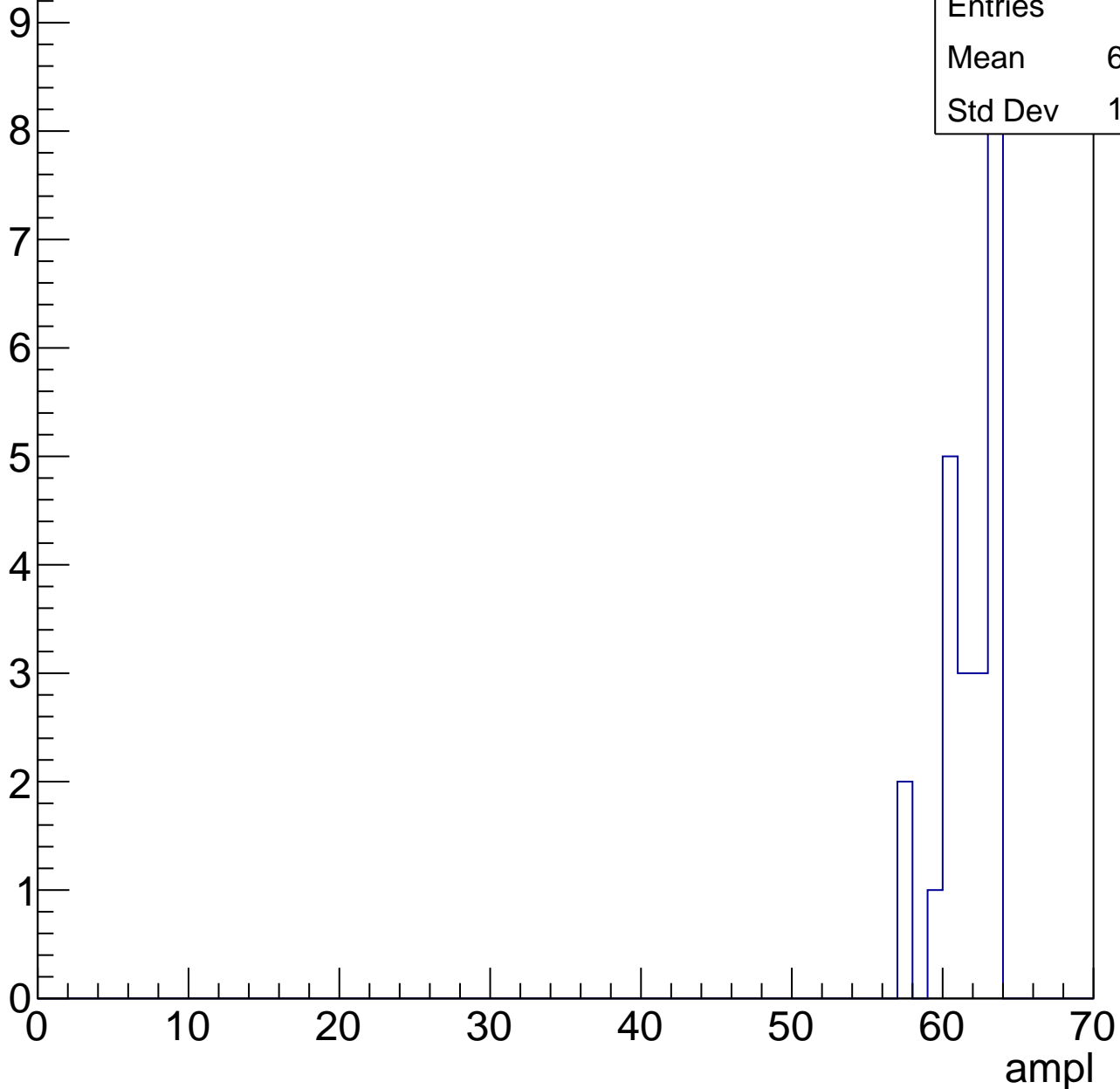


B1L103S, U14-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

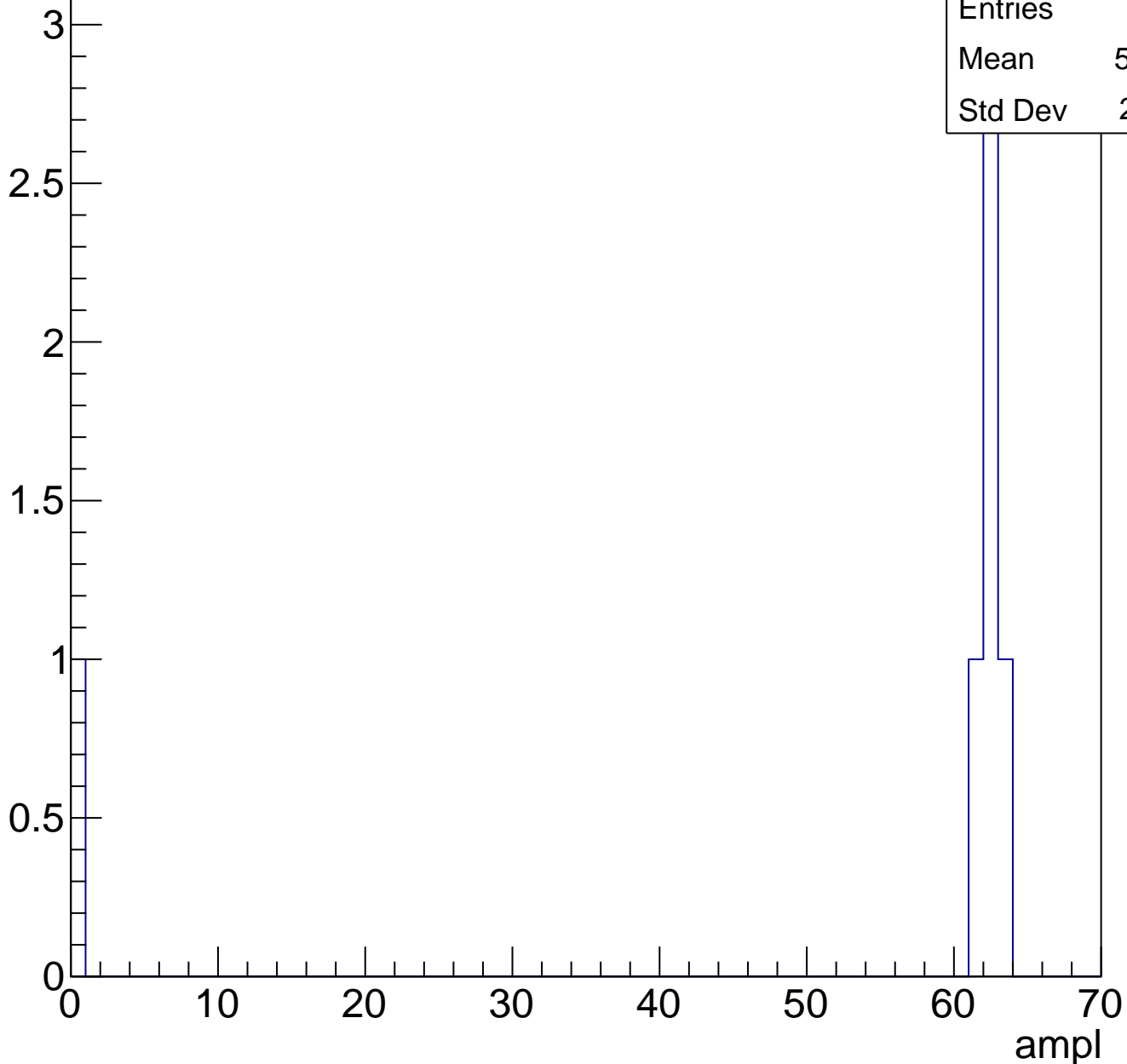
Entries	23
Mean	61.26
Std Dev	1.847



B1L103S, U14-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	51.67
Std Dev	23.11

B1L103S, U14-ch91, adc0

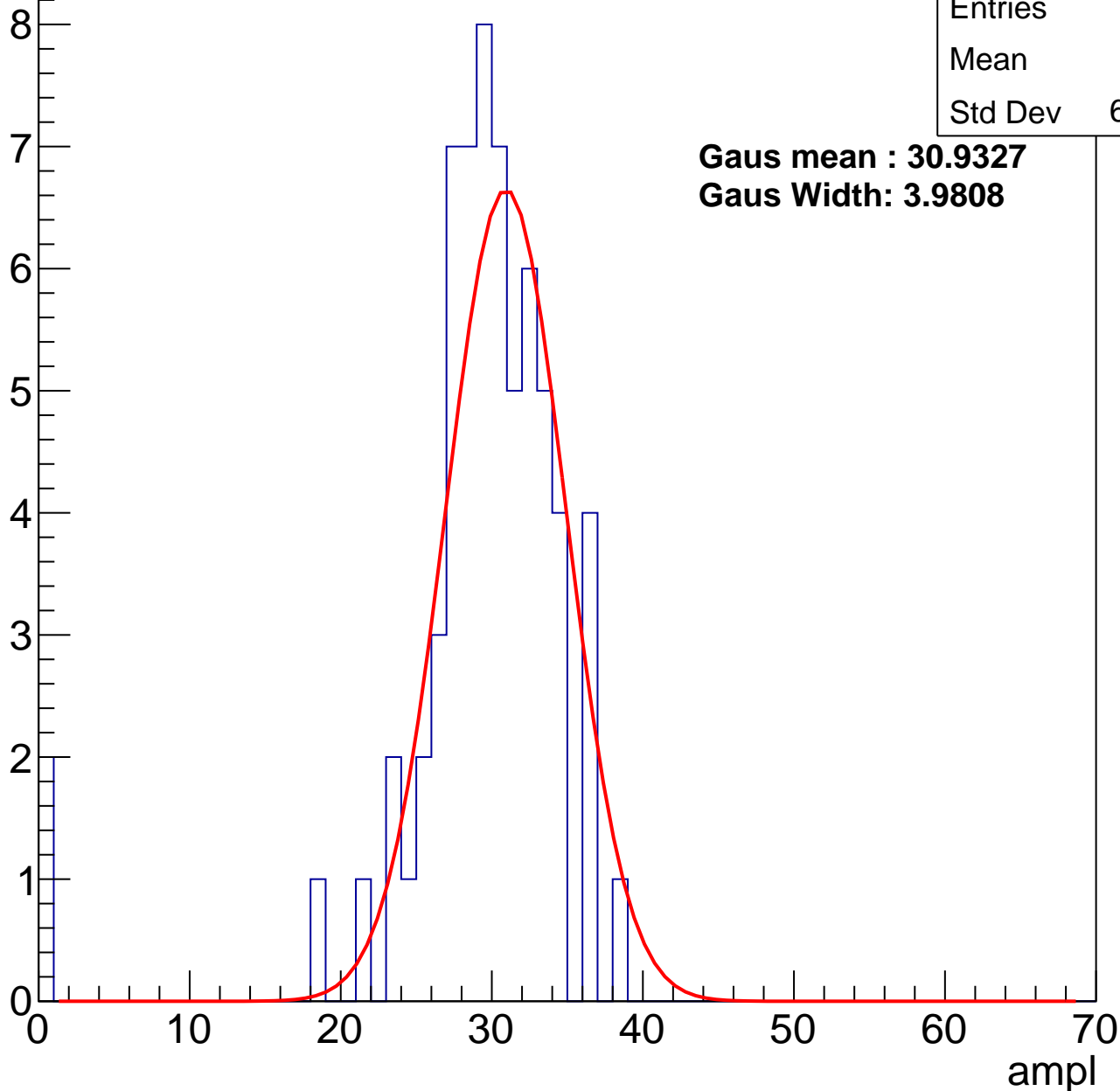
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.7
Std Dev	6.264

Gaus mean : 30.9327

Gaus Width: 3.9808



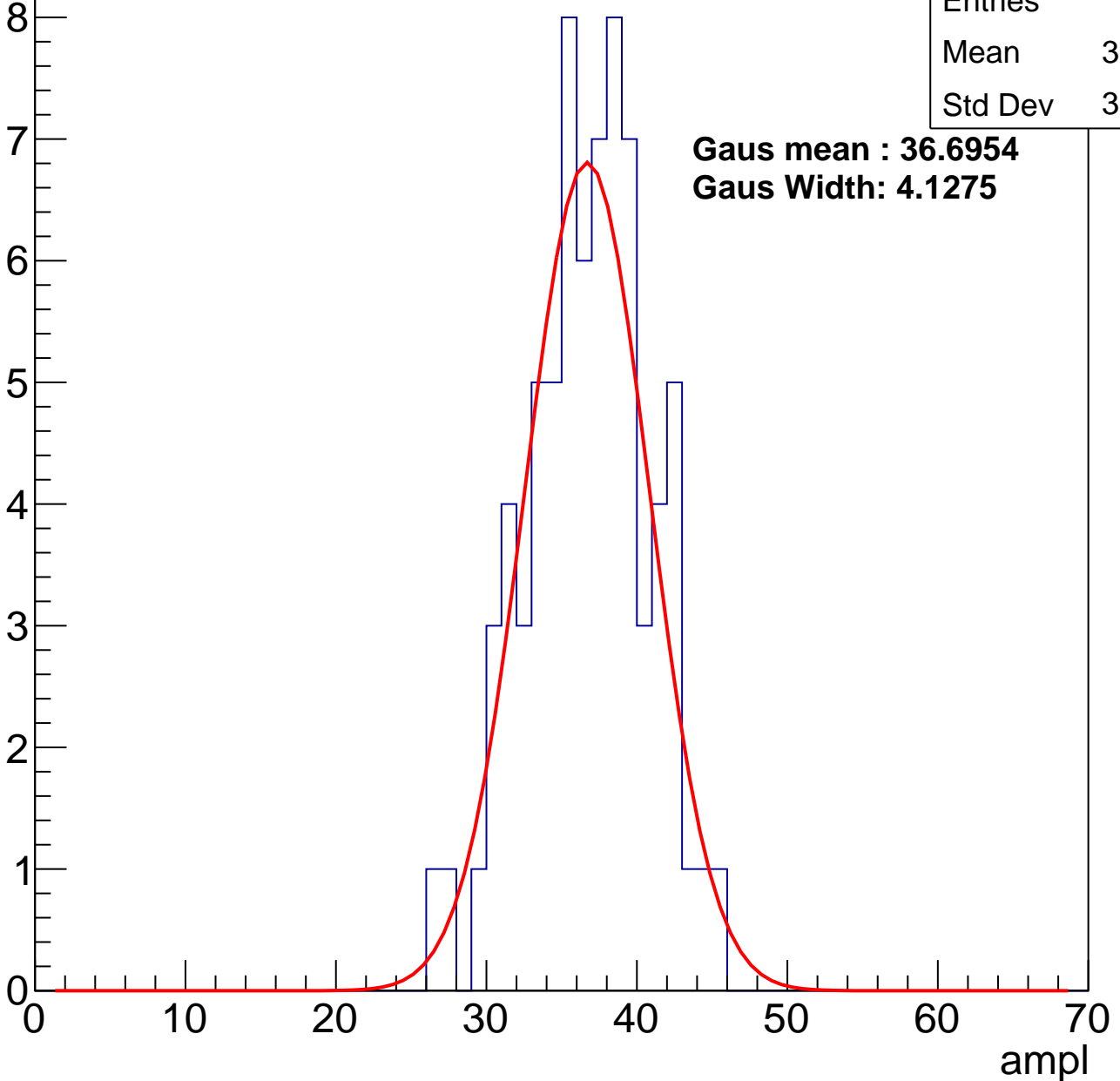
B1L103S, U14-ch91, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	36.28
Std Dev	3.988

Gaus mean : 36.6954
Gaus Width: 4.1275



B1L103S, U14-ch91, adc2

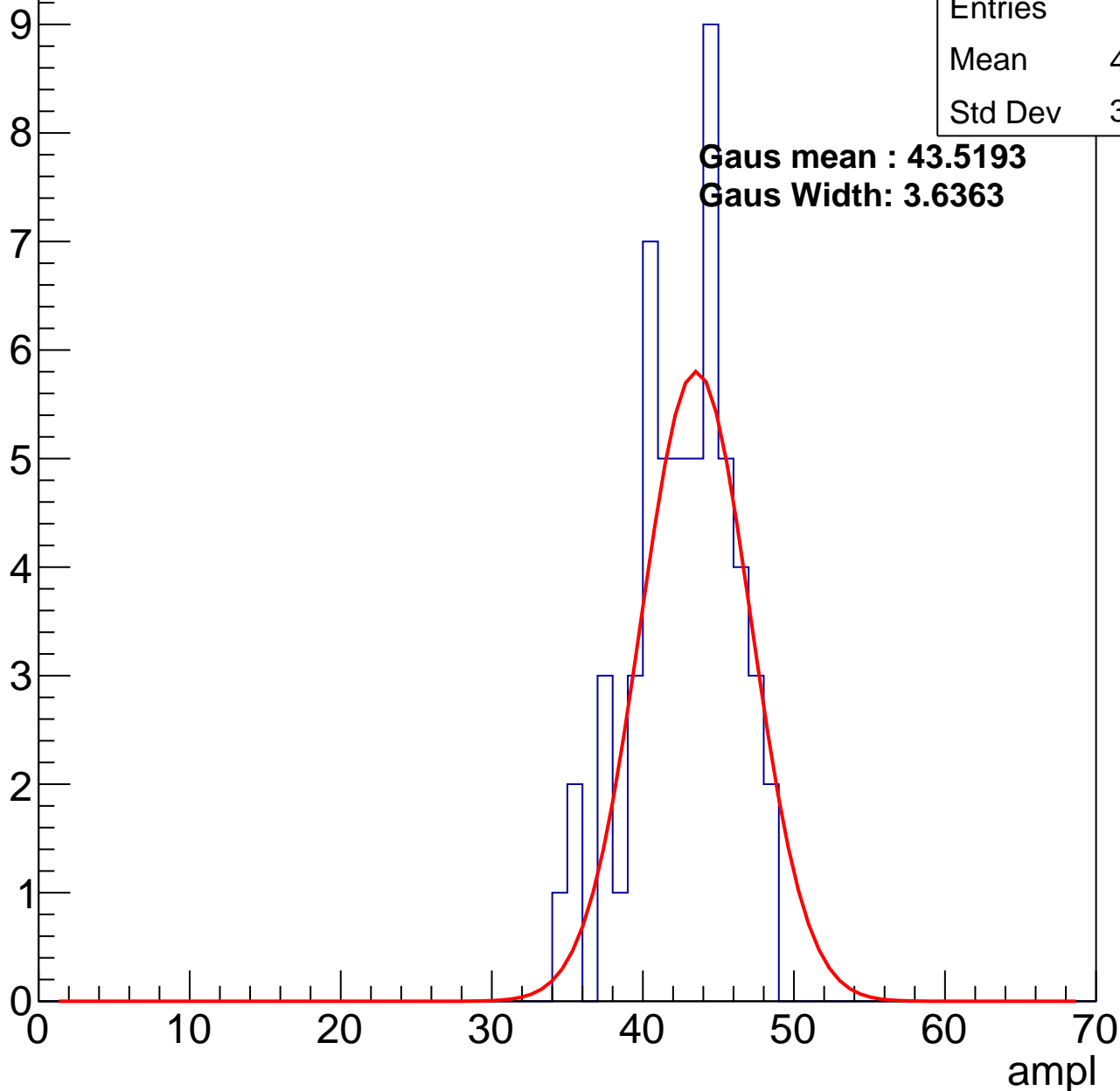
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	42.22
Std Dev	3.323

Gaus mean : 43.5193

Gaus Width: 3.6363

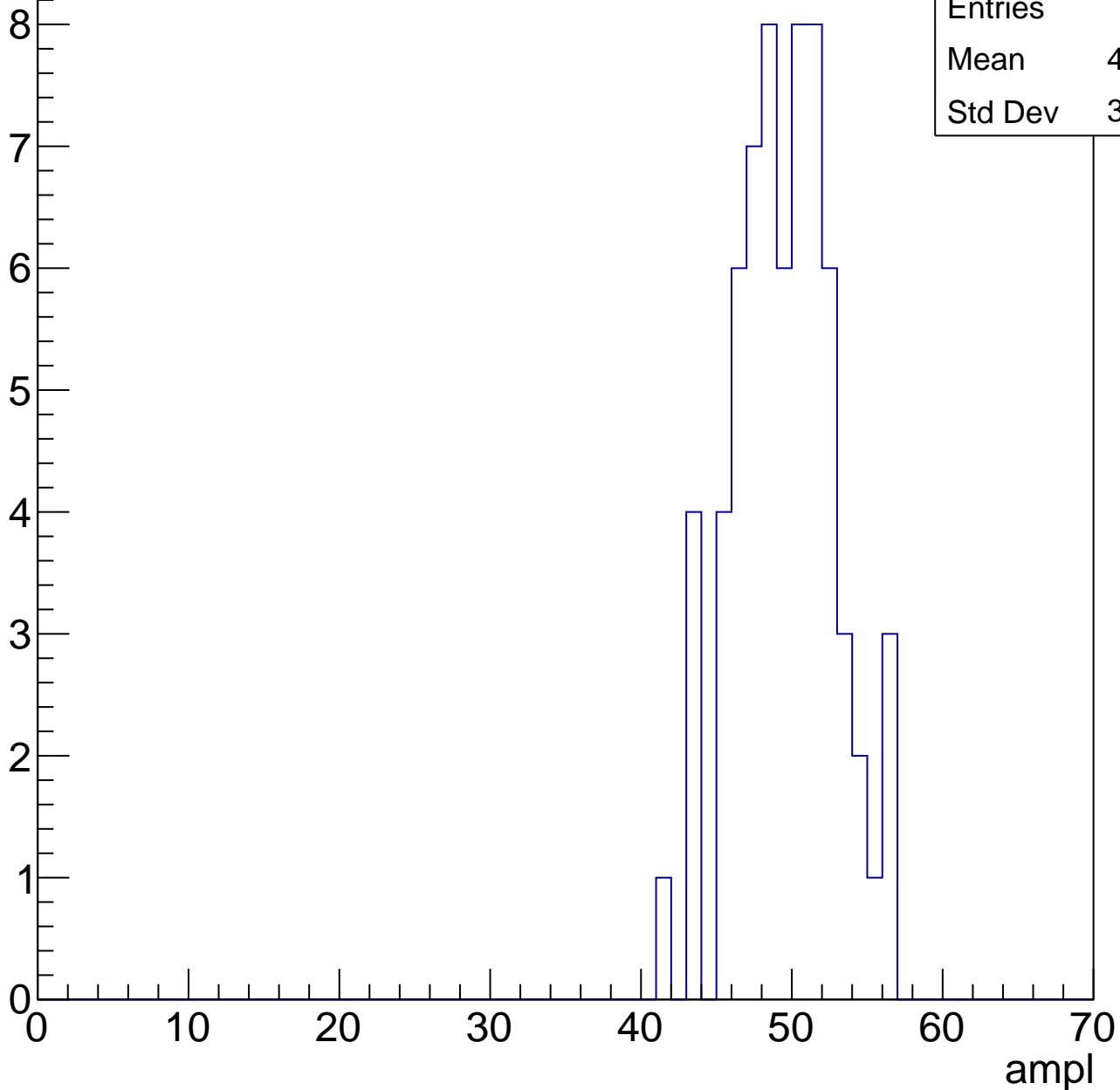


B1L103S, U14-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.04
Std Dev	3.316

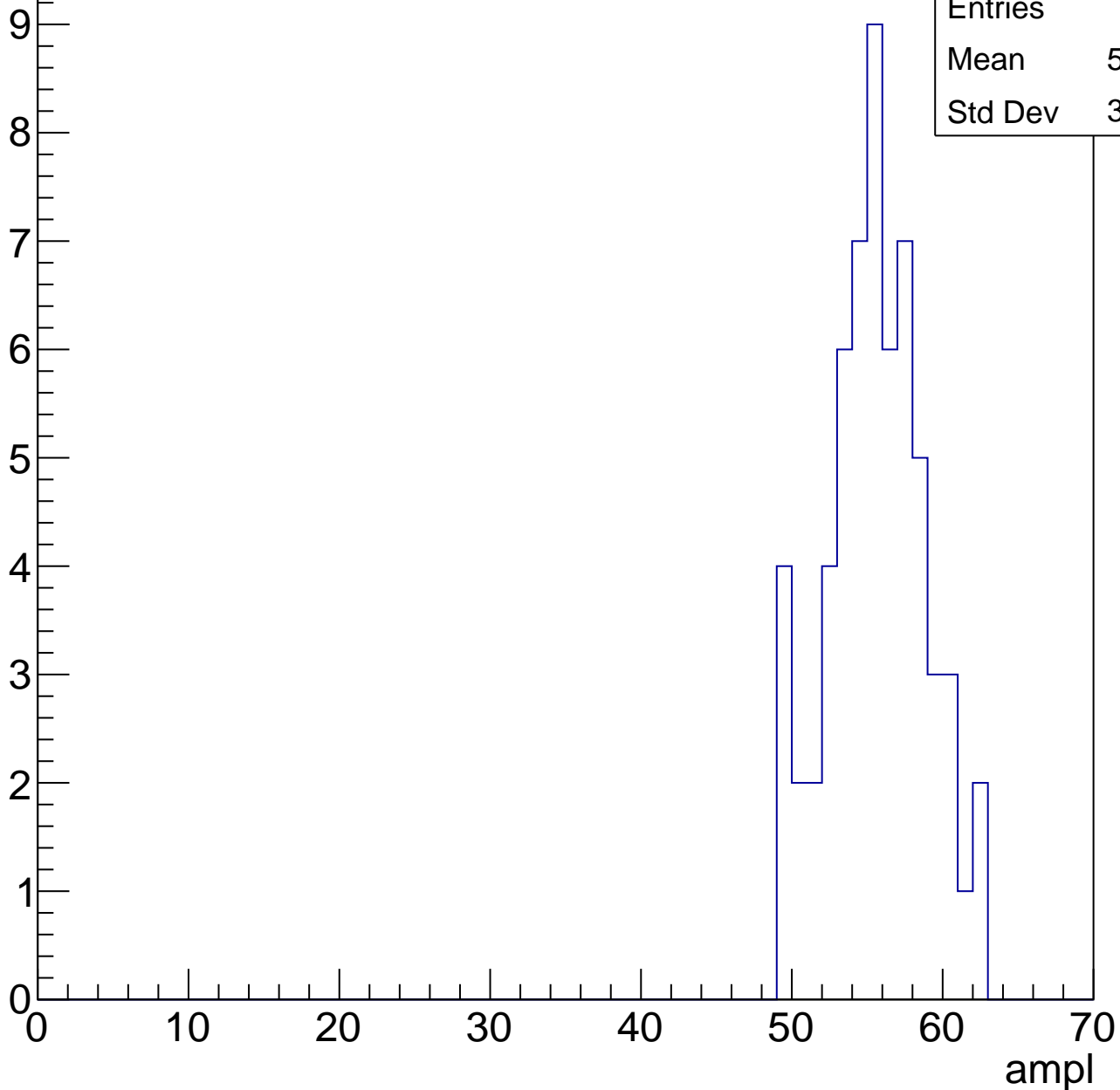


B1L103S, U14-ch91, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.15
Std Dev	3.208

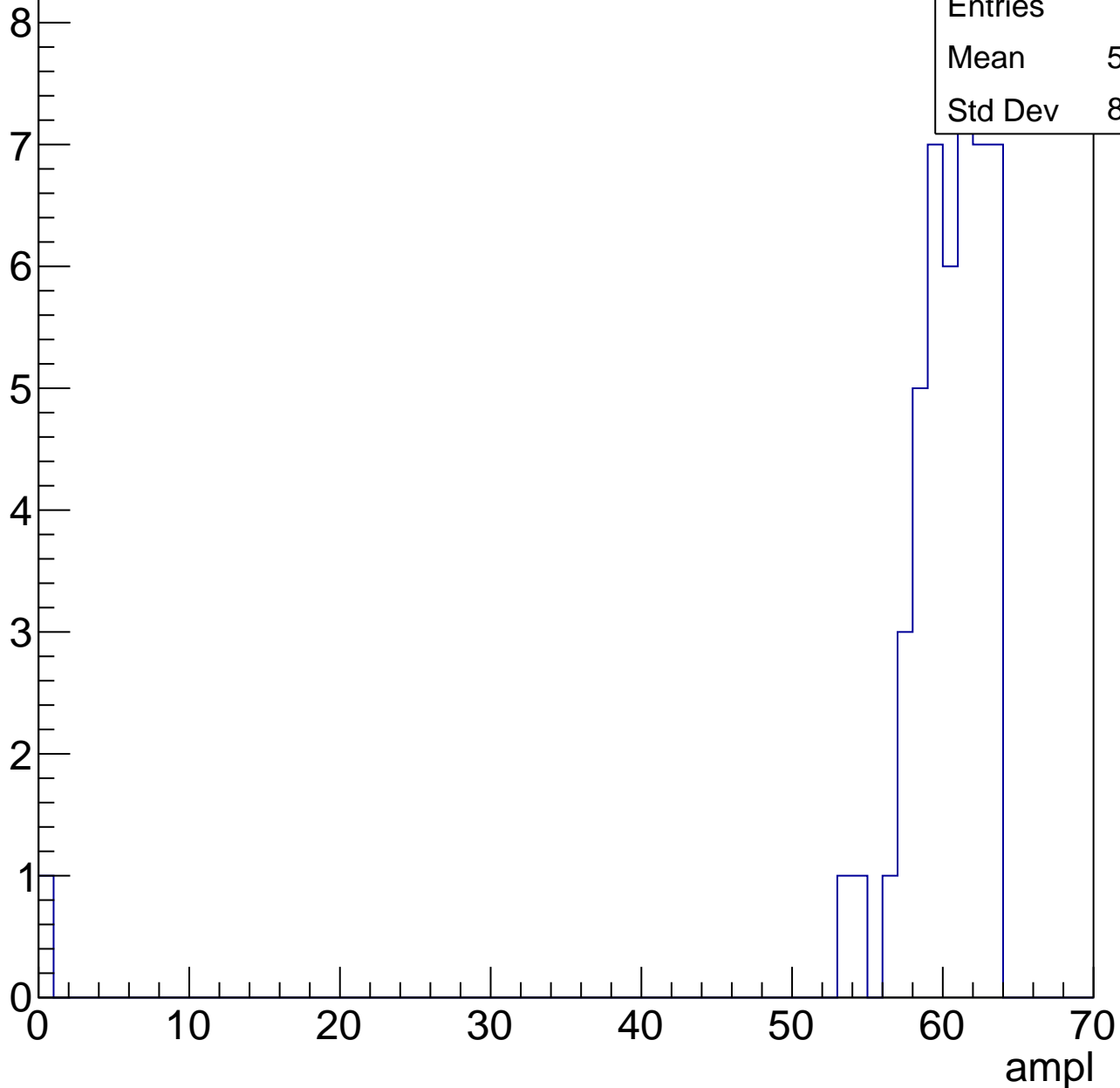


B1L103S, U14-ch91, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	58.72
Std Dev	8.965



B1L103S, U14-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch92, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	28.09
Std Dev	3.985

Gaus mean : 28.5079

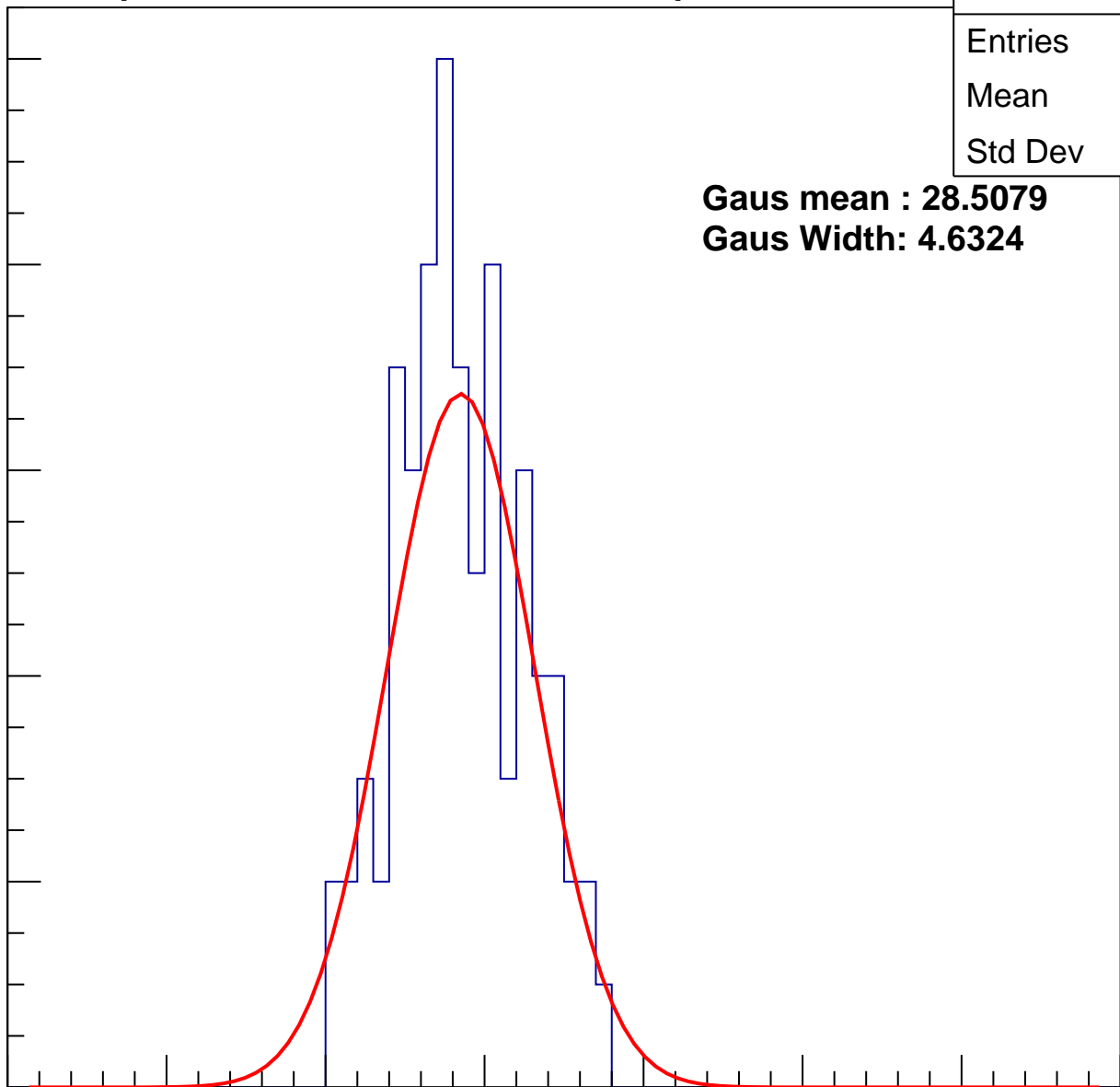
Gaus Width: 4.6324

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch92, adc1

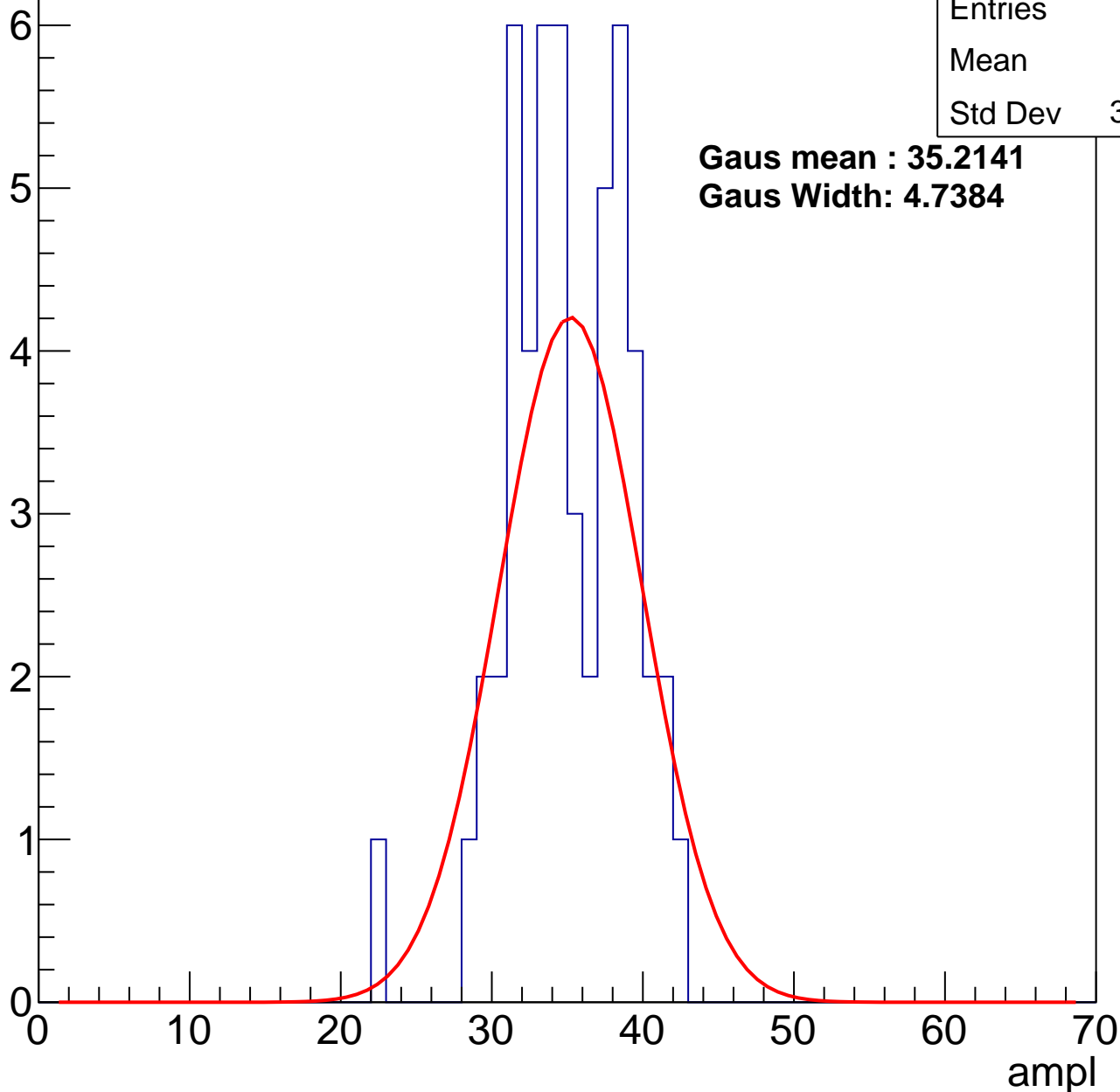
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	34.6
Std Dev	3.896

Gaus mean : 35.2141

Gaus Width: 4.7384



B1L103S, U14-ch92, adc2

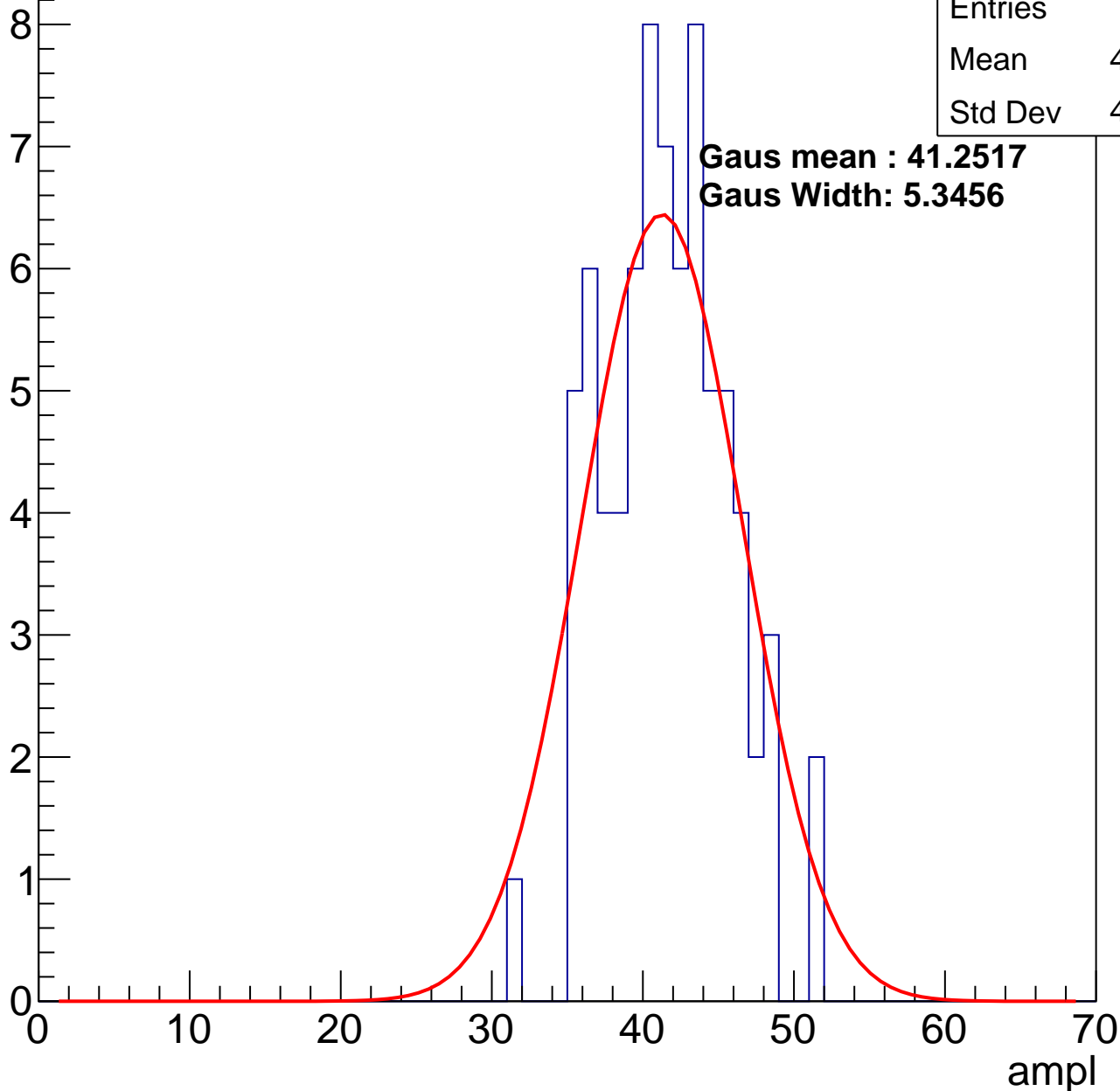
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	41.16
Std Dev	4.043

Gaus mean : 41.2517

Gaus Width: 5.3456

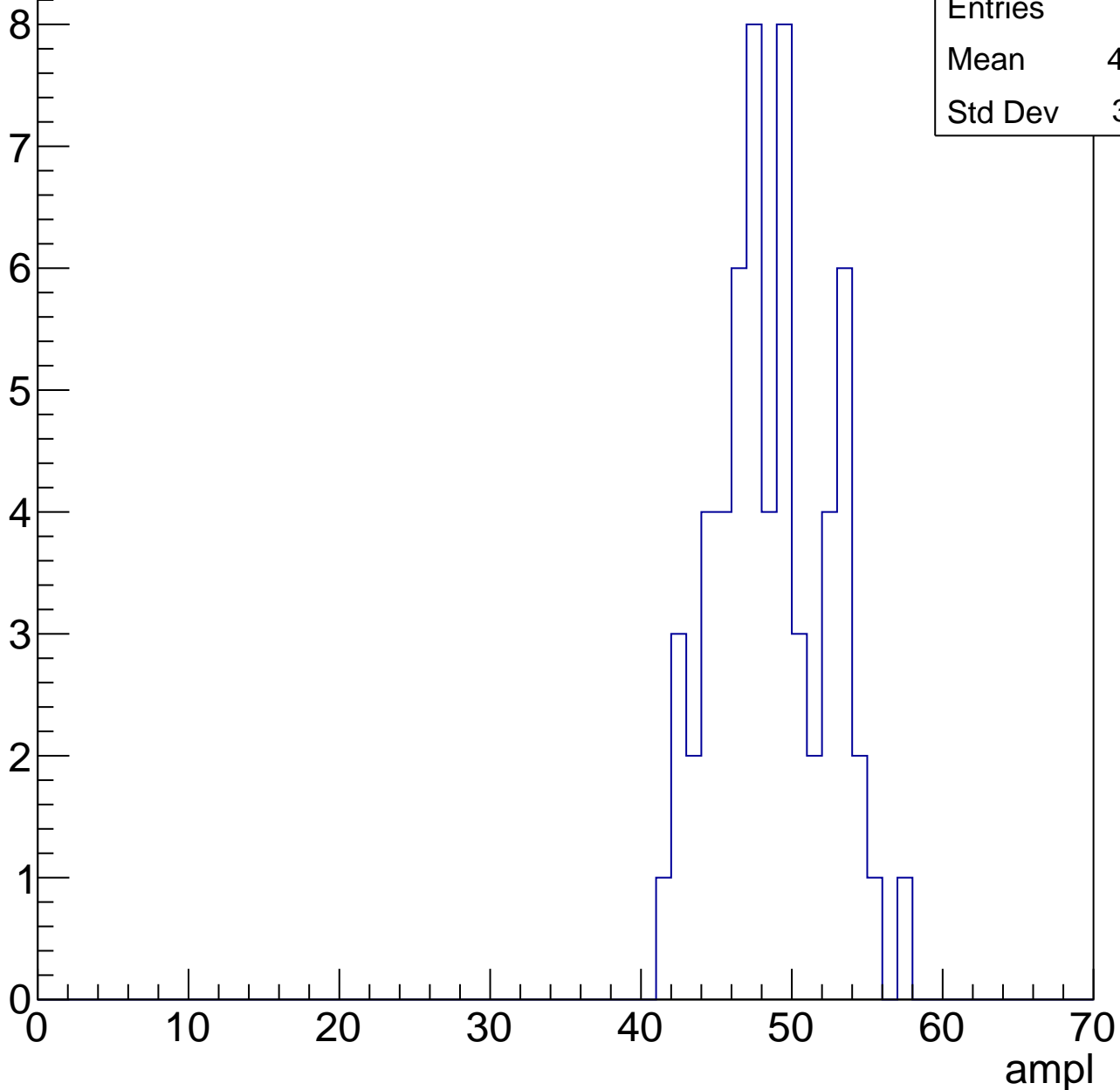


B1L103S, U14-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	48.19
Std Dev	3.661

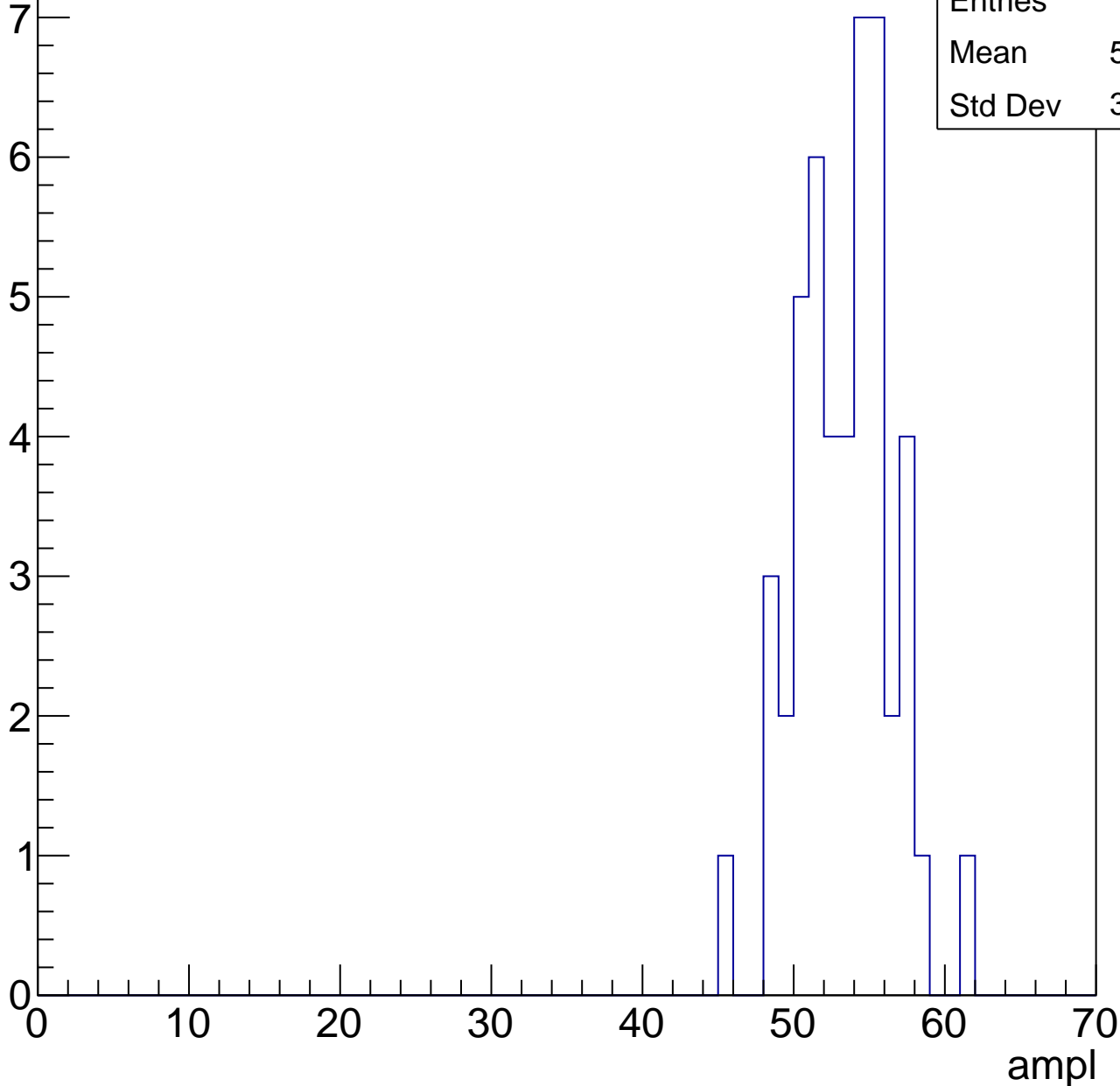


B1L103S, U14-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	52.87
Std Dev	3.092

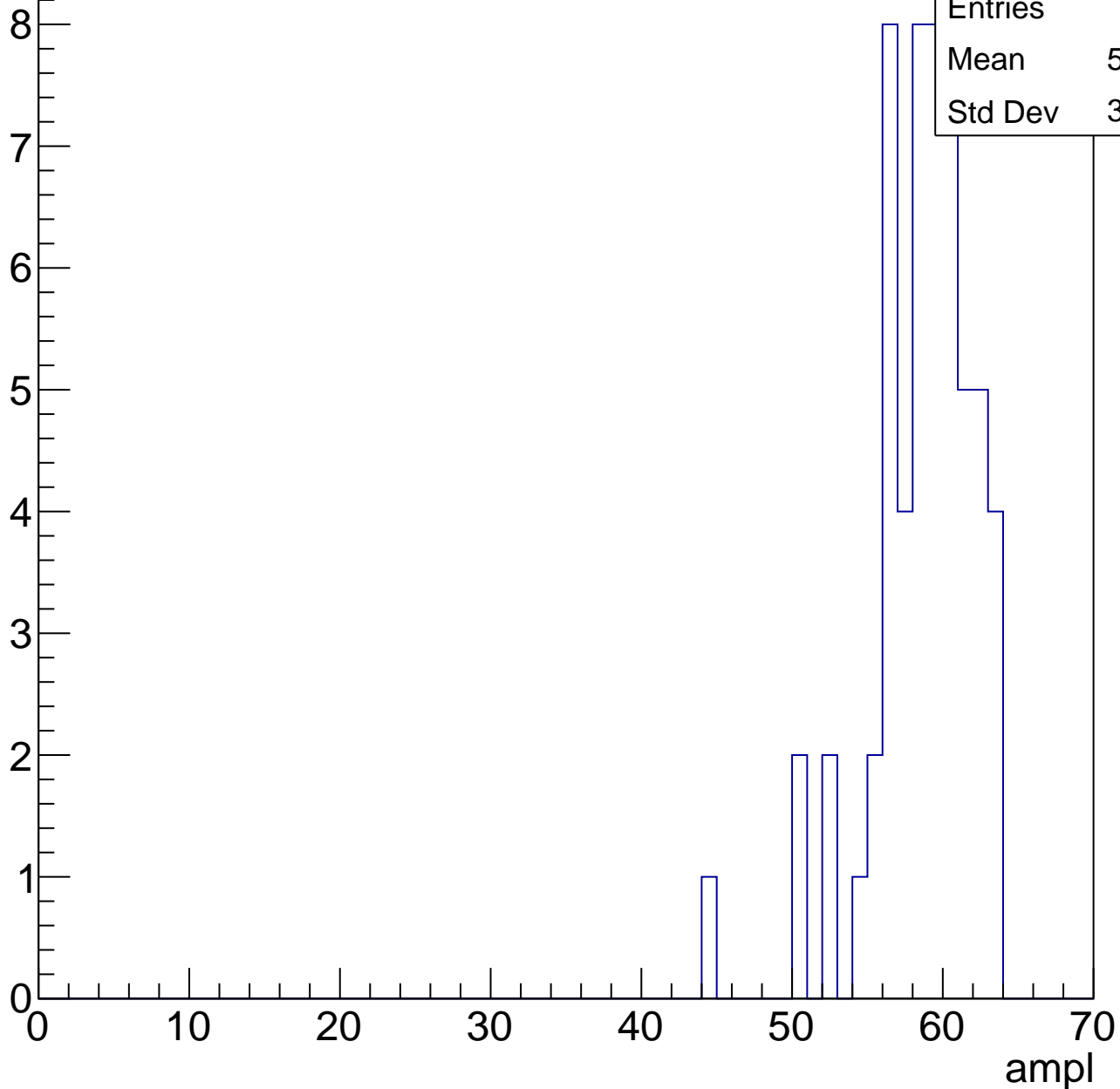


B1L103S, U14-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	58.12
Std Dev	3.548

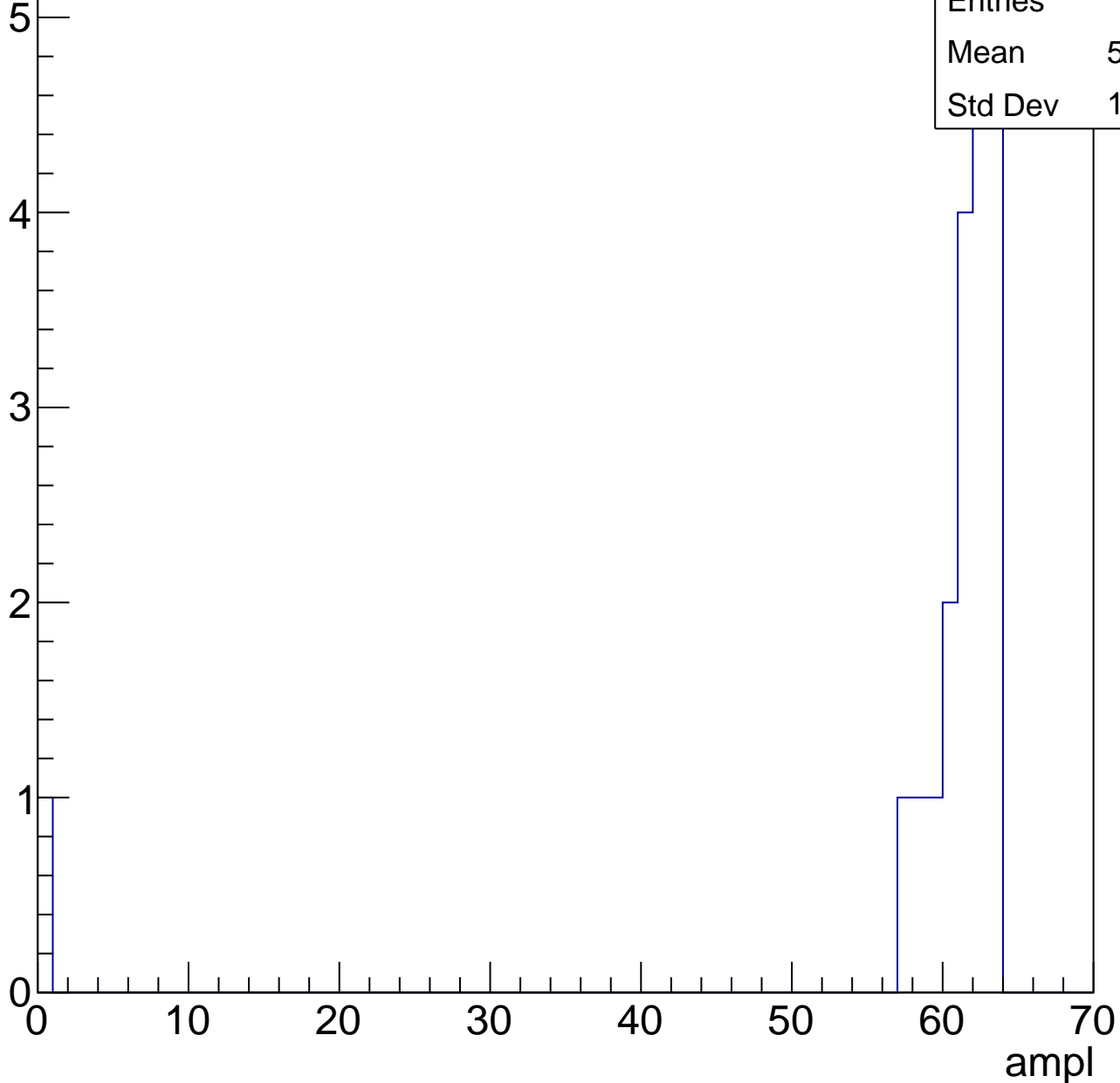


B1L103S, U14-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

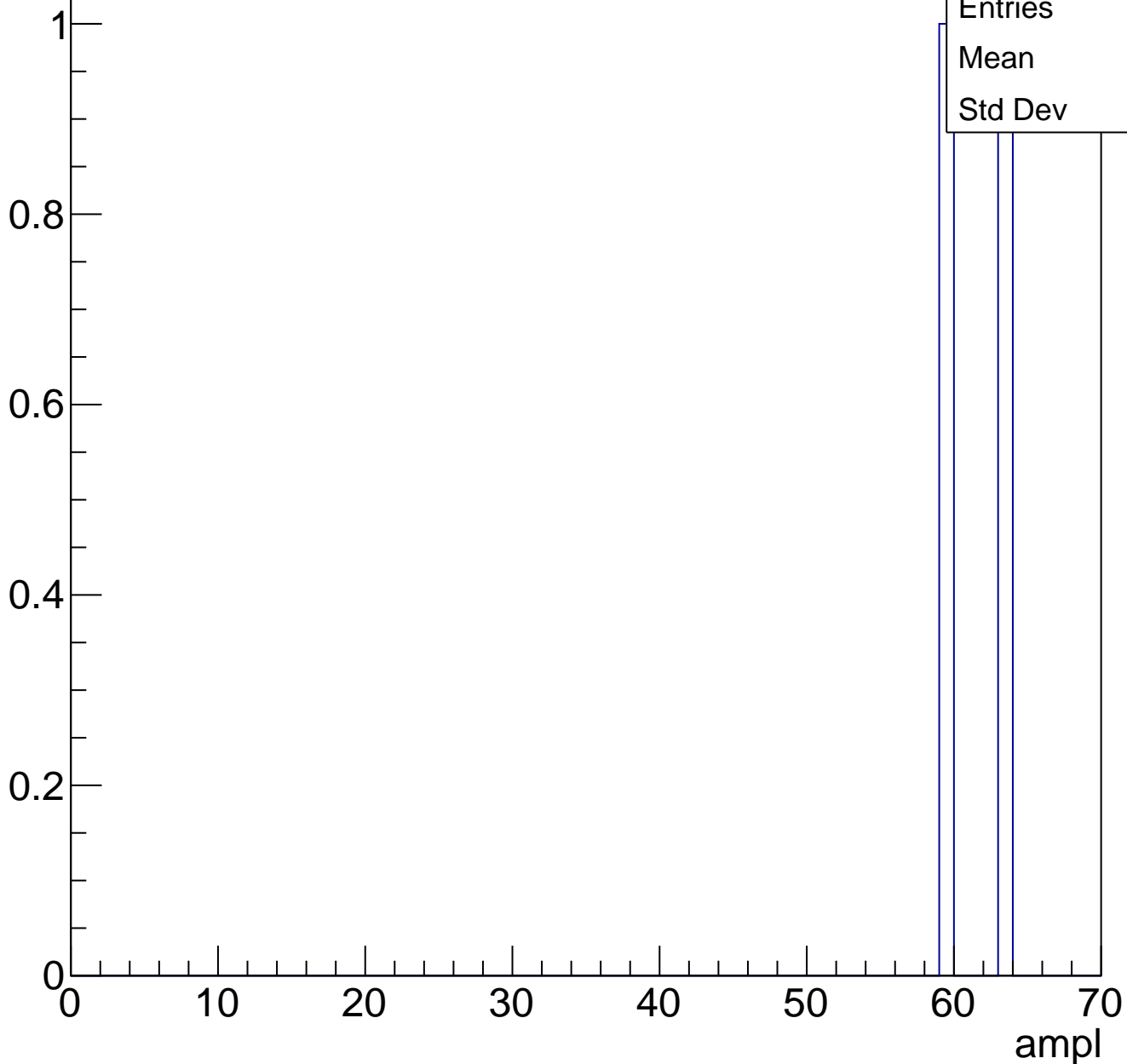
Entries	20
Mean	58.15
Std Dev	13.44



B1L103S, U14-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch93, adc0

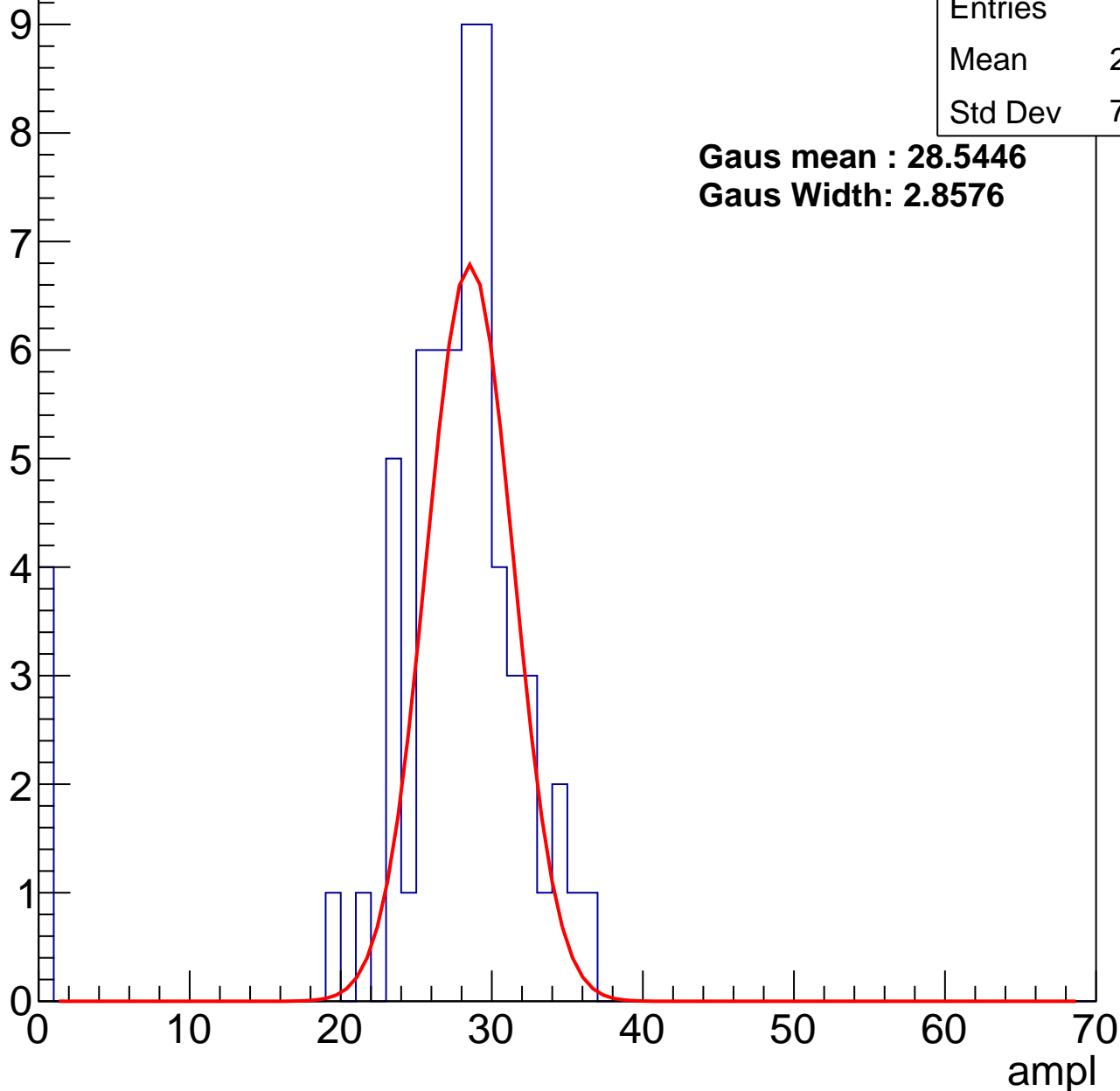
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	26.05
Std Dev	7.518

Gaus mean : 28.5446

Gaus Width: 2.8576



B1L103S, U14-ch93, adc1

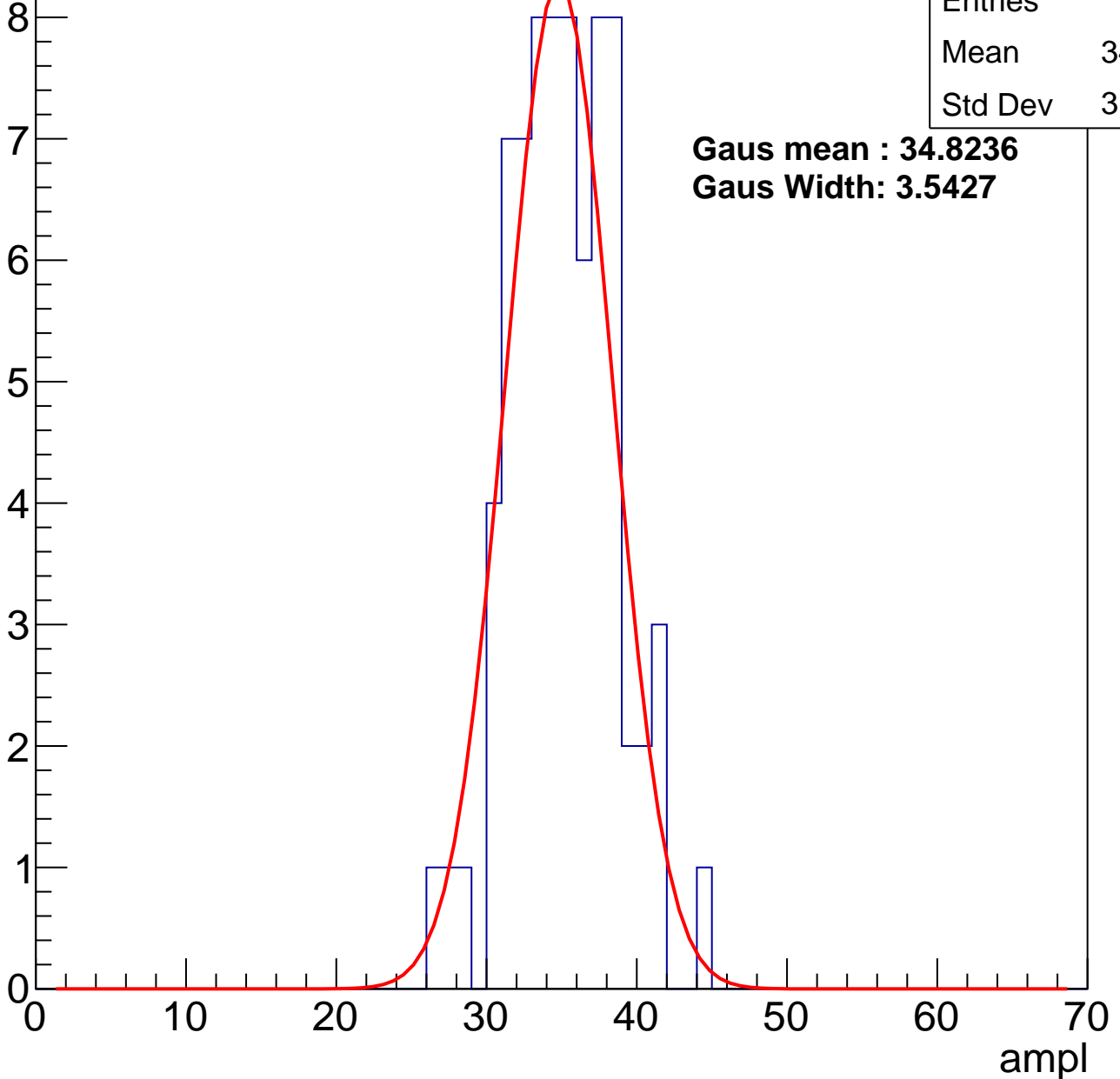
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	34.65
Std Dev	3.427

Gaus mean : 34.8236

Gaus Width: 3.5427



B1L103S, U14-ch93, adc2

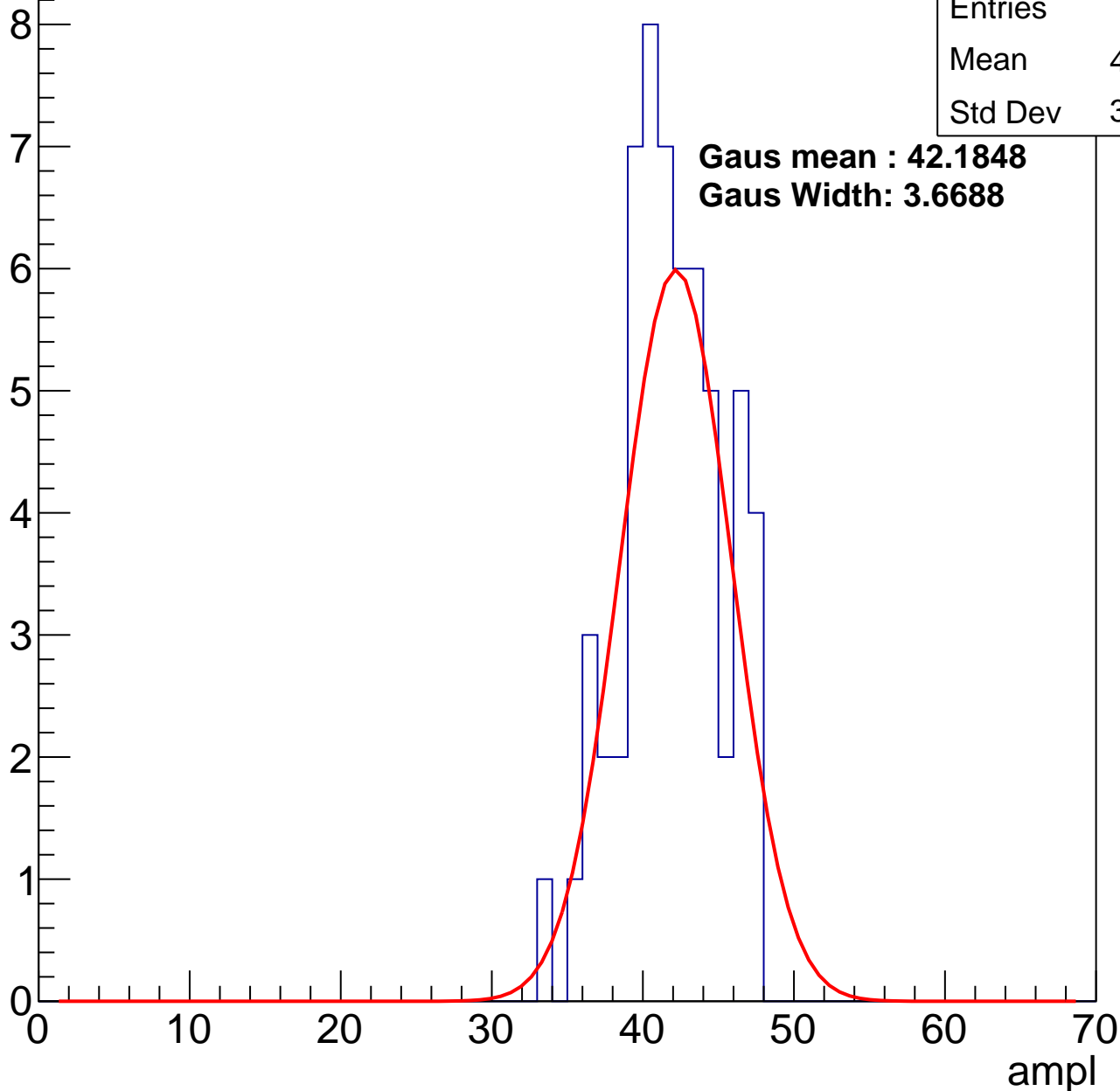
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	41.42
Std Dev	3.258

Gaus mean : 42.1848

Gaus Width: 3.6688

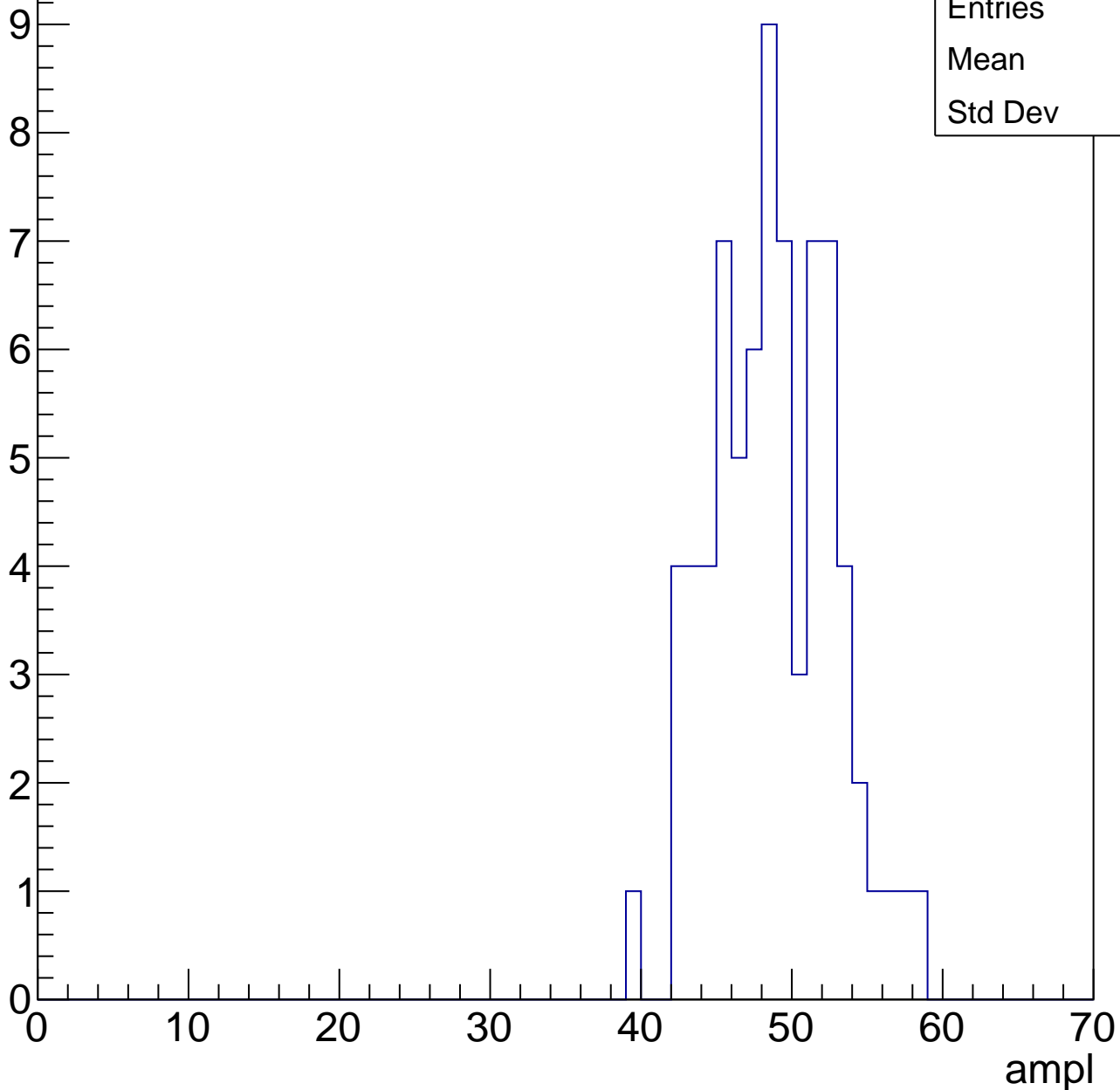


B1L103S, U14-ch93, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	48.3
Std Dev	3.91

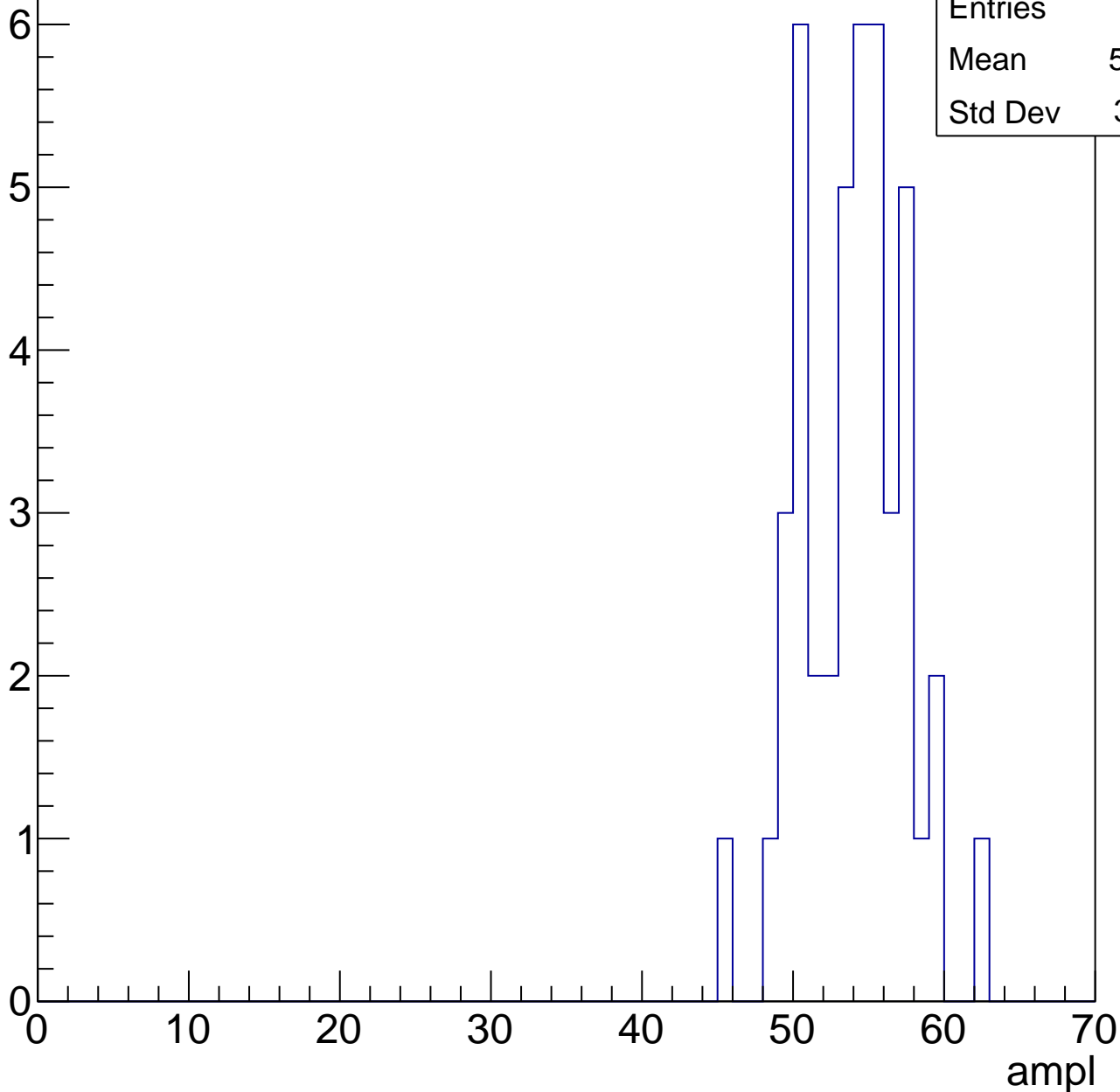


B1L103S, U14-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	53.55
Std Dev	3.381

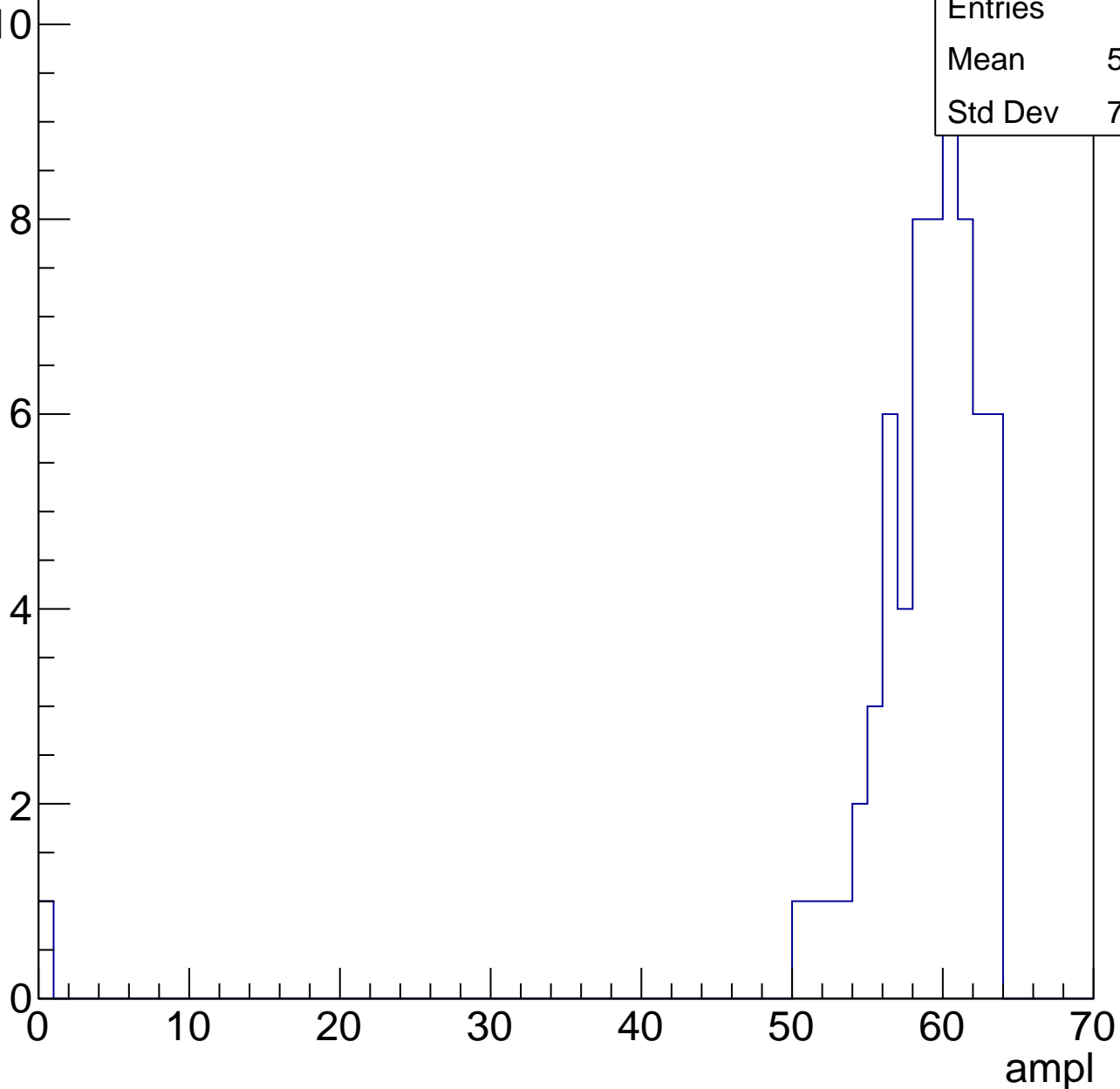


B1L103S, U14-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	57.83
Std Dev	7.774



B1L103S, U14-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch94, adc0

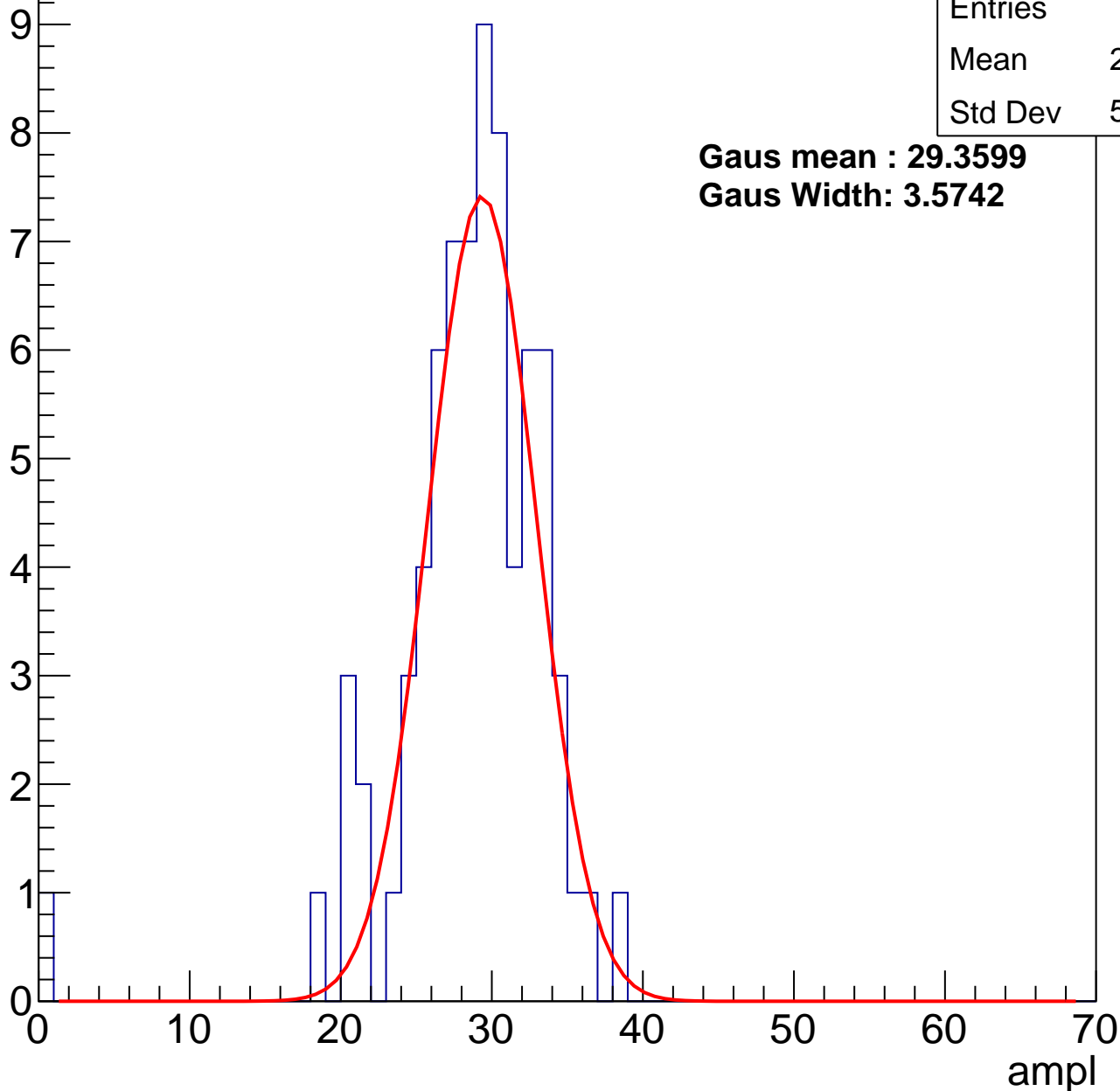
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.14
Std Dev	5.142

Gaus mean : 29.3599

Gaus Width: 3.5742



B1L103S, U14-ch94, adc1

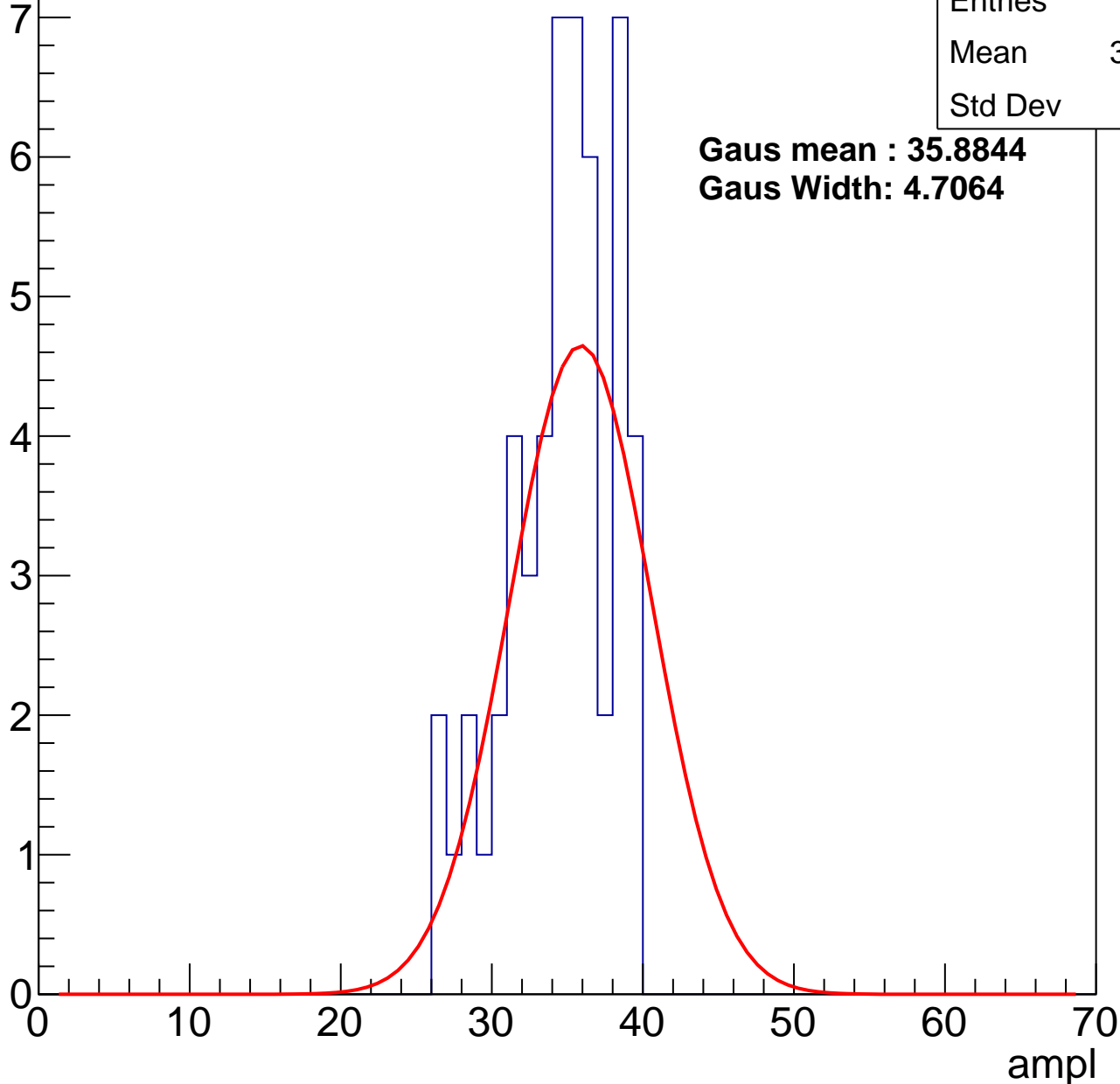
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	34.06
Std Dev	3.45

Gaus mean : 35.8844

Gaus Width: 4.7064



B1L103S, U14-ch94, adc2

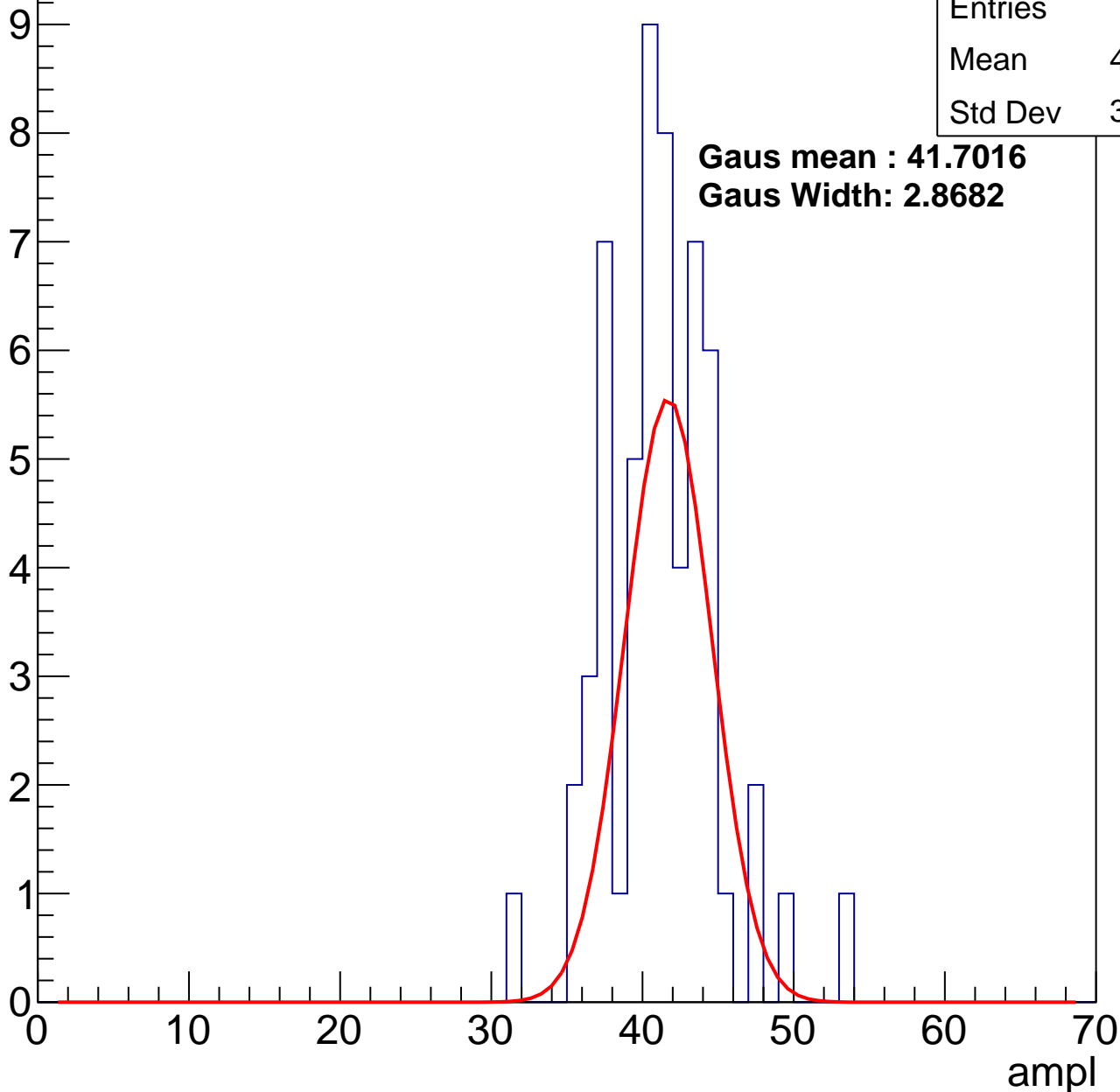
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	40.74
Std Dev	3.656

Gaus mean : 41.7016

Gaus Width: 2.8682



B1L103S, U14-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

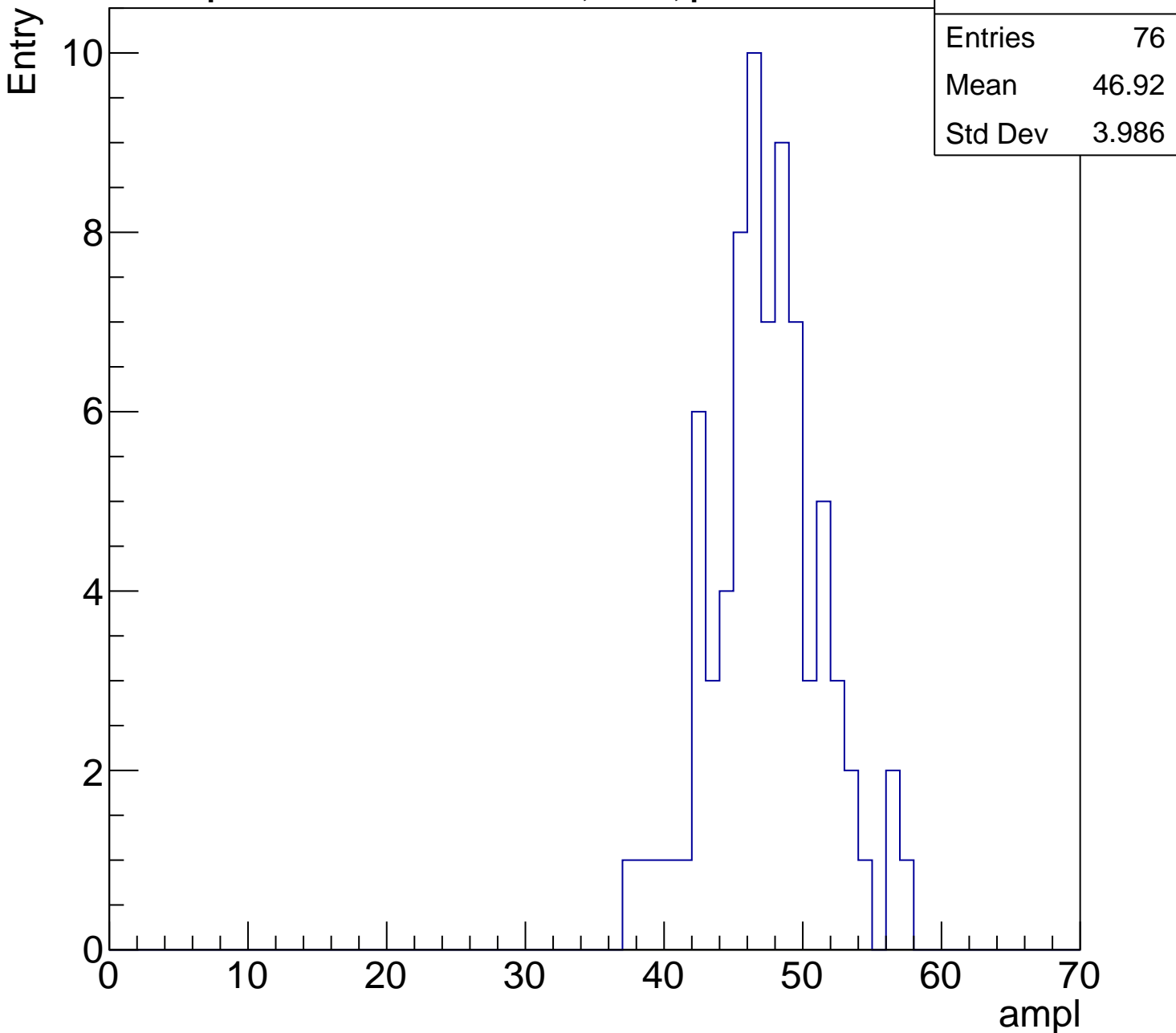
Entries	76
Mean	46.92
Std Dev	3.986

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

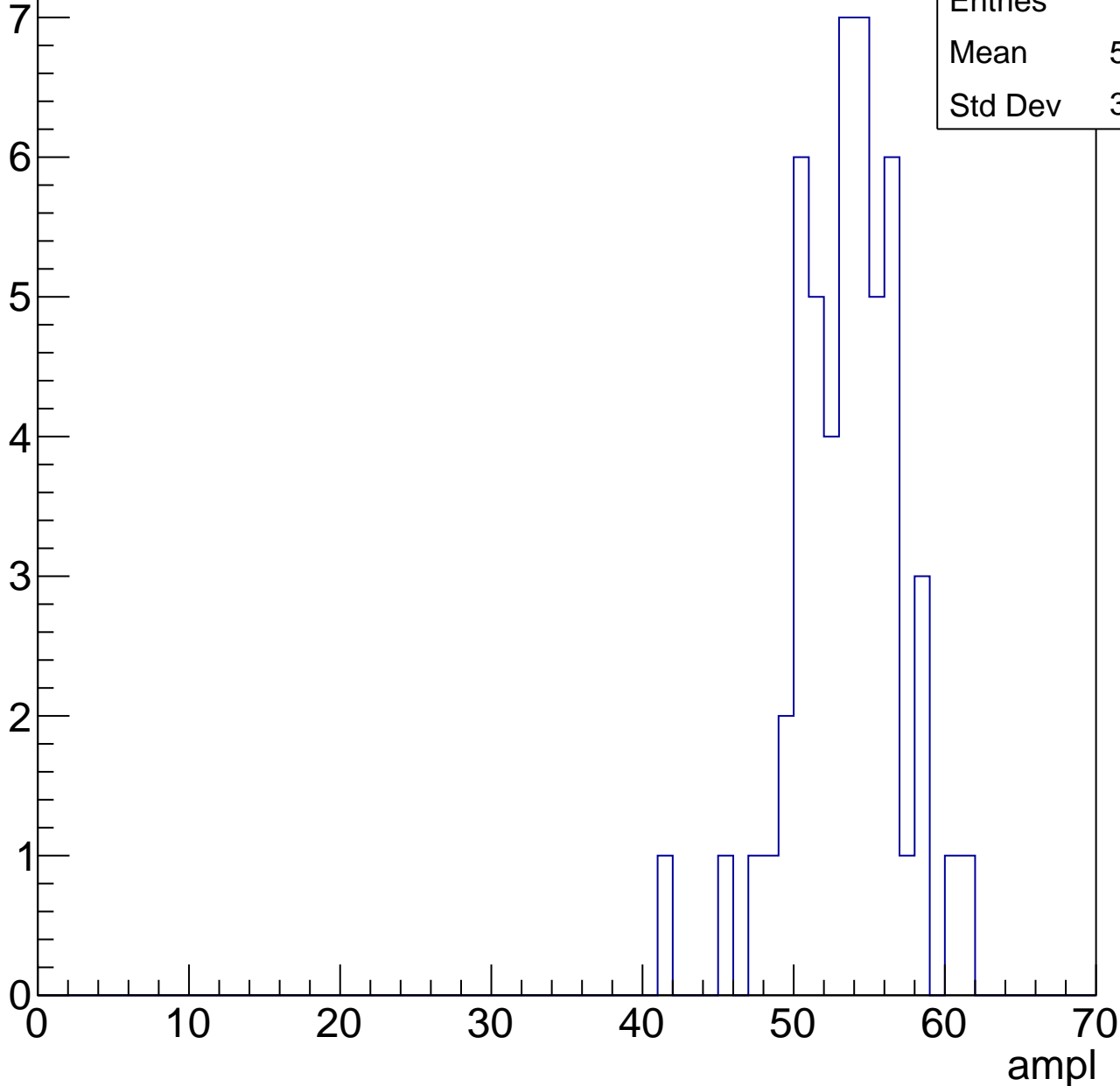


B1L103S, U14-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	52.96
Std Dev	3.573

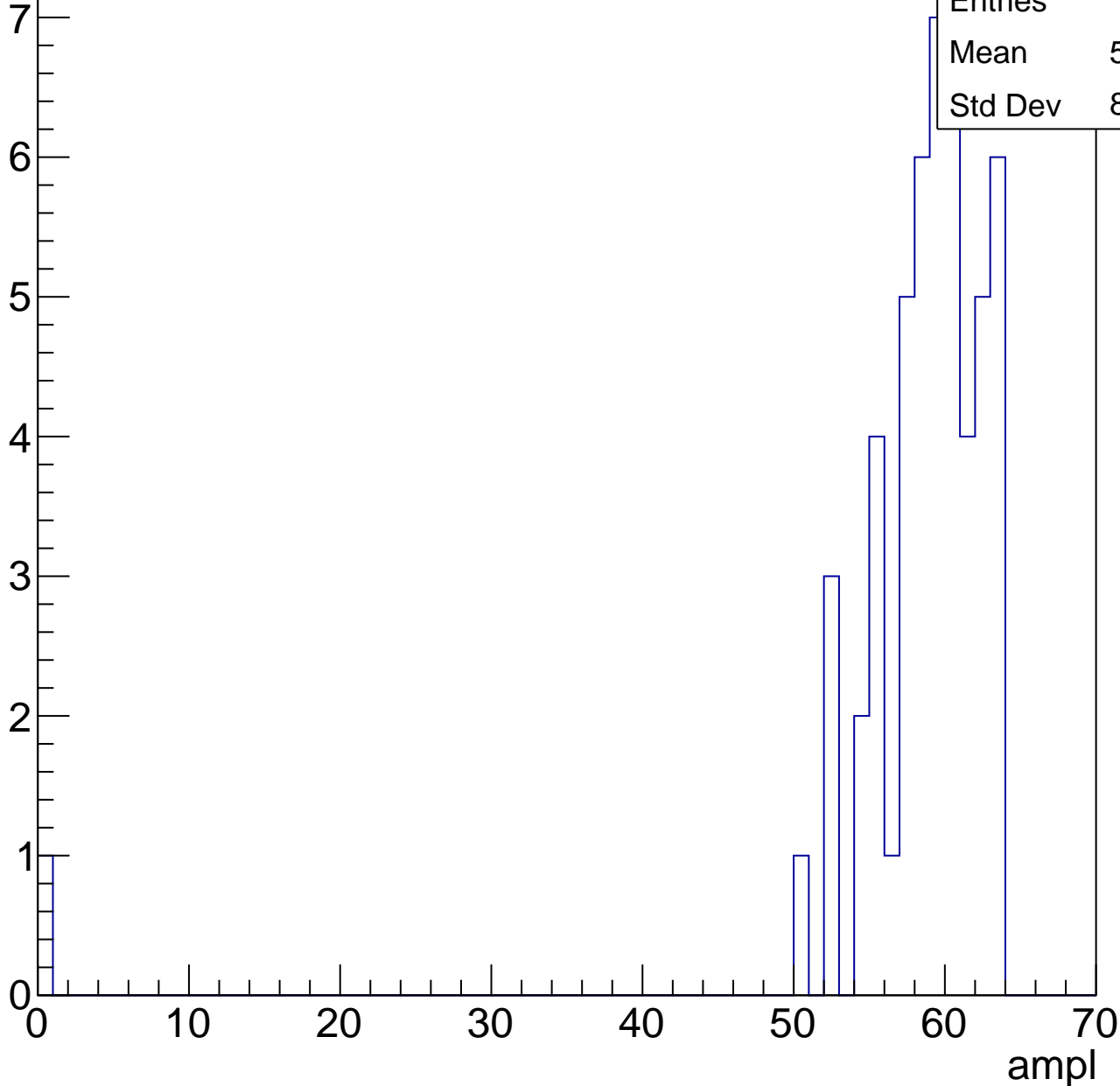


B1L103S, U14-ch94, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	57.46
Std Dev	8.659

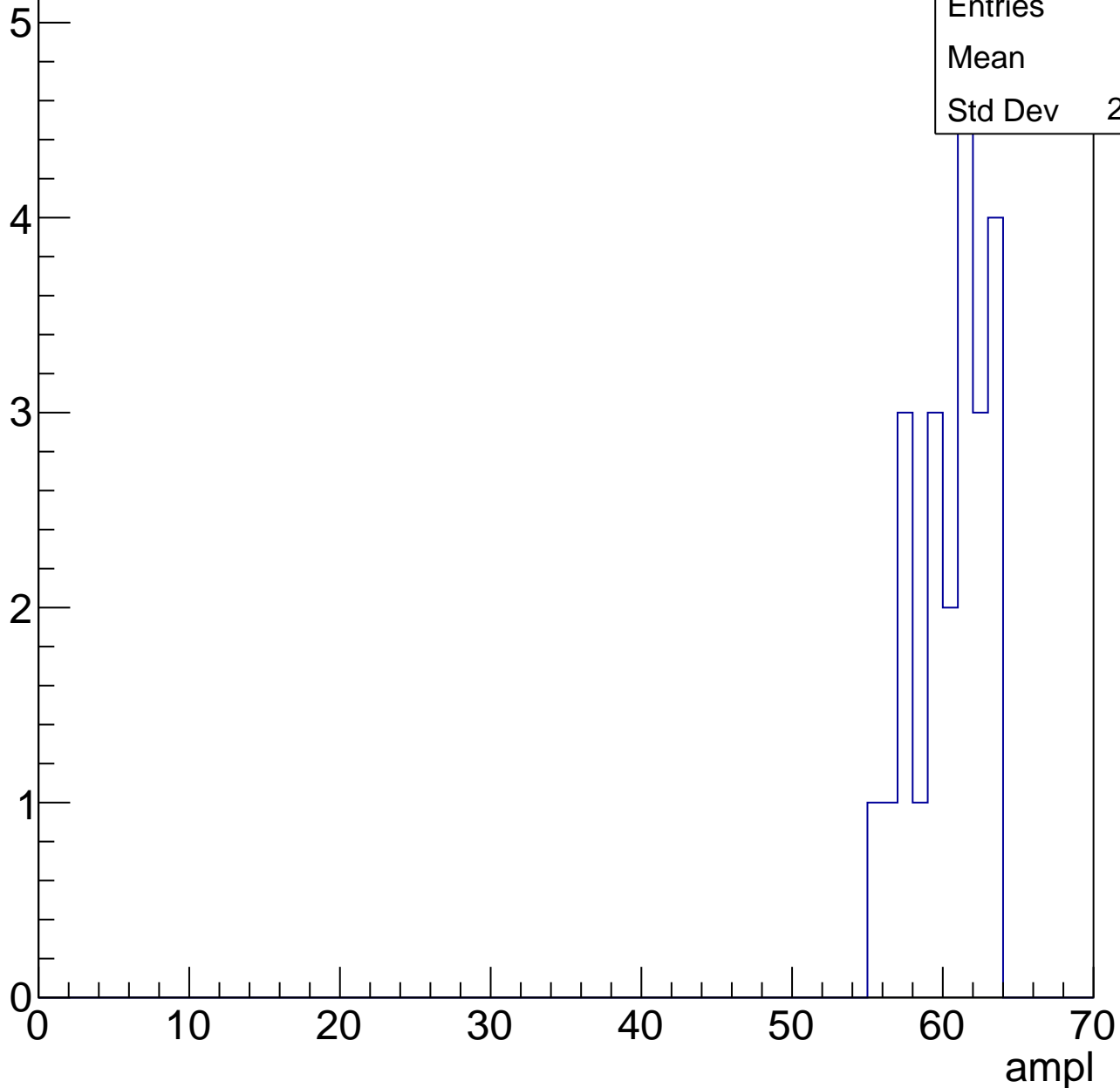


B1L103S, U14-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

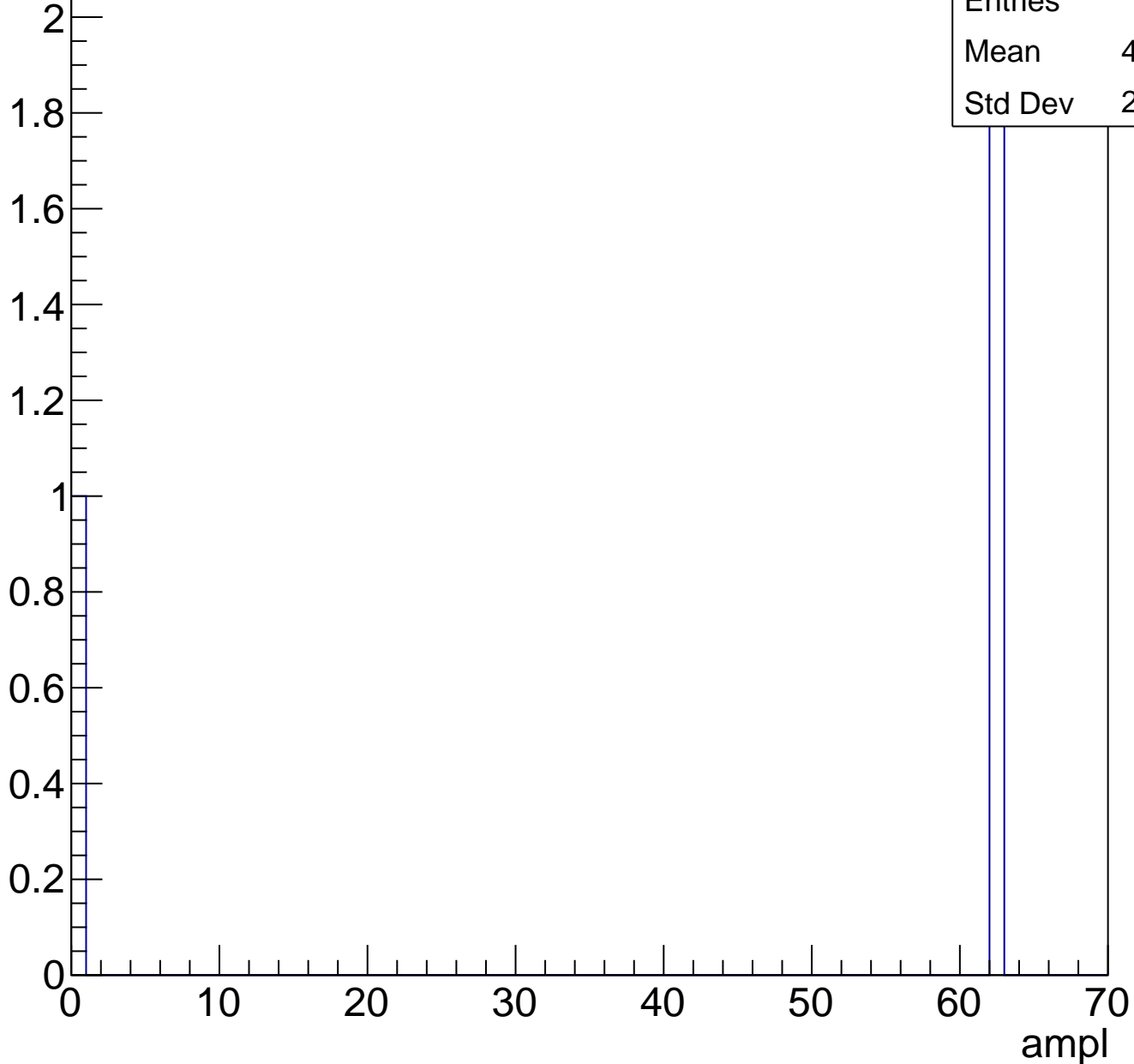
Entries	23
Mean	60
Std Dev	2.359



B1L103S, U14-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch95, adc0

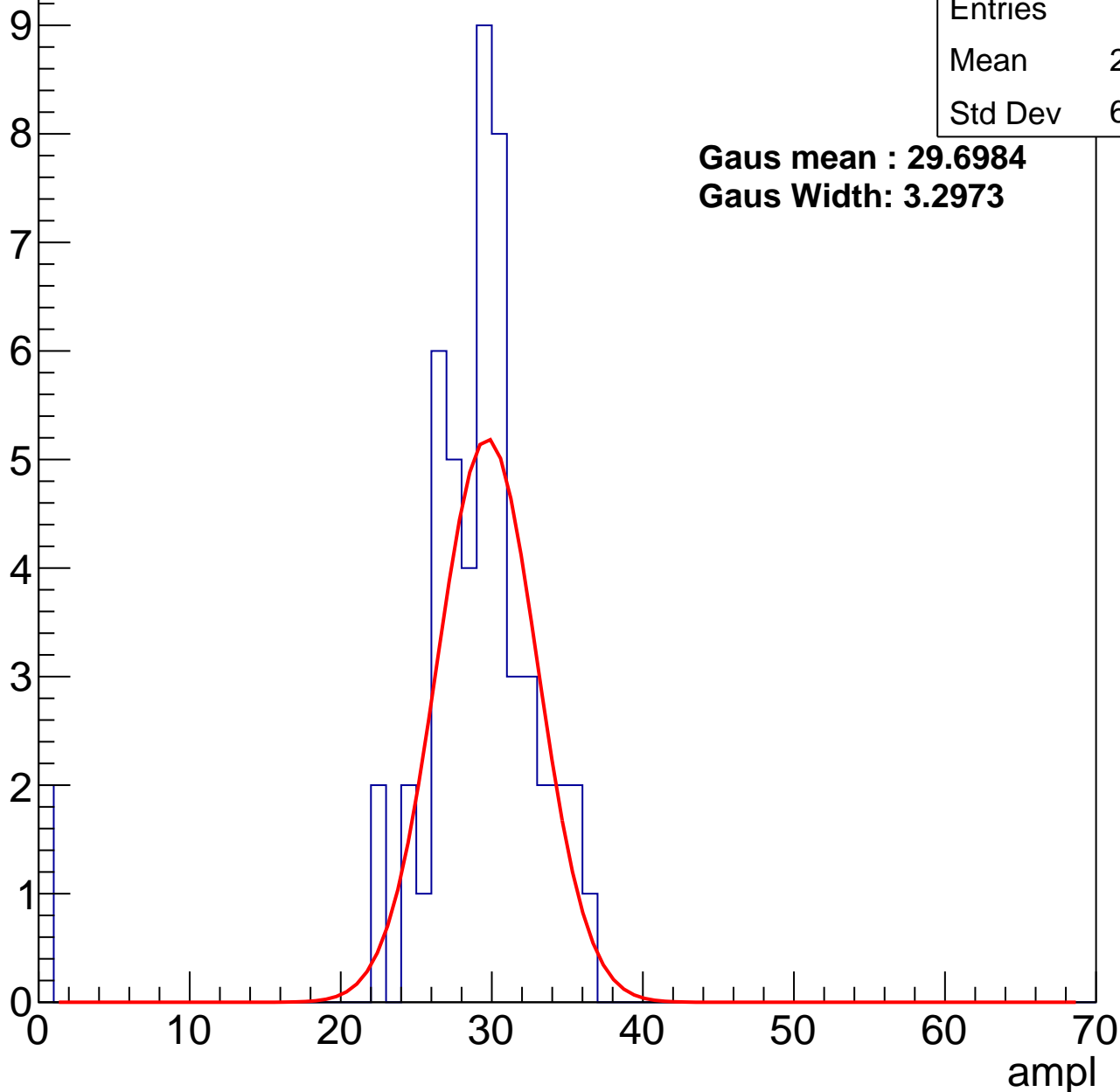
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	27.88
Std Dev	6.369

Gaus mean : 29.6984

Gaus Width: 3.2973



B1L103S, U14-ch95, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	35.81
Std Dev	3.383

Gaus mean : 35.9866

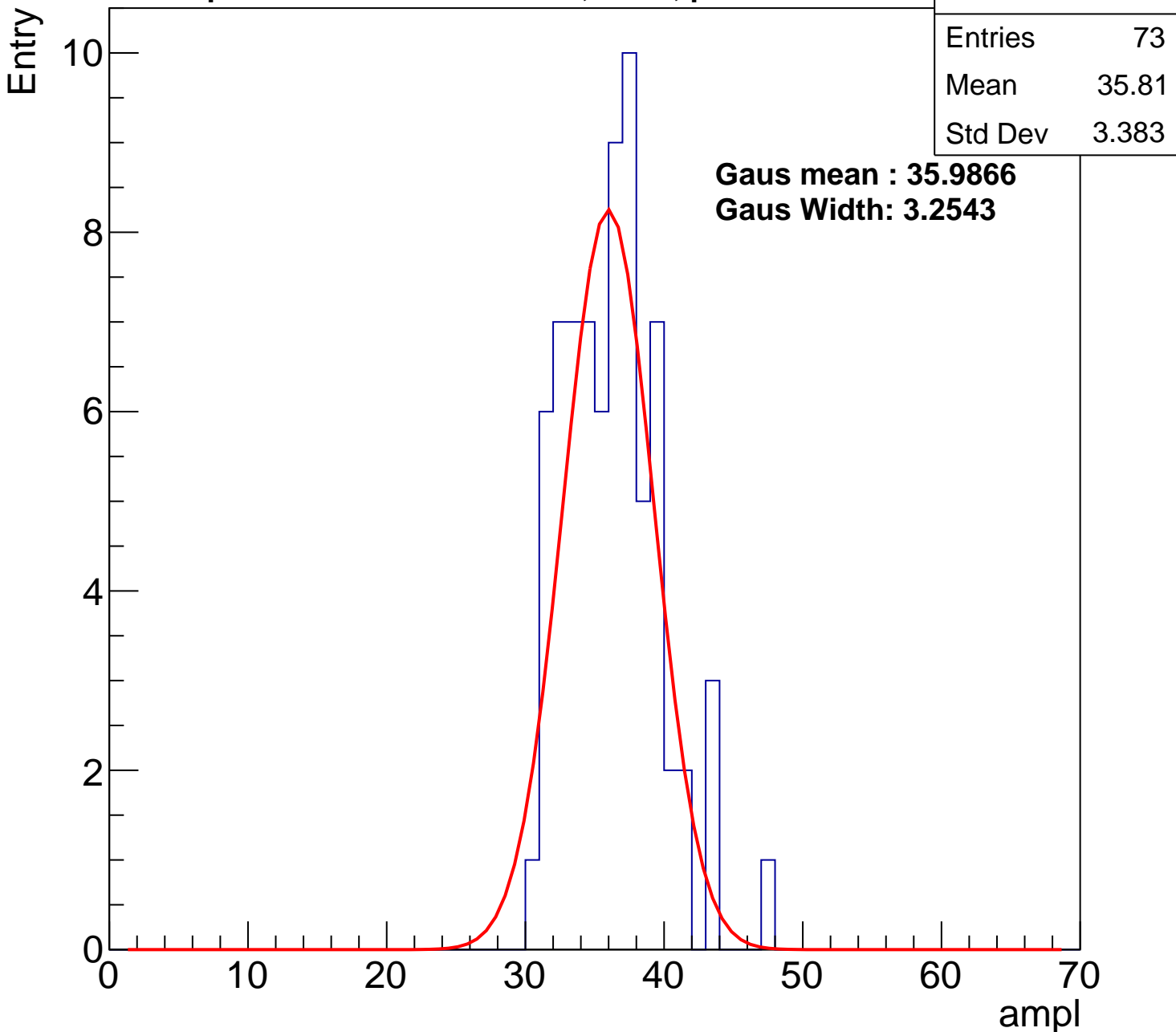
Gaus Width: 3.2543

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U14-ch95, adc2

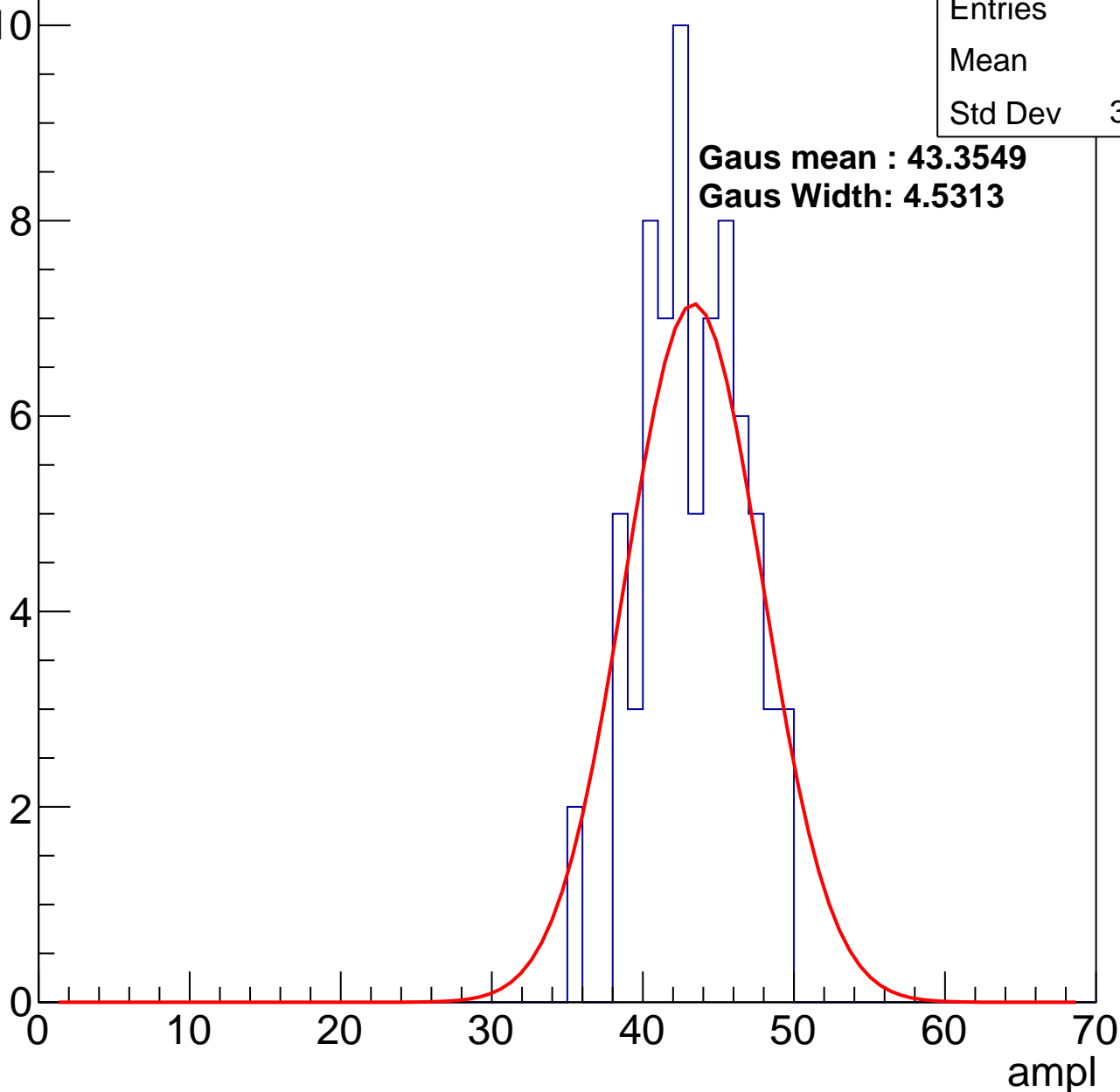
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.9
Std Dev	3.262

Gaus mean : 43.3549

Gaus Width: 4.5313

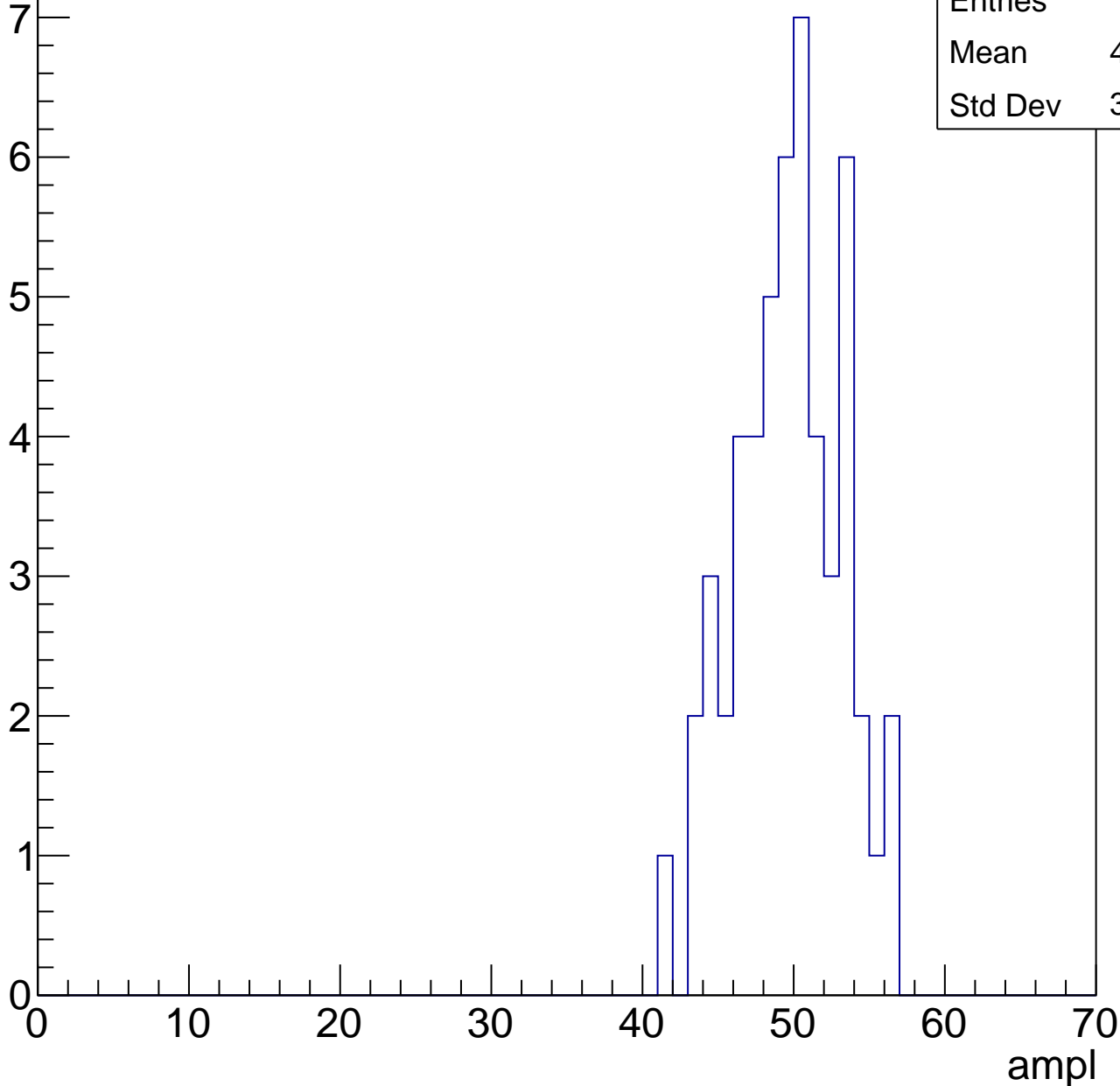


B1L103S, U14-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	49.19
Std Dev	3.475

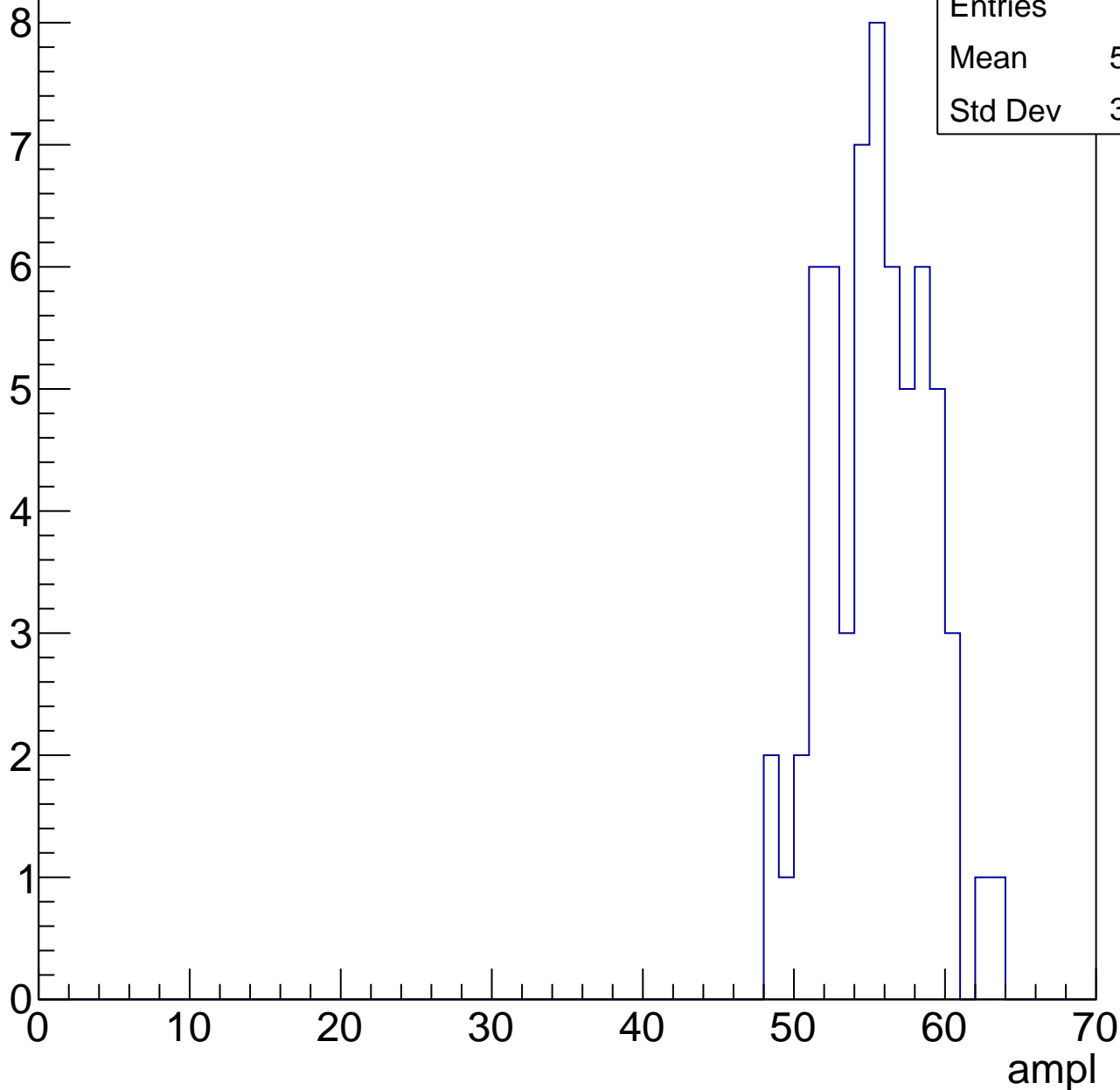


B1L103S, U14-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	54.98
Std Dev	3.362

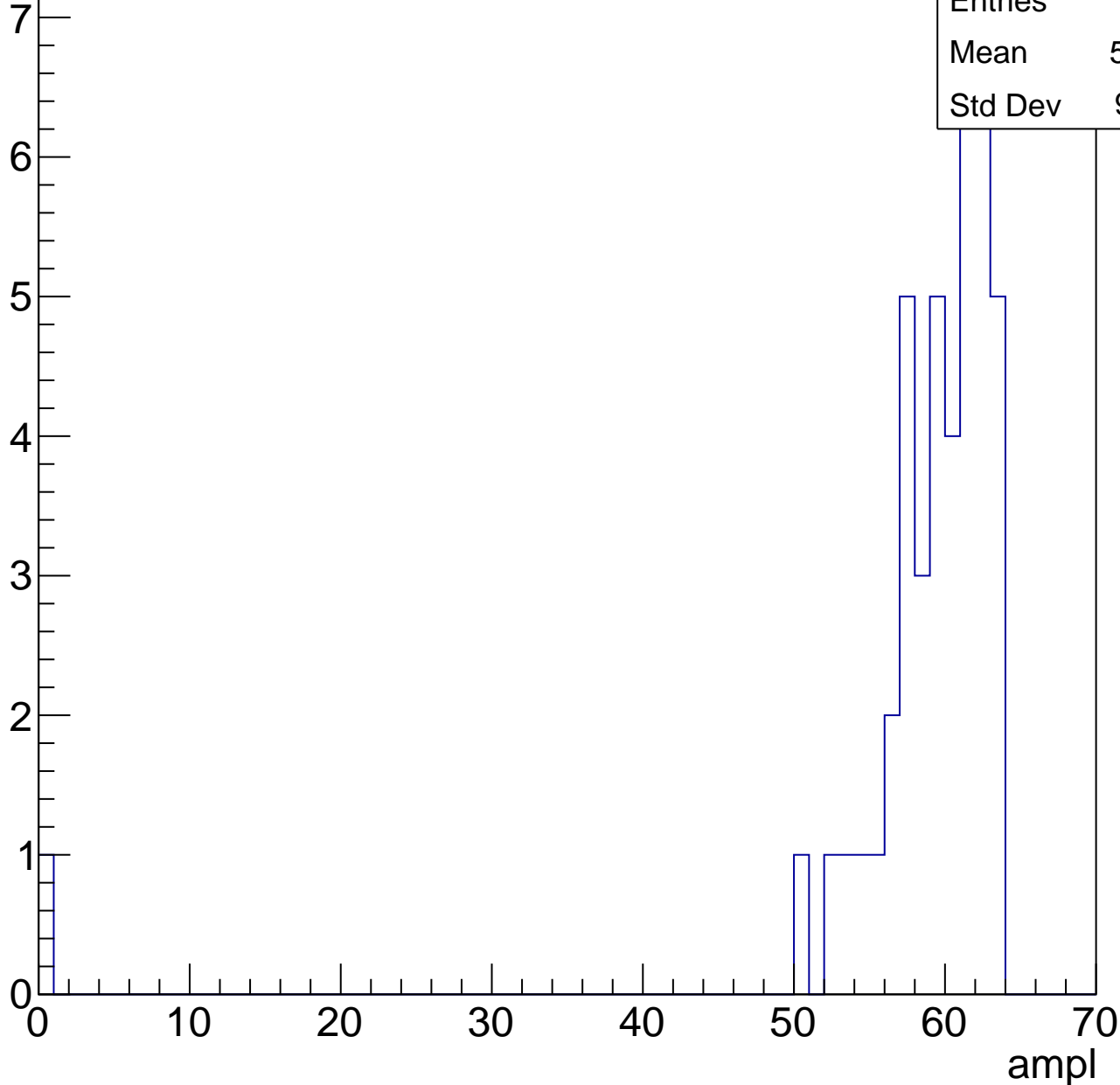


B1L103S, U14-ch95, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

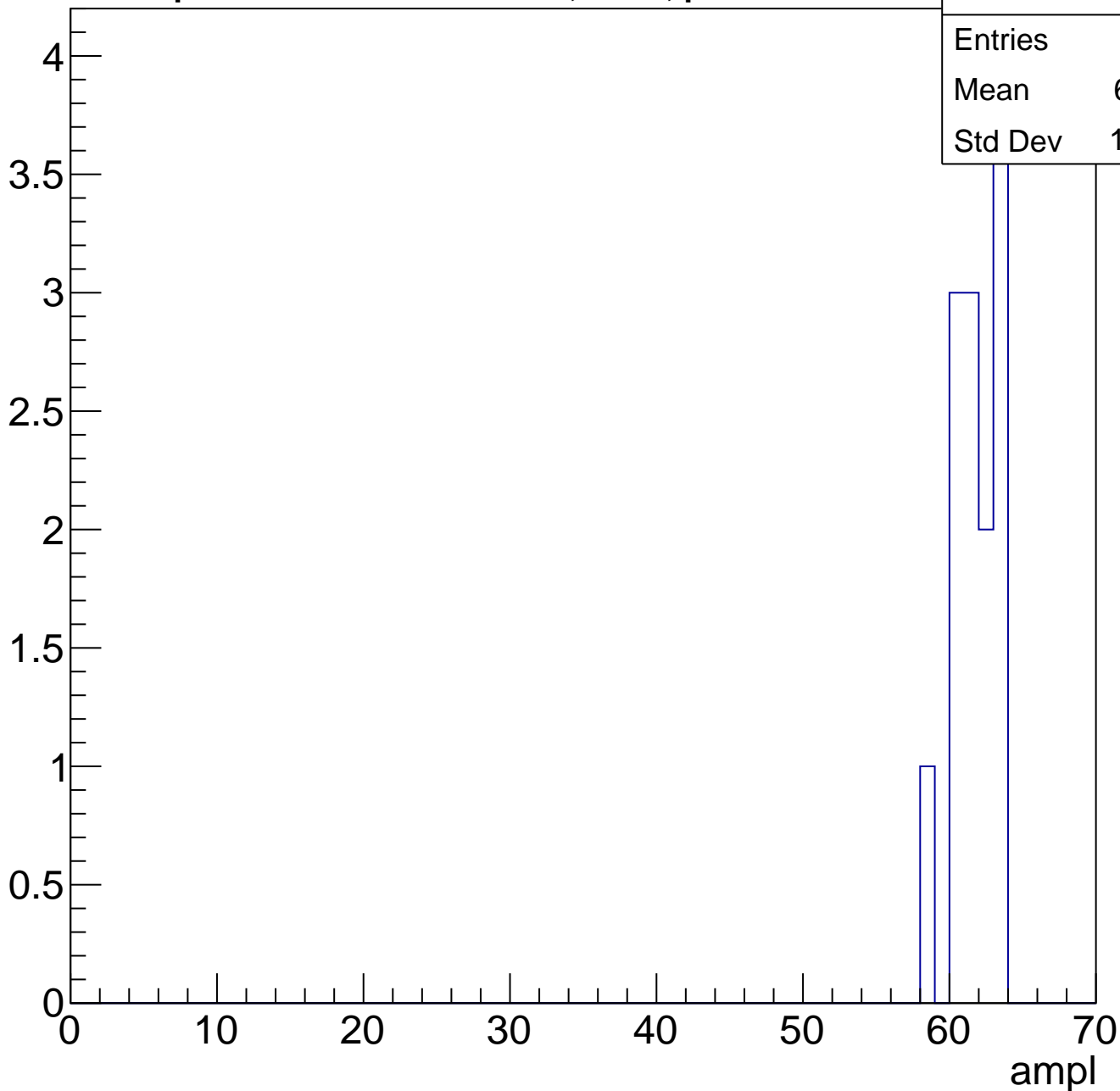
Entries	44
Mean	57.86
Std Dev	9.351



B1L103S, U14-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch96, adc0

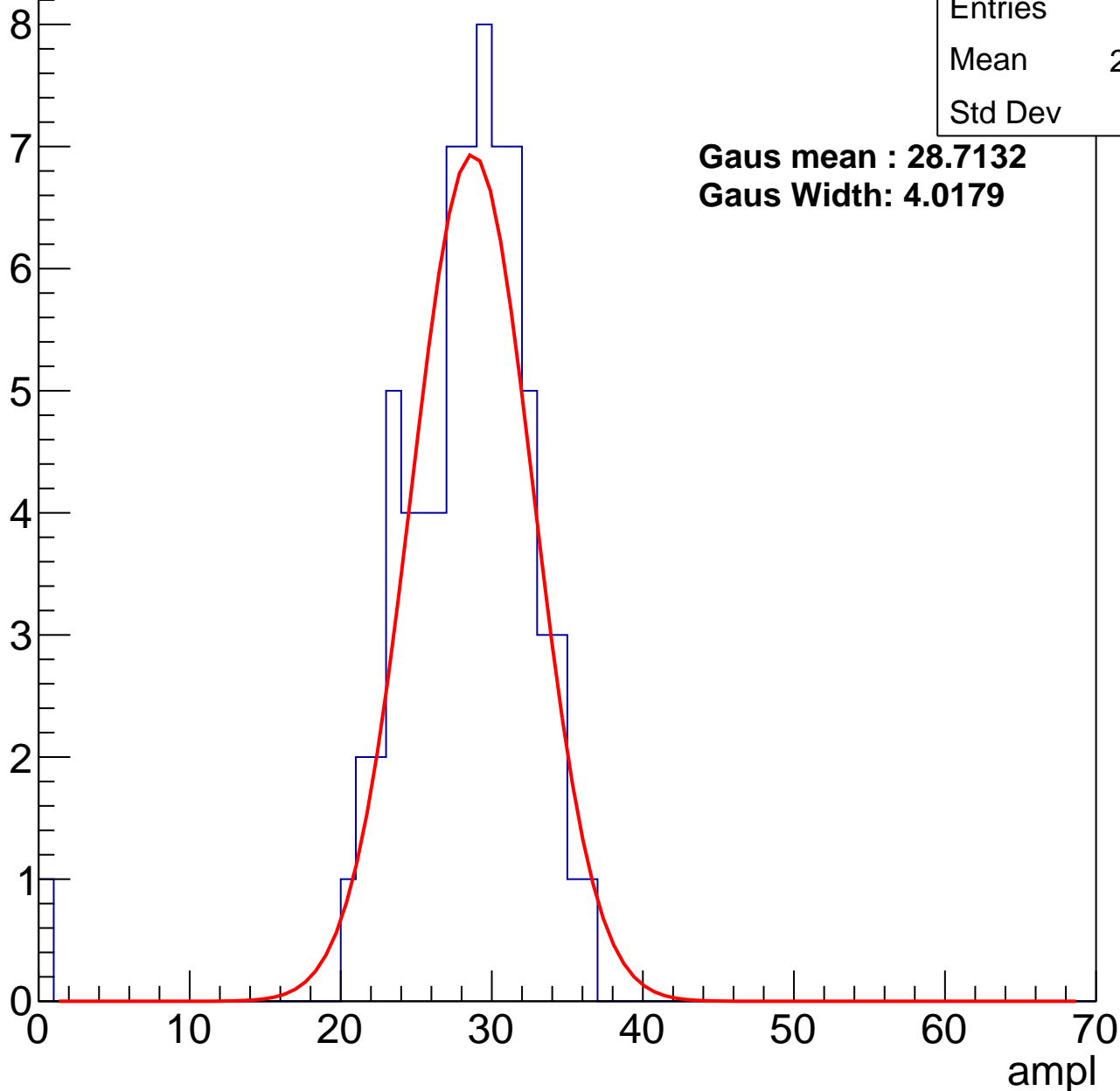
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.74
Std Dev	4.91

Gaus mean : 28.7132

Gaus Width: 4.0179



B1L103S, U14-ch96, adc1

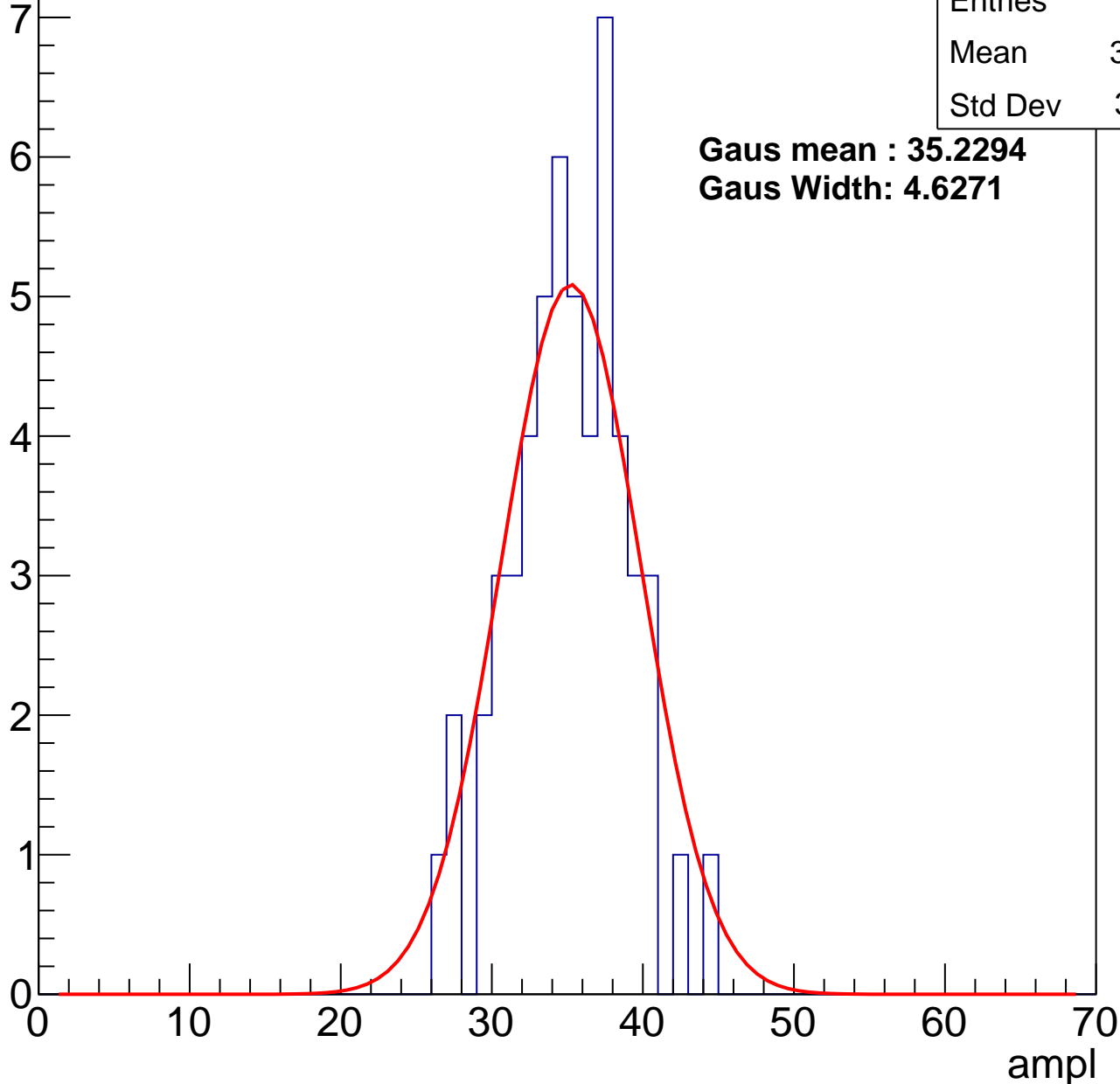
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	34.65
Std Dev	3.801

Gaus mean : 35.2294

Gaus Width: 4.6271



B1L103S, U14-ch96, adc2

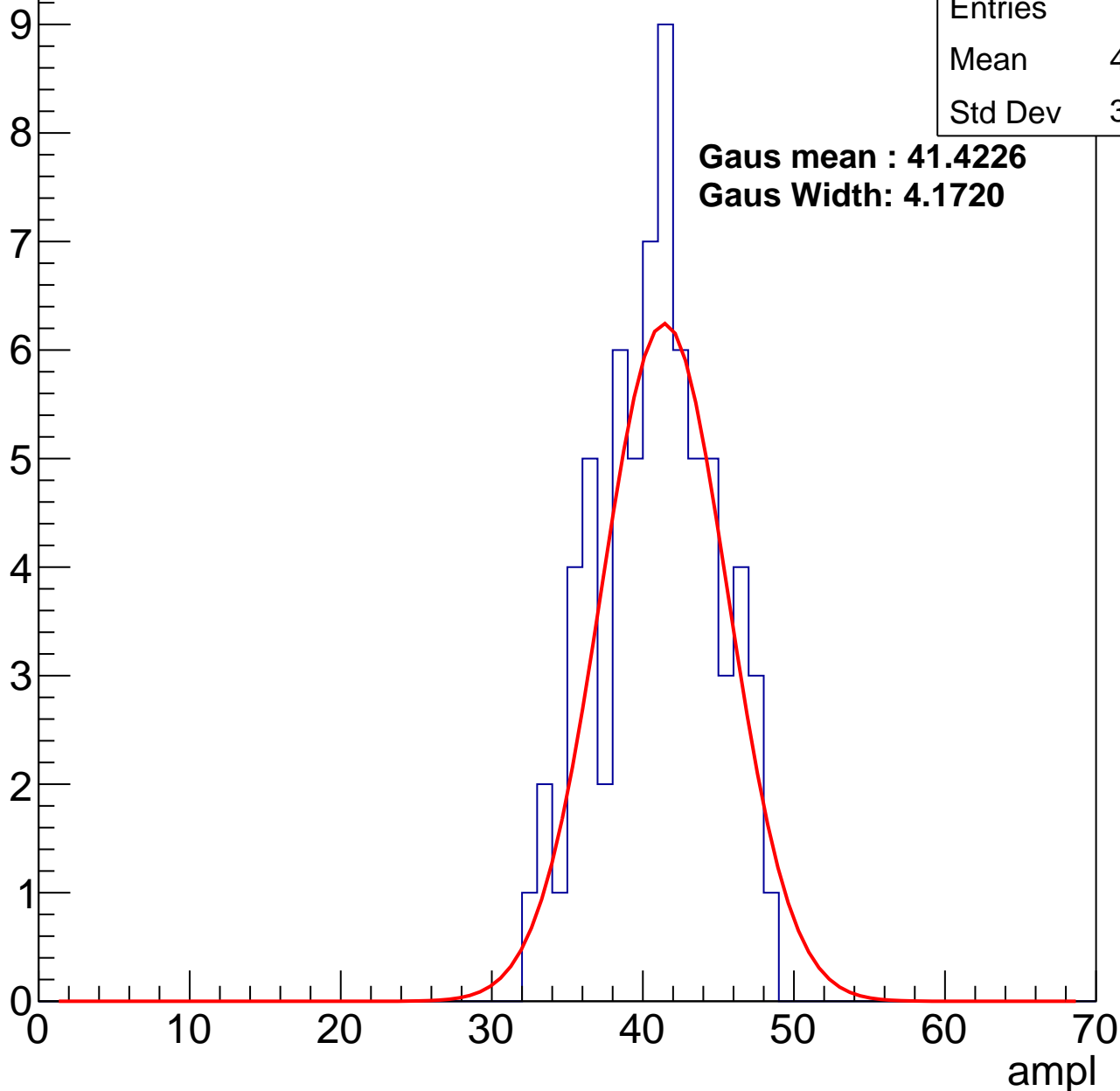
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	40.48
Std Dev	3.802

Gaus mean : 41.4226

Gaus Width: 4.1720

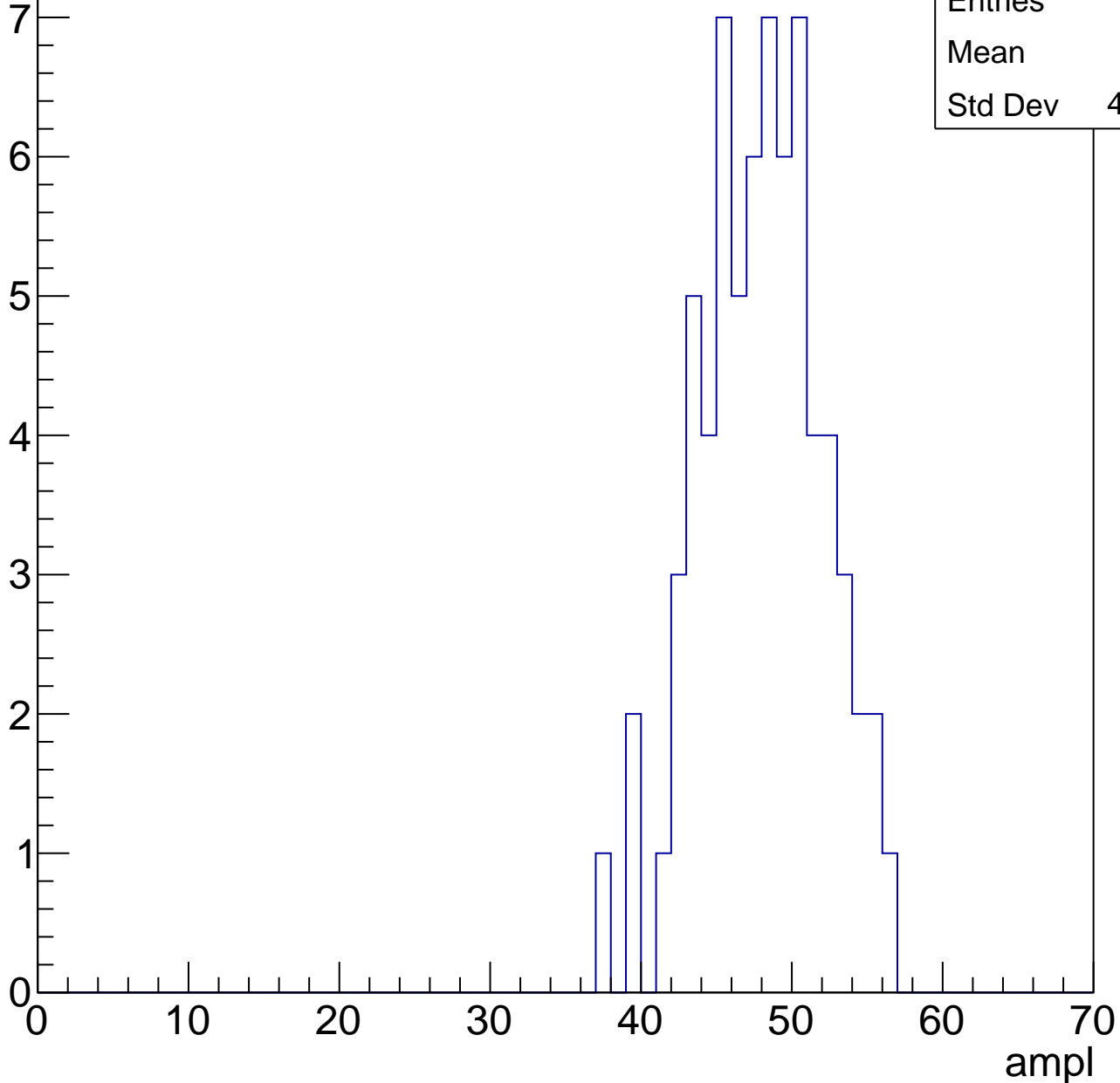


B1L103S, U14-ch96, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	47.5
Std Dev	4.042

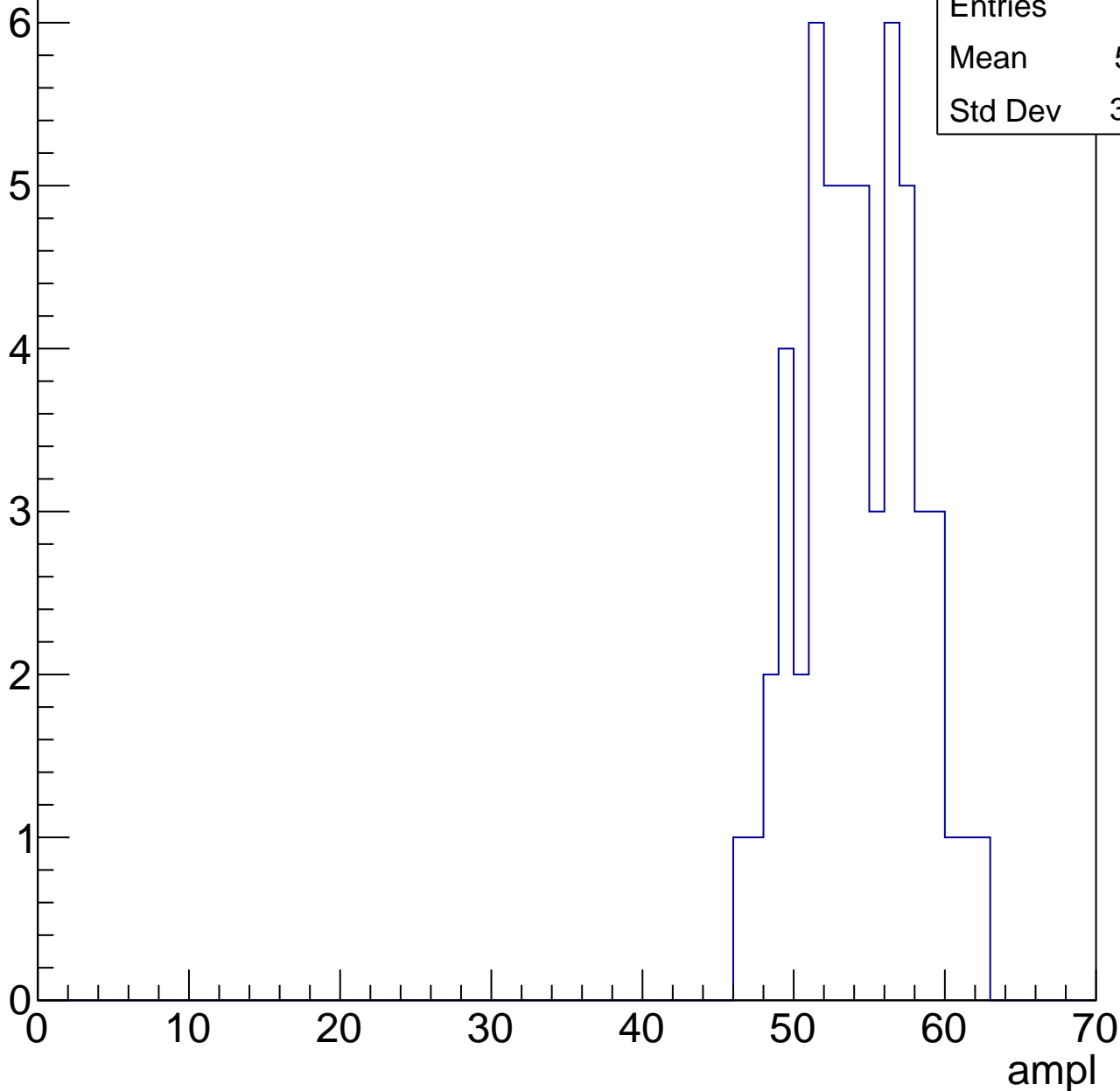


B1L103S, U14-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	53.81
Std Dev	3.697



B1L103S, U14-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	48
Mean	58.19
Std Dev	3.153

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

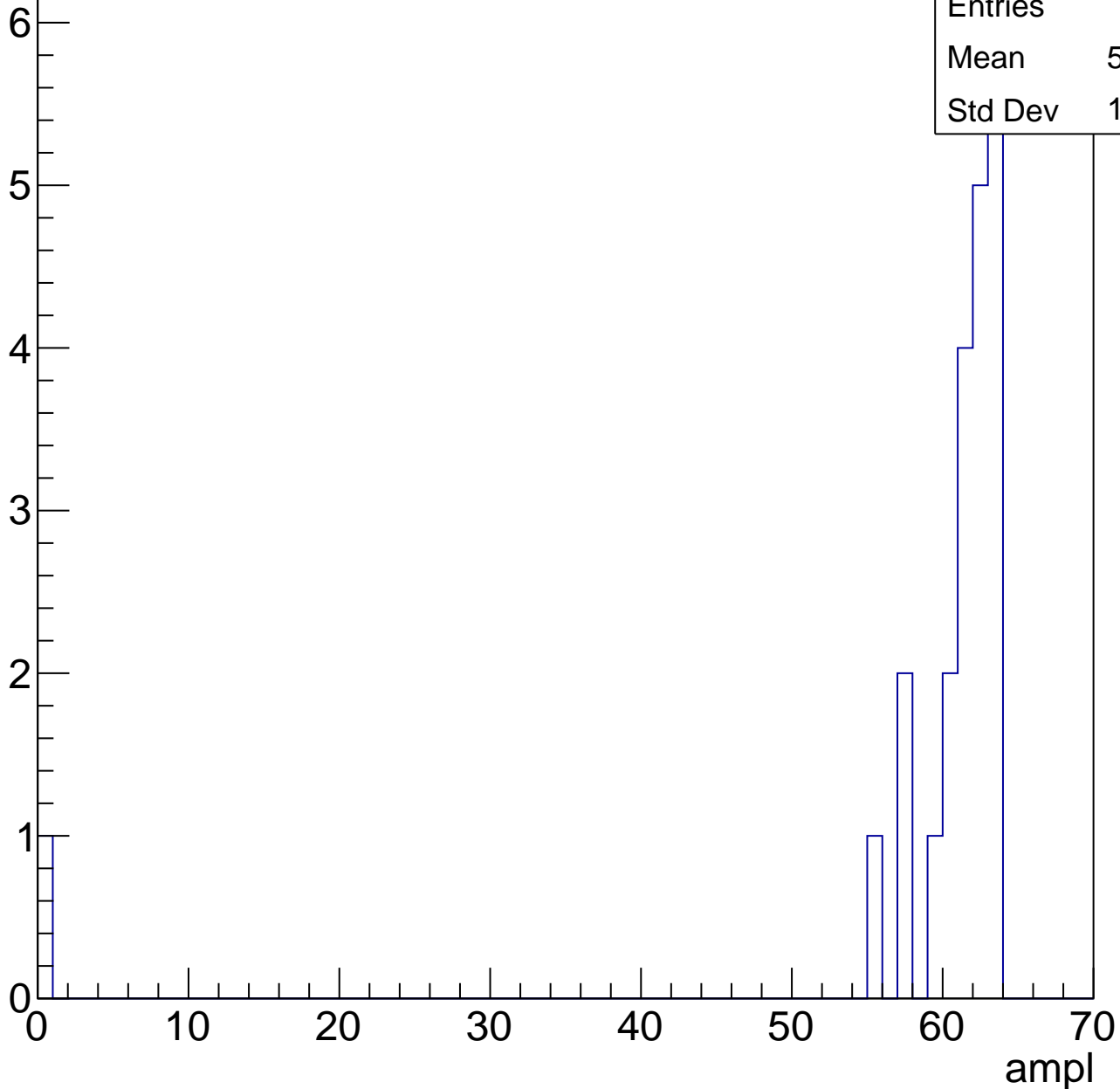
ampl

B1L103S, U14-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

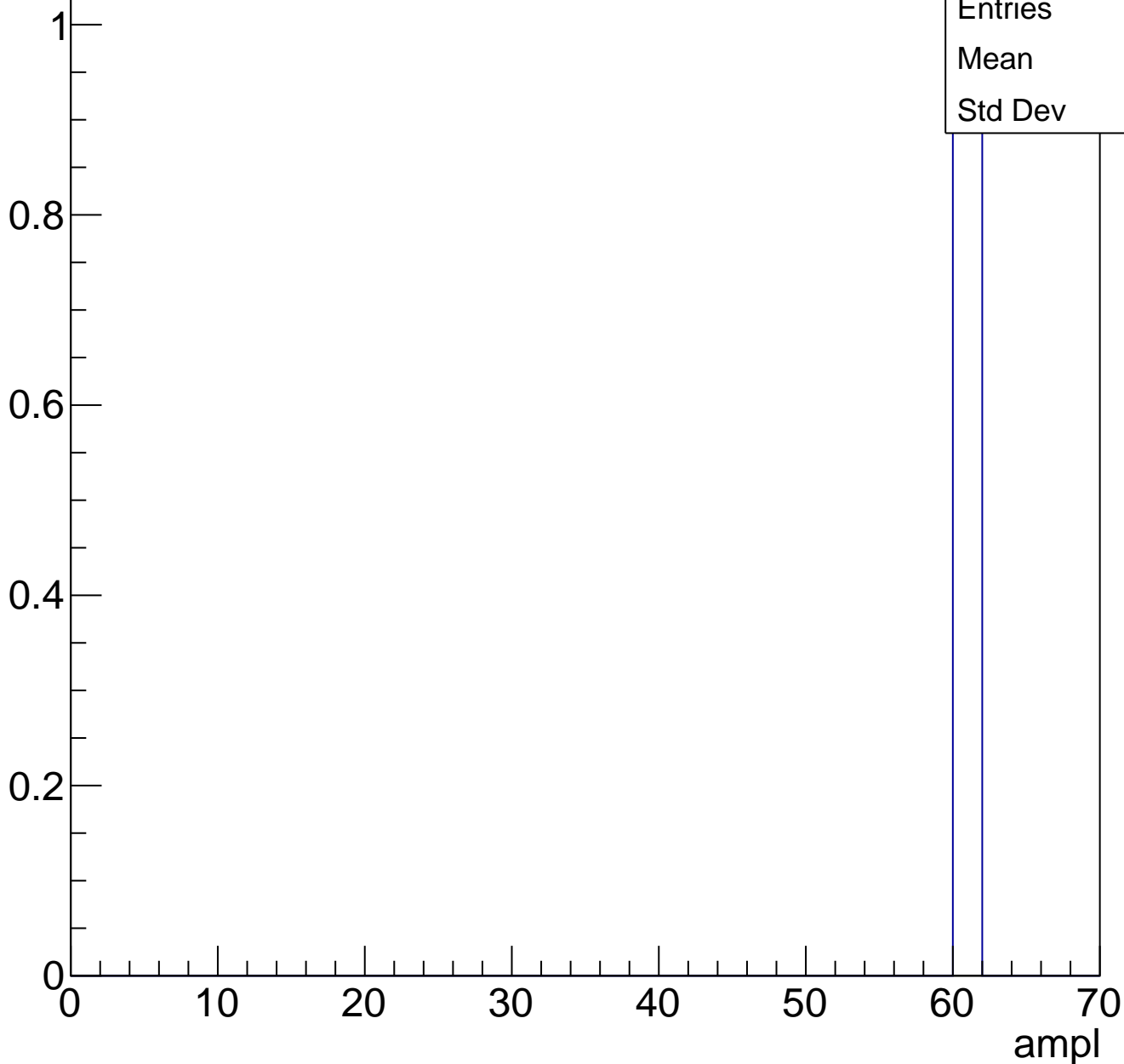
Entries	22
Mean	58.18
Std Dev	12.88



B1L103S, U14-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch97, adc0

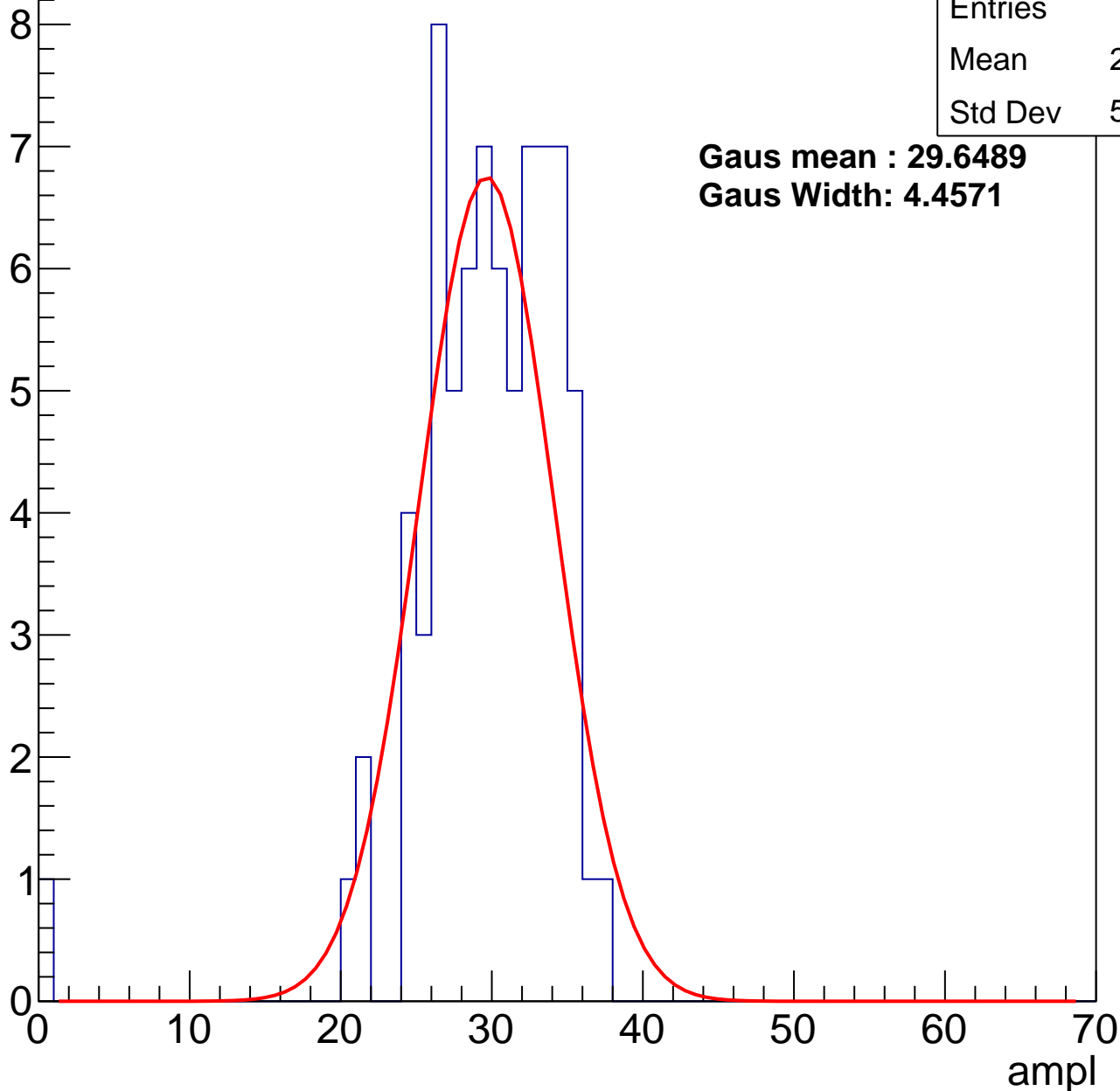
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	29.25
Std Dev	5.079

Gaus mean : 29.6489

Gaus Width: 4.4571



B1L103S, U14-ch97, adc1

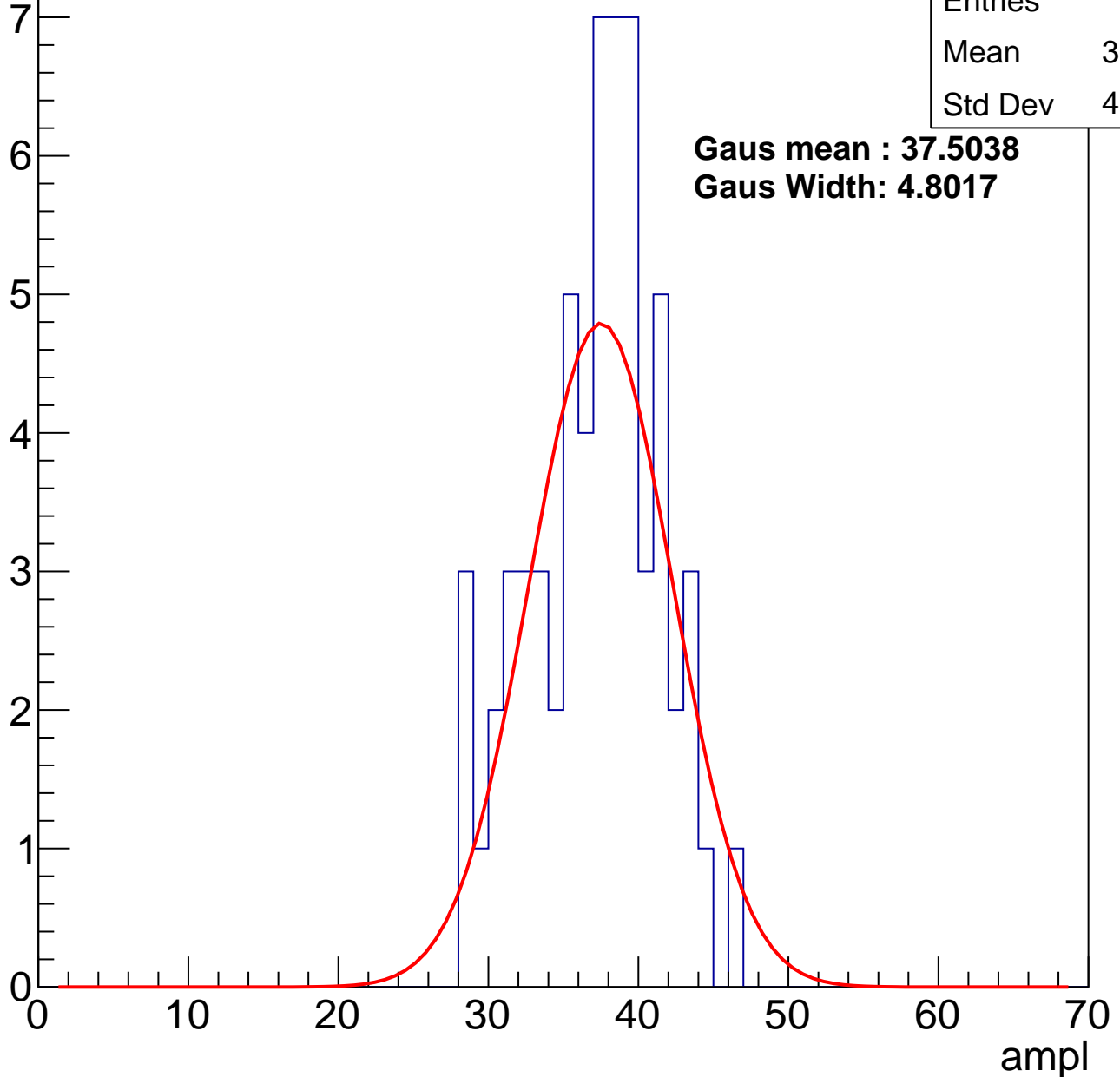
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	36.68
Std Dev	4.215

Gaus mean : 37.5038

Gaus Width: 4.8017



B1L103S, U14-ch97, adc2

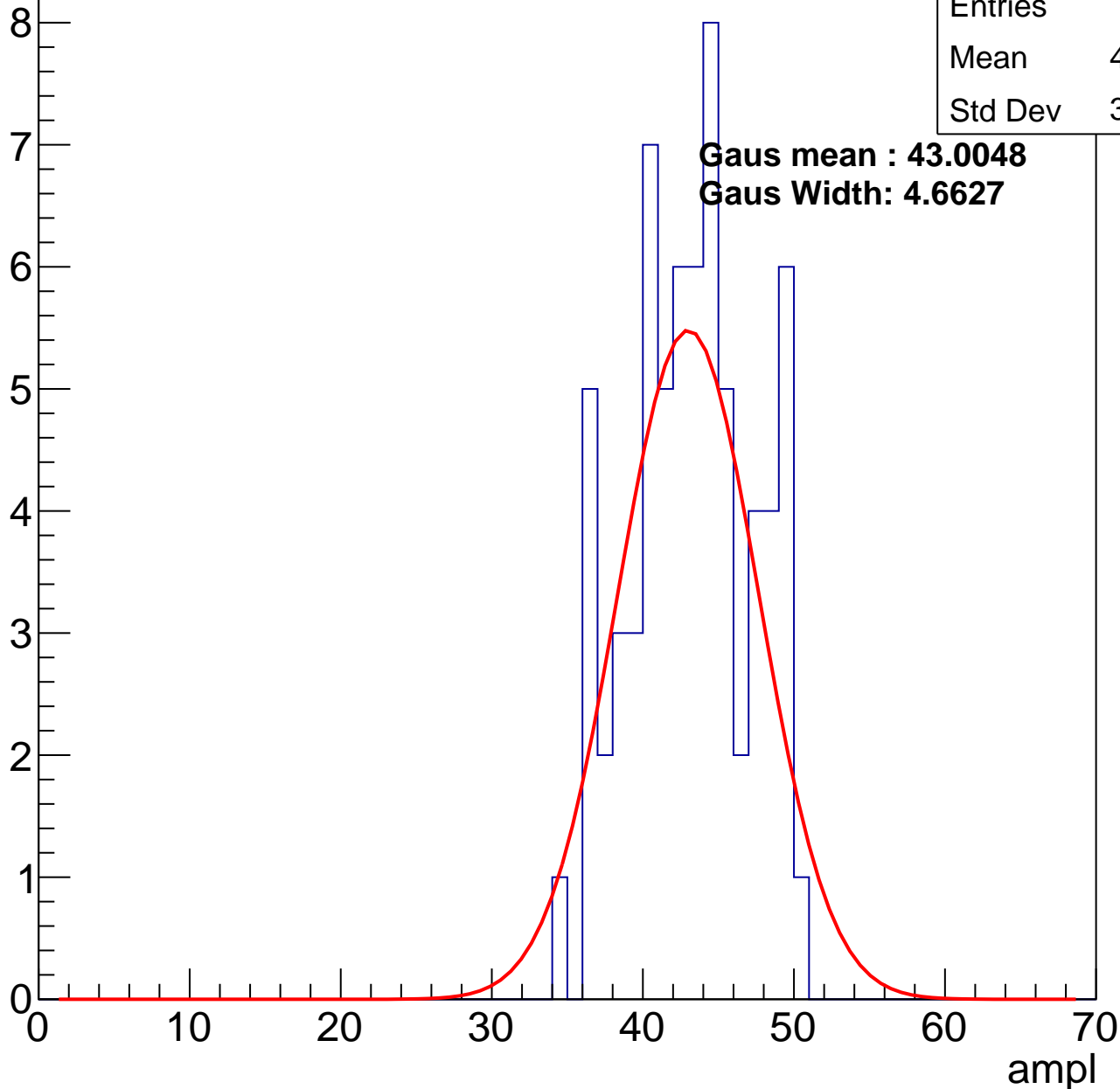
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.75
Std Dev	3.987

Gaus mean : 43.0048

Gaus Width: 4.6627

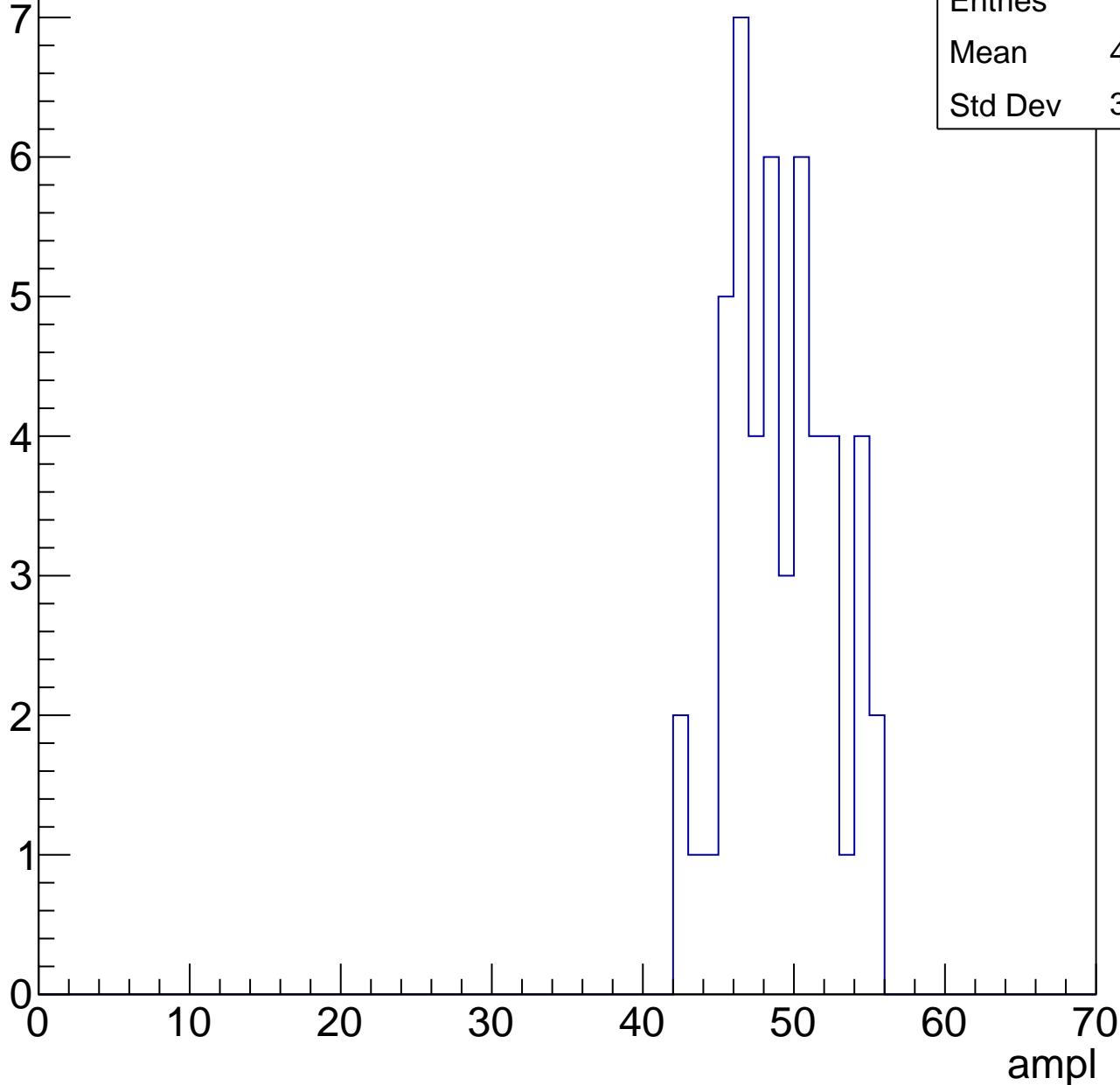


B1L103S, U14-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	48.64
Std Dev	3.357

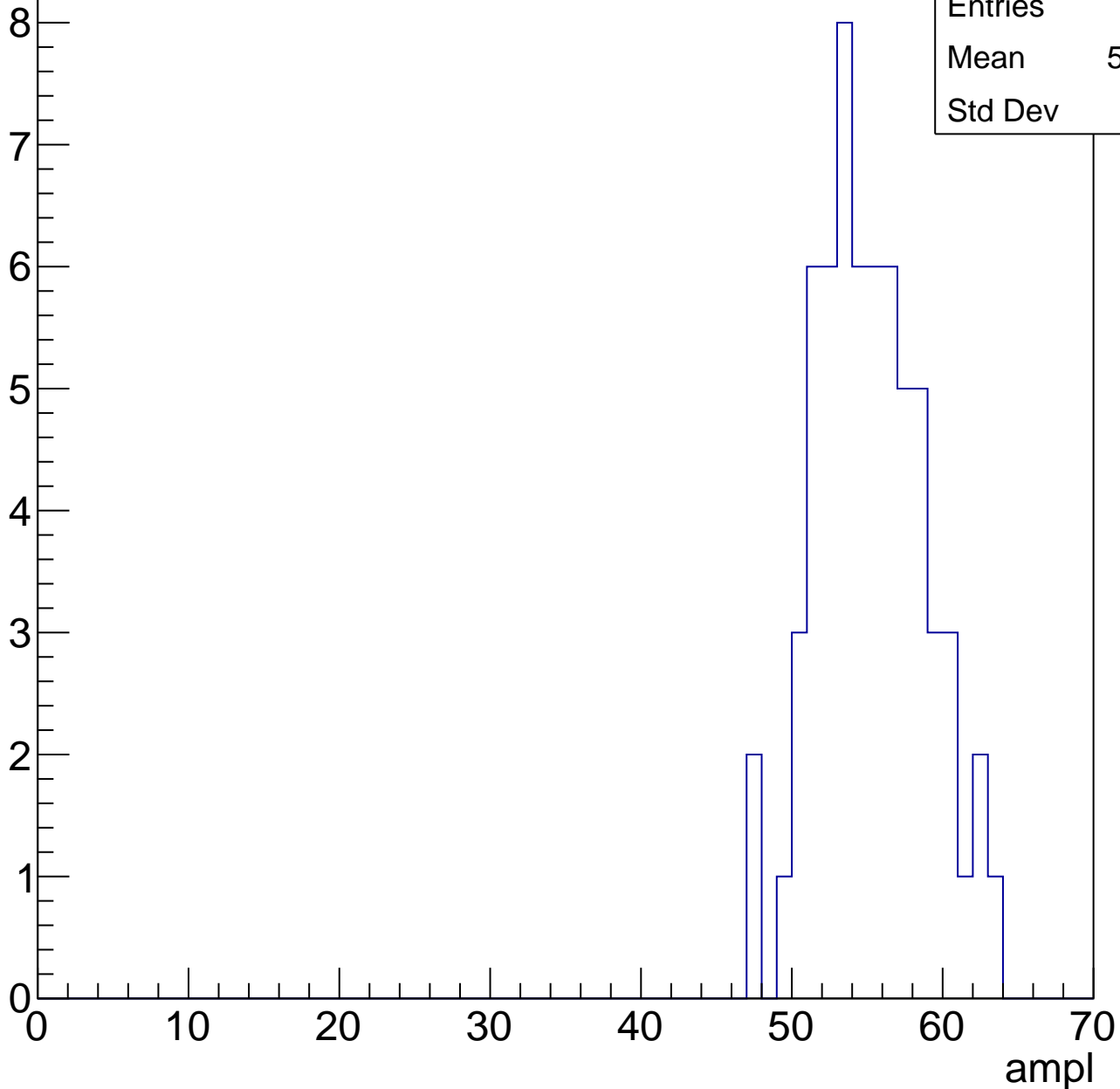


B1L103S, U14-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

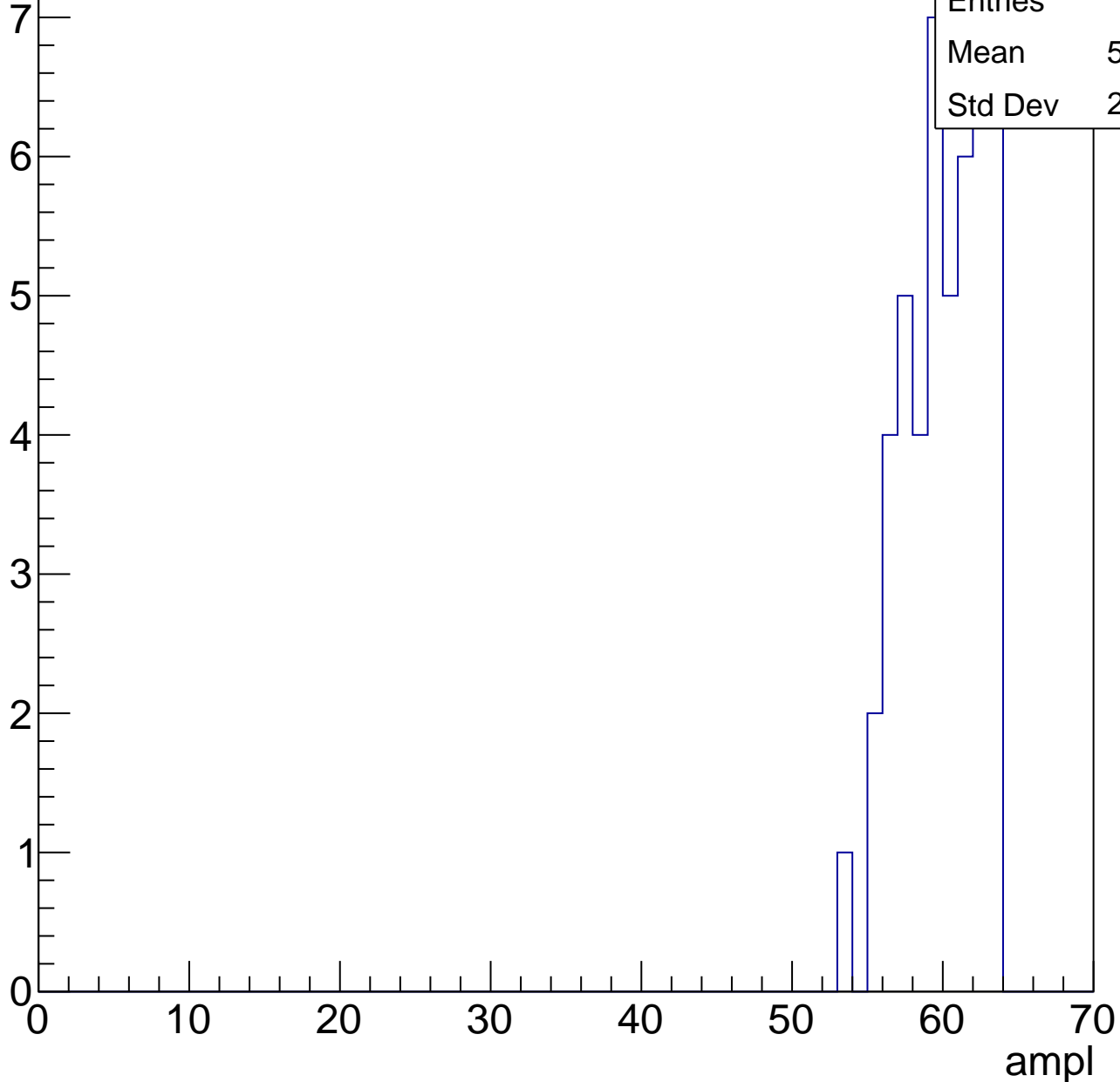
Entries	64
Mean	54.77
Std Dev	3.57



B1L103S, U14-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

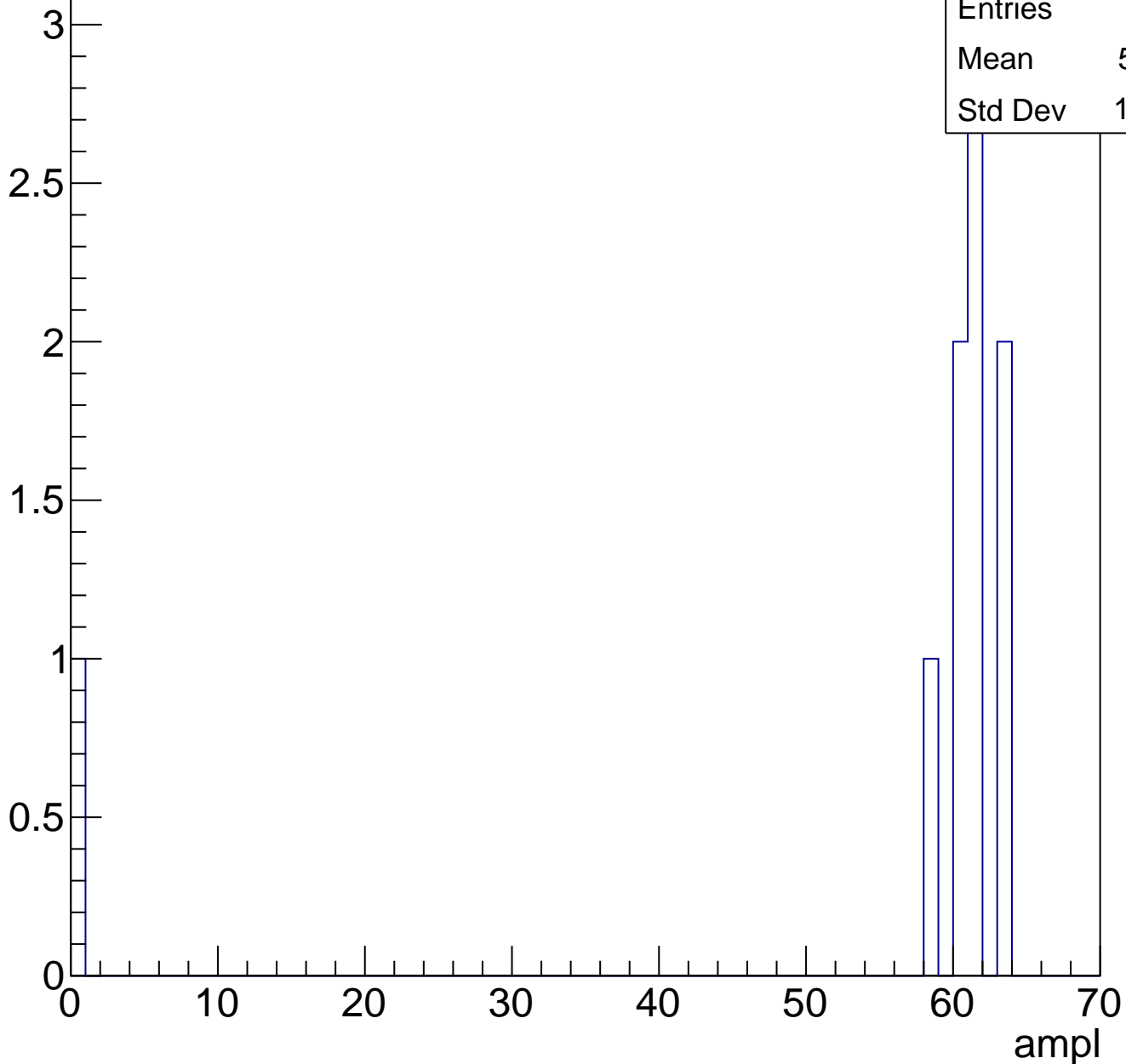
Entry



B1L103S, U14-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

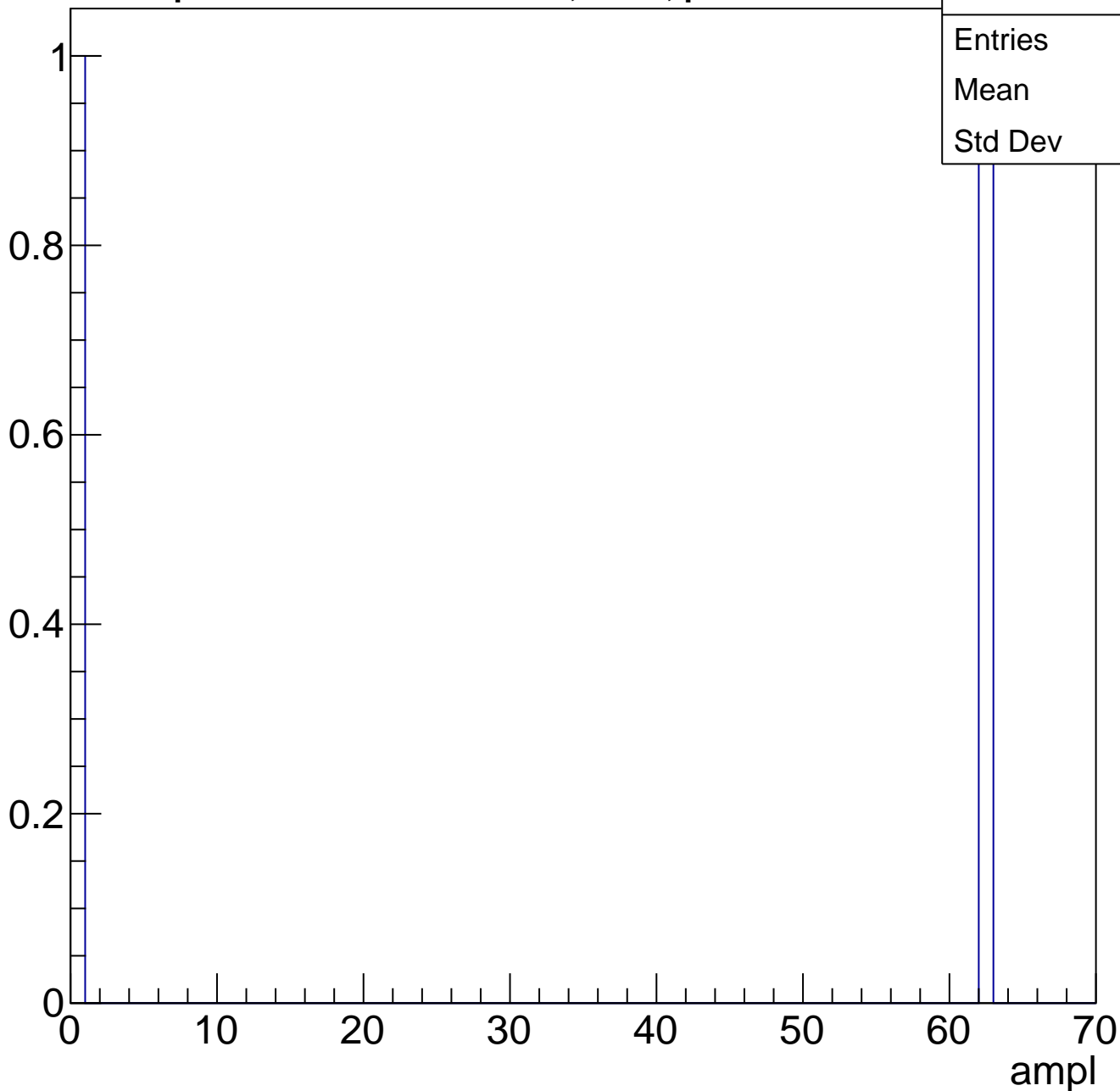
Entry



B1L103S, U14-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch98, adc0

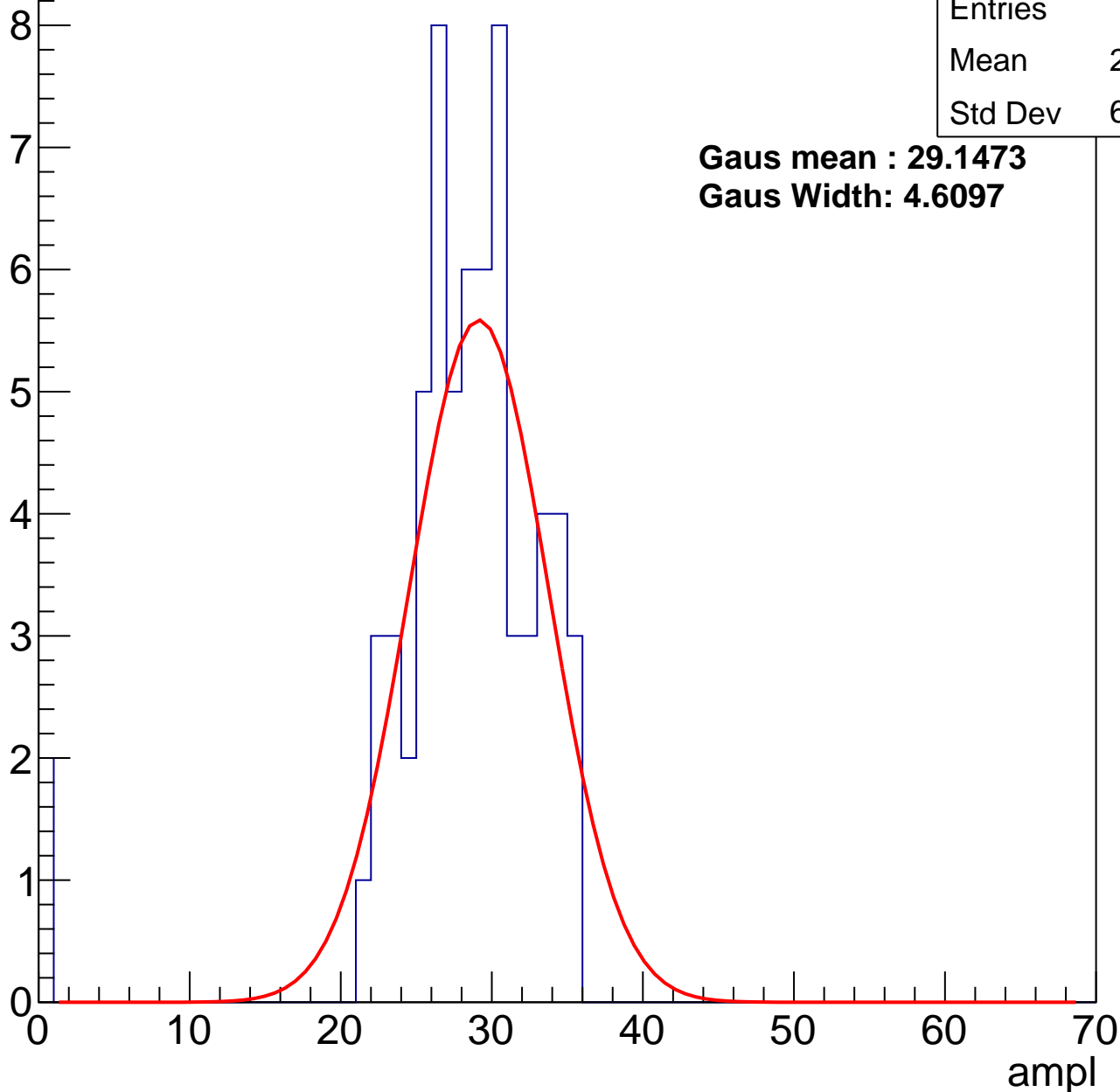
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	27.52
Std Dev	6.033

Gaus mean : 29.1473

Gaus Width: 4.6097



B1L103S, U14-ch98, adc1

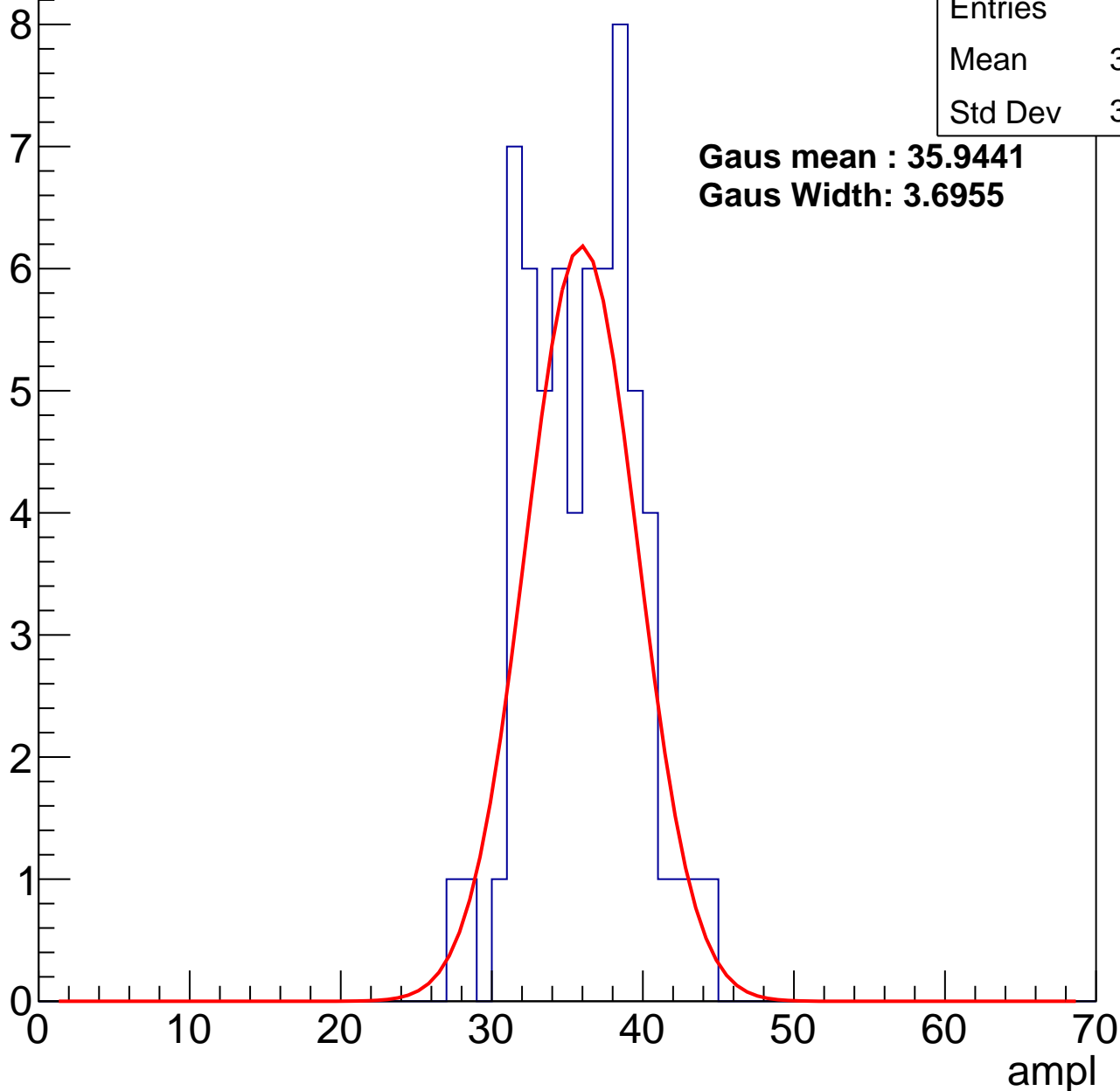
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.47
Std Dev	3.597

Gaus mean : 35.9441

Gaus Width: 3.6955

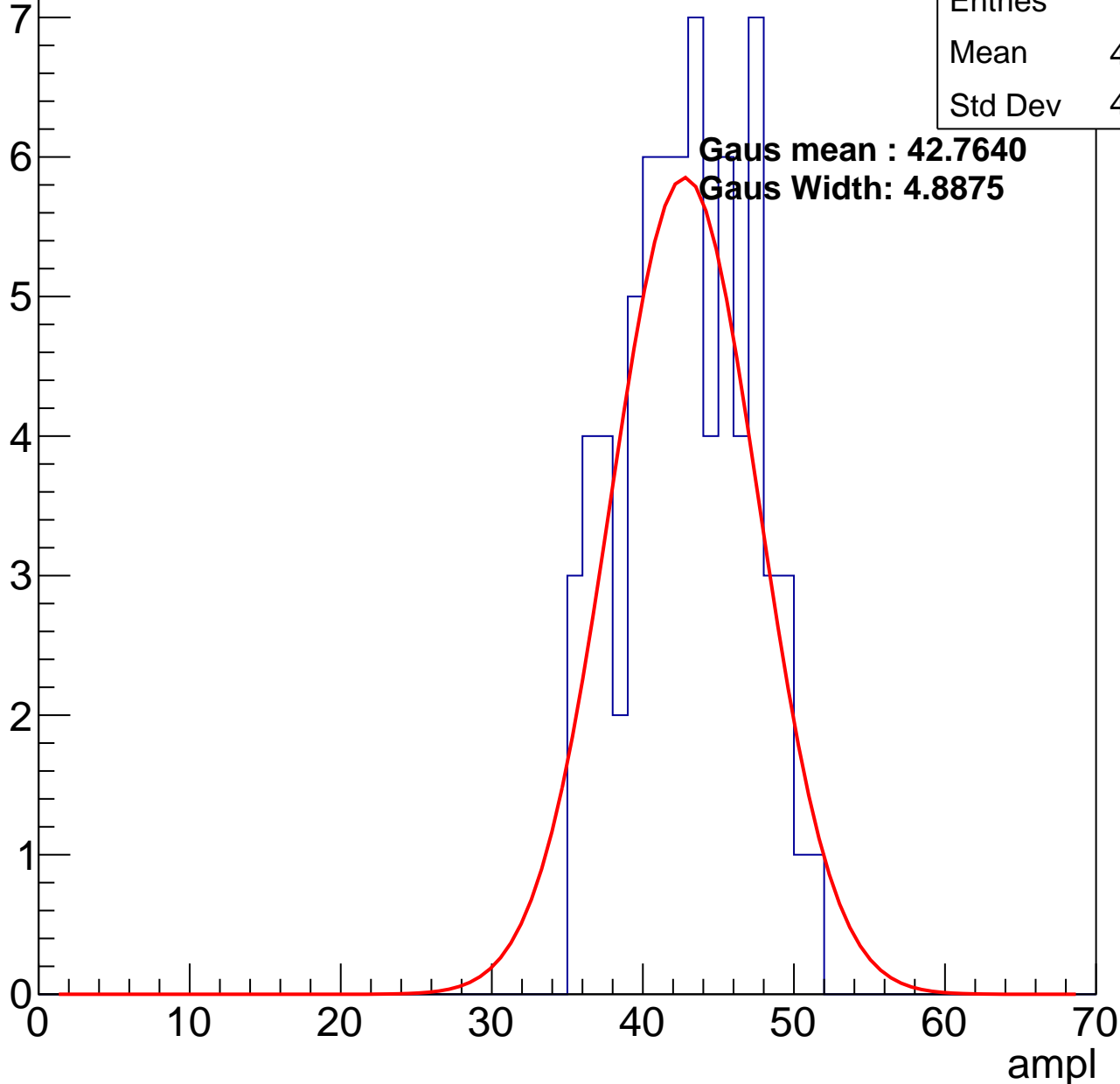


B1L103S, U14-ch98, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

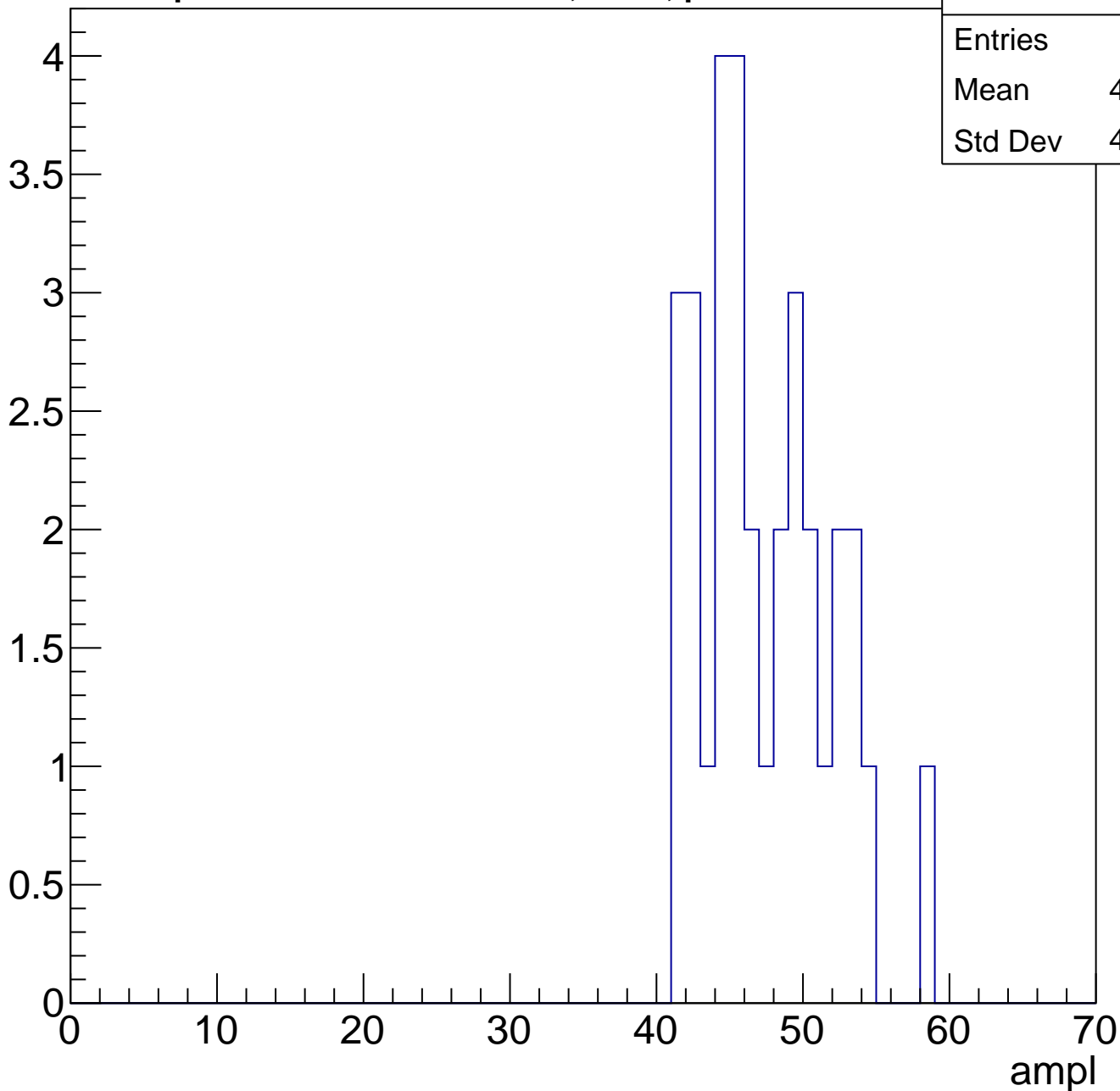
Entries	72
Mean	42.47
Std Dev	4.079



B1L103S, U14-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

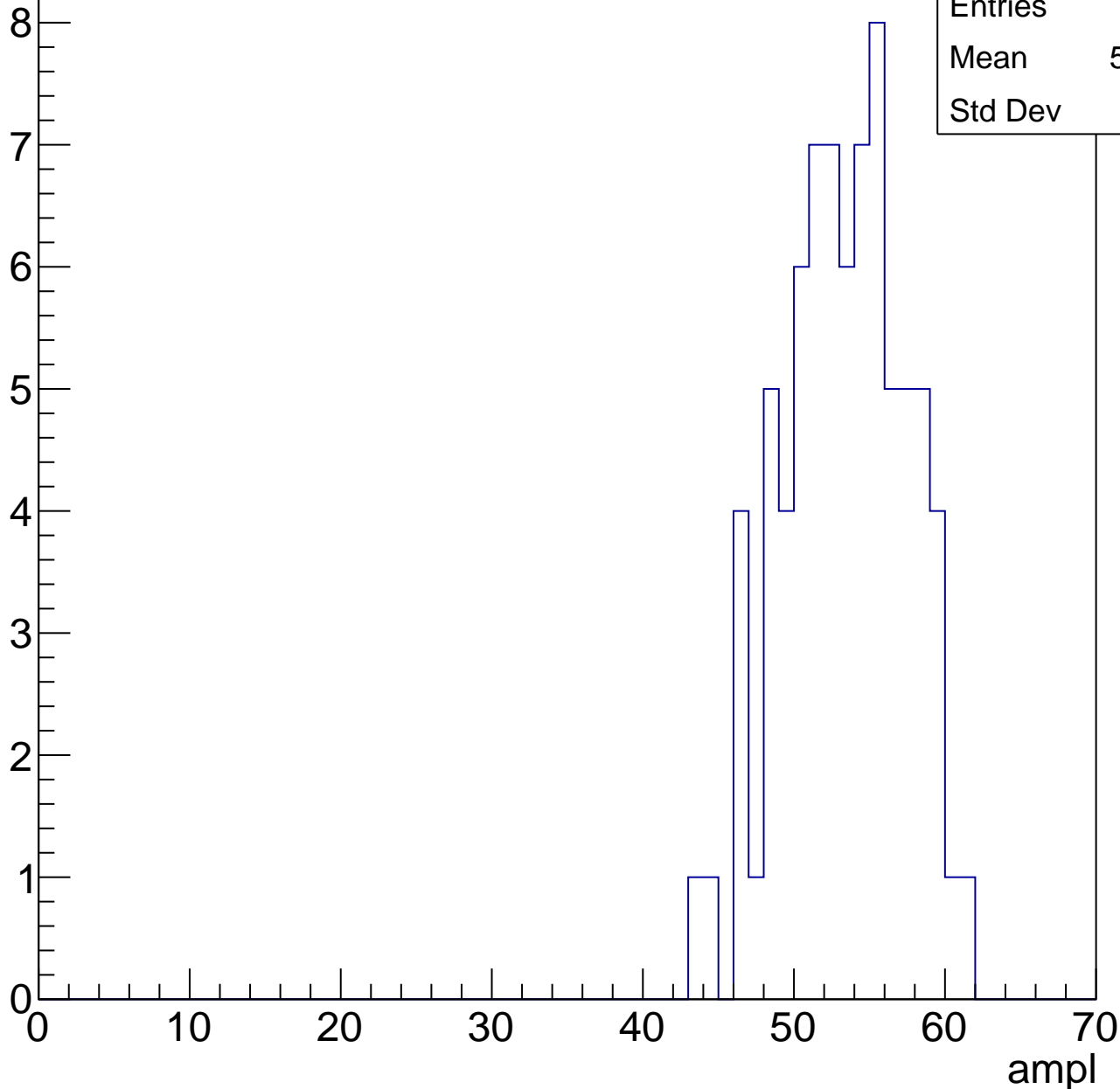


B1L103S, U14-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	52.86
Std Dev	3.98

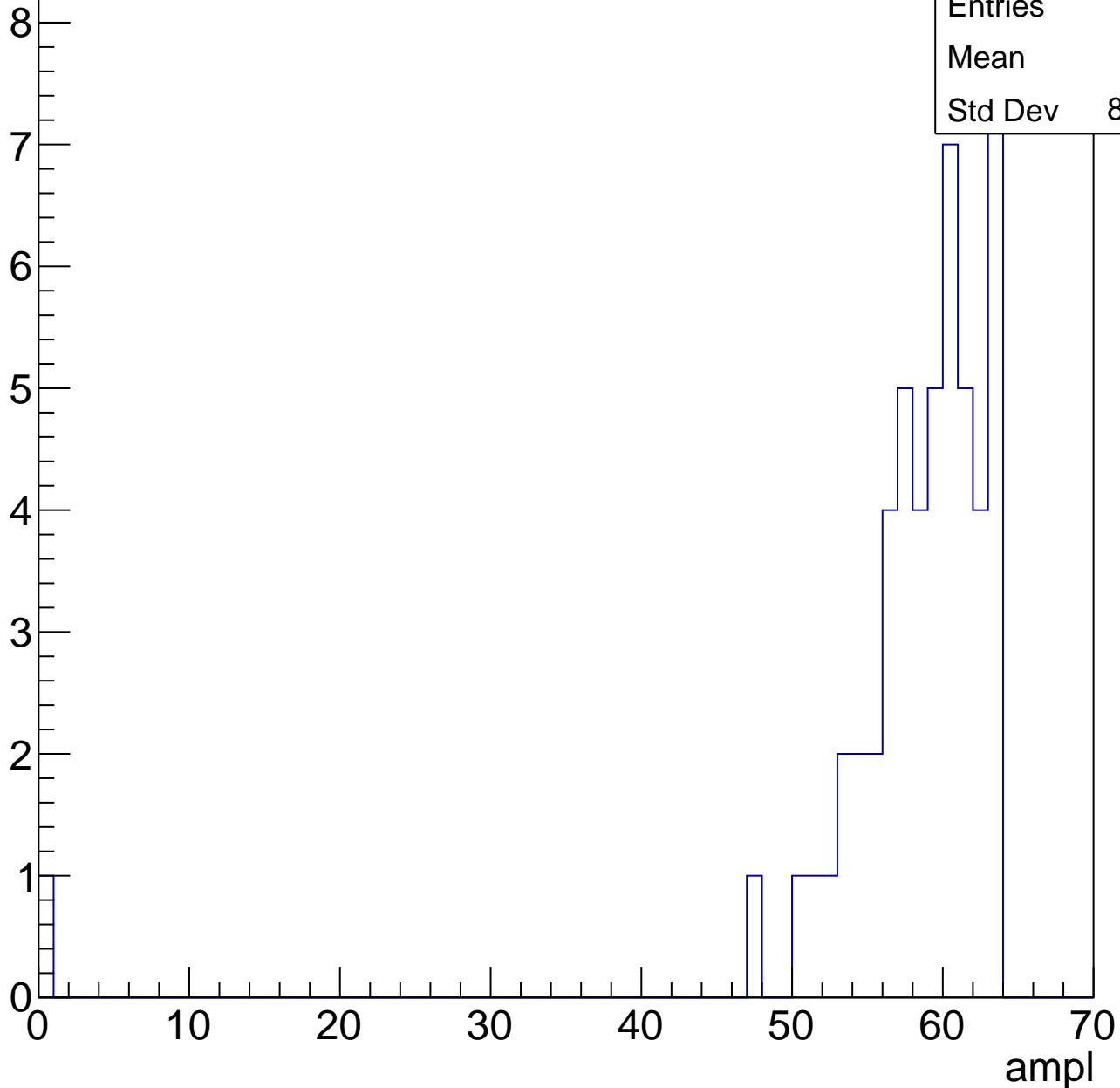


B1L103S, U14-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

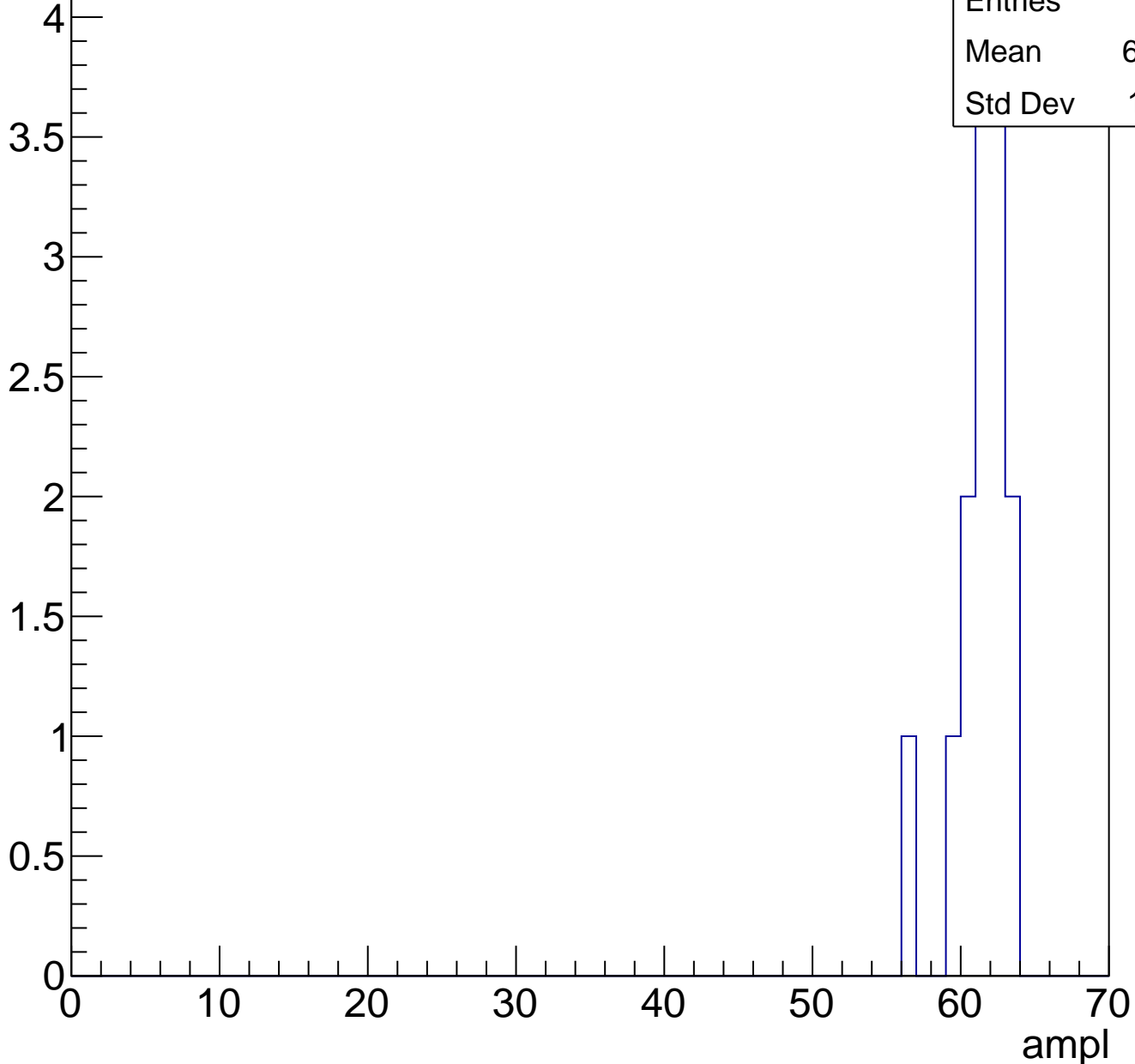
Entries	53
Mean	57.3
Std Dev	8.764



B1L103S, U14-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

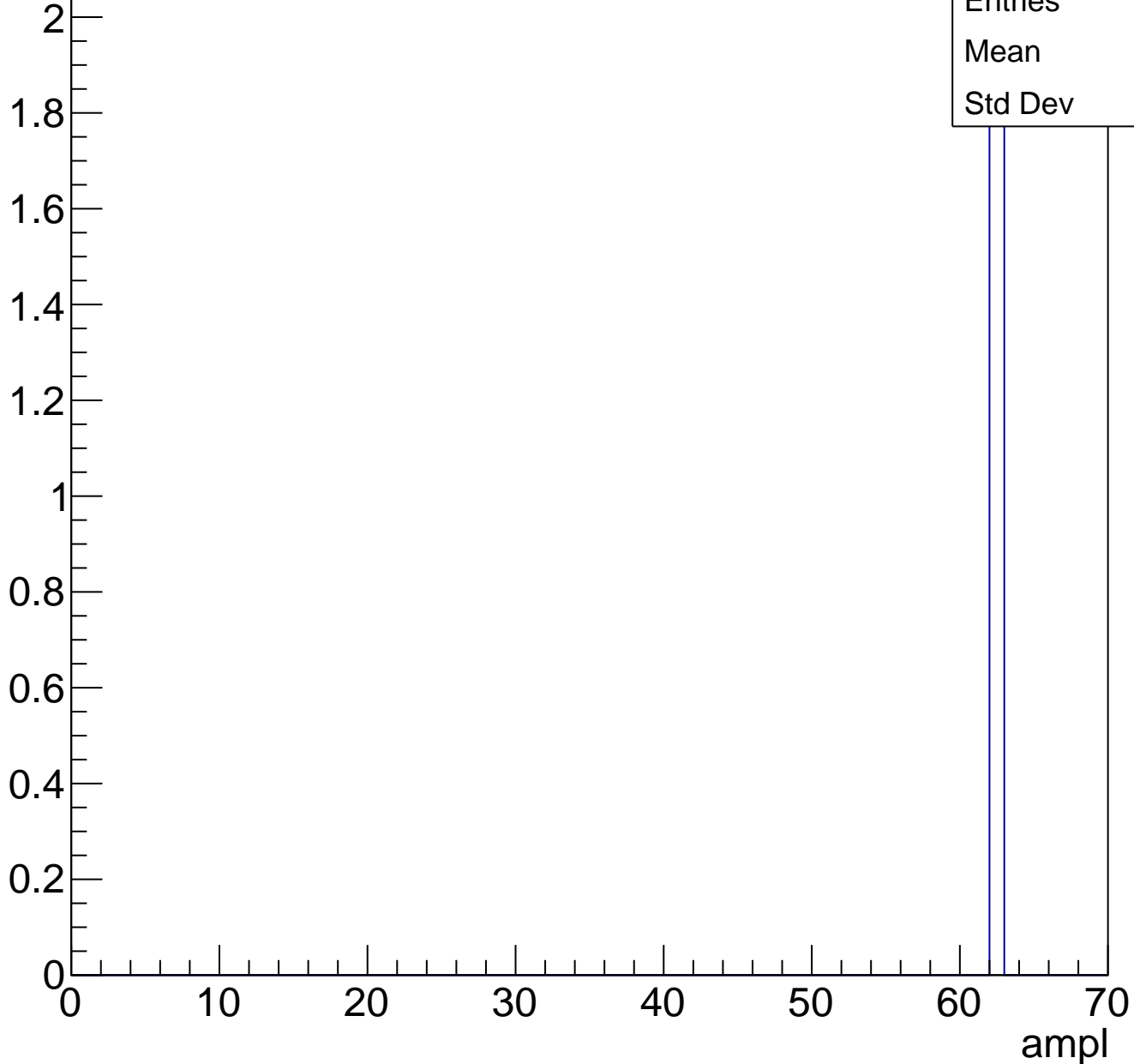


Entries	14
Mean	60.93
Std Dev	1.751

B1L103S, U14-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch99, adc0

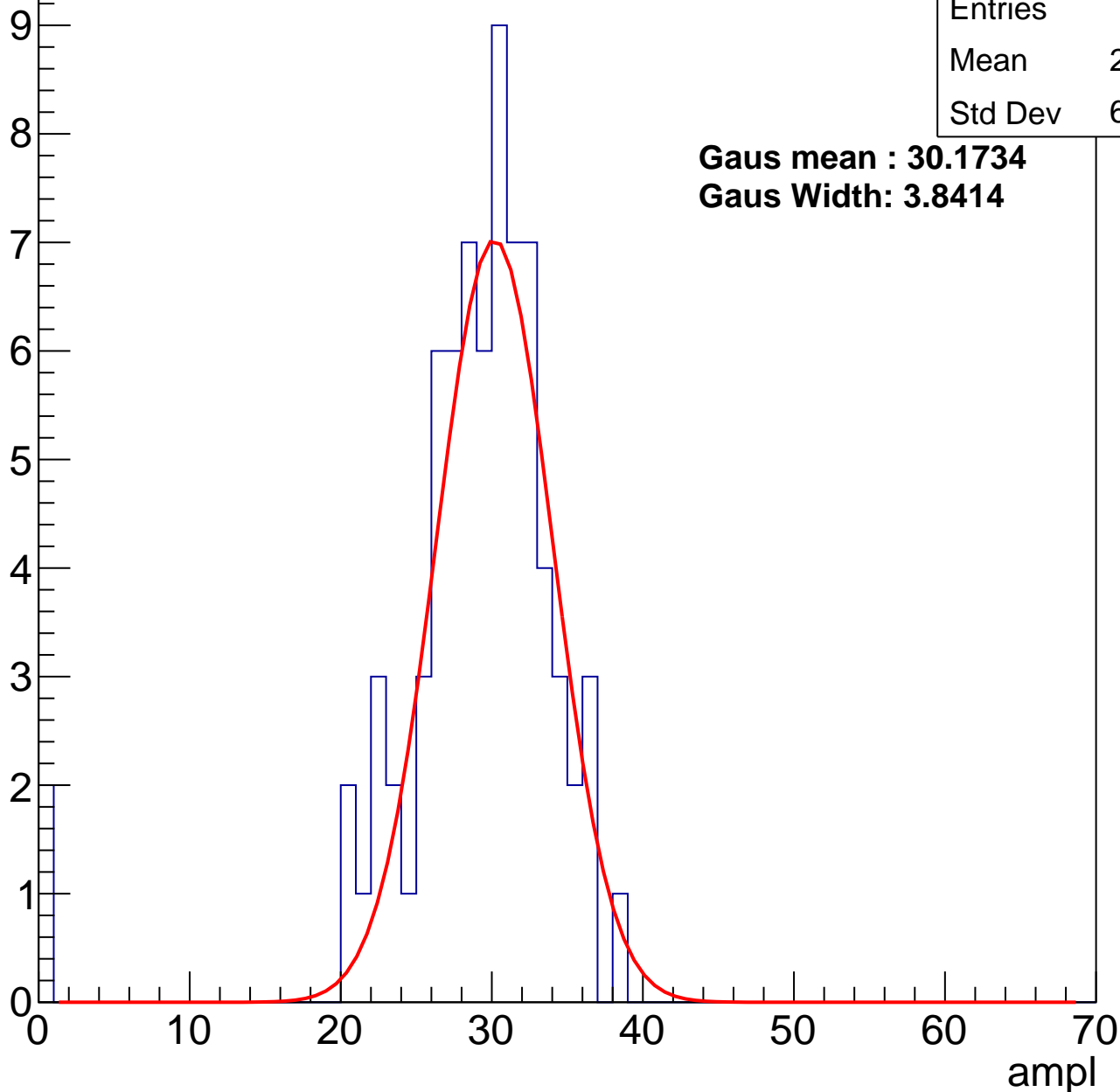
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.28
Std Dev	6.094

Gaus mean : 30.1734

Gaus Width: 3.8414



B1L103S, U14-ch99, adc1

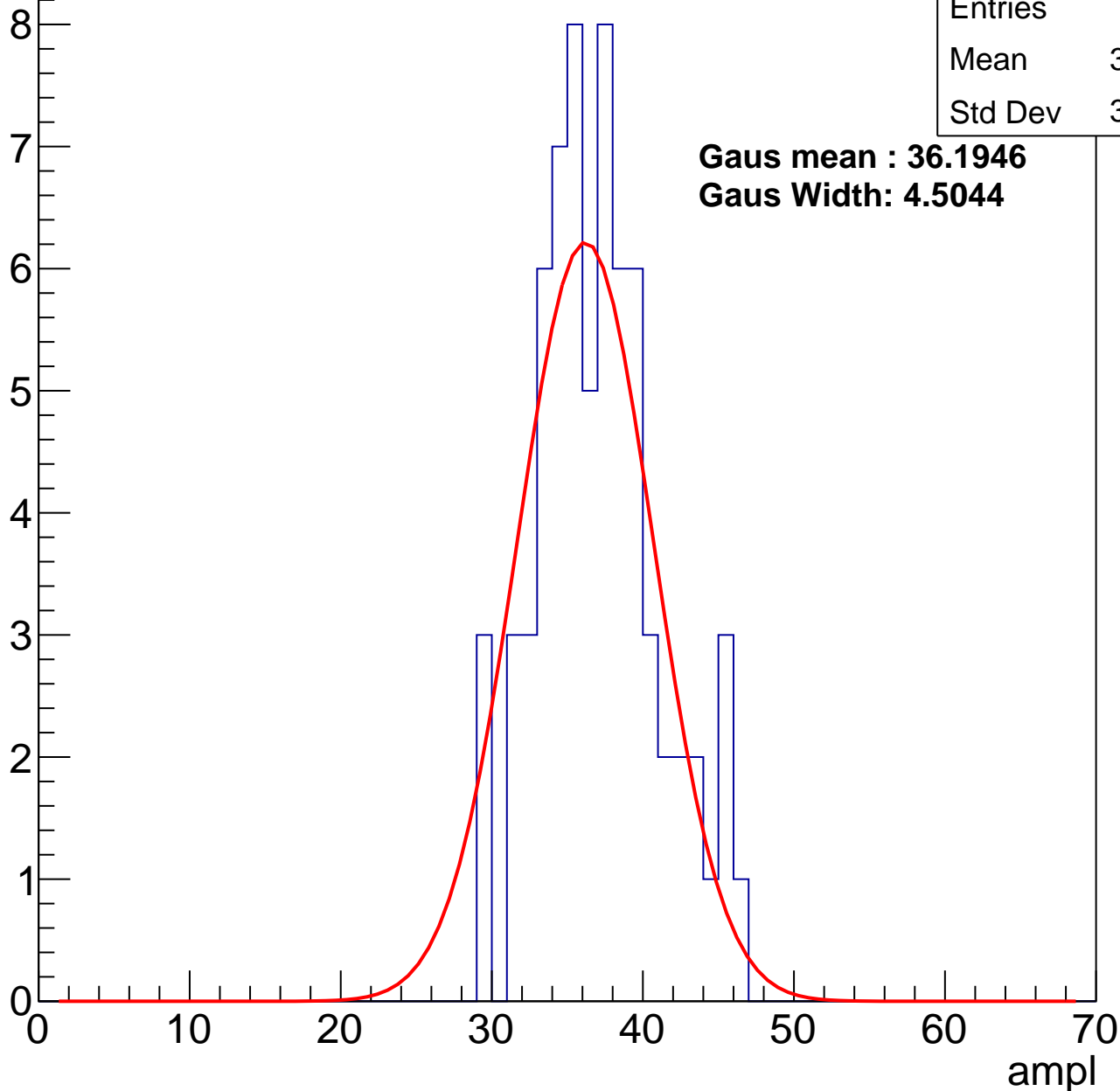
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.62
Std Dev	3.982

Gaus mean : 36.1946

Gaus Width: 4.5044



B1L103S, U14-ch99, adc2

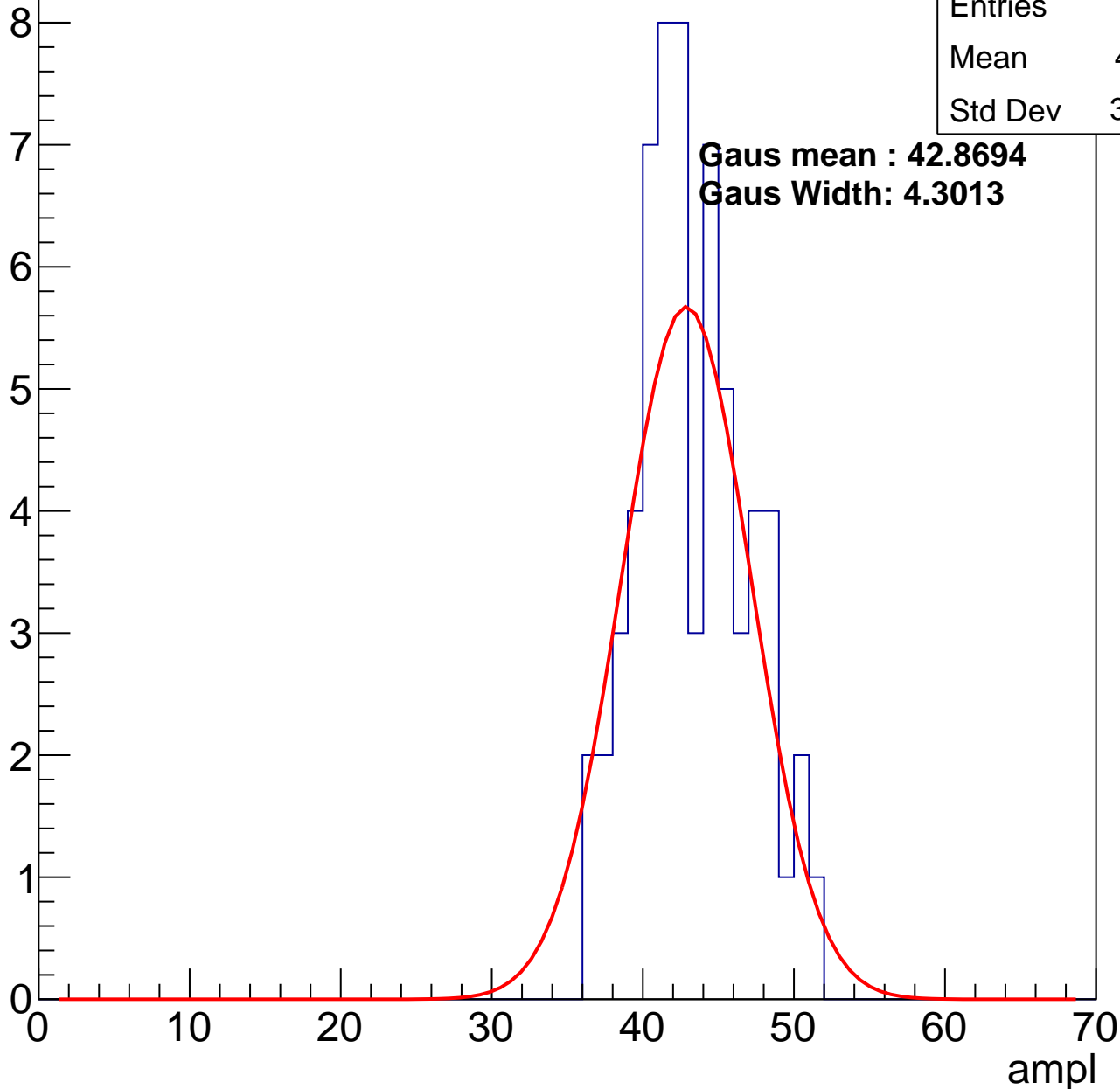
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.81
Std Dev	3.592

Gaus mean : 42.8694

Gaus Width: 4.3013

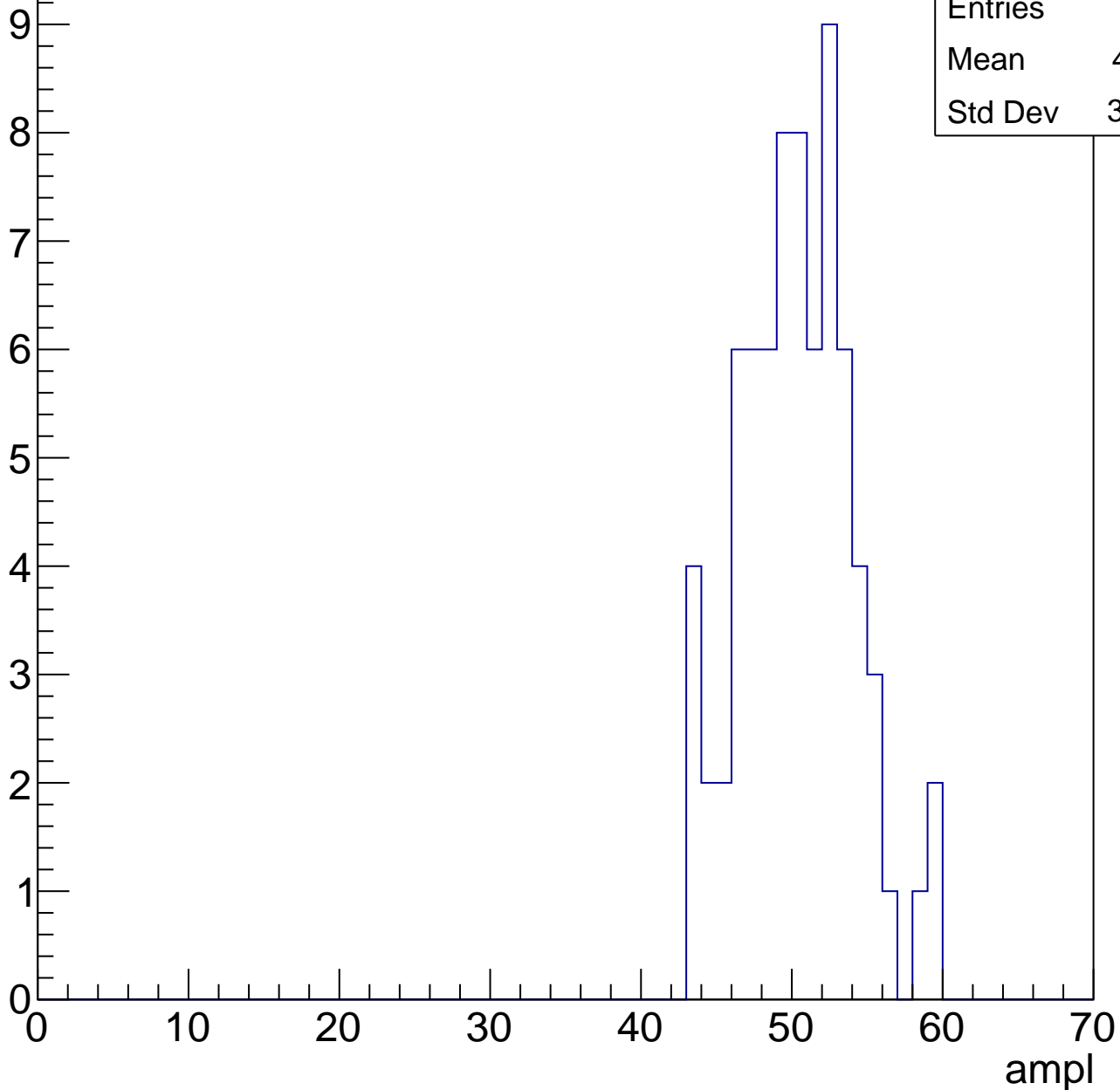


B1L103S, U14-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	49.91
Std Dev	3.669

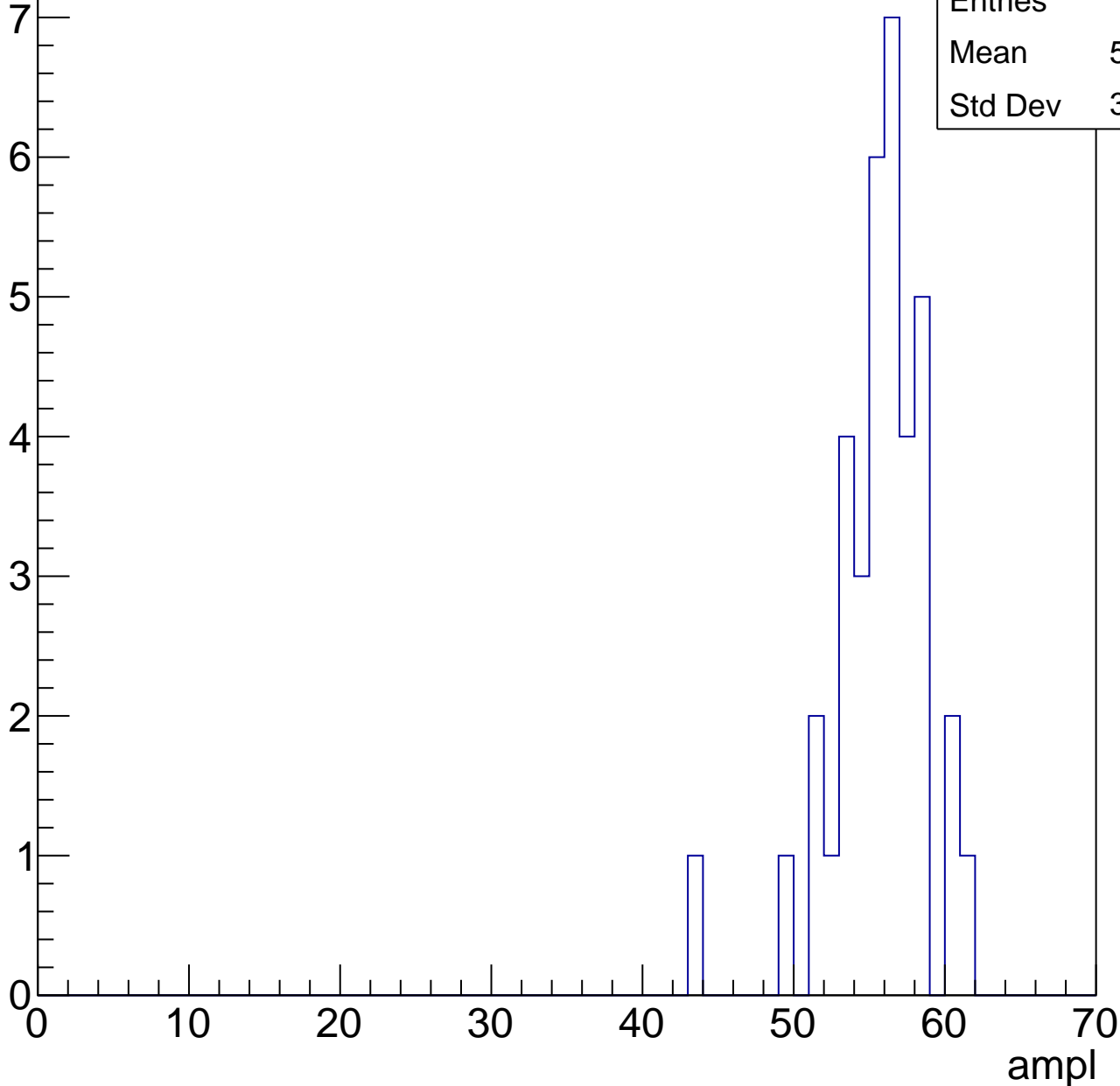


B1L103S, U14-ch99, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

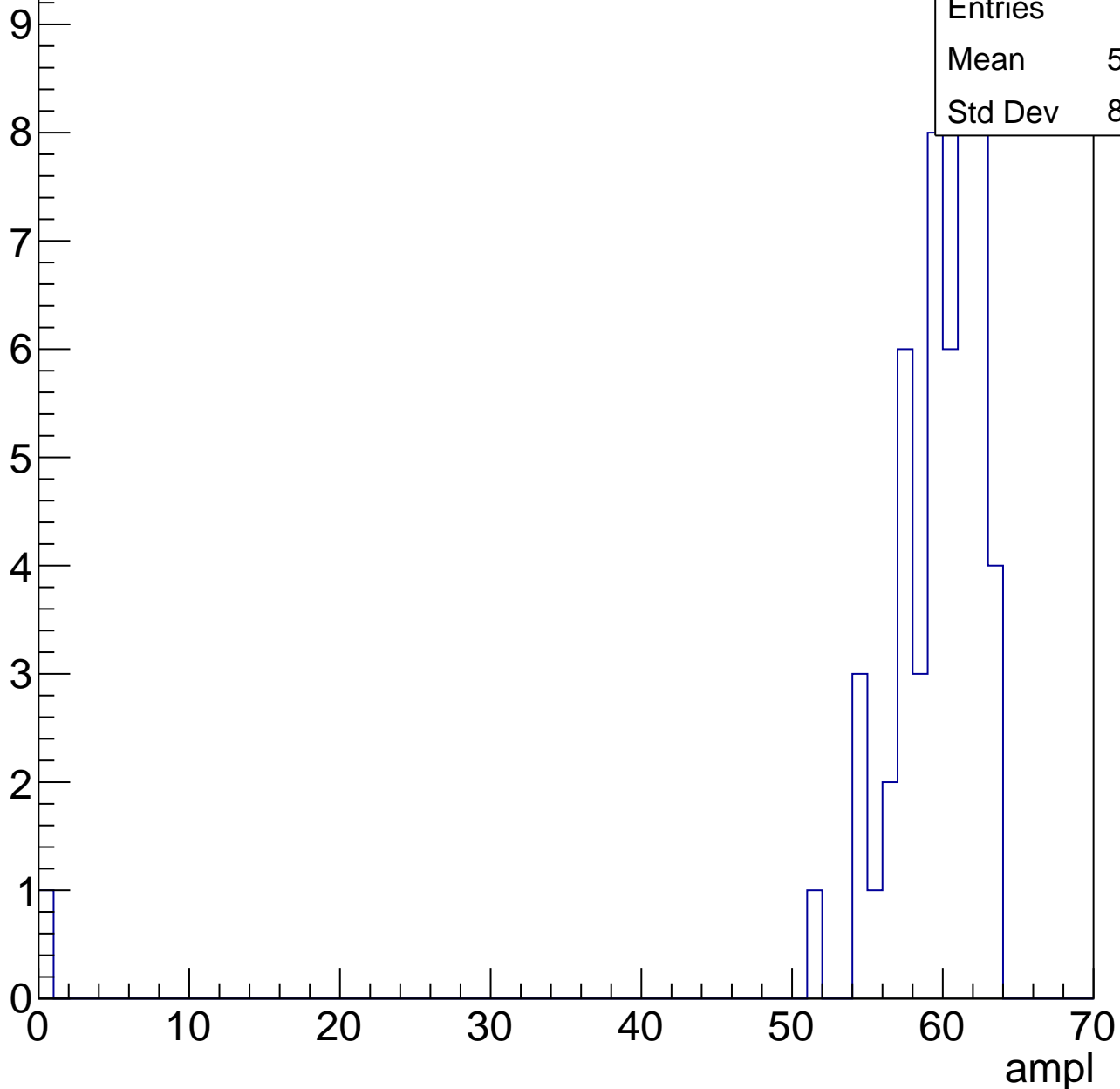
Entries	37
Mean	55.16
Std Dev	3.259



B1L103S, U14-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

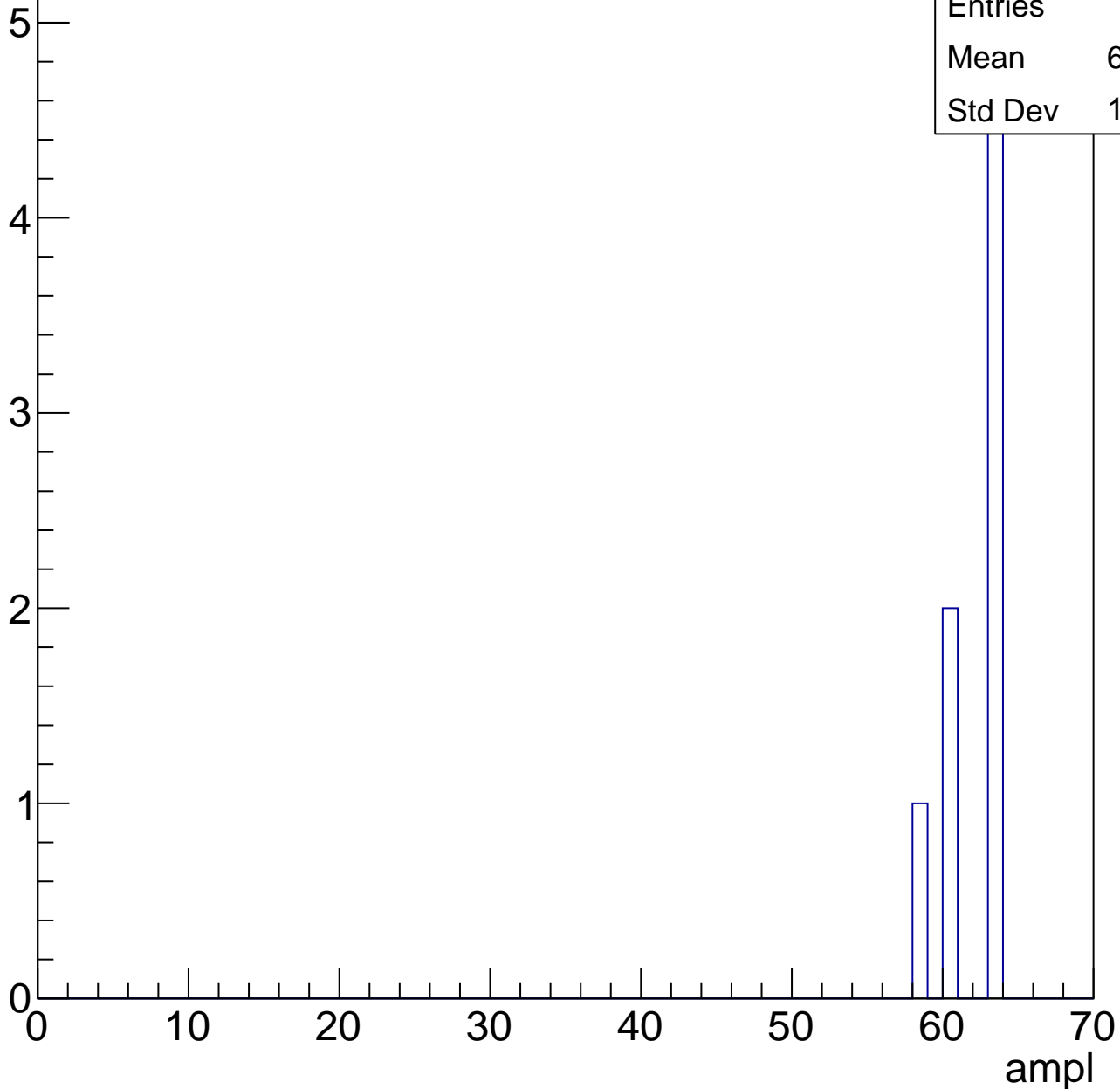


B1L103S, U14-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

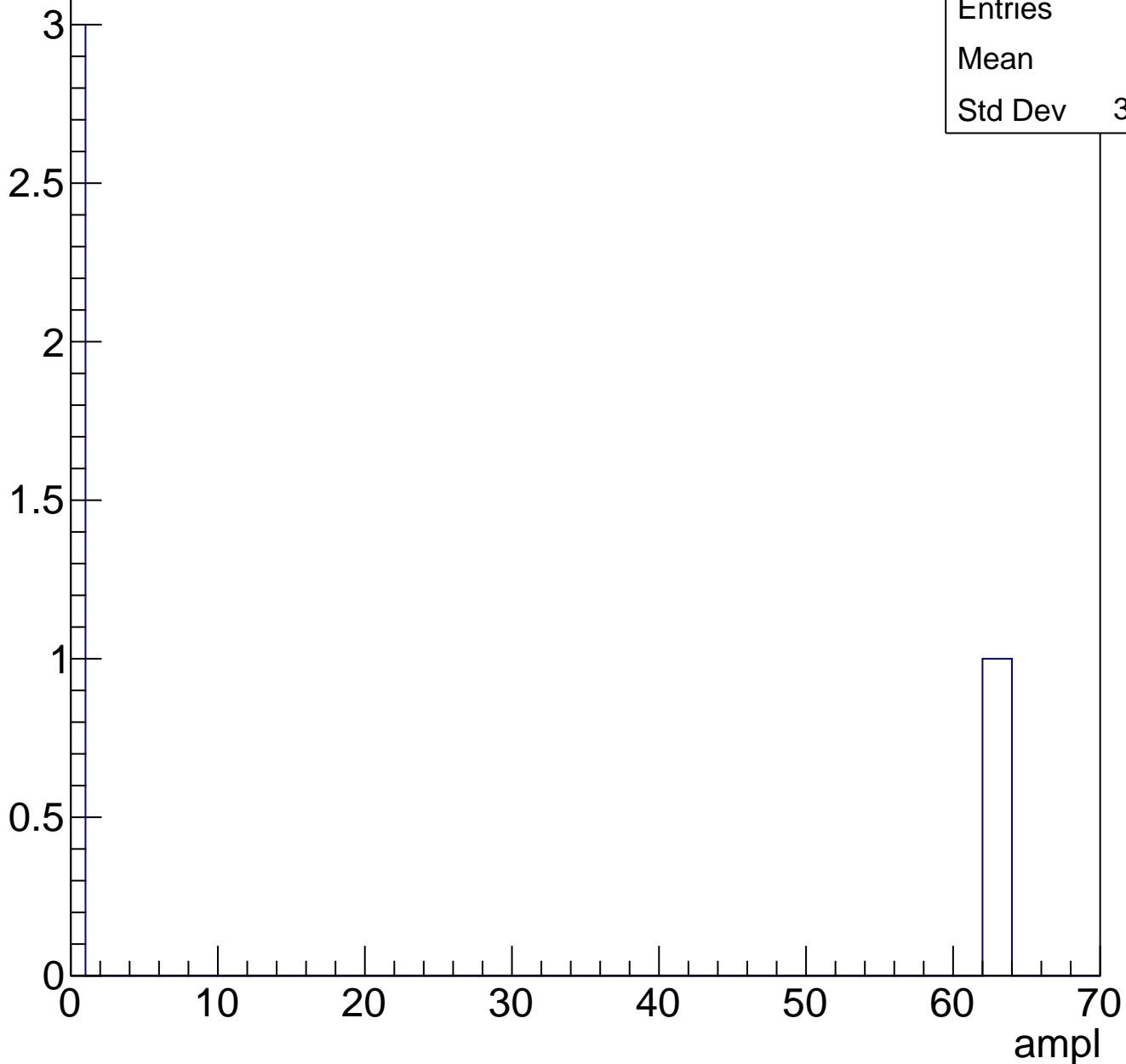
Entries	8
Mean	61.62
Std Dev	1.867



B1L103S, U14-ch99, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch100, adc0

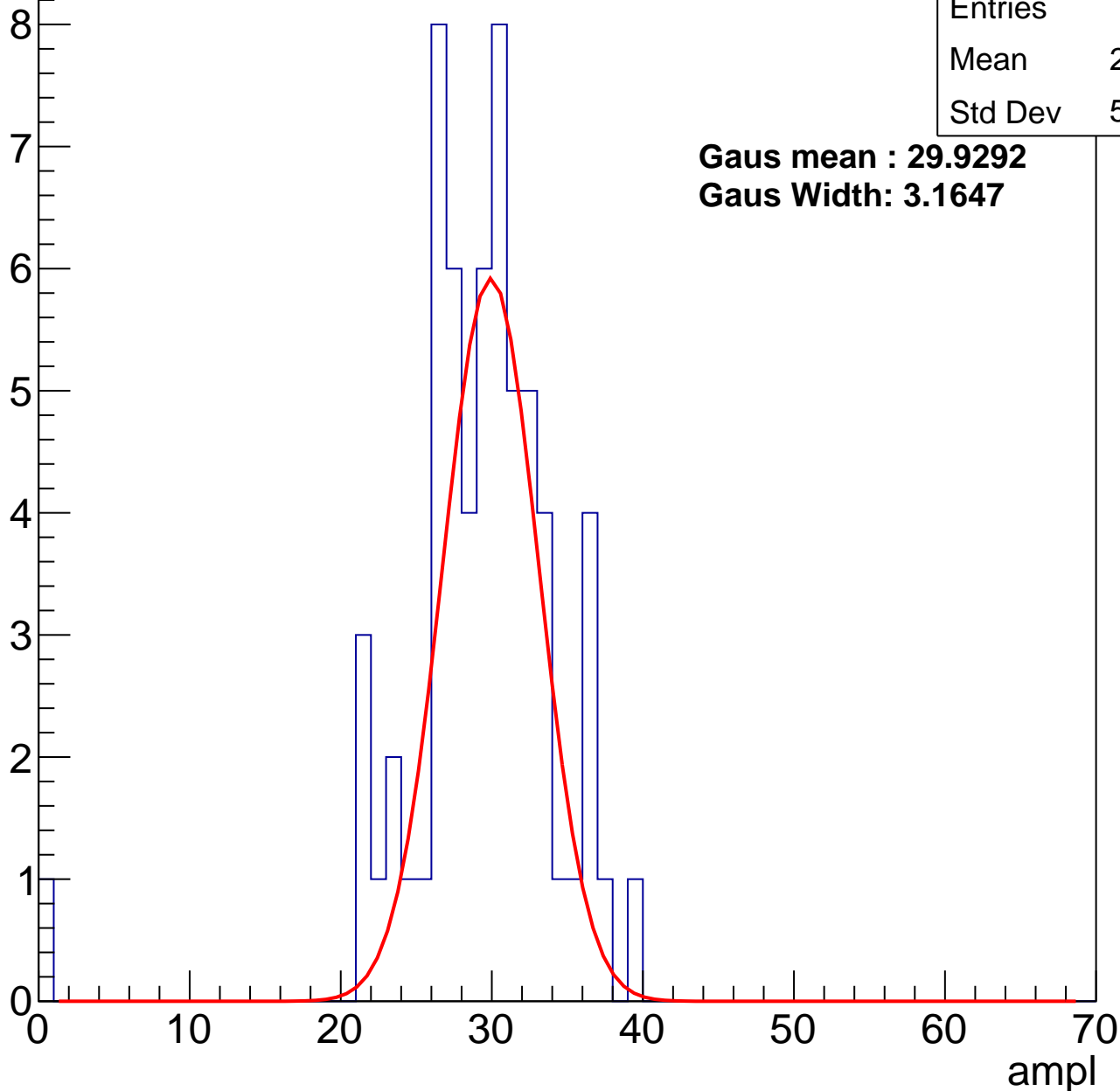
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.76
Std Dev	5.418

Gaus mean : 29.9292

Gaus Width: 3.1647



B1L103S, U14-ch100, adc1

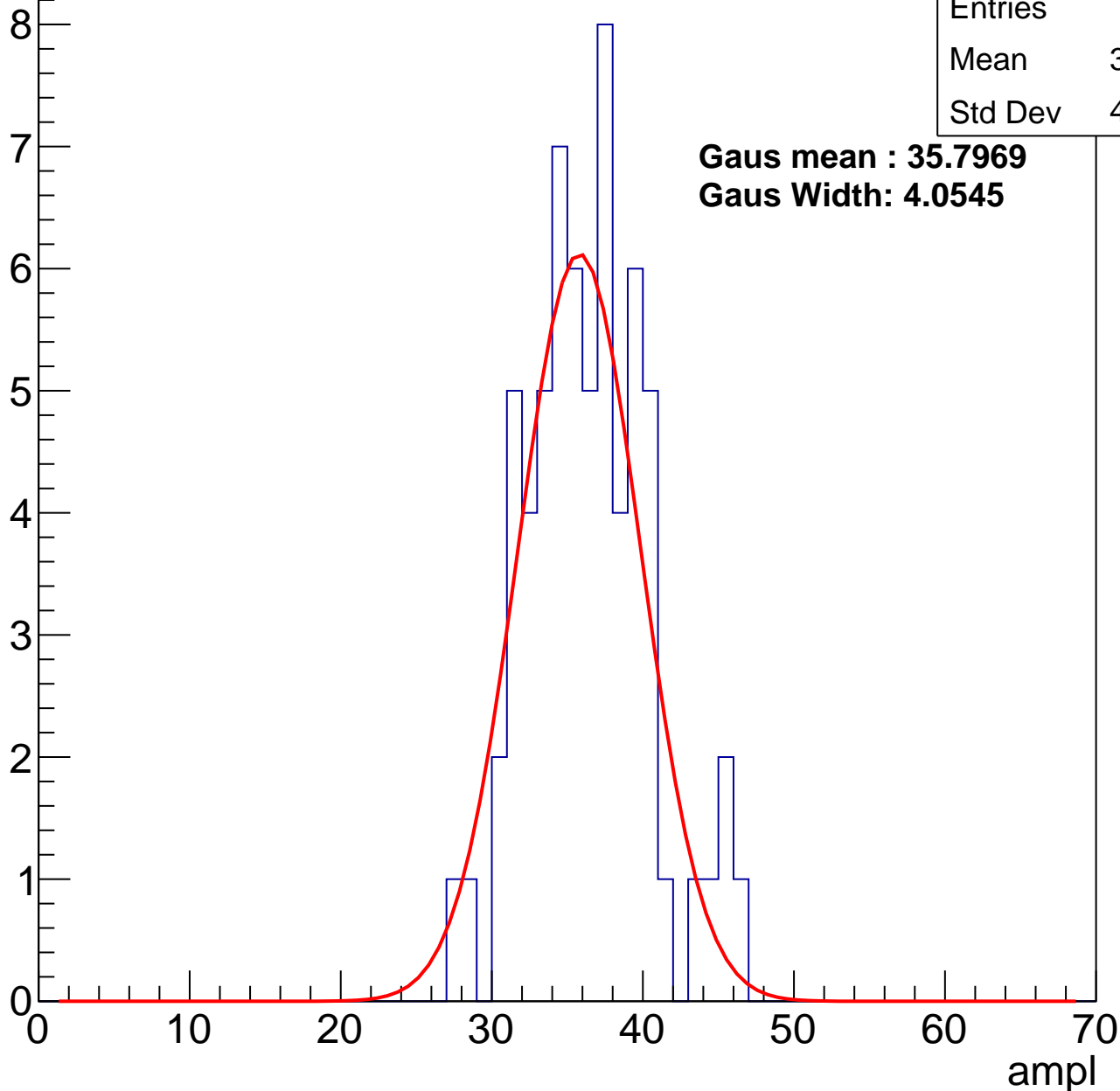
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.95
Std Dev	4.005

Gaus mean : 35.7969

Gaus Width: 4.0545



B1L103S, U14-ch100, adc2

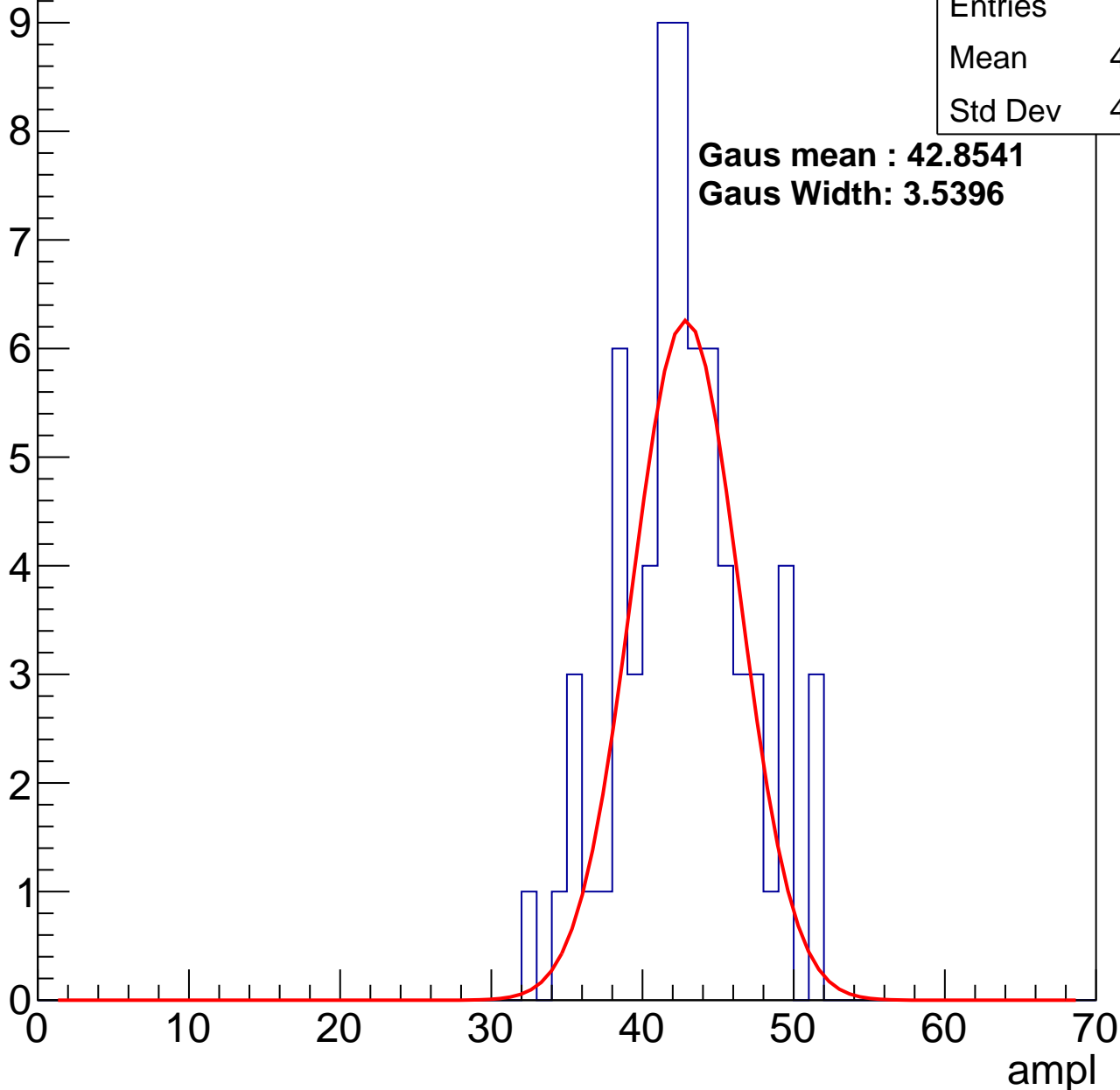
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	42.26
Std Dev	4.164

Gaus mean : 42.8541

Gaus Width: 3.5396

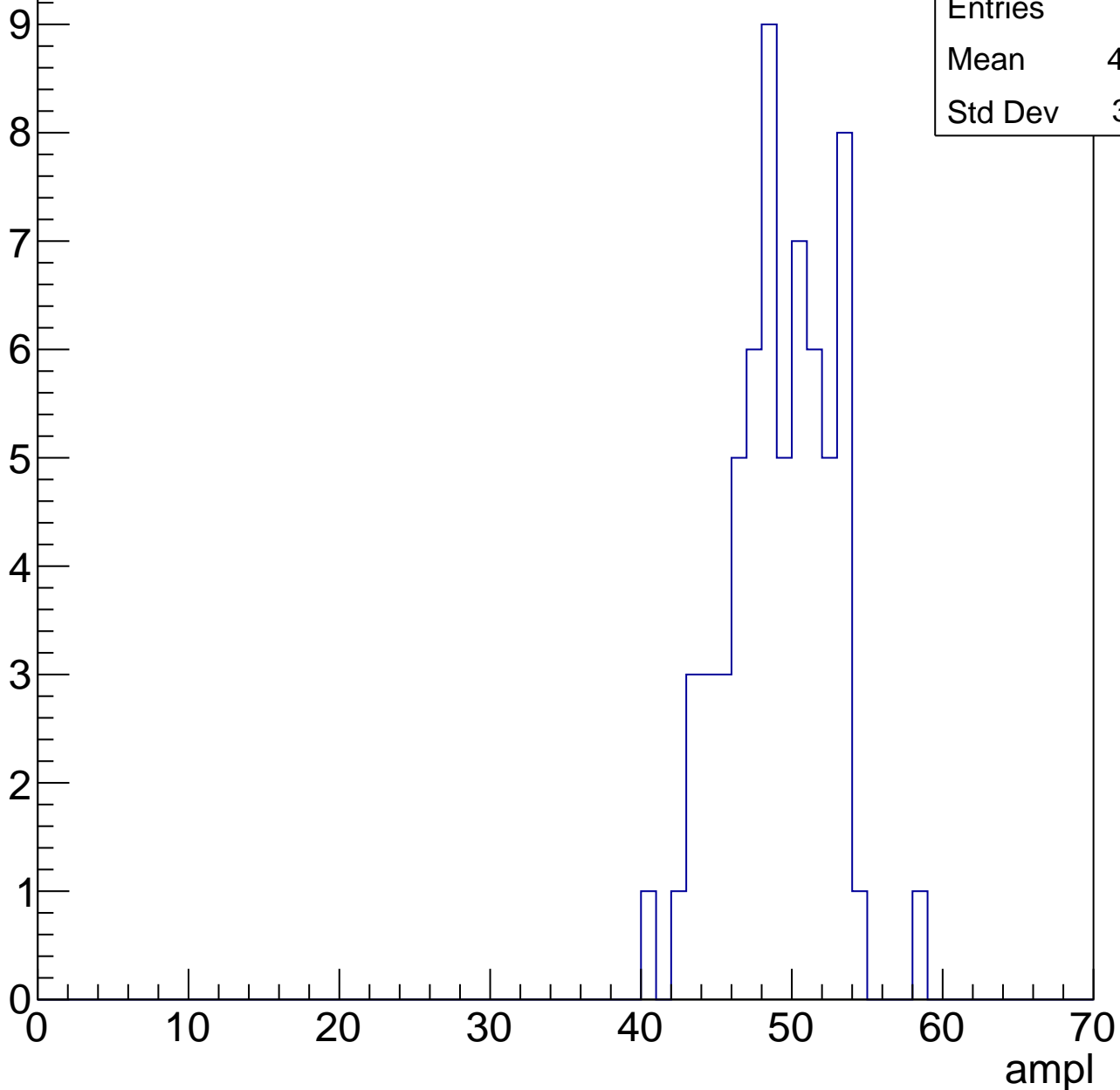


B1L103S, U14-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	48.73
Std Dev	3.411

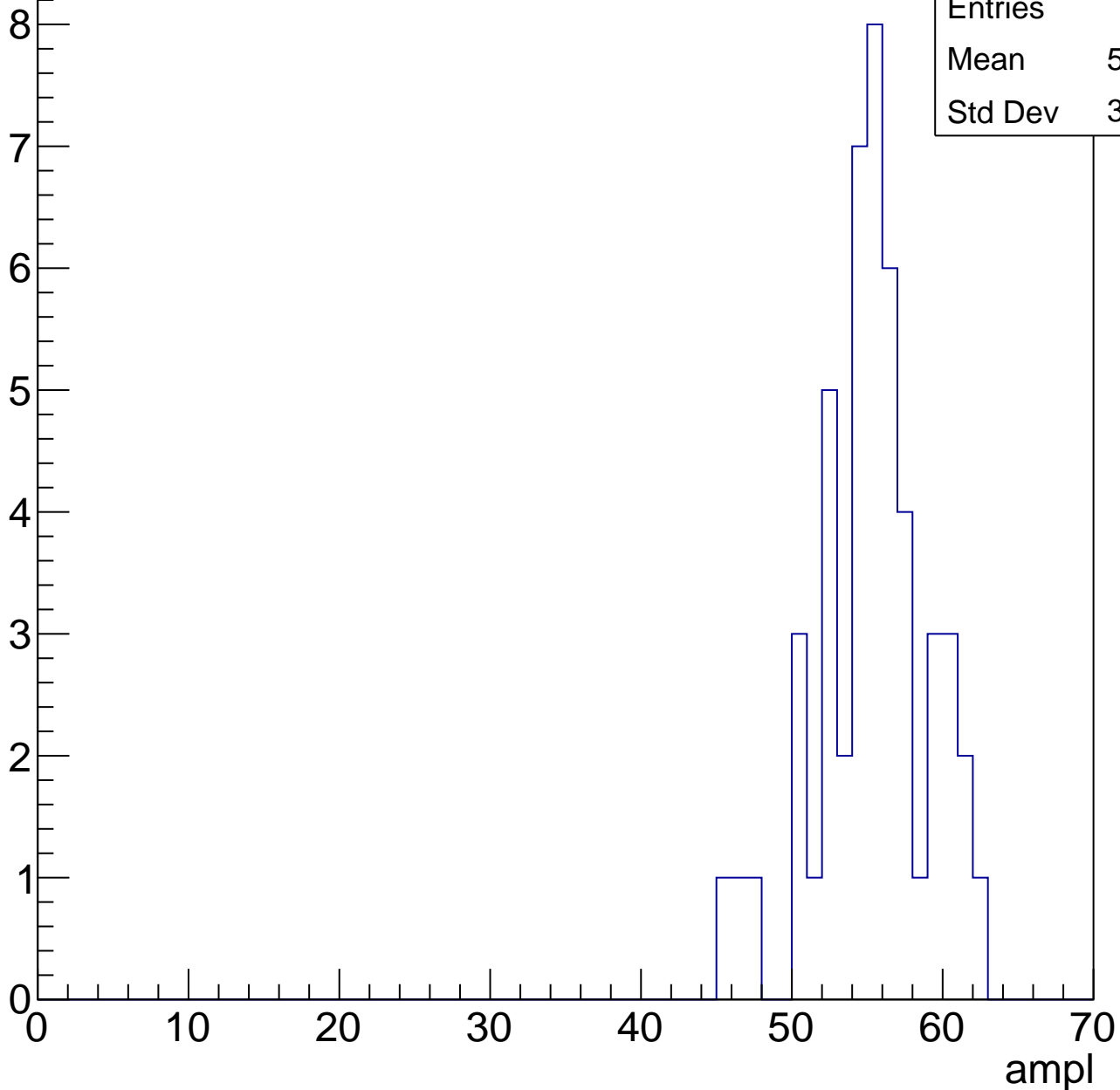


B1L103S, U14-ch100, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

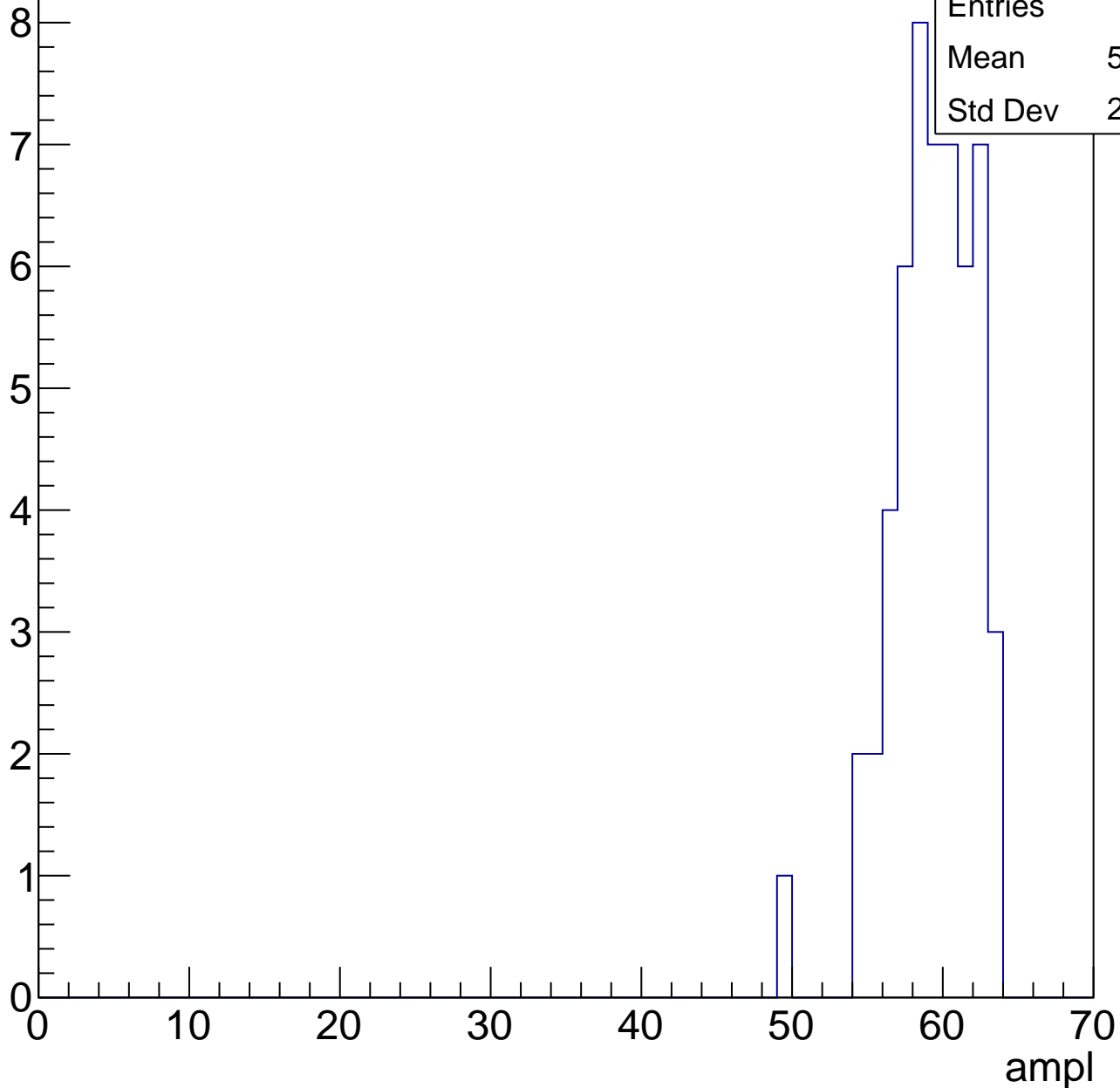
Entries	49
Mean	54.82
Std Dev	3.696



B1L103S, U14-ch100, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

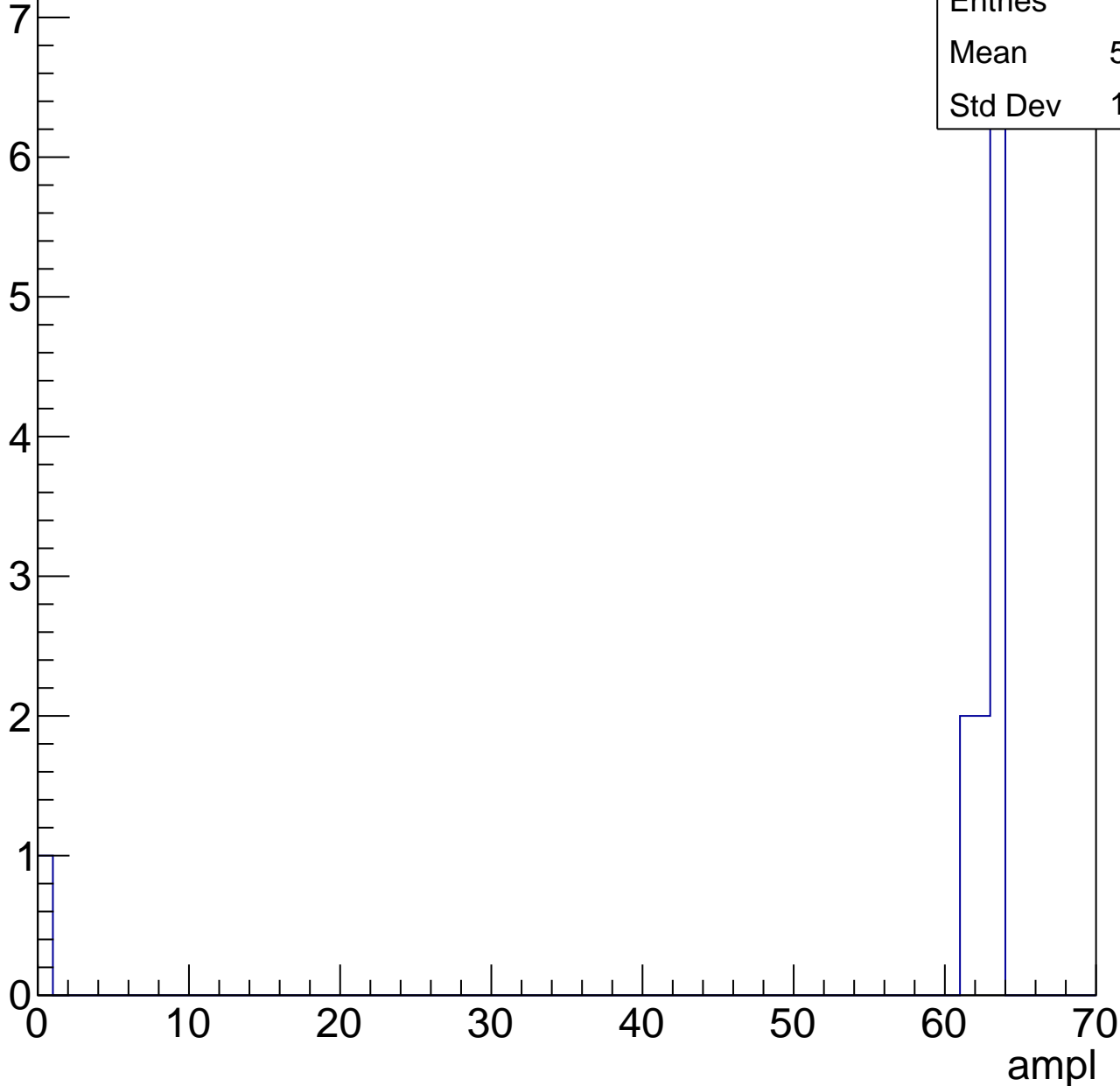


B1L103S, U14-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	57.25
Std Dev	17.28



B1L103S, U14-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U14-ch101, adc0

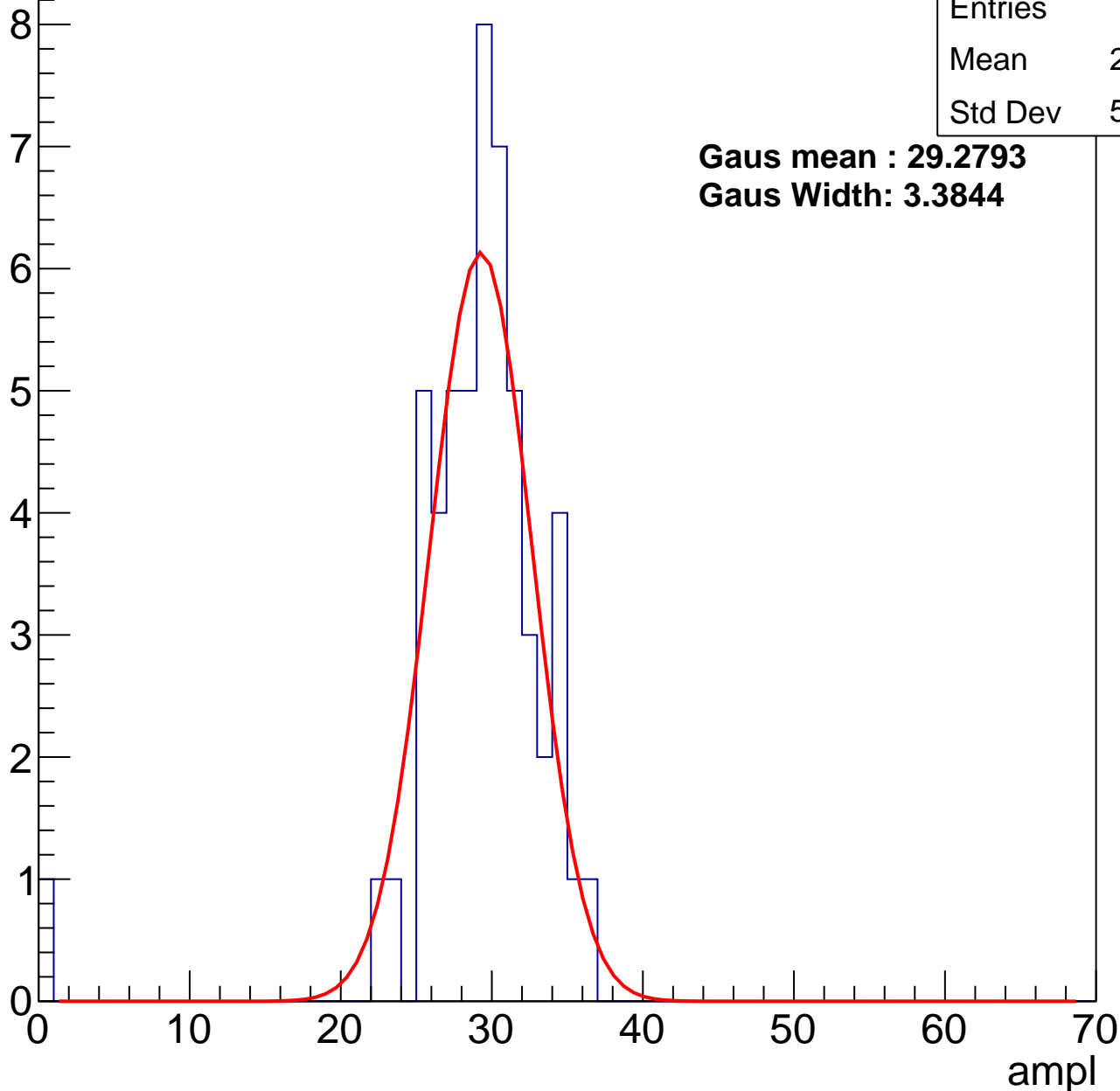
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	28.58
Std Dev	5.007

Gaus mean : 29.2793

Gaus Width: 3.3844



B1L103S, U14-ch101, adc1

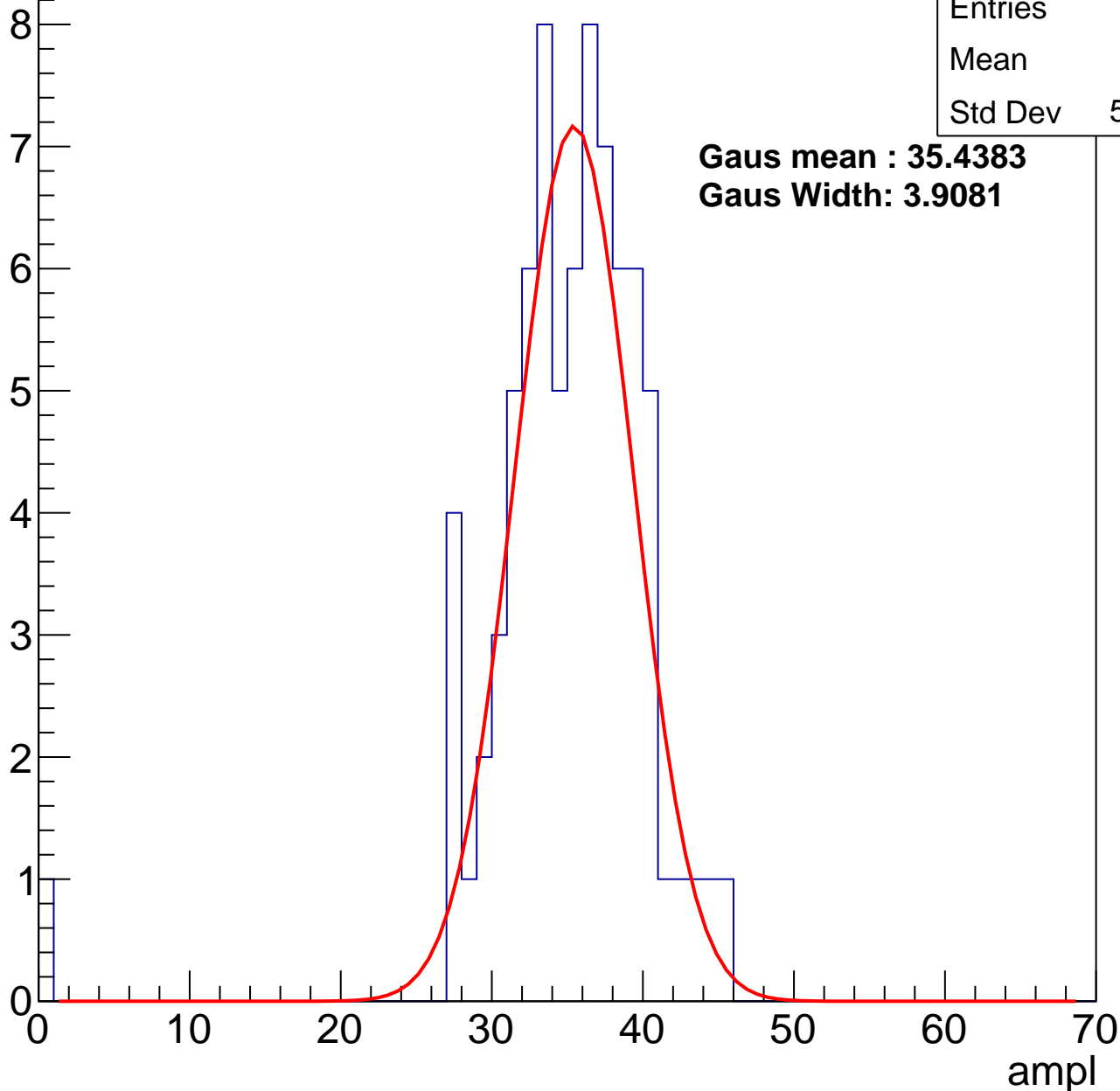
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	34.6
Std Dev	5.635

Gaus mean : 35.4383

Gaus Width: 3.9081



B1L103S, U14-ch101, adc2

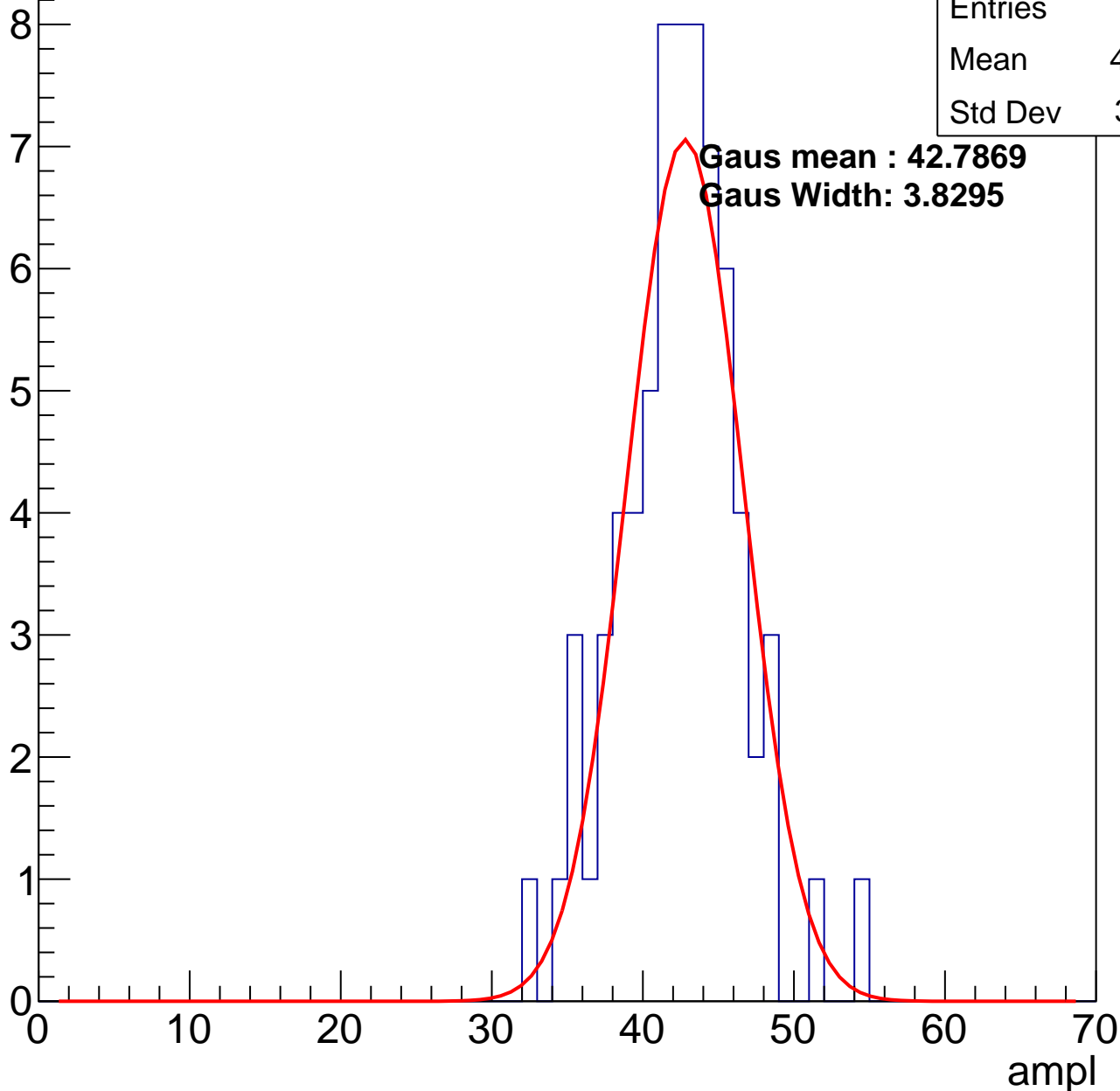
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	41.99
Std Dev	3.941

Gaus mean : 42.7869

Gaus Width: 3.8295

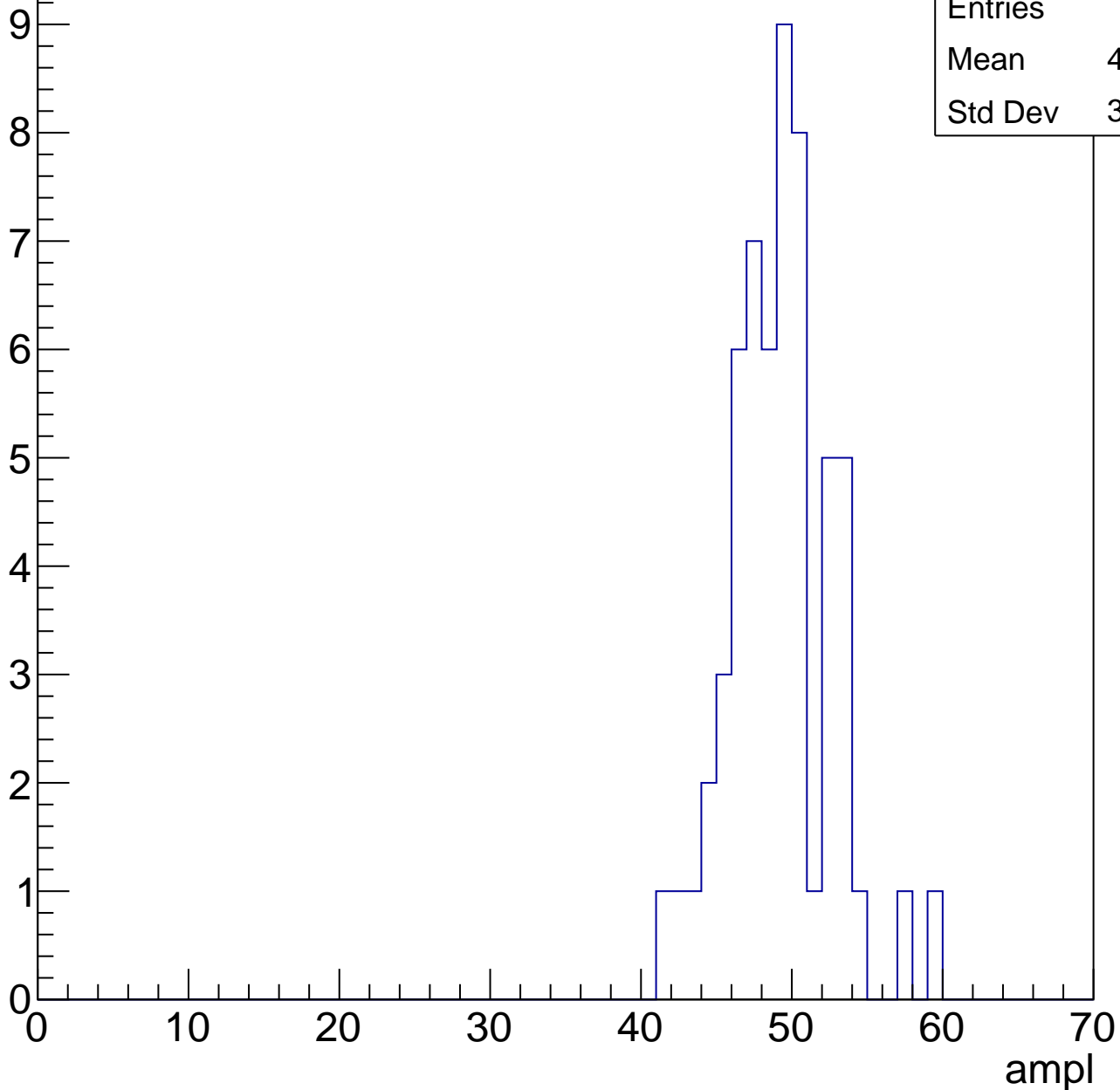


B1L103S, U14-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	48.78
Std Dev	3.368

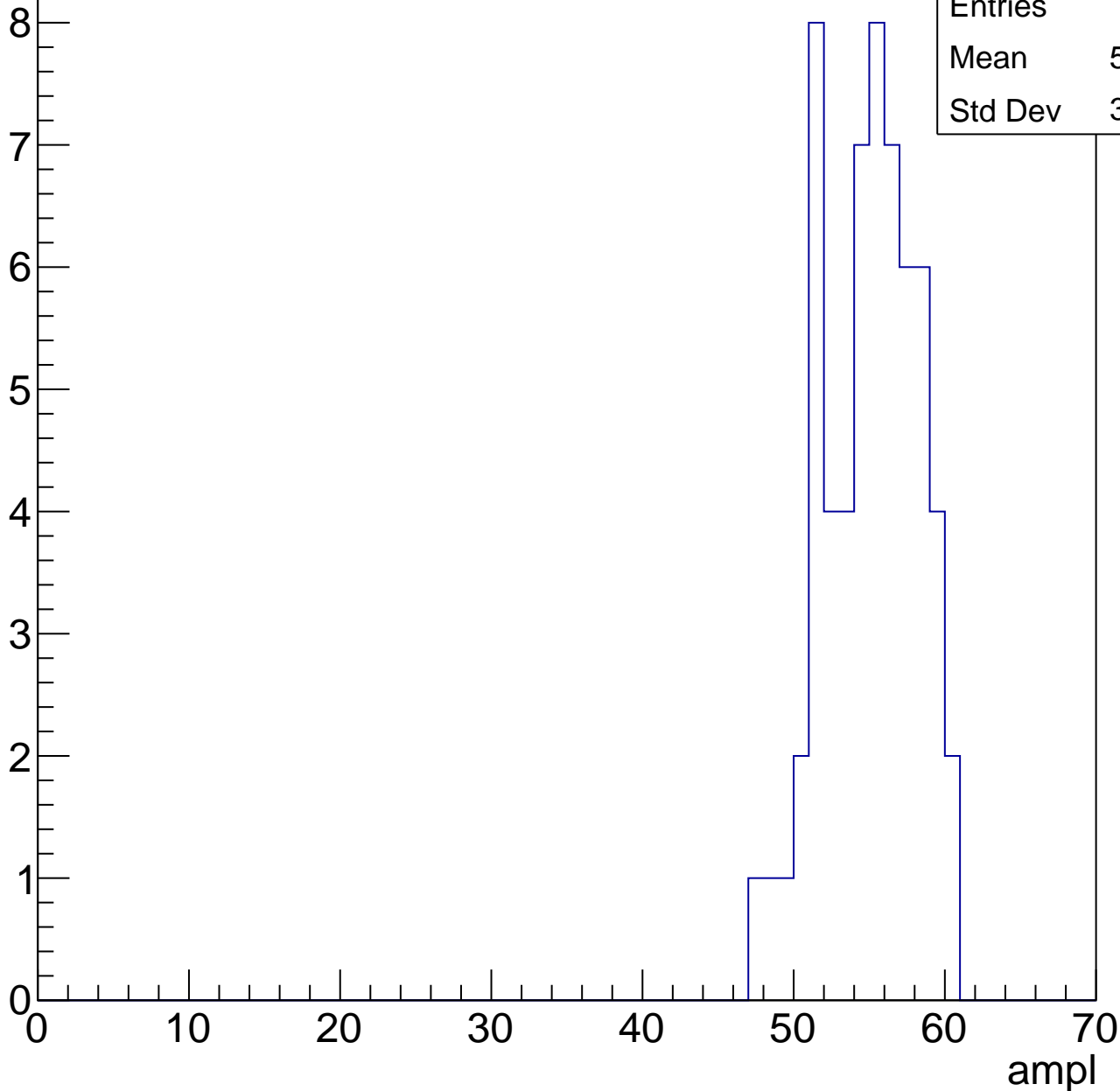


B1L103S, U14-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

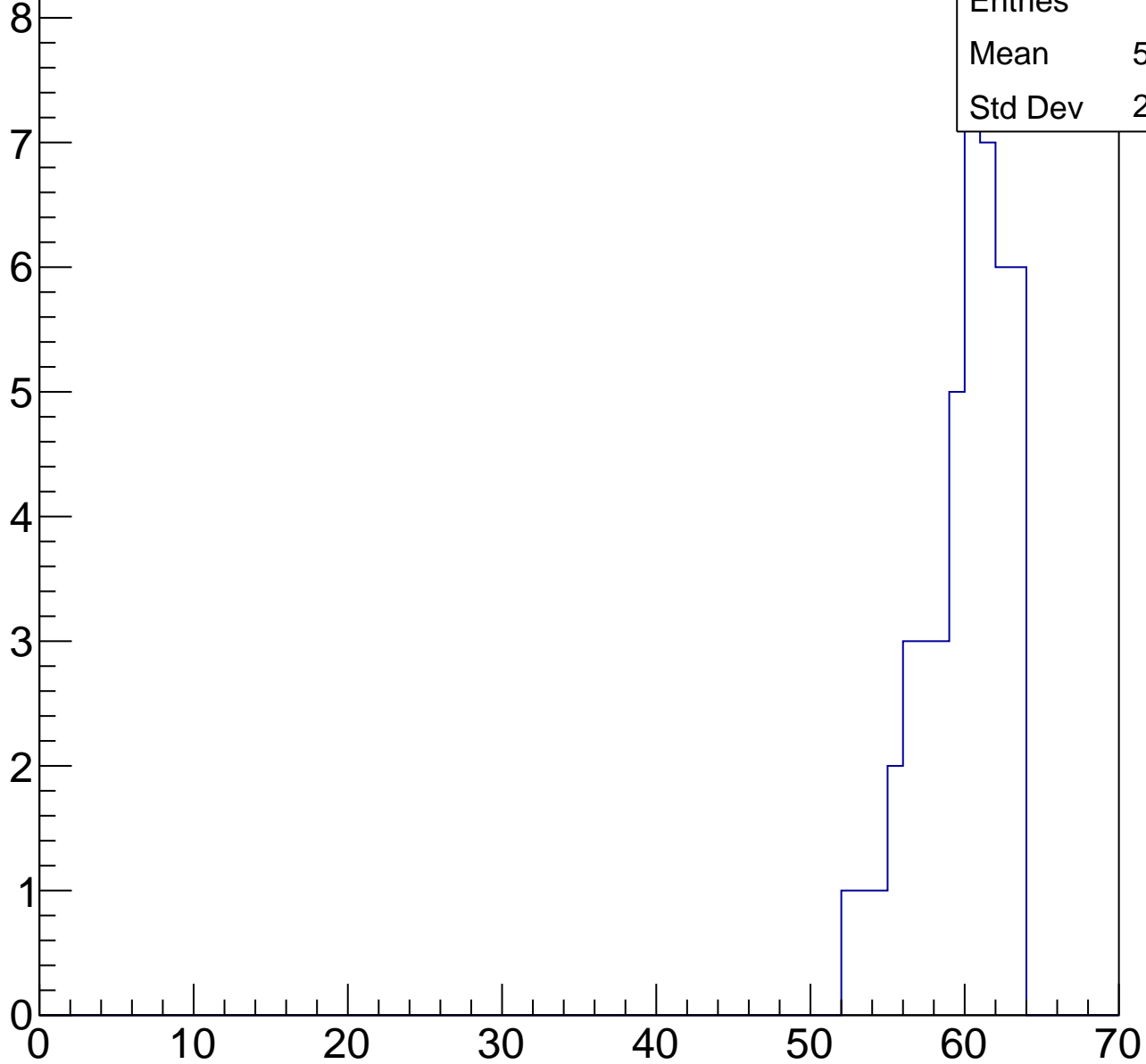
Entries	61
Mean	54.56
Std Dev	3.065



B1L103S, U14-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



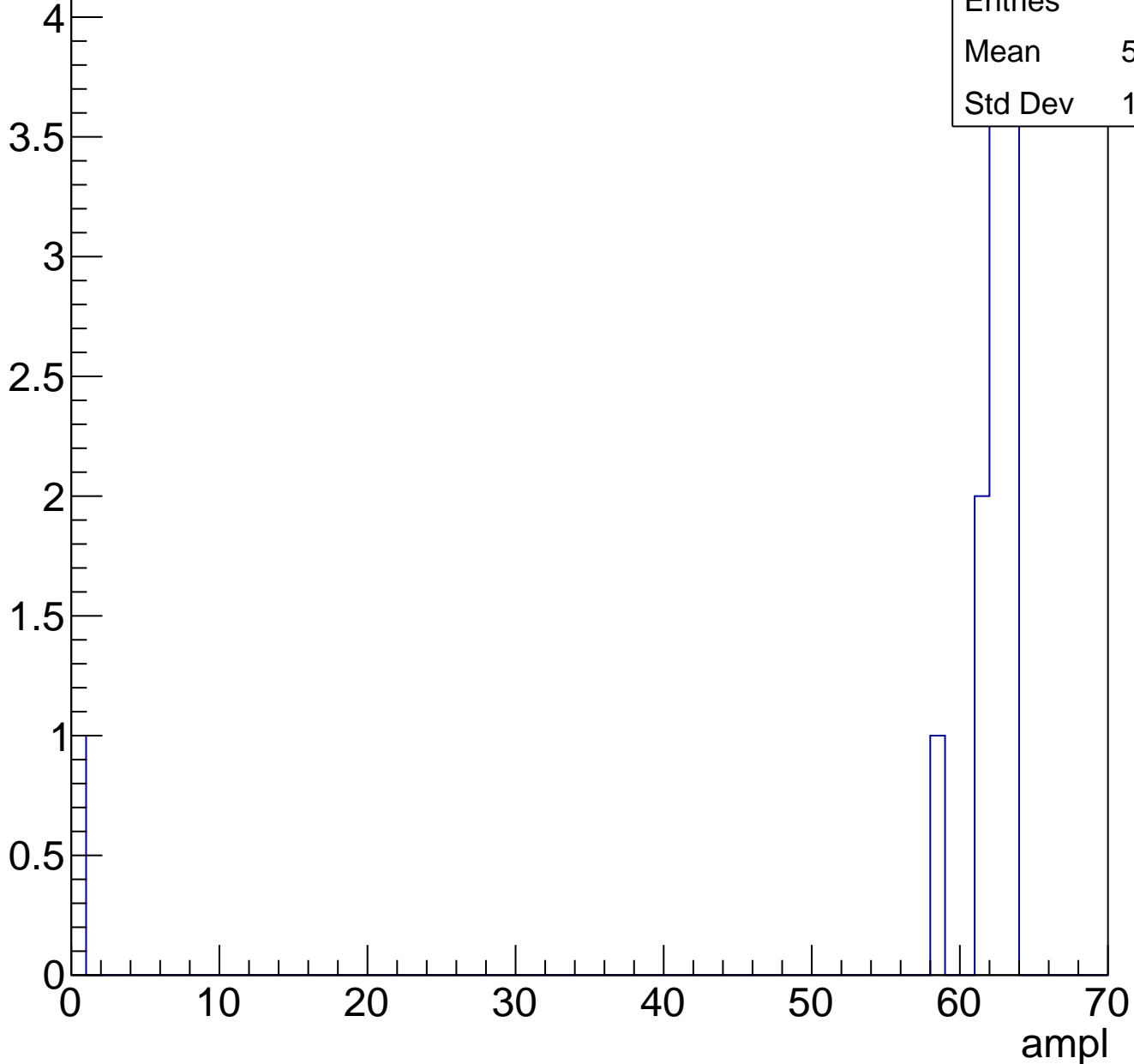
Entries	46
Mean	59.43
Std Dev	2.803

ampl

B1L103S, U14-ch101, adc6

calib_packv5_042523_0143.root, FC#7, port C2

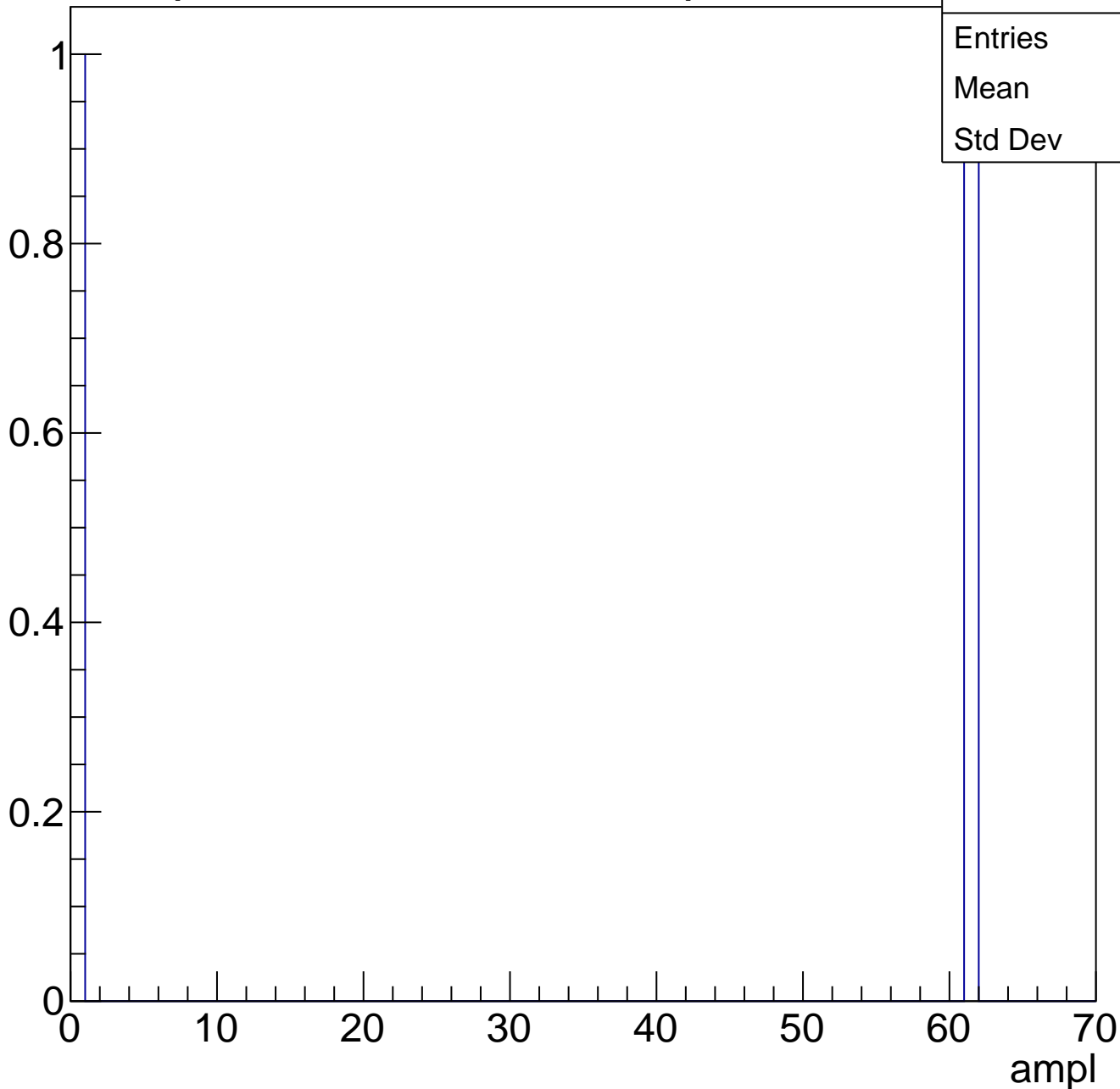
Entry



B1L103S, U14-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch102, adc0

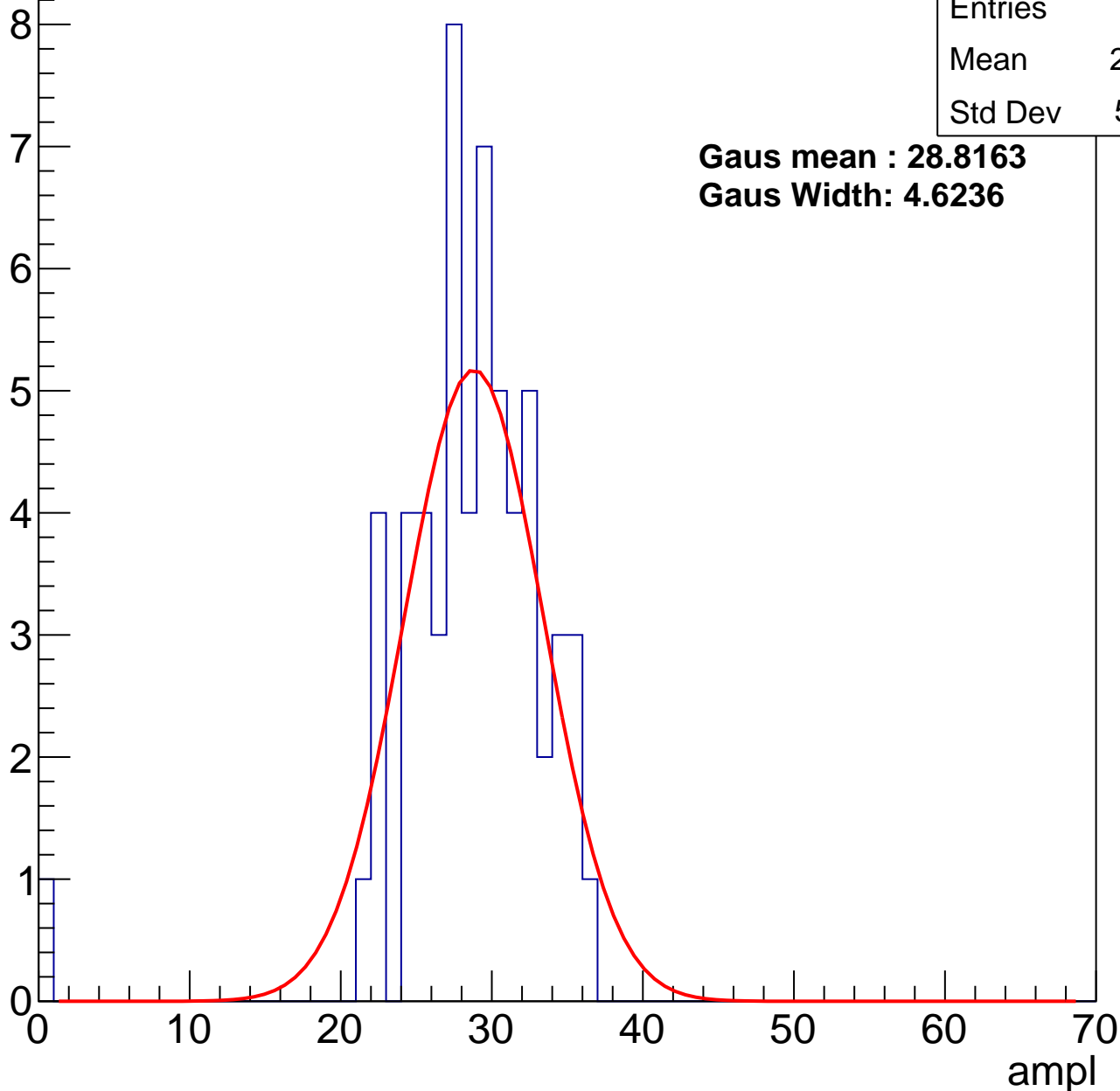
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	28.08
Std Dev	5.221

Gaus mean : 28.8163

Gaus Width: 4.6236



B1L103S, U14-ch102, adc1

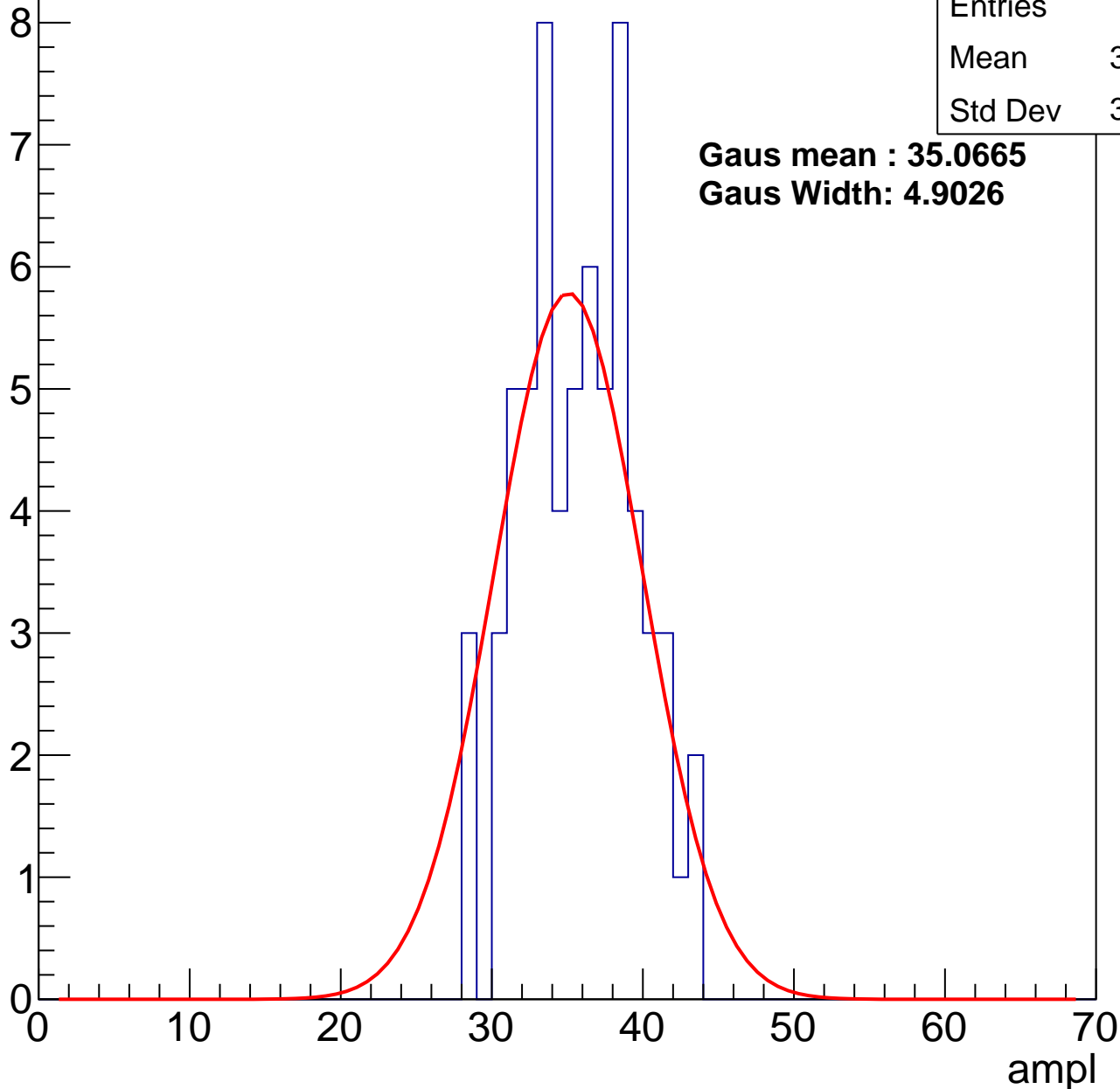
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.32
Std Dev	3.717

Gaus mean : 35.0665

Gaus Width: 4.9026



B1L103S, U14-ch102, adc2

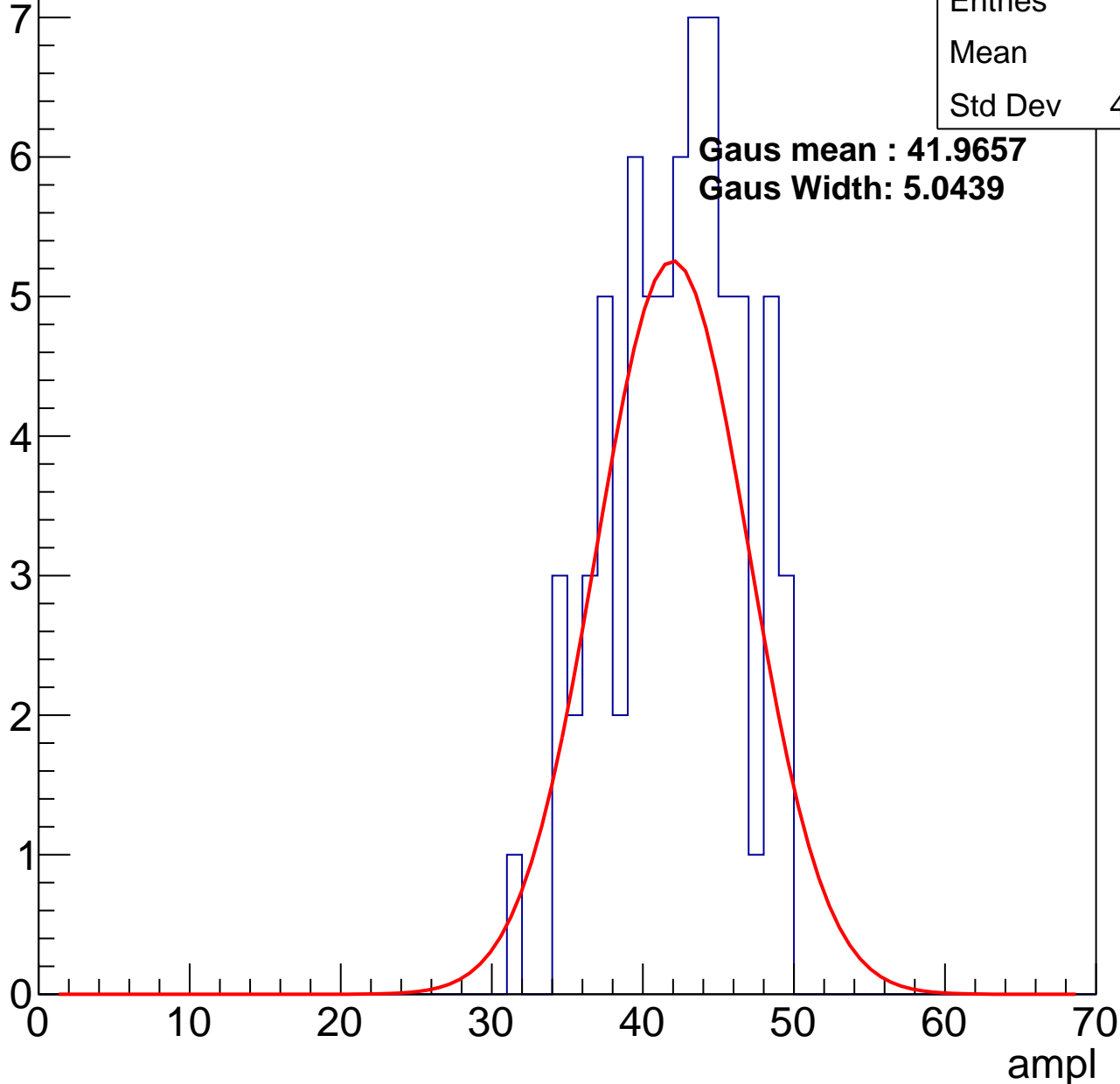
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	41.7
Std Dev	4.214

Gaus mean : 41.9657

Gaus Width: 5.0439

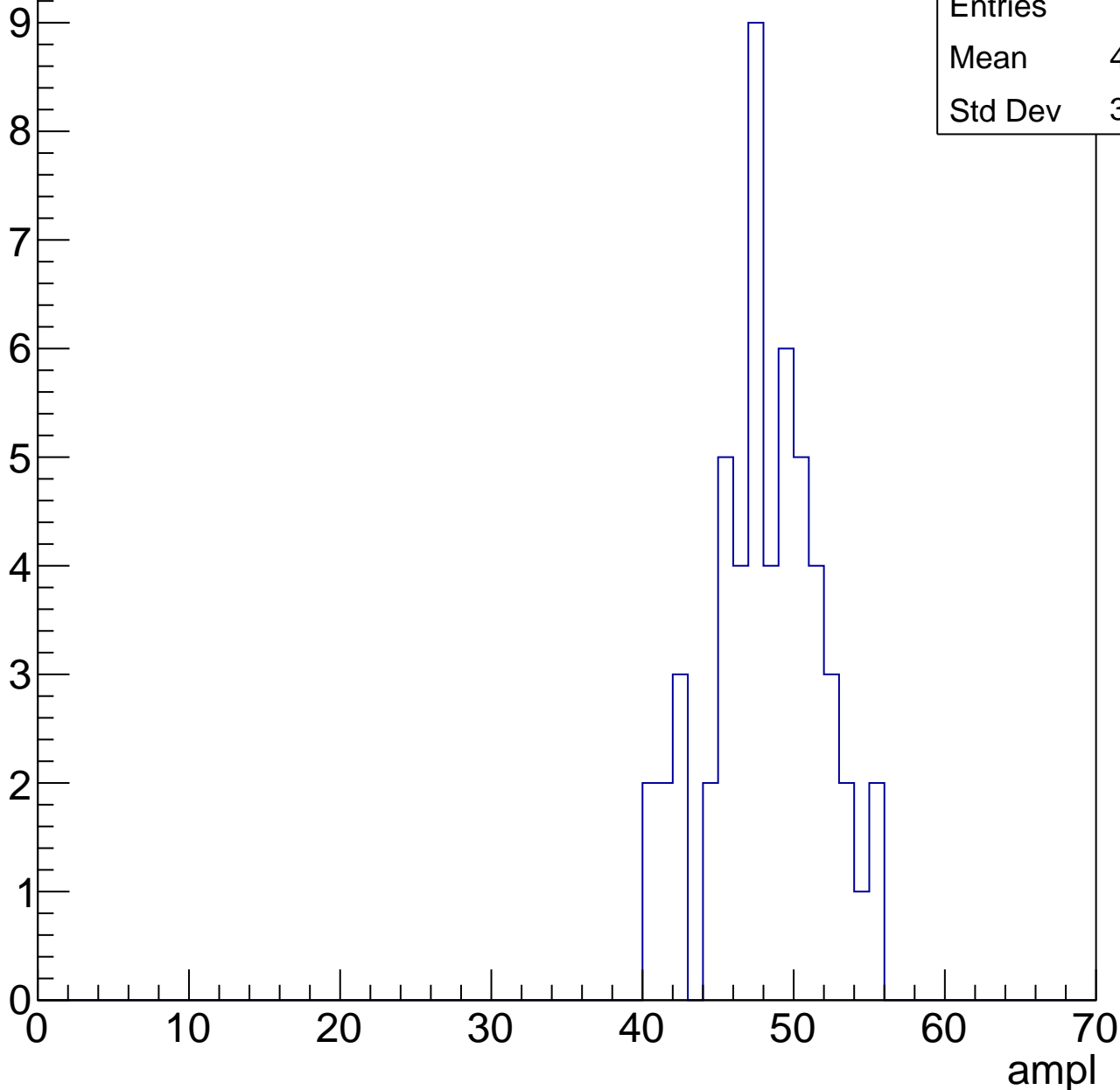


B1L103S, U14-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	47.67
Std Dev	3.662

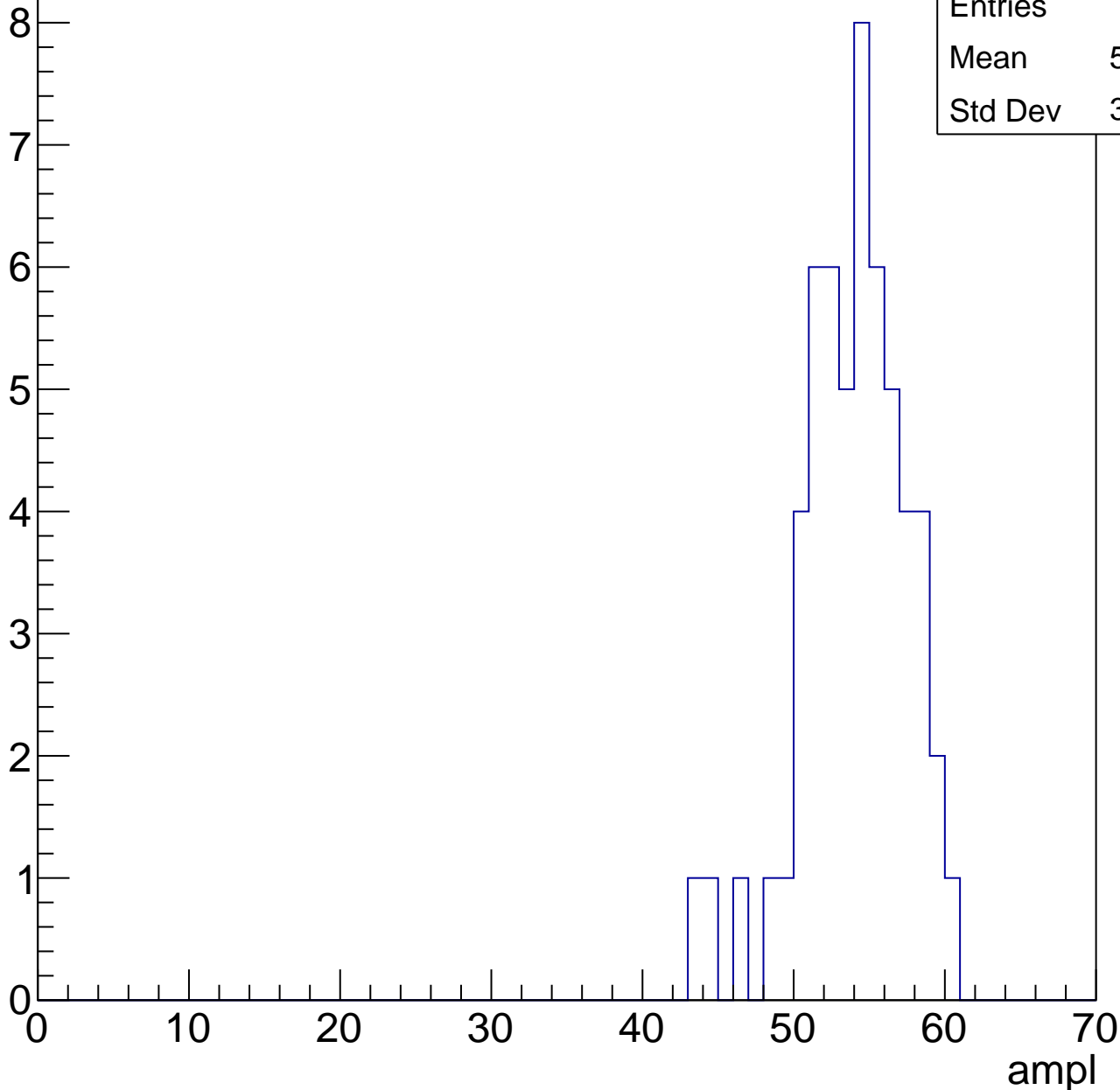


B1L103S, U14-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	53.45
Std Dev	3.505

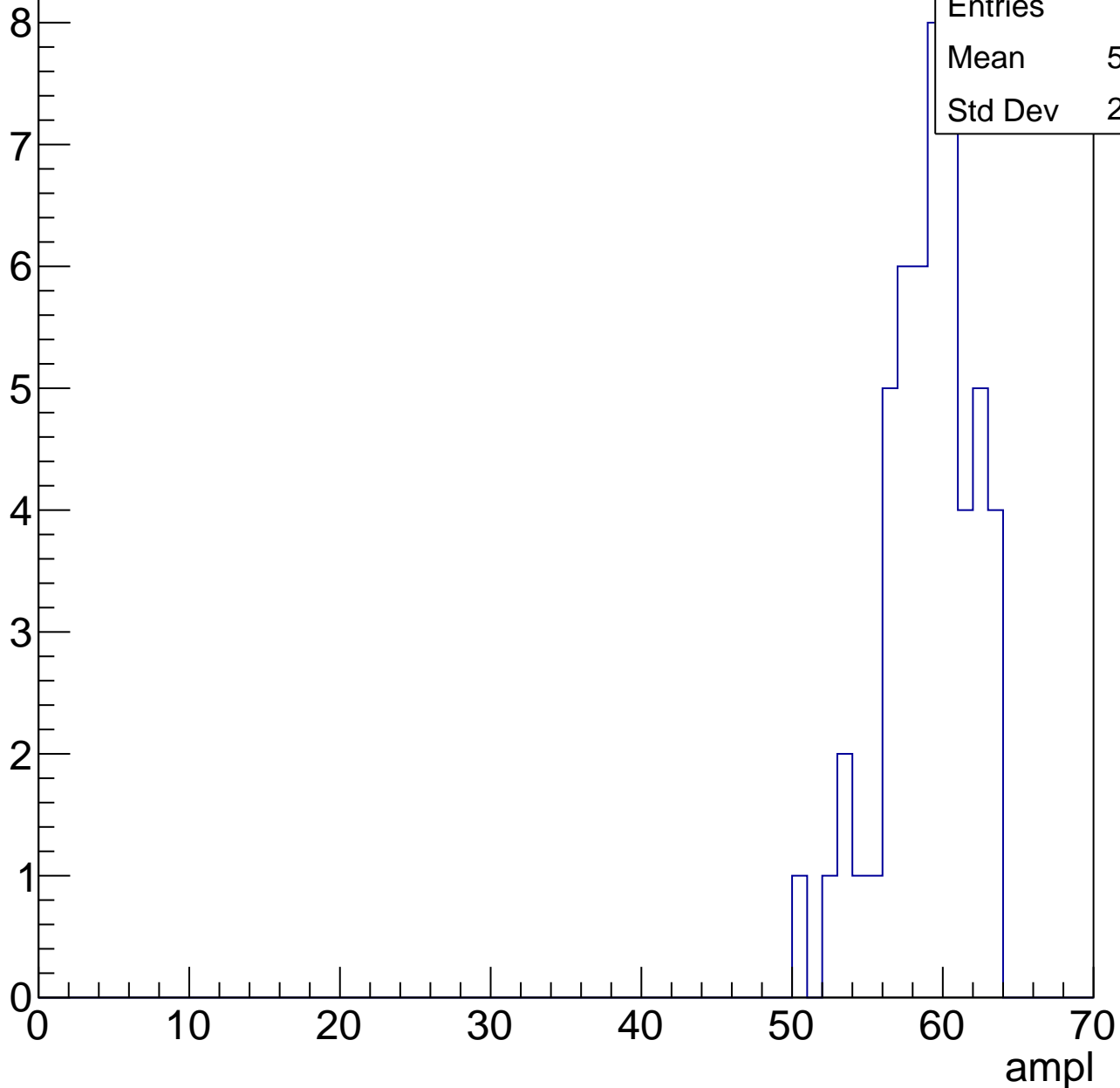


B1L103S, U14-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

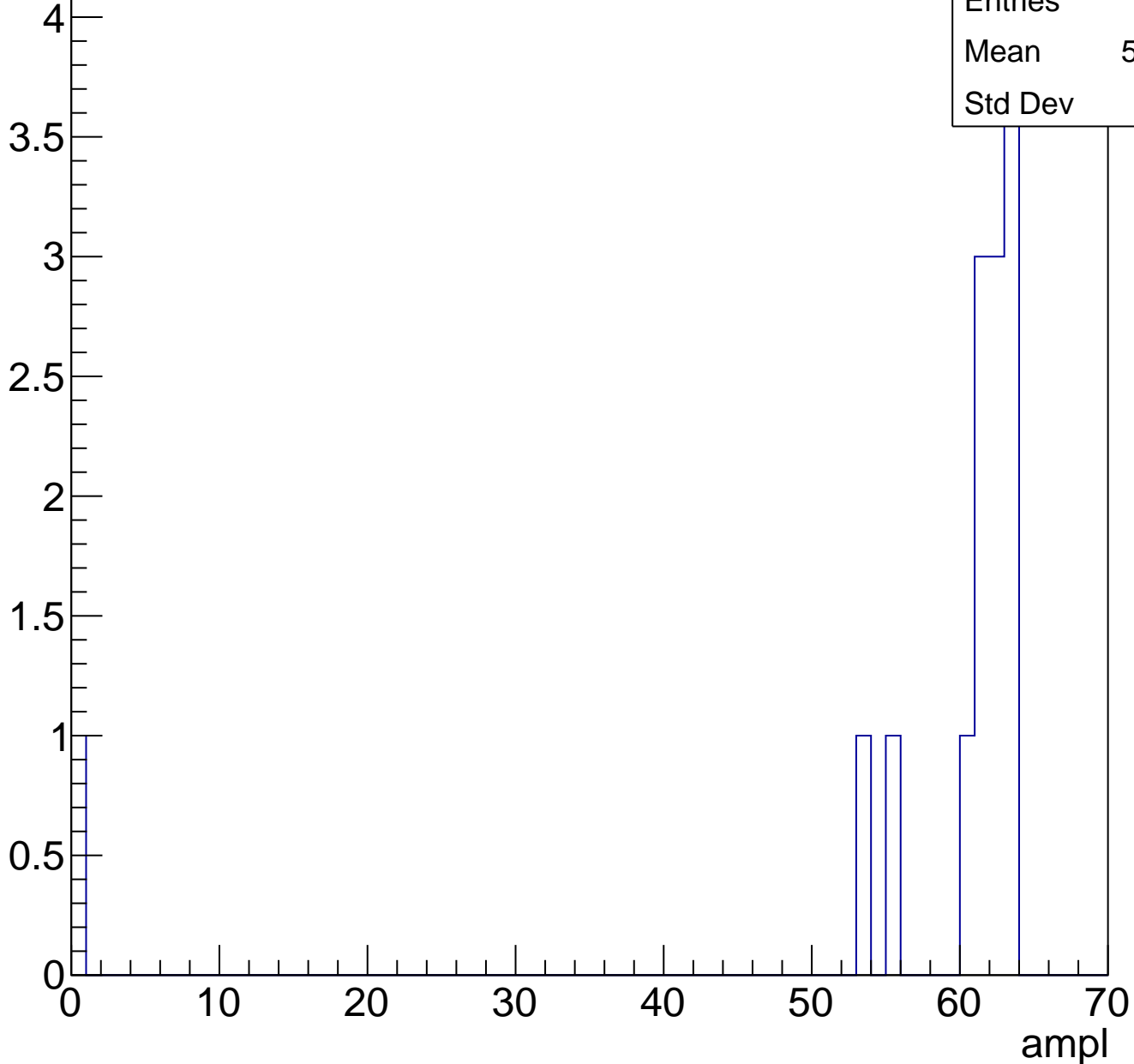
Entries	52
Mean	58.56
Std Dev	2.912



B1L103S, U14-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

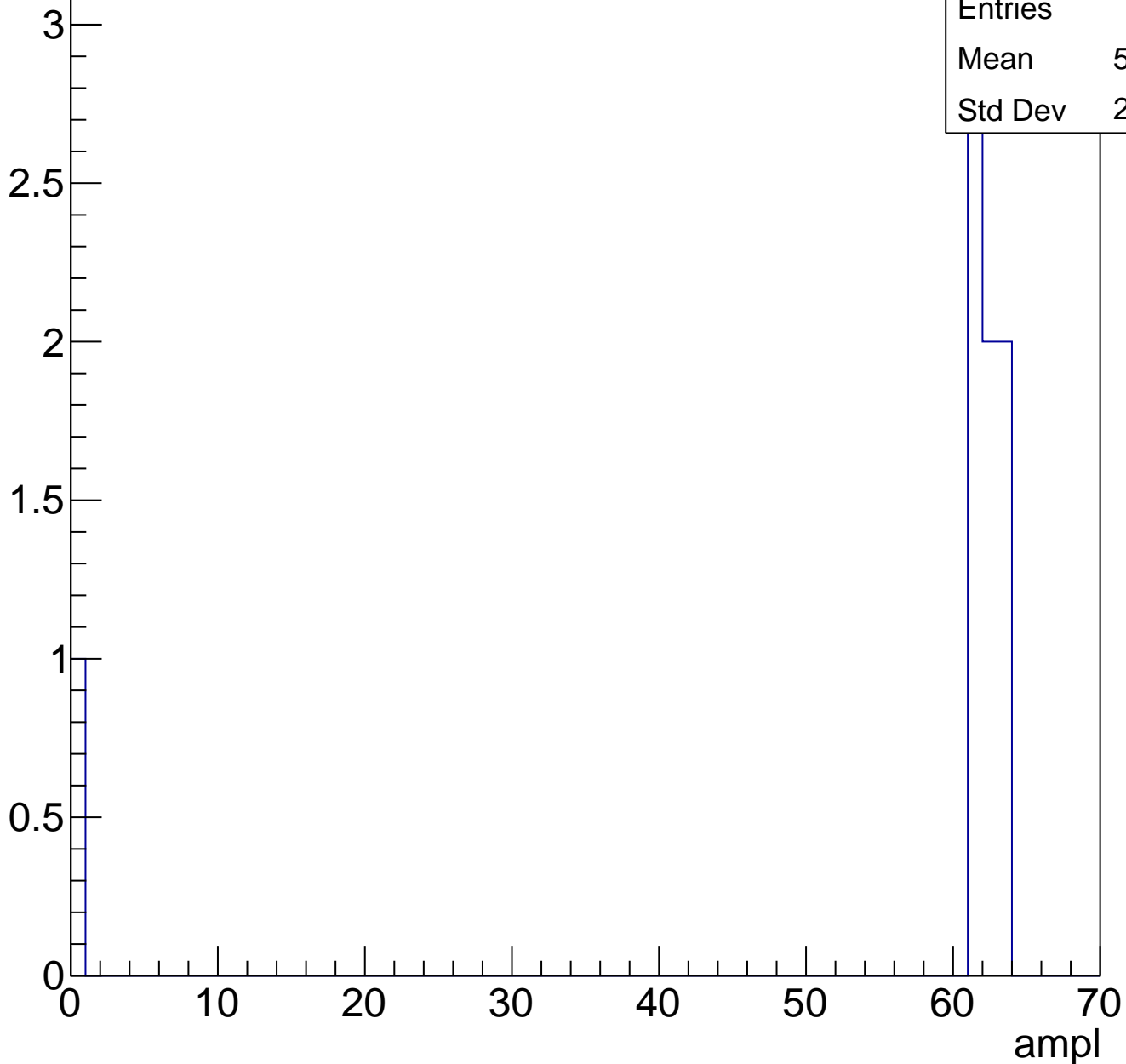
Entry



B1L103S, U14-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch103, adc0

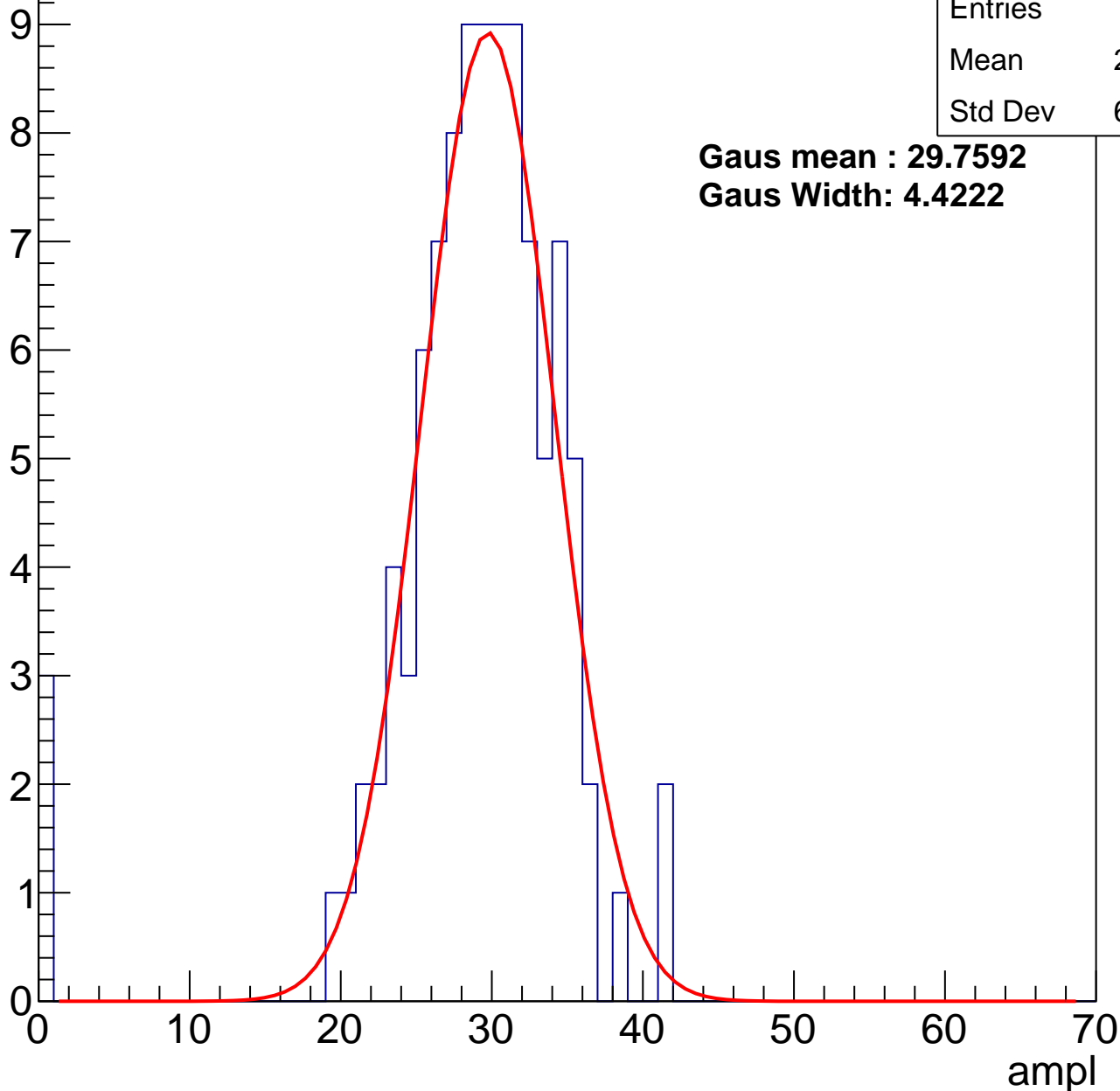
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	102
Mean	28.36
Std Dev	6.506

Gaus mean : 29.7592

Gaus Width: 4.4222



B1L103S, U14-ch103, adc1

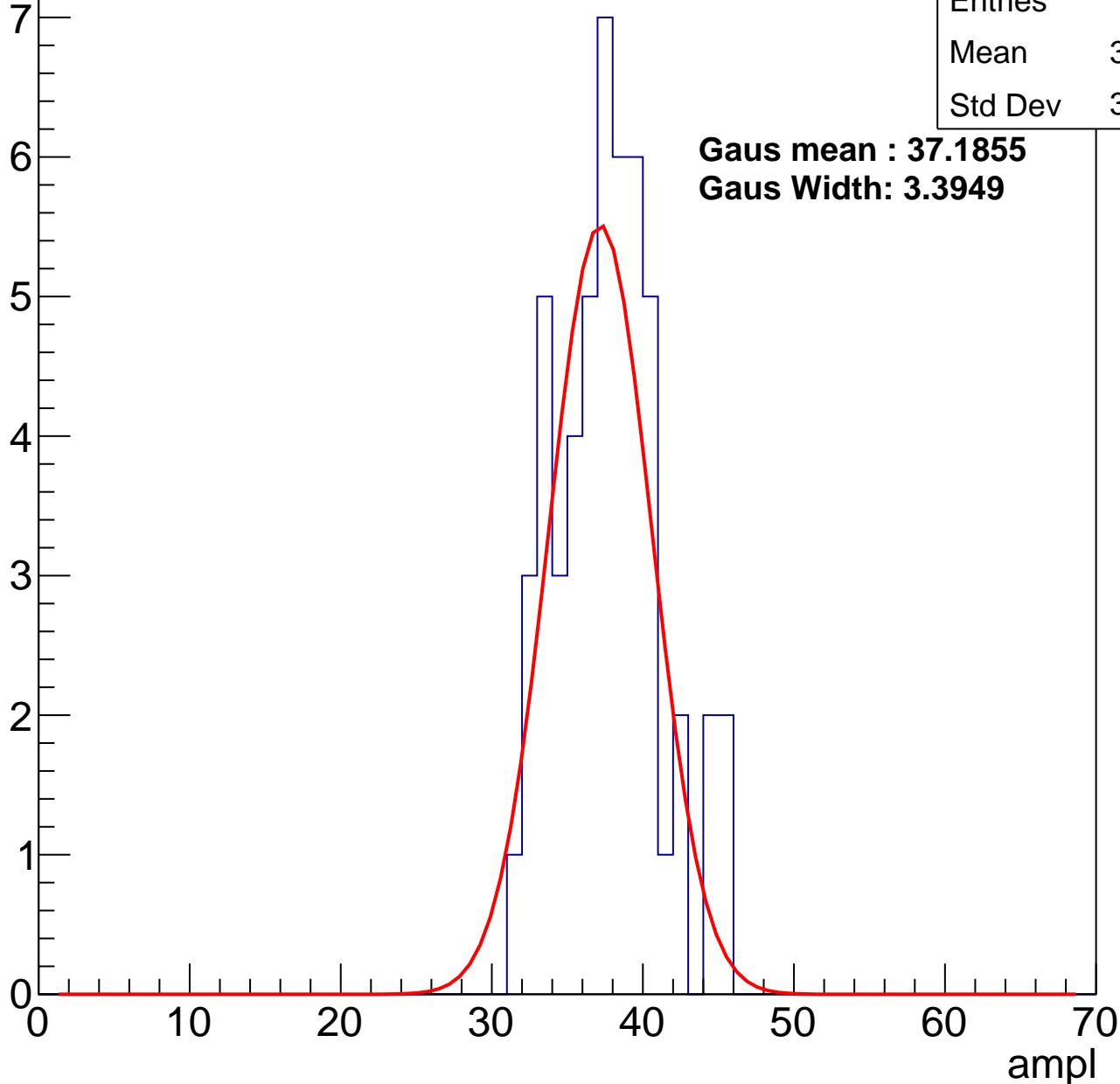
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	37.27
Std Dev	3.403

Gaus mean : 37.1855

Gaus Width: 3.3949



B1L103S, U14-ch103, adc2

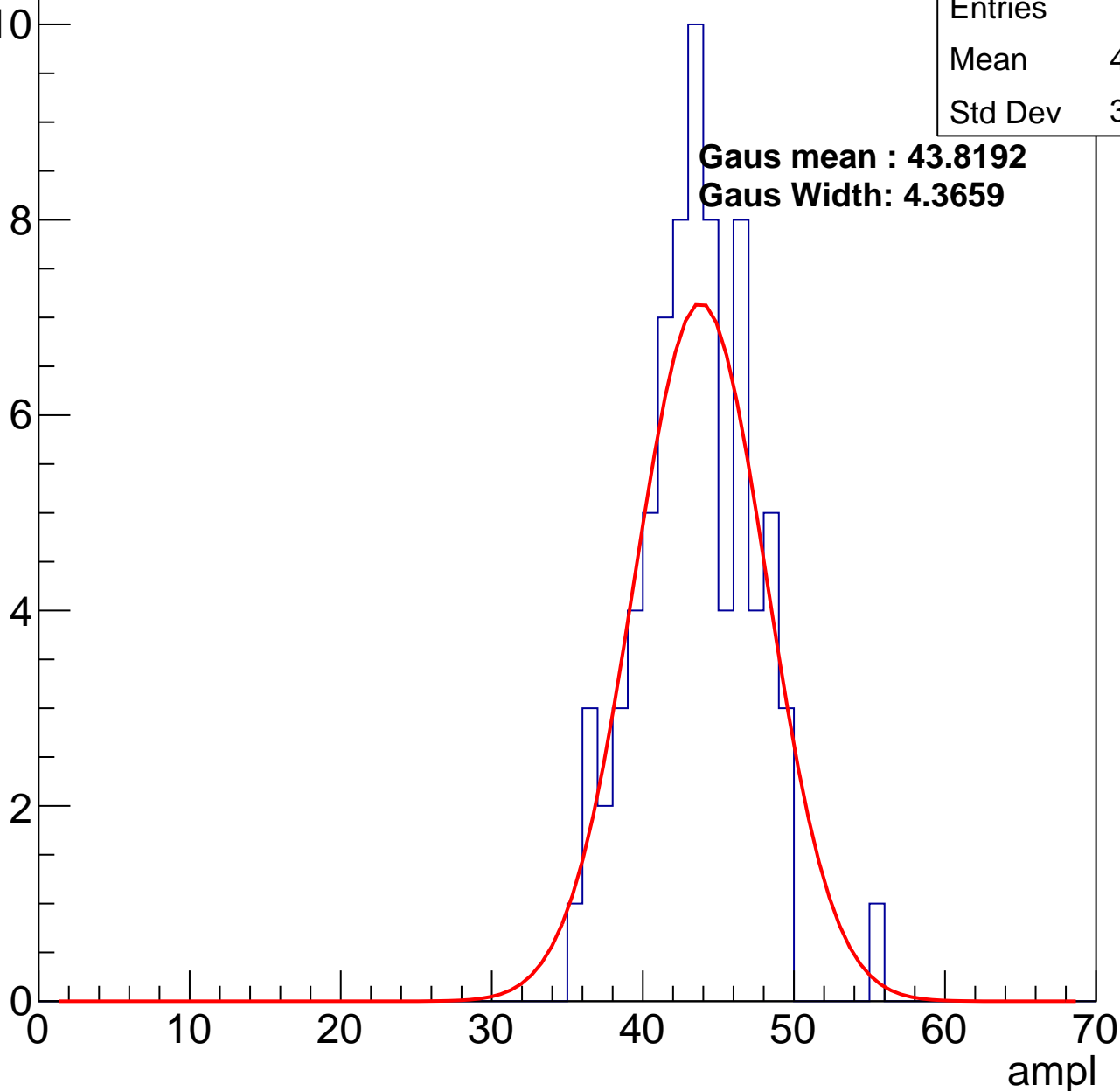
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.03
Std Dev	3.699

Gaus mean : 43.8192

Gaus Width: 4.3659

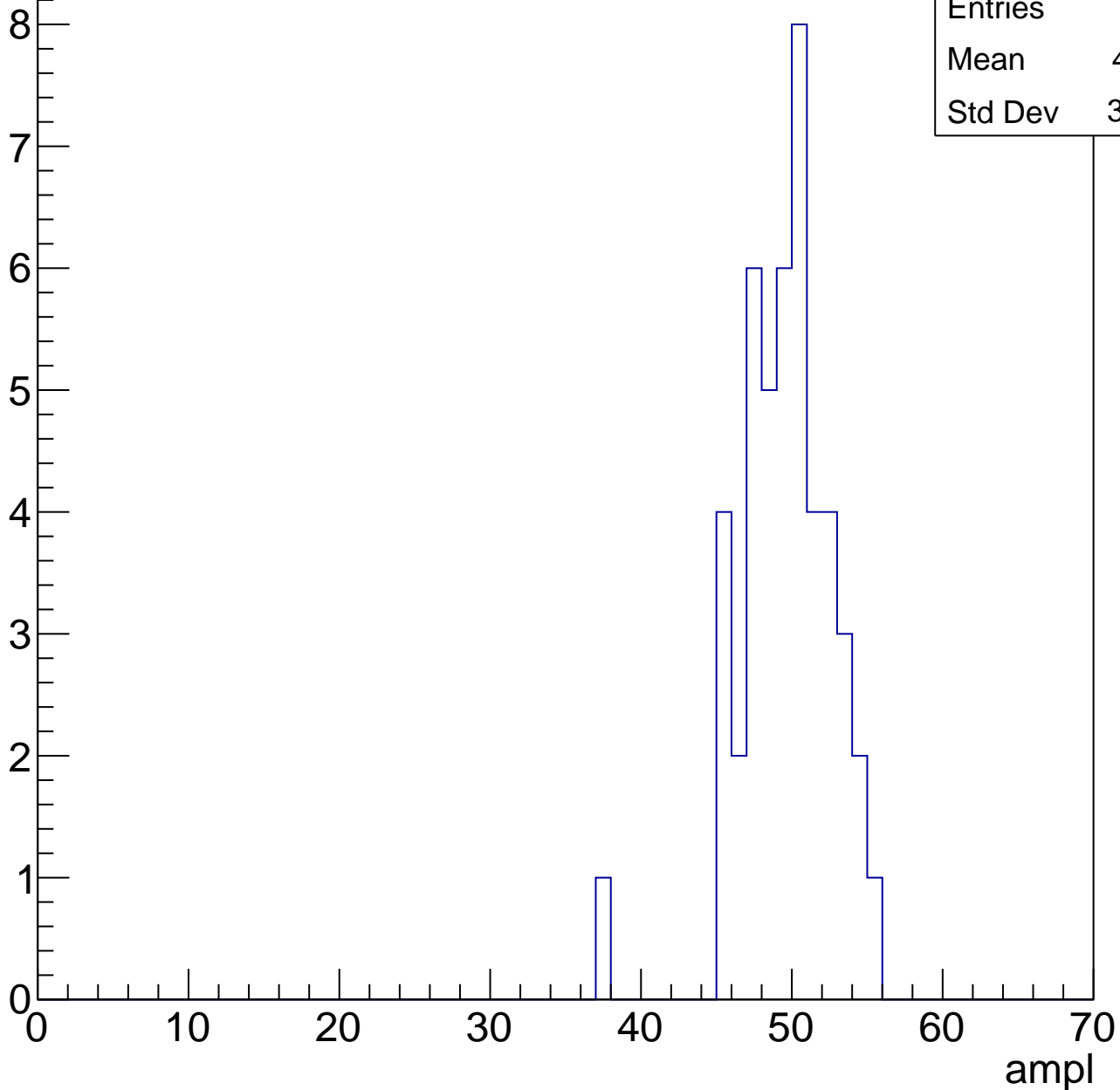


B1L103S, U14-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	49.11
Std Dev	3.122

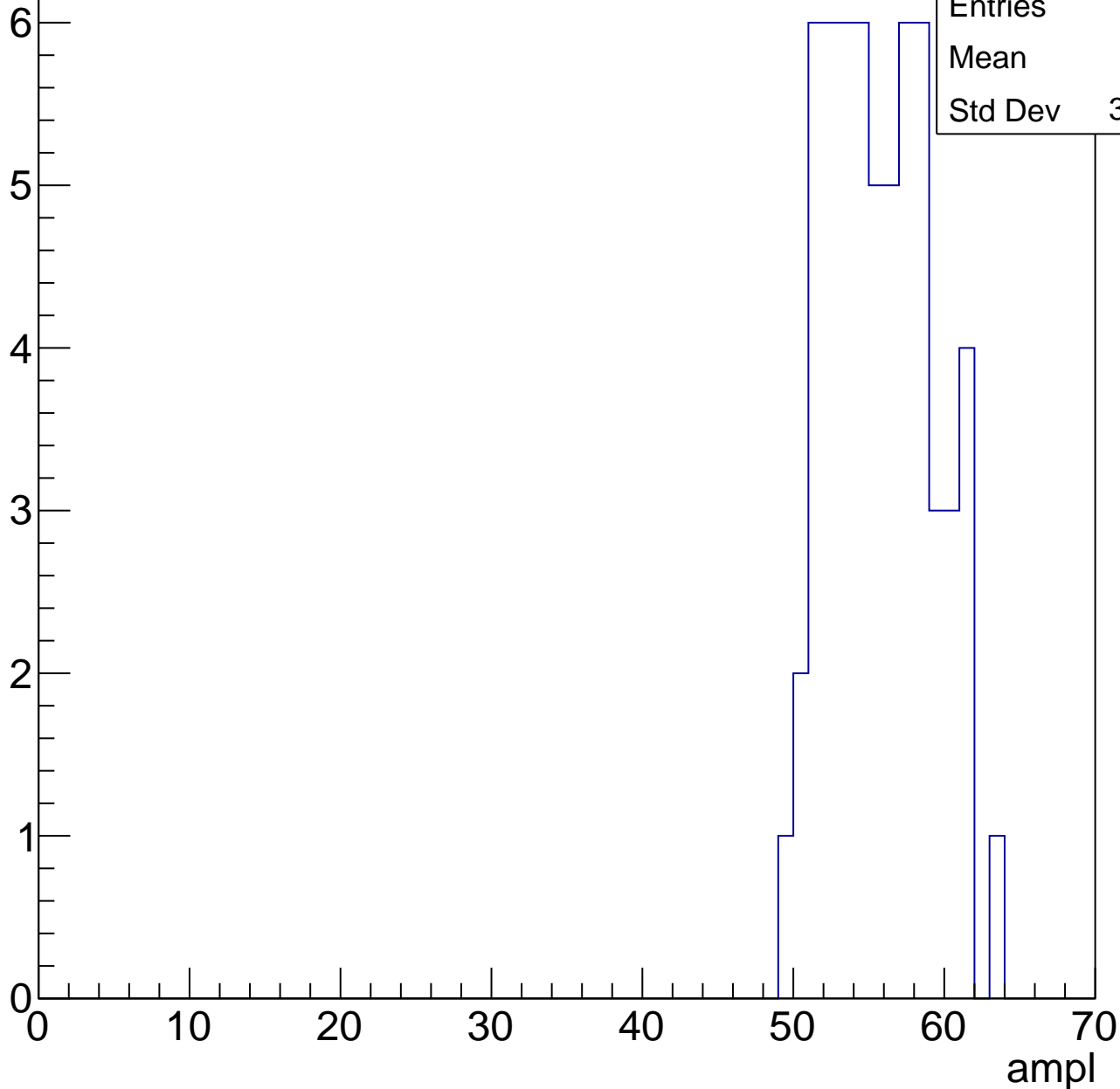


B1L103S, U14-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

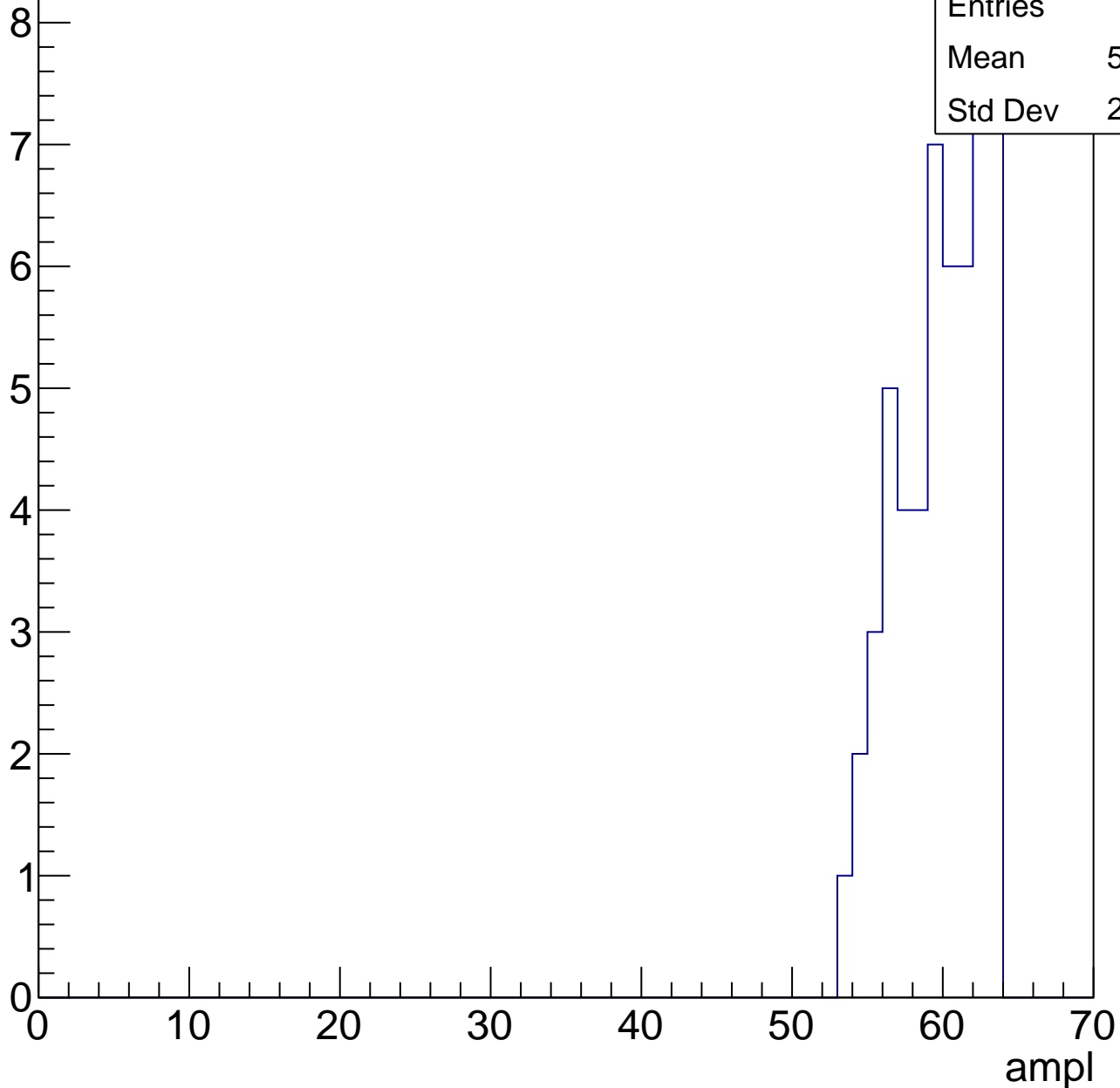
Entries	60
Mean	55.3
Std Dev	3.353



B1L103S, U14-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch103, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49.4
Std Dev	24.72

ampl

0 10 20 30 40 50 60 70

B1L103S, U14-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch104, adc0

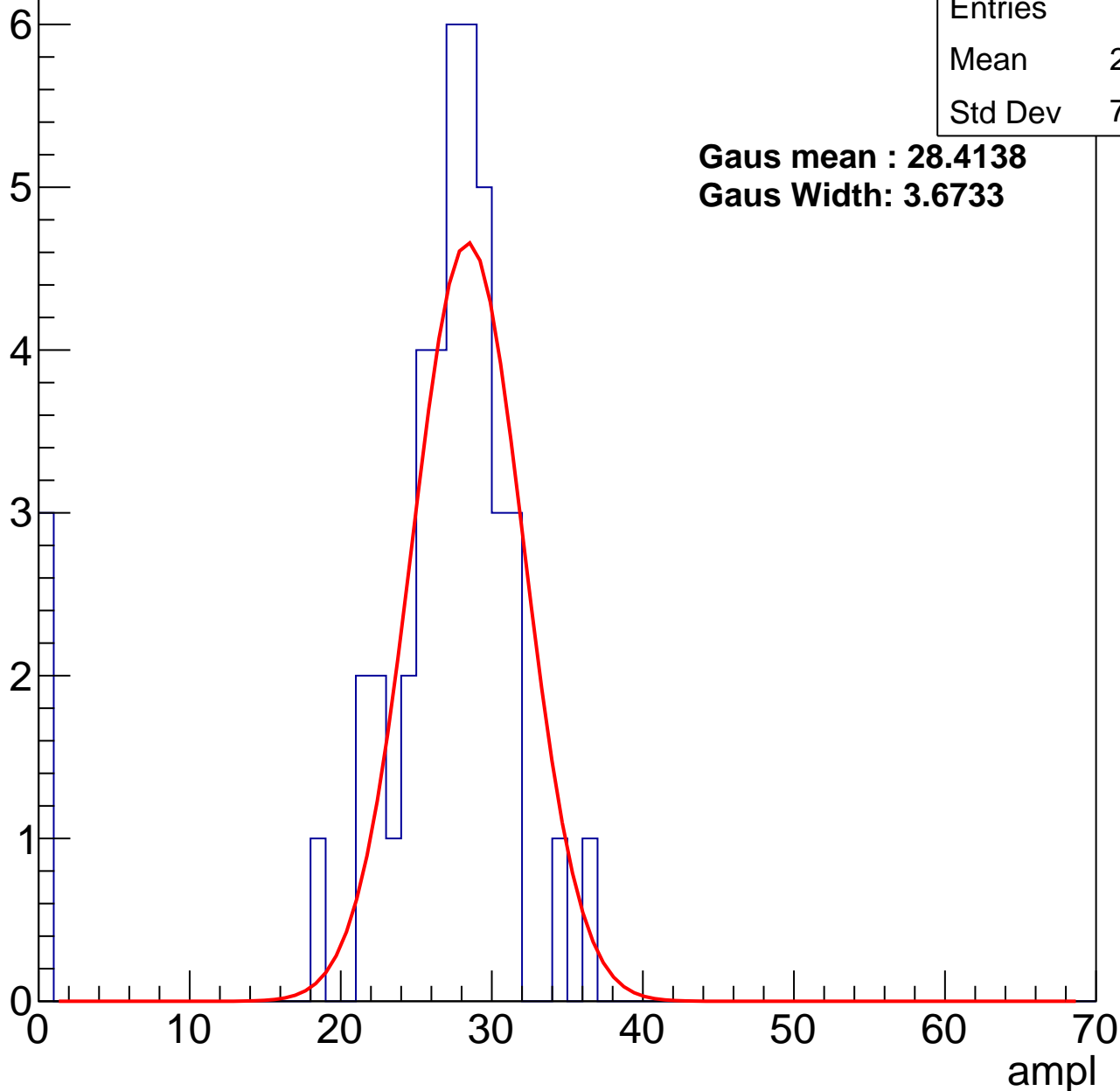
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	25.16
Std Dev	7.577

Gaus mean : 28.4138

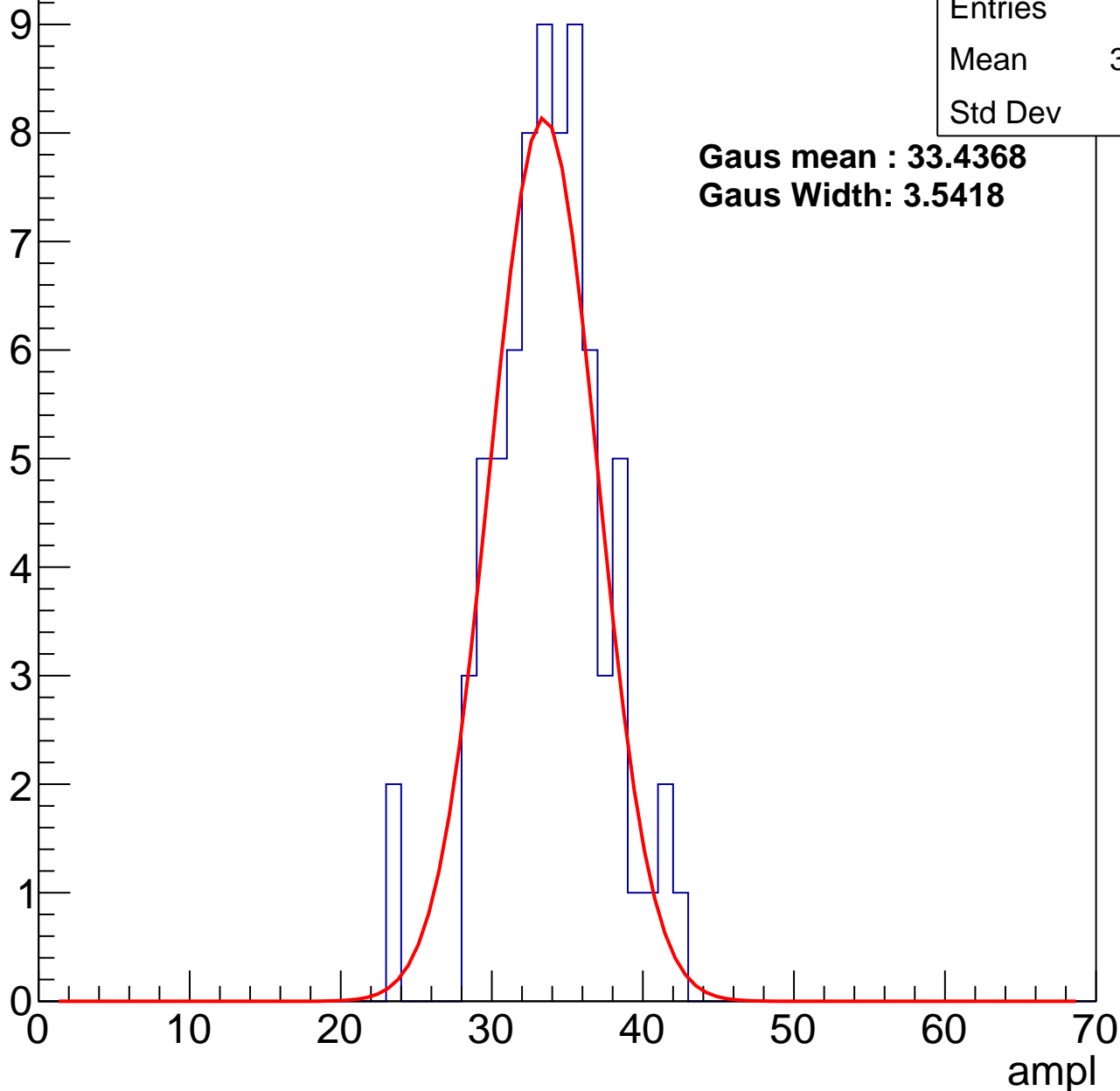
Gaus Width: 3.6733



B1L103S, U14-ch104, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch104, adc2

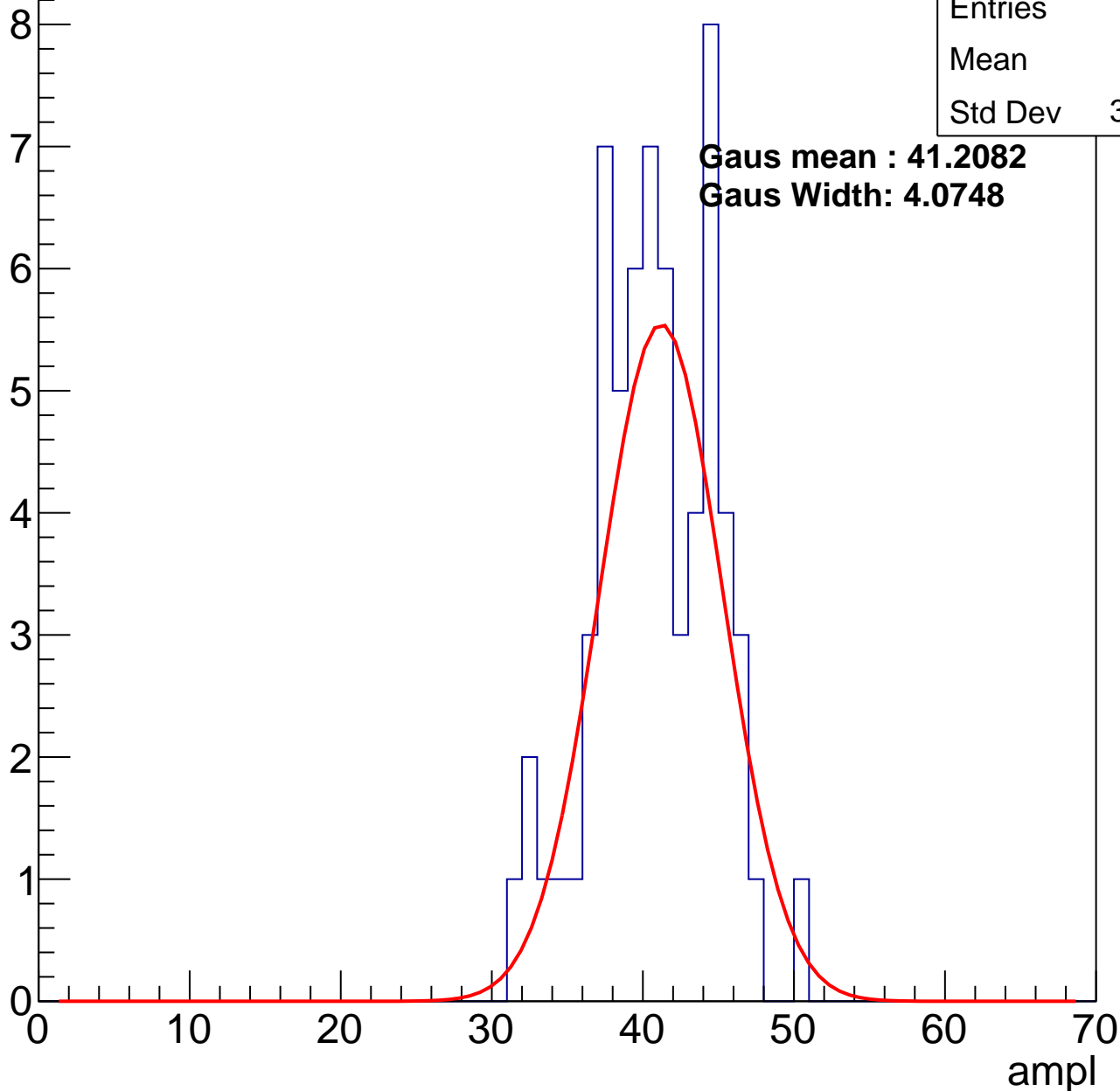
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	40.3
Std Dev	3.932

Gaus mean : 41.2082

Gaus Width: 4.0748

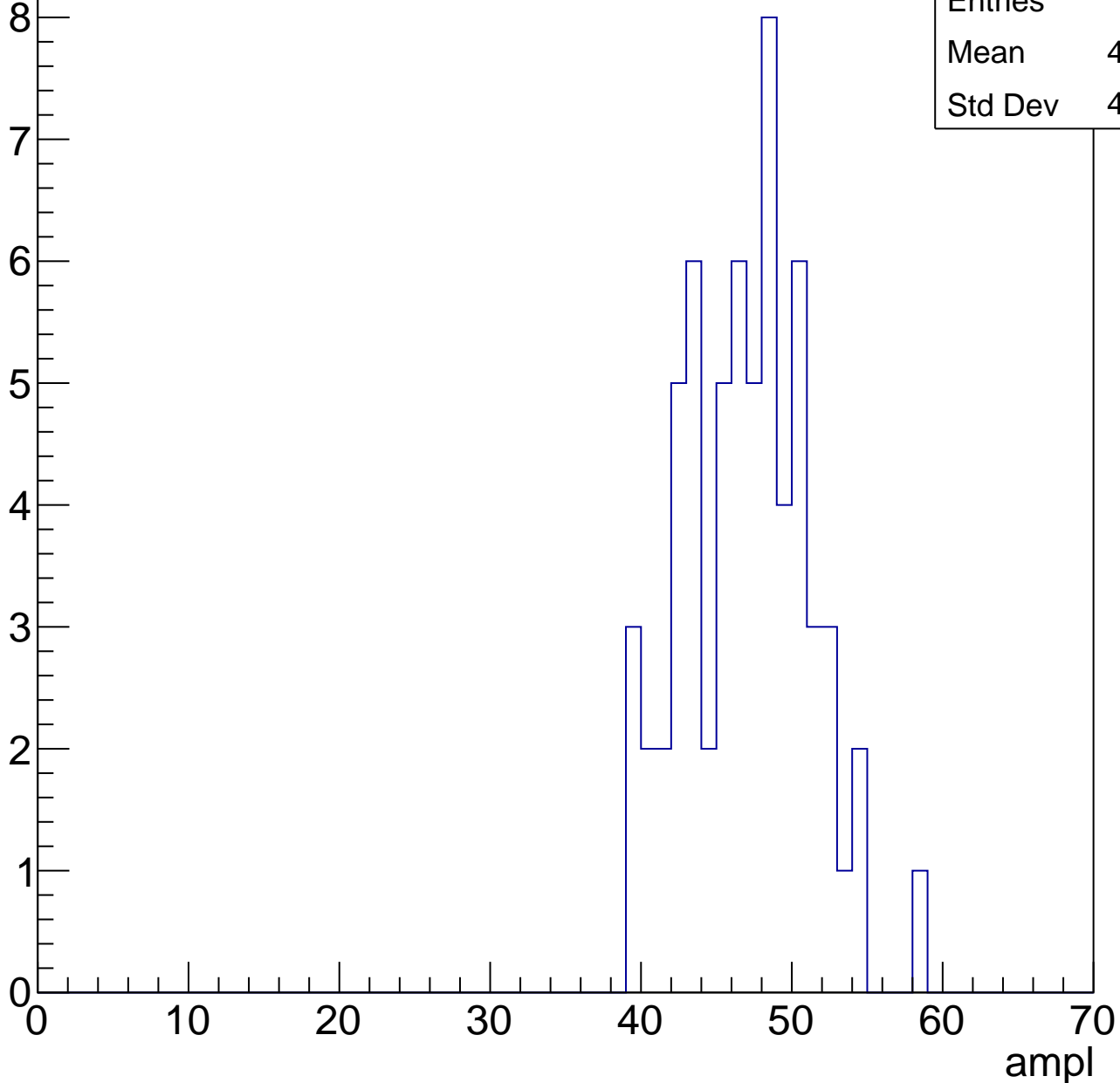


B1L103S, U14-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	46.55
Std Dev	4.089

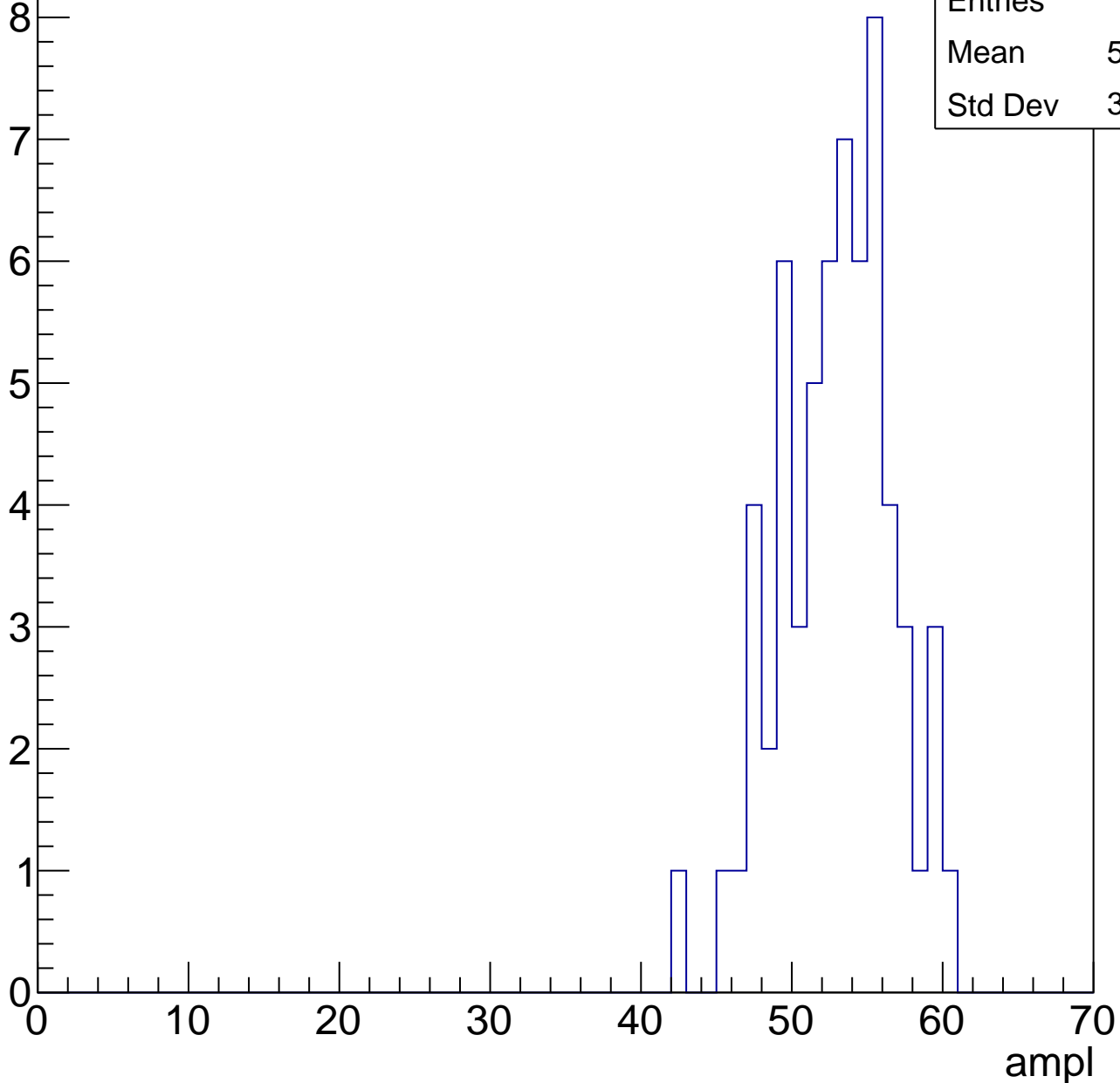


B1L103S, U14-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	52.47
Std Dev	3.732

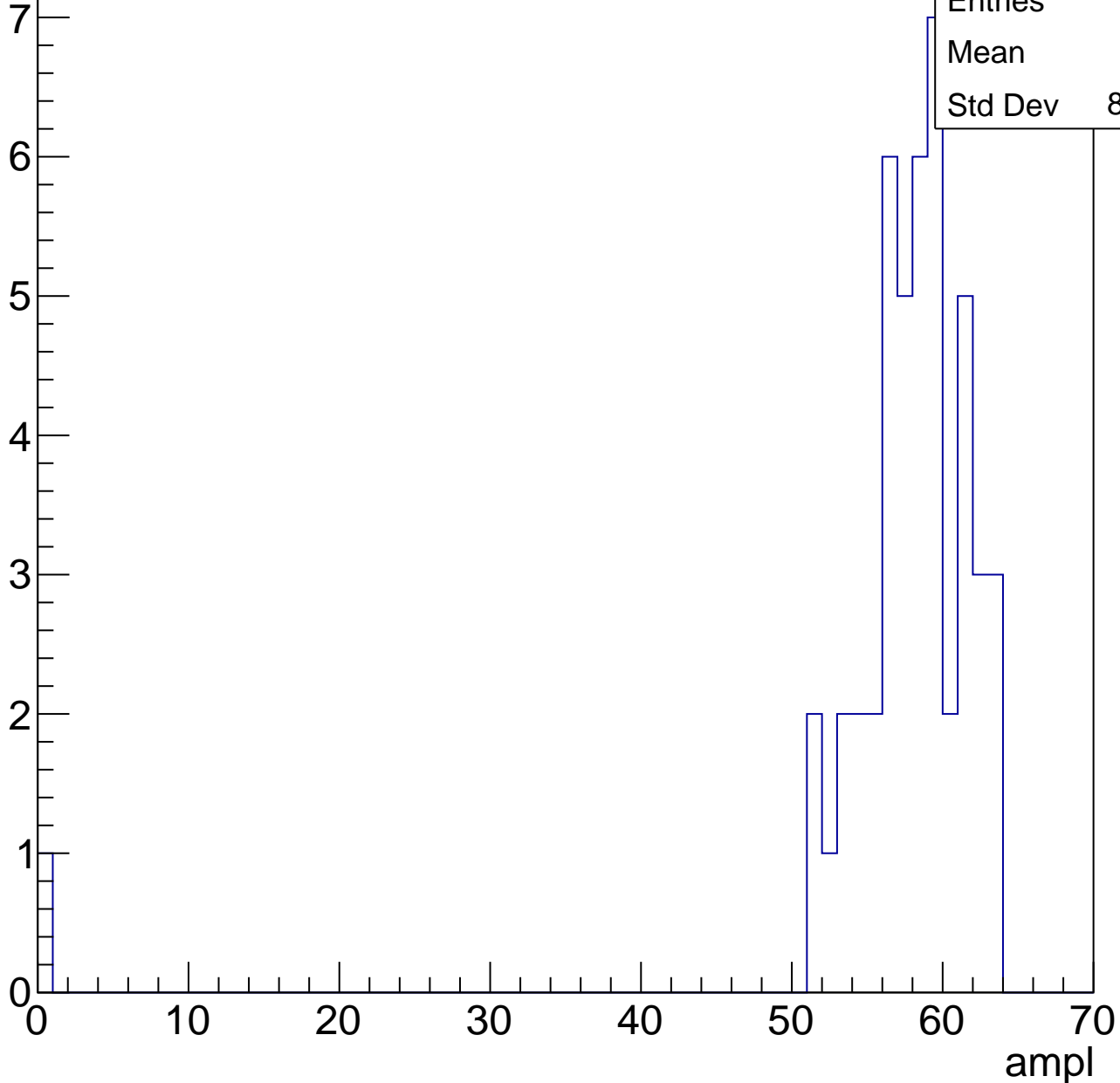


B1L103S, U14-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

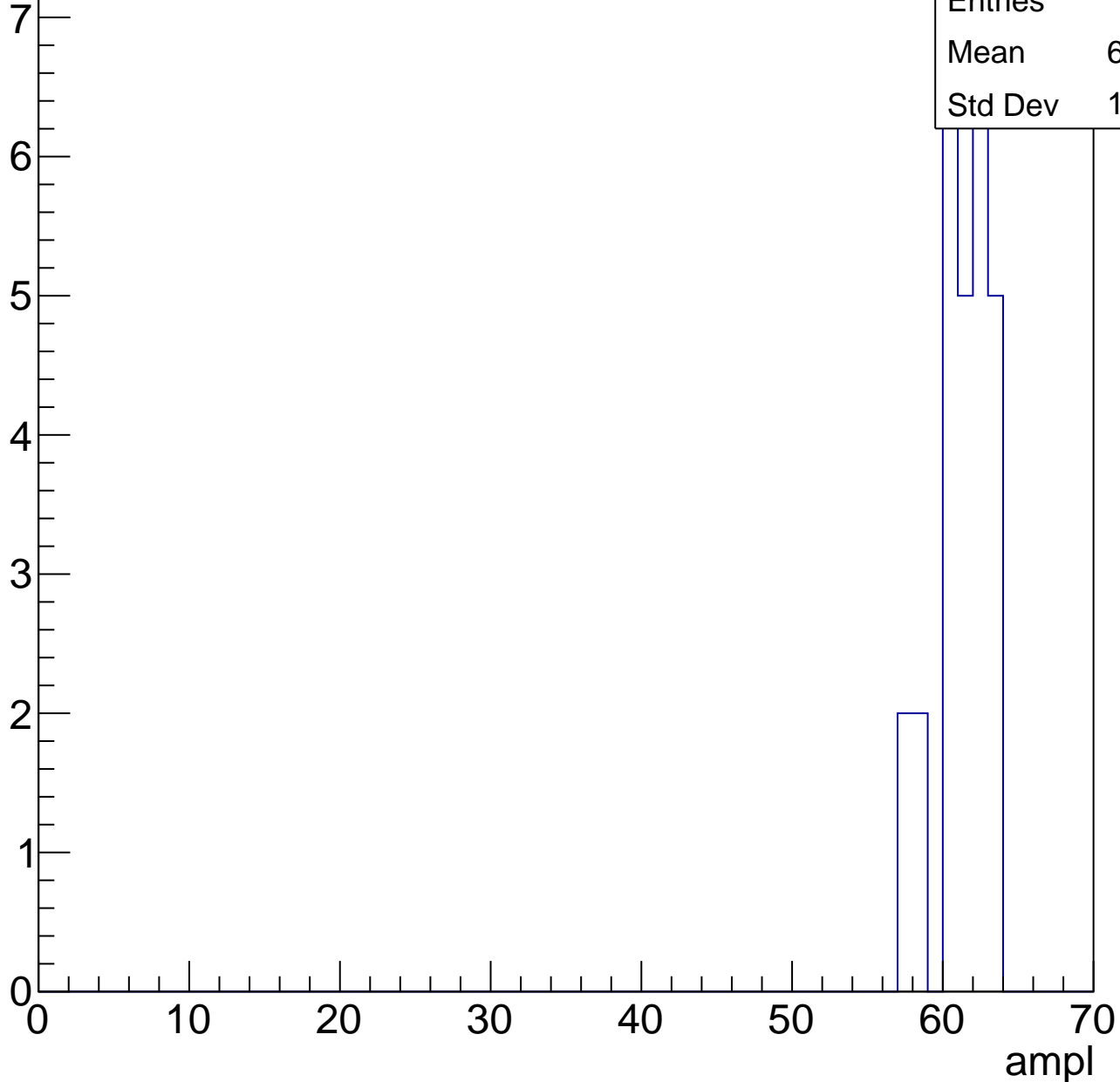
Entries	47
Mean	56.6
Std Dev	8.893



B1L103S, U14-ch104, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	28
Mean	60.86
Std Dev	1.726

B1L103S, U14-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch105, adc0

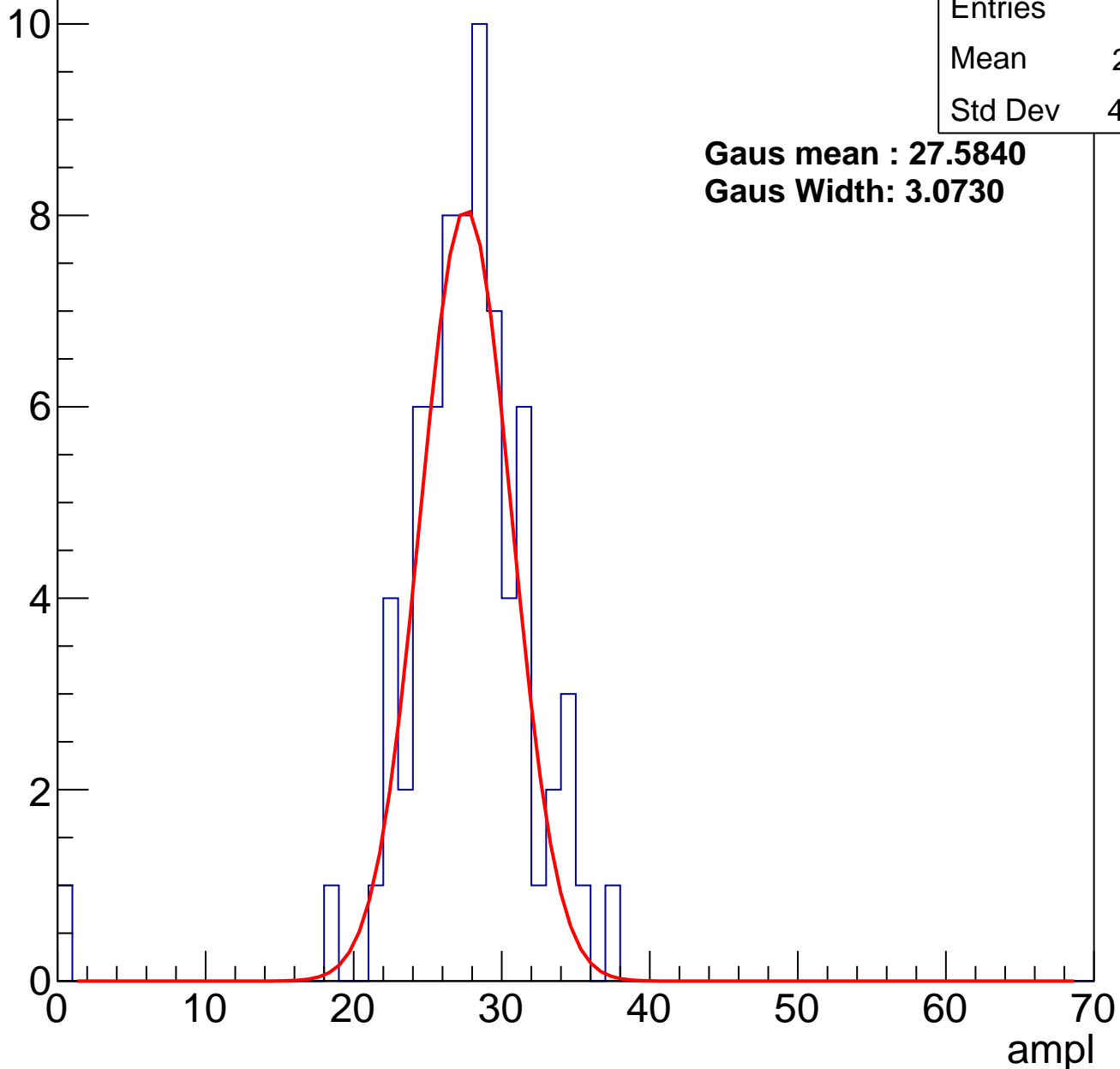
calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	27.11
Std Dev	4.774

Gaus mean : 27.5840

Gaus Width: 3.0730

Entry



B1L103S, U14-ch105, adc1

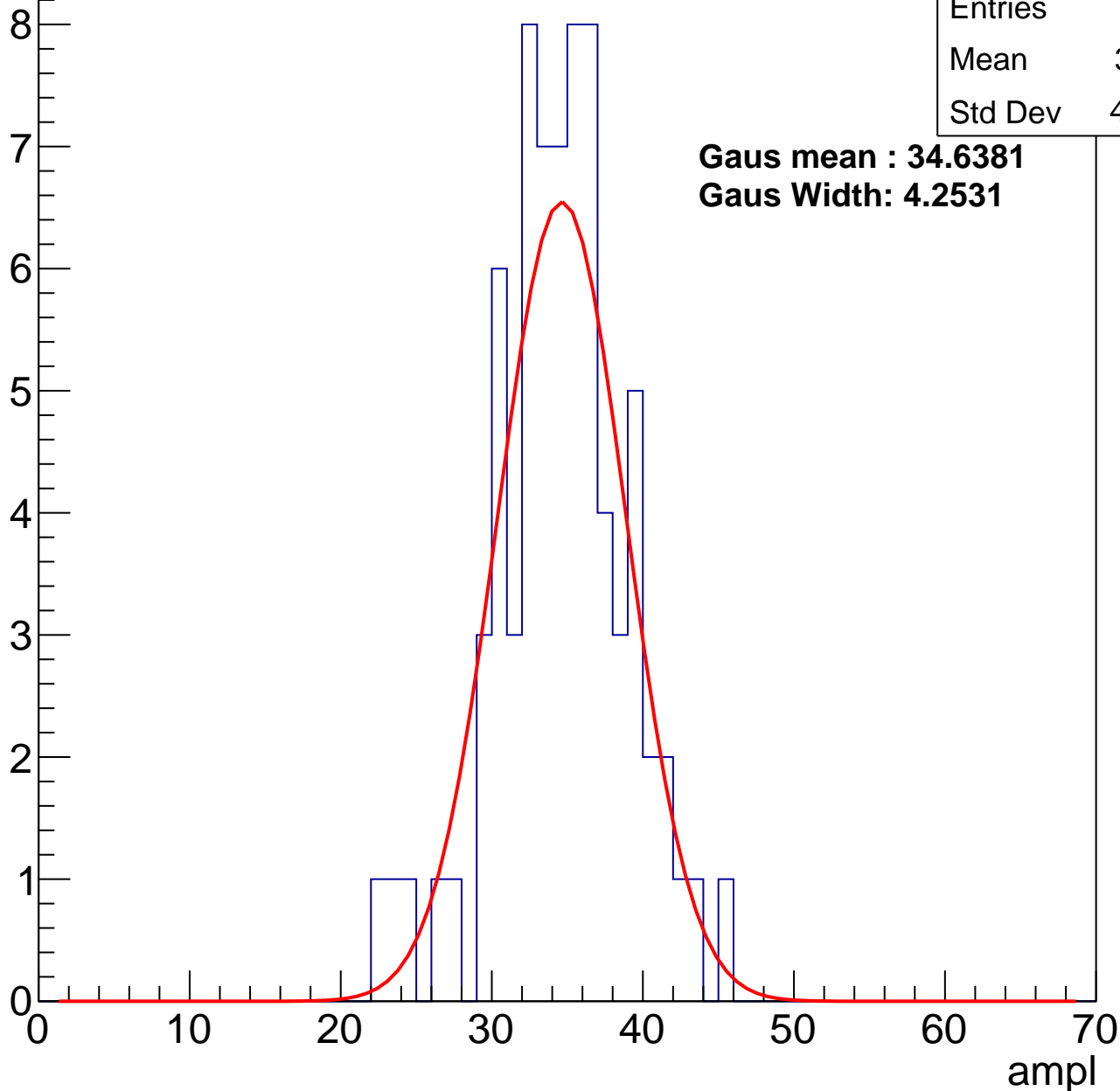
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	34.11
Std Dev	4.348

Gaus mean : 34.6381

Gaus Width: 4.2531



B1L103S, U14-ch105, adc2

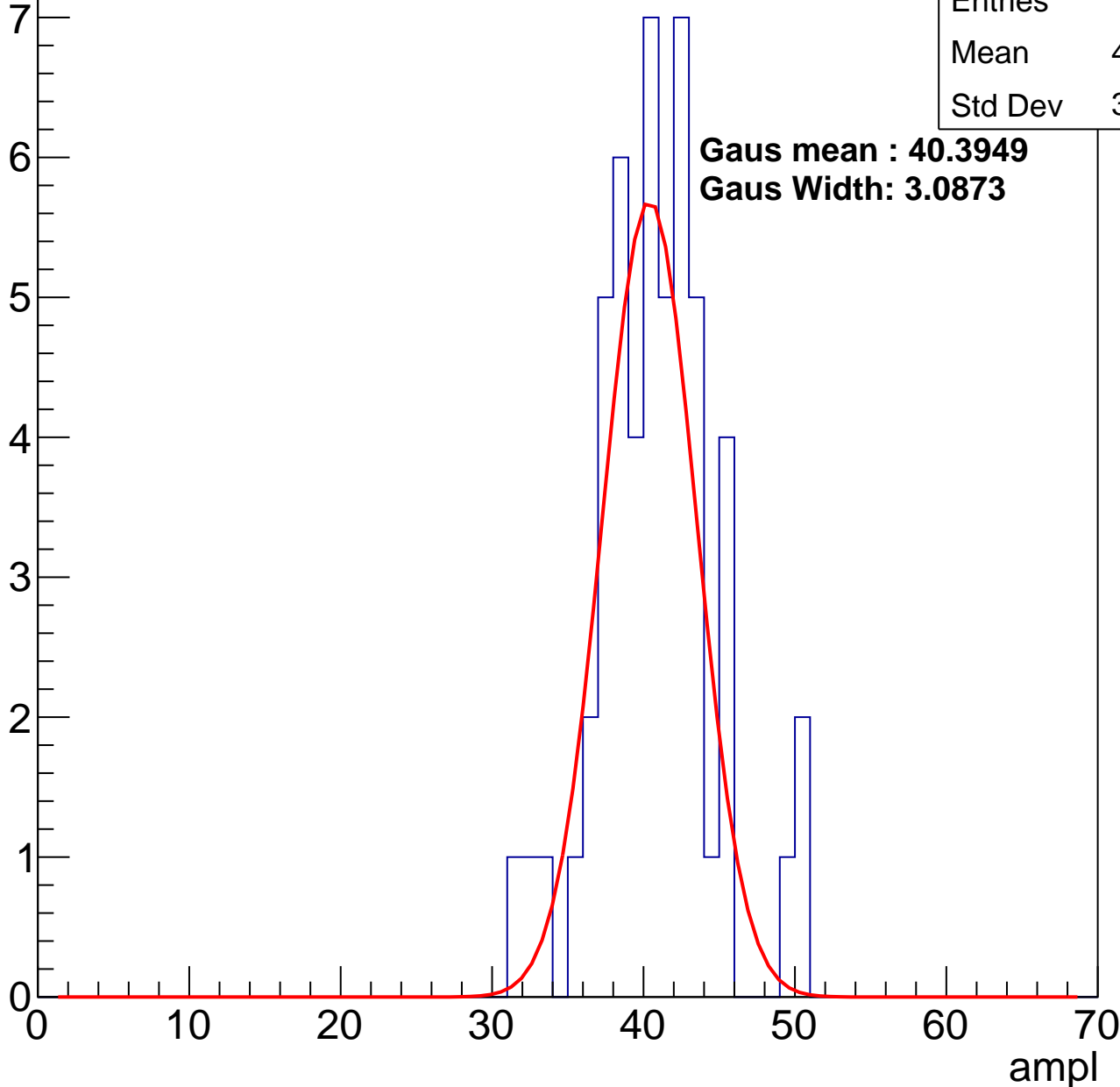
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	40.36
Std Dev	3.866

Gaus mean : 40.3949

Gaus Width: 3.0873

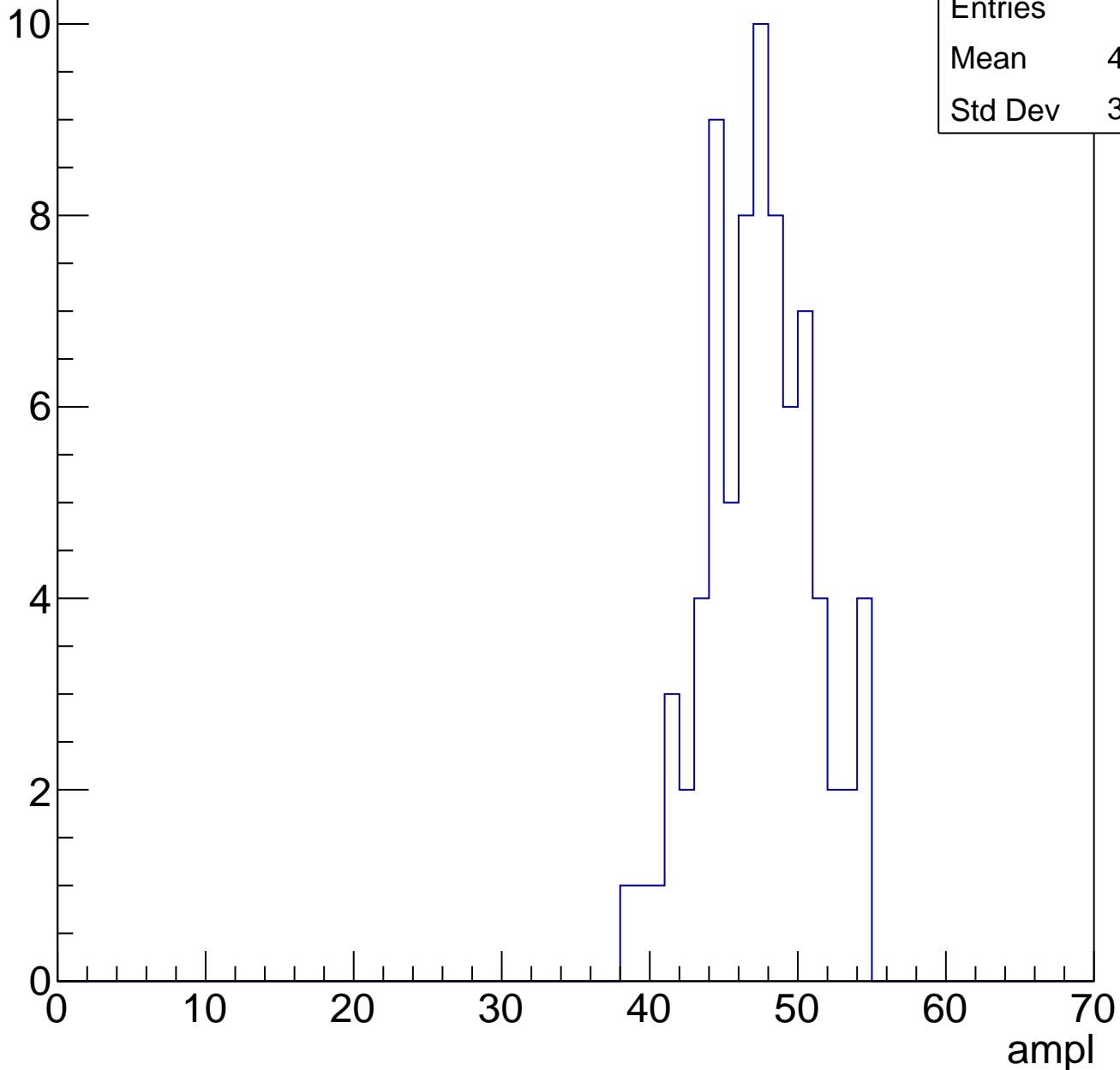


B1L103S, U14-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	46.92
Std Dev	3.607

Entry

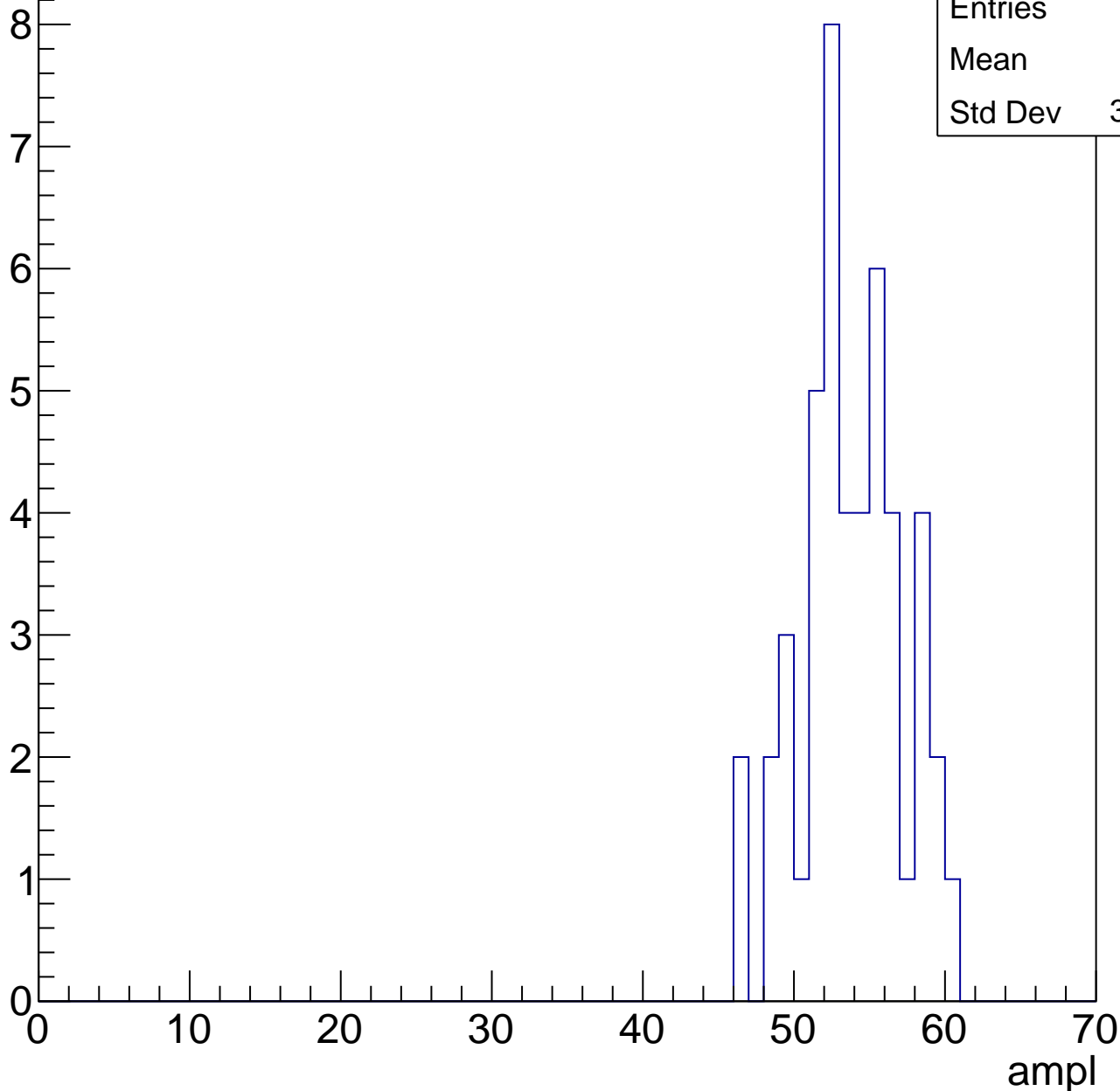


B1L103S, U14-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	53.3
Std Dev	3.358

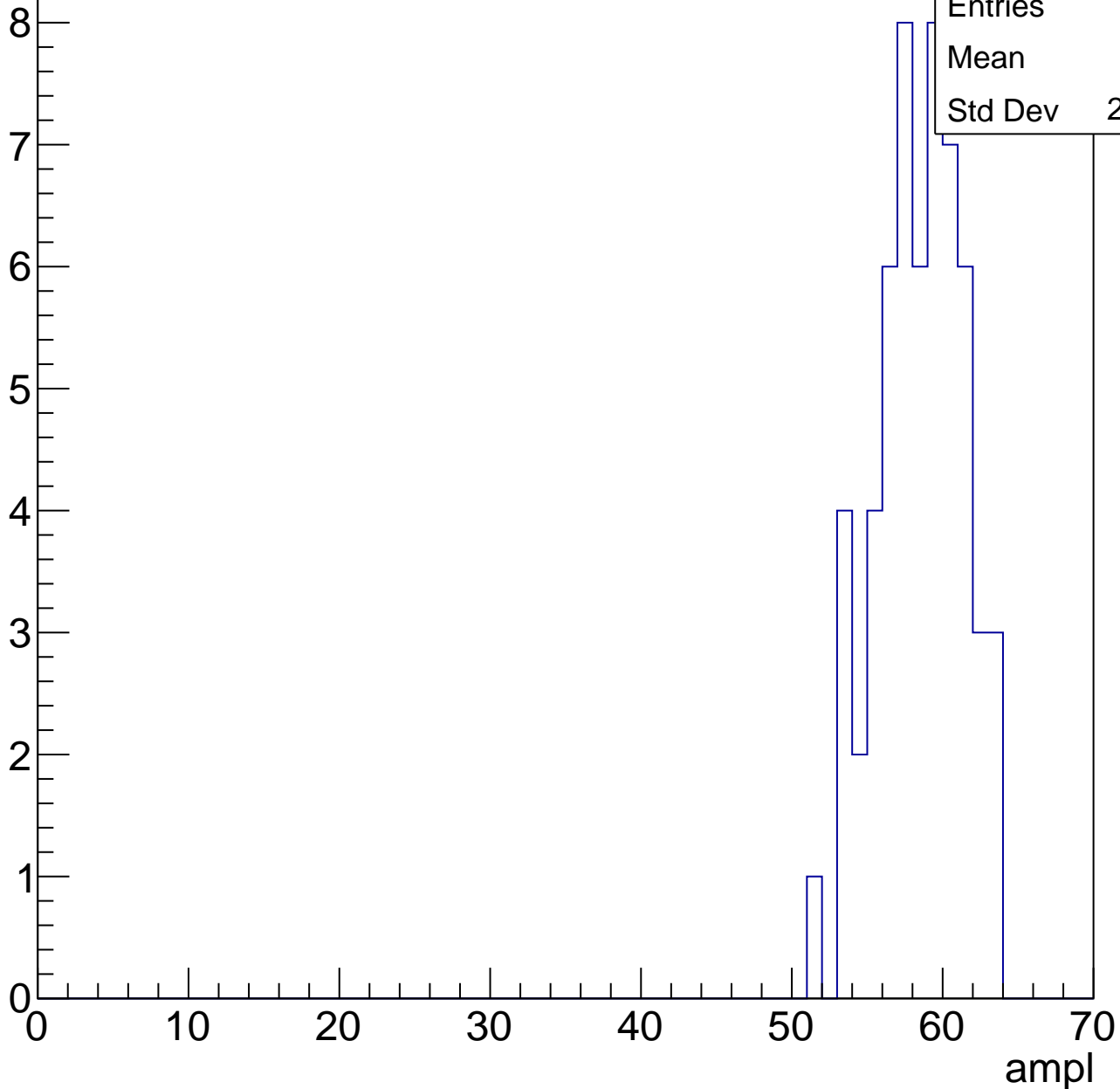


B1L103S, U14-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	58
Std Dev	2.822

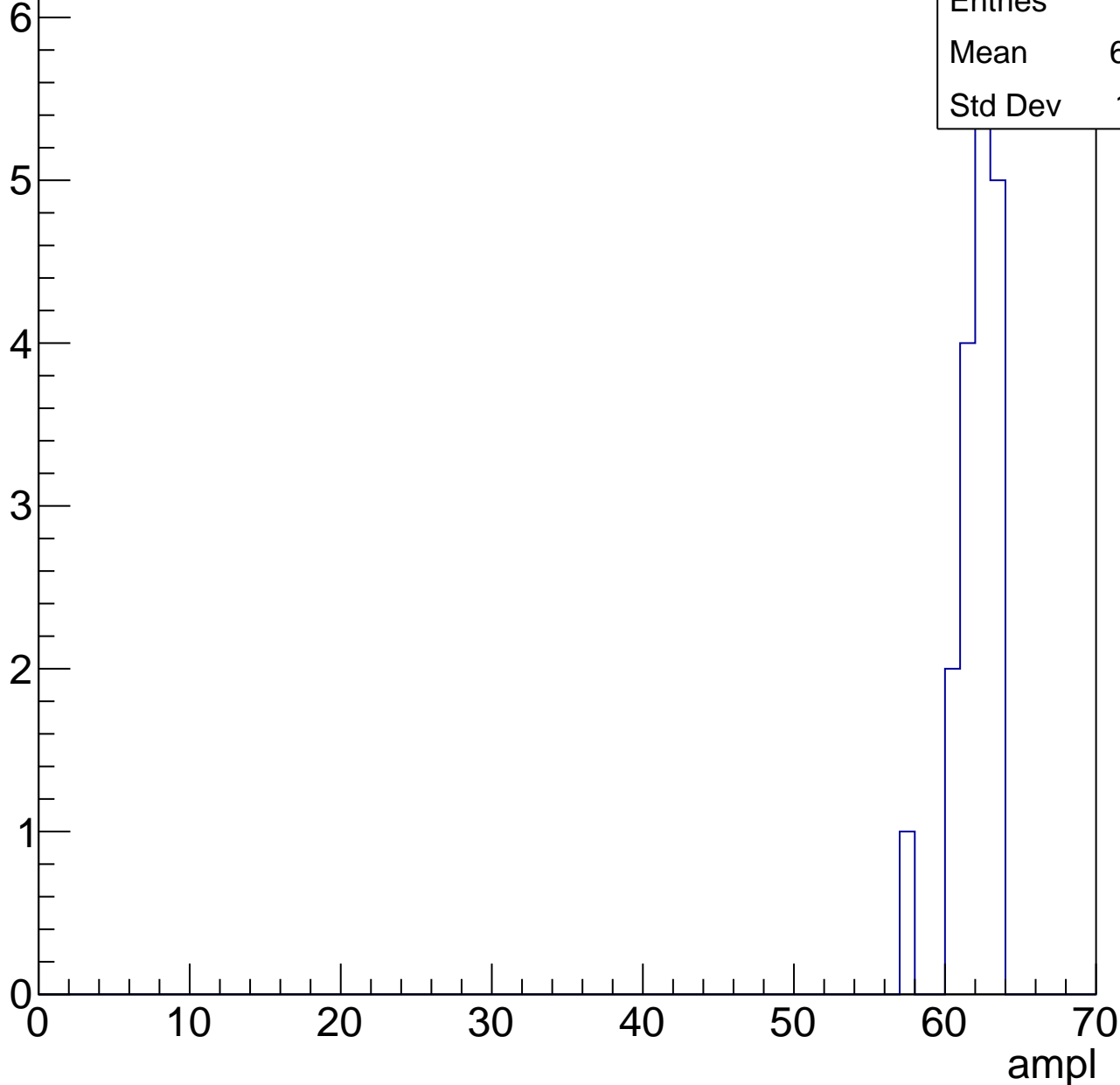


B1L103S, U14-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

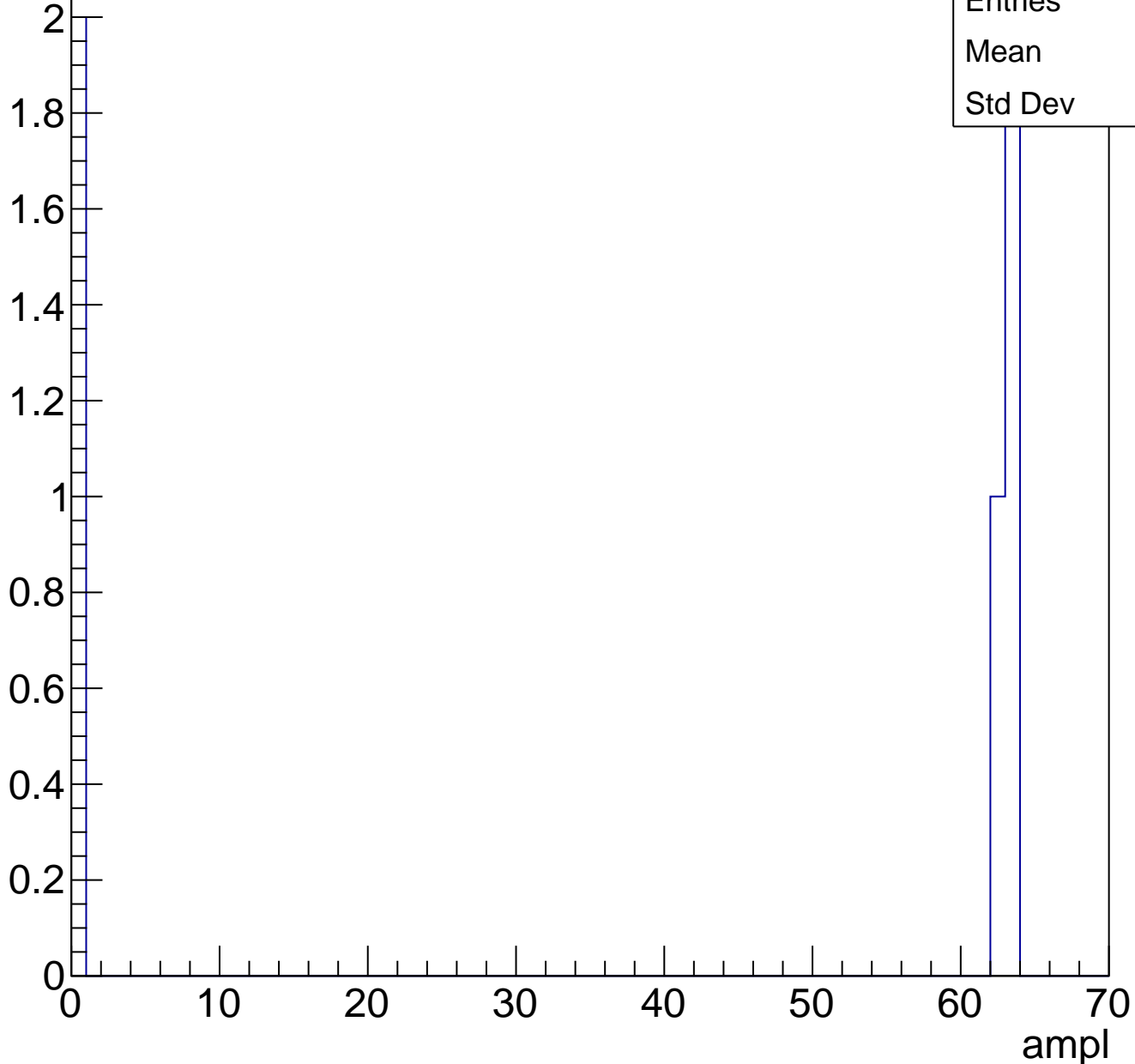
Entries	18
Mean	61.56
Std Dev	1.461



B1L103S, U14-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch106, adc0

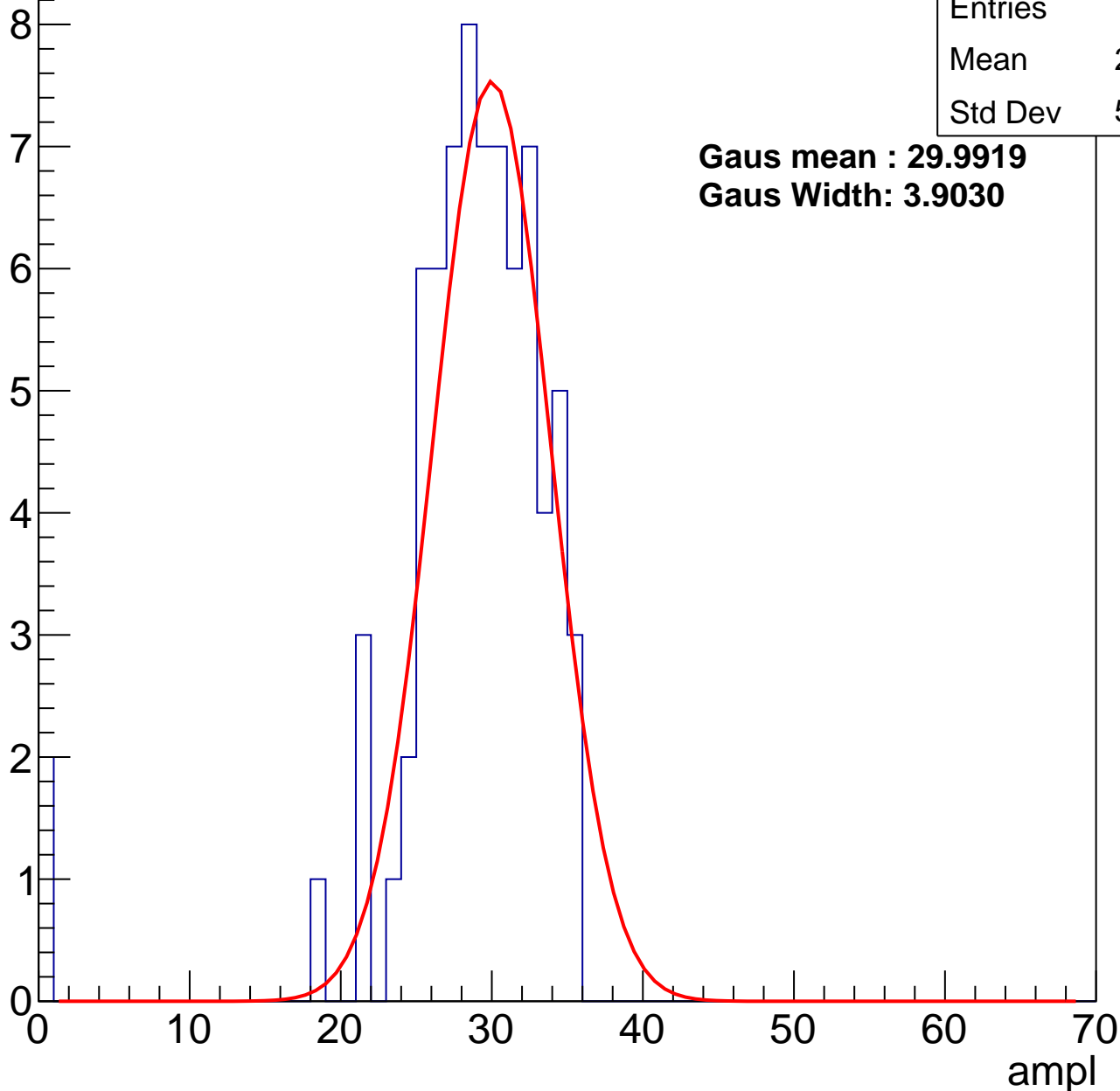
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.01
Std Dev	5.871

Gaus mean : 29.9919

Gaus Width: 3.9030



B1L103S, U14-ch106, adc1

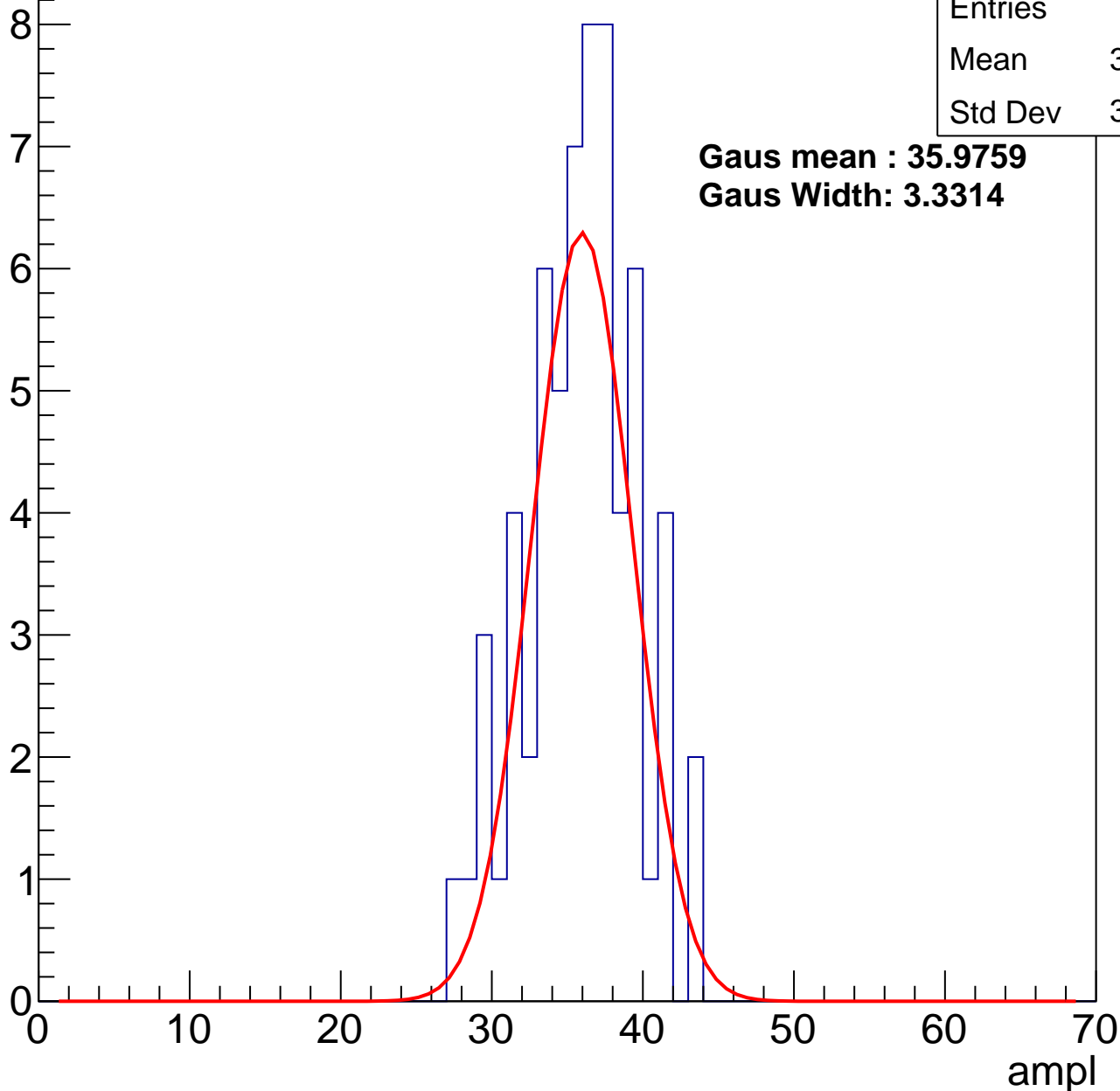
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.44
Std Dev	3.594

Gaus mean : 35.9759

Gaus Width: 3.3314



B1L103S, U14-ch106, adc2

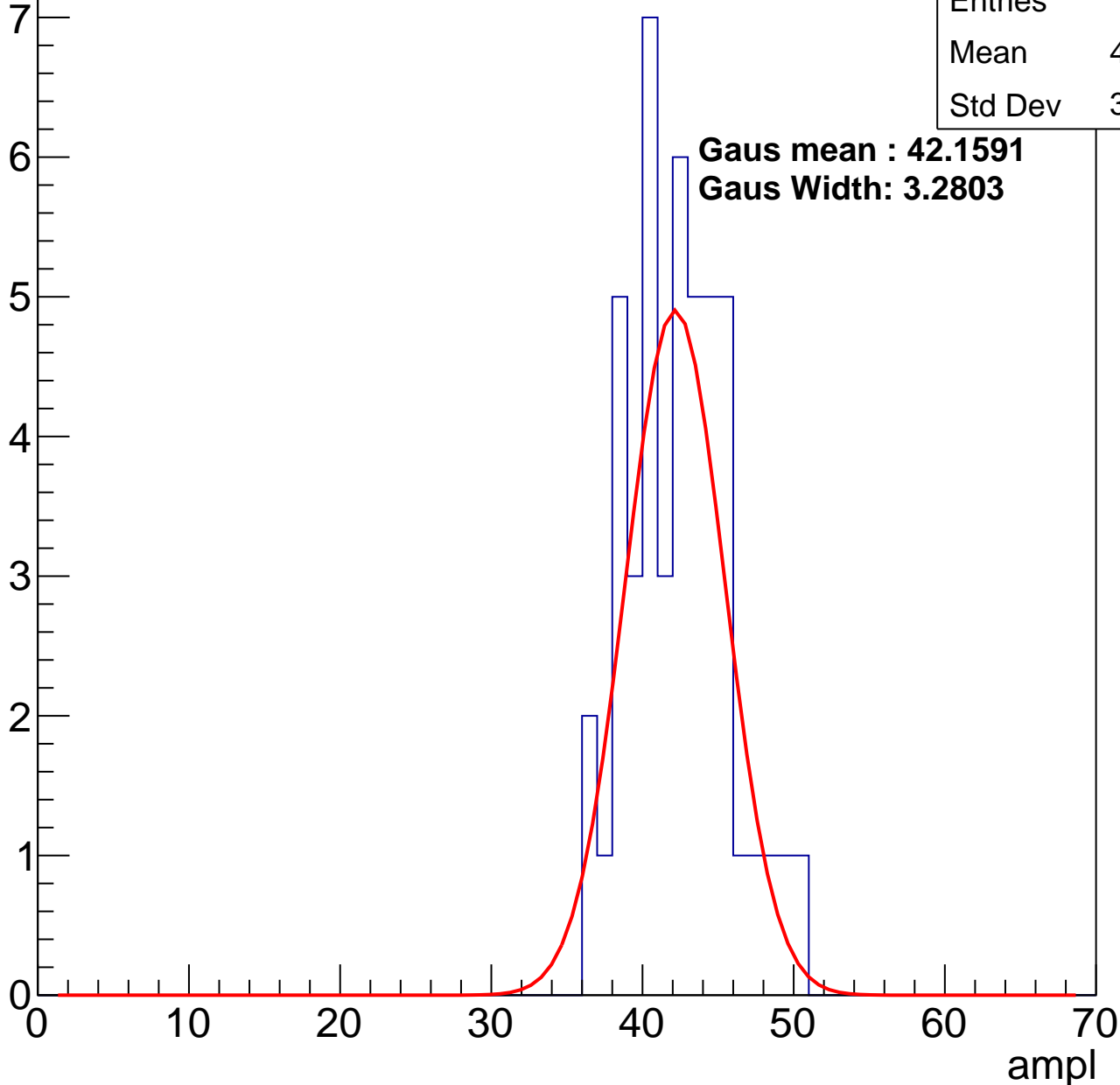
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	41.94
Std Dev	3.245

Gaus mean : 42.1591

Gaus Width: 3.2803

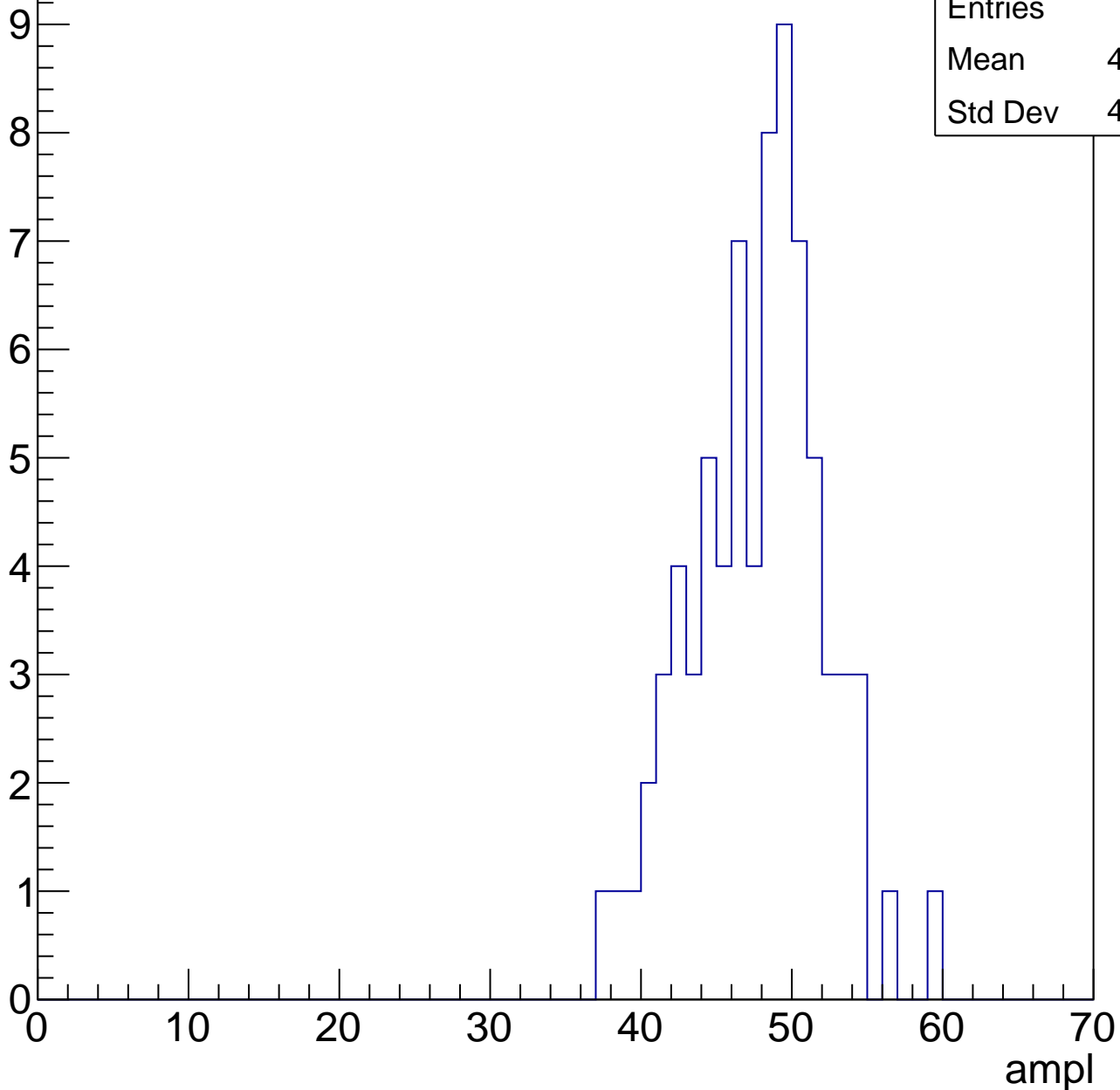


B1L103S, U14-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	47.28
Std Dev	4.325

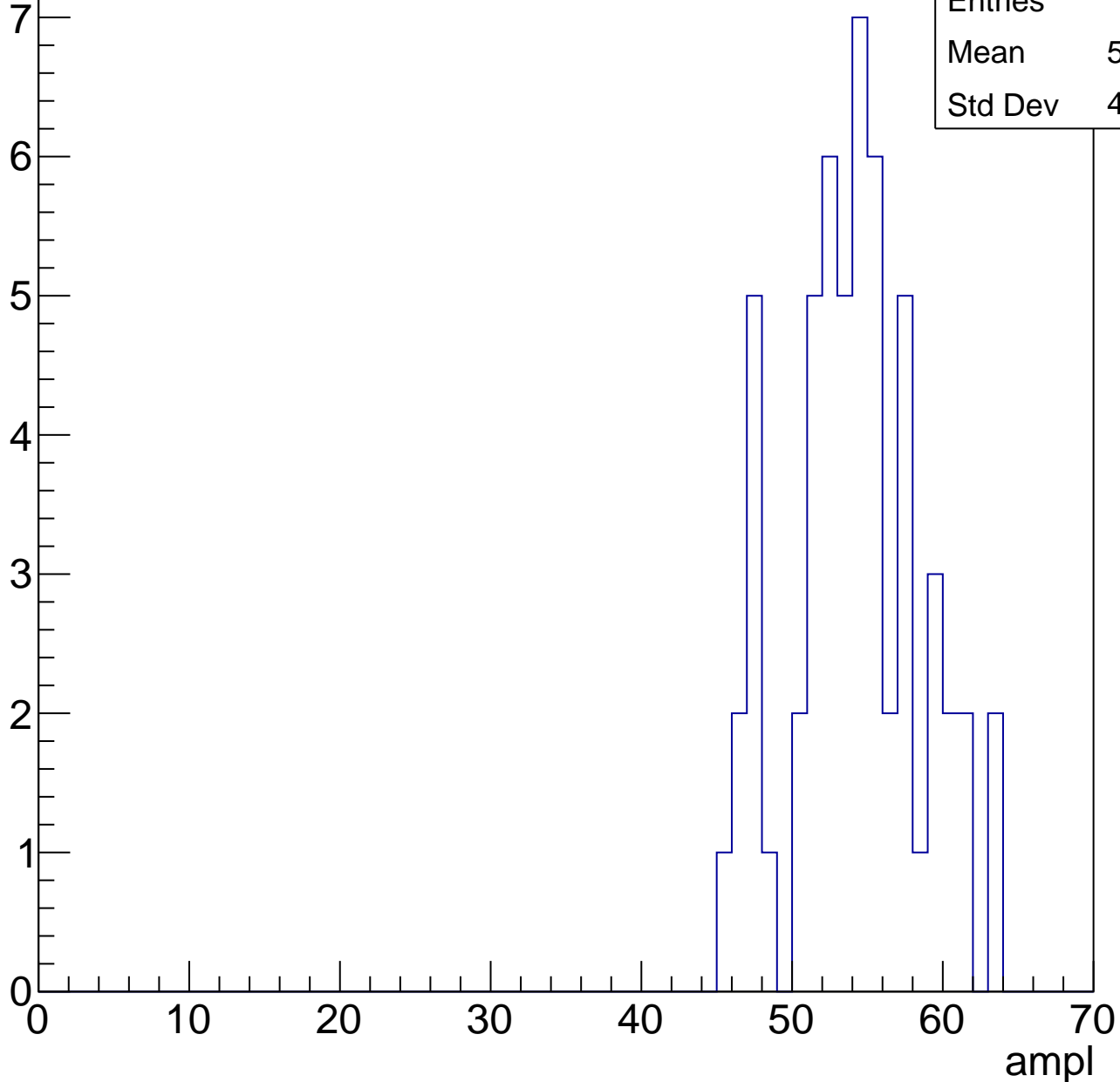


B1L103S, U14-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	53.68
Std Dev	4.337

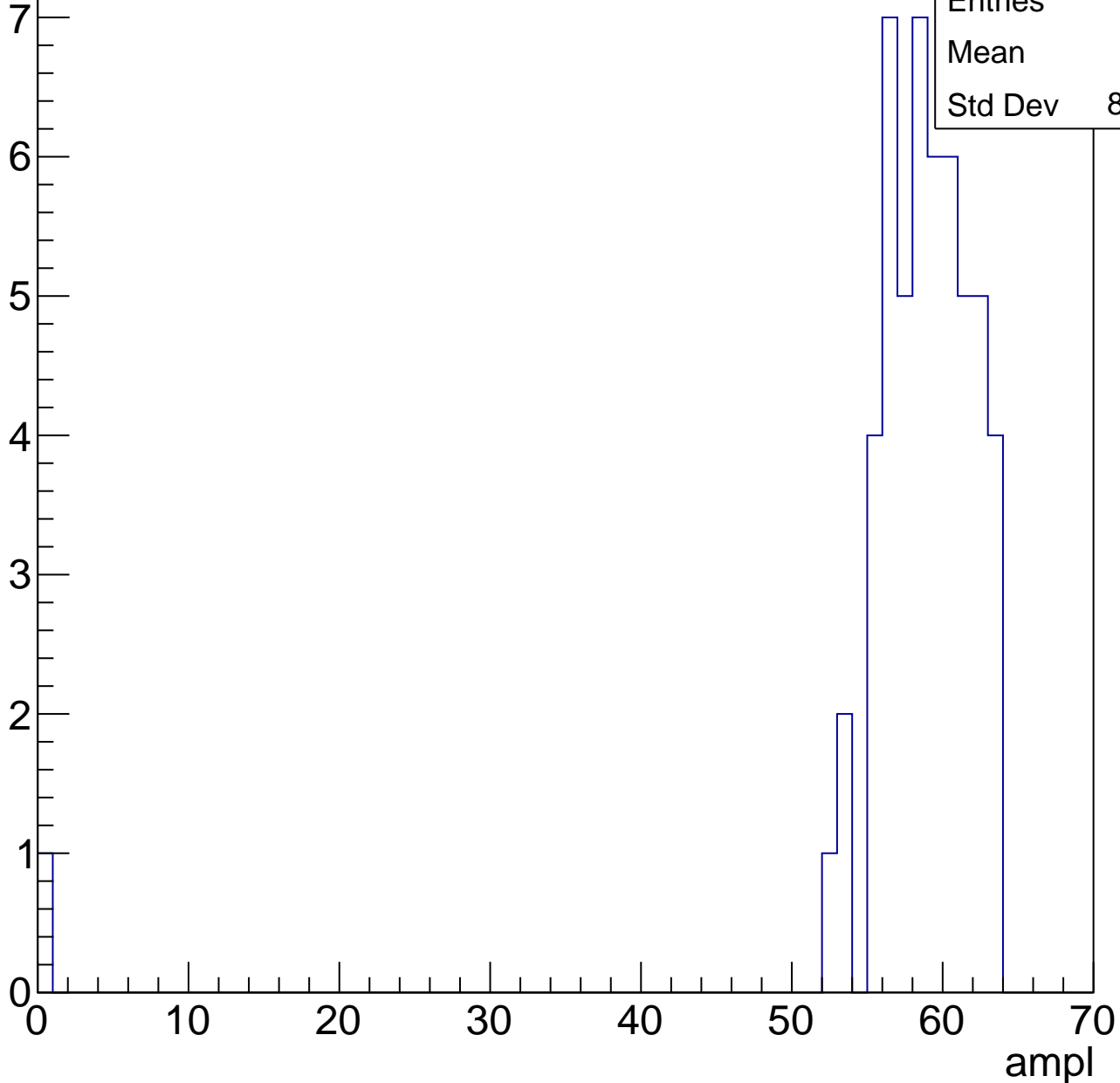


B1L103S, U14-ch106, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	57.4
Std Dev	8.417

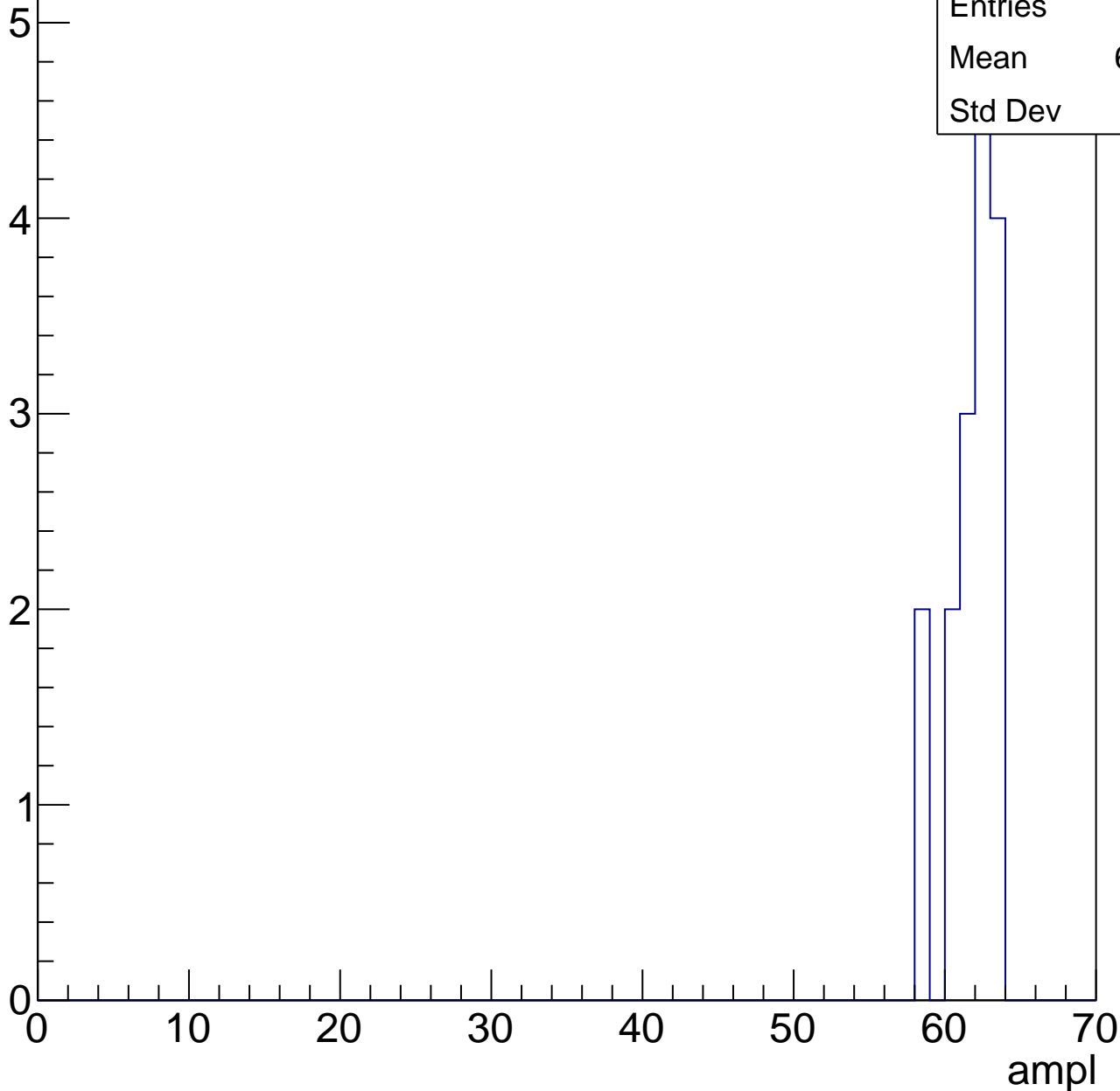


B1L103S, U14-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	61.31
Std Dev	1.57



B1L103S, U14-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U14-ch107, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	29.09
Std Dev	6.625

Gaus mean : 30.2139

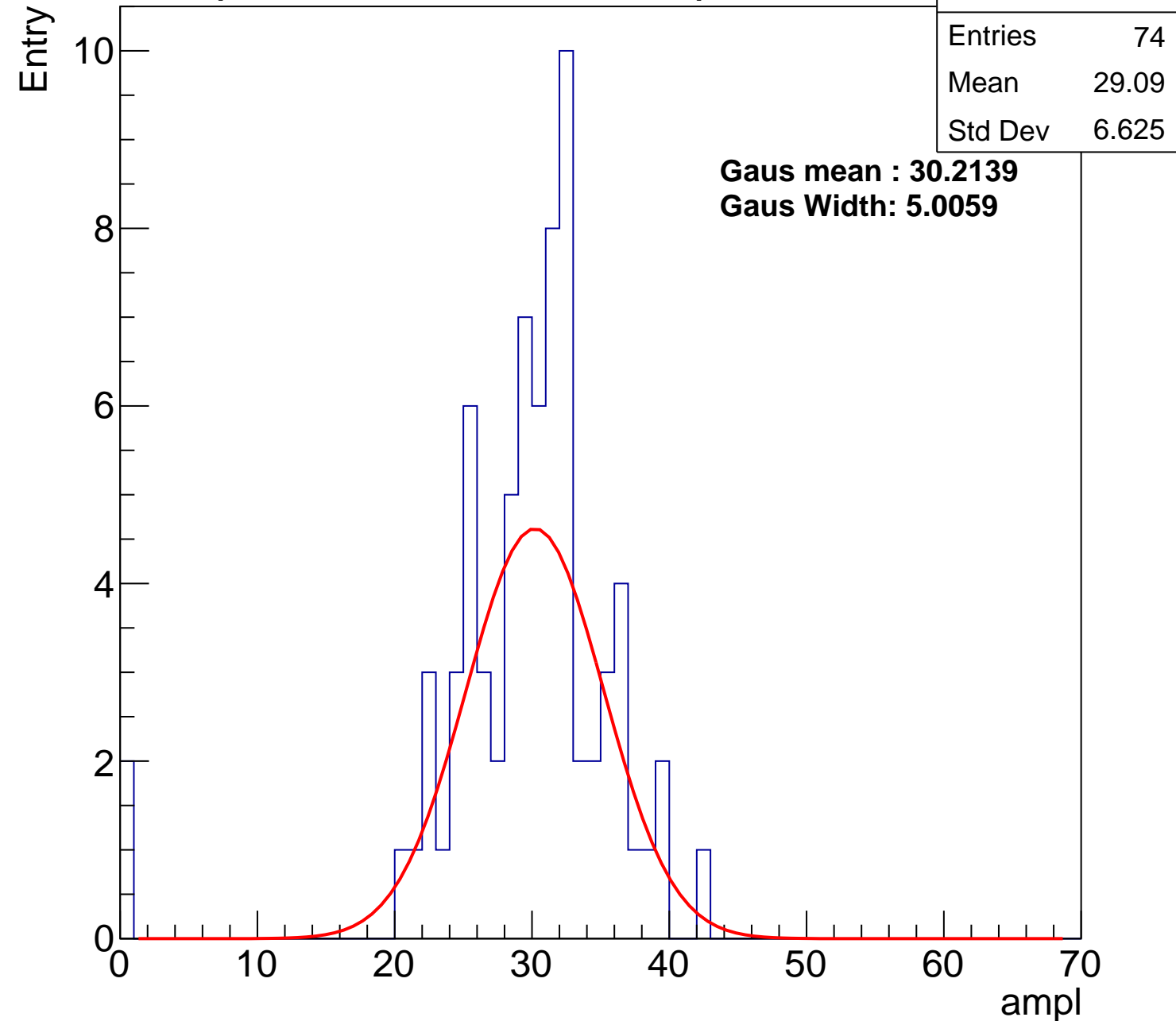
Gaus Width: 5.0059

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch107, adc1

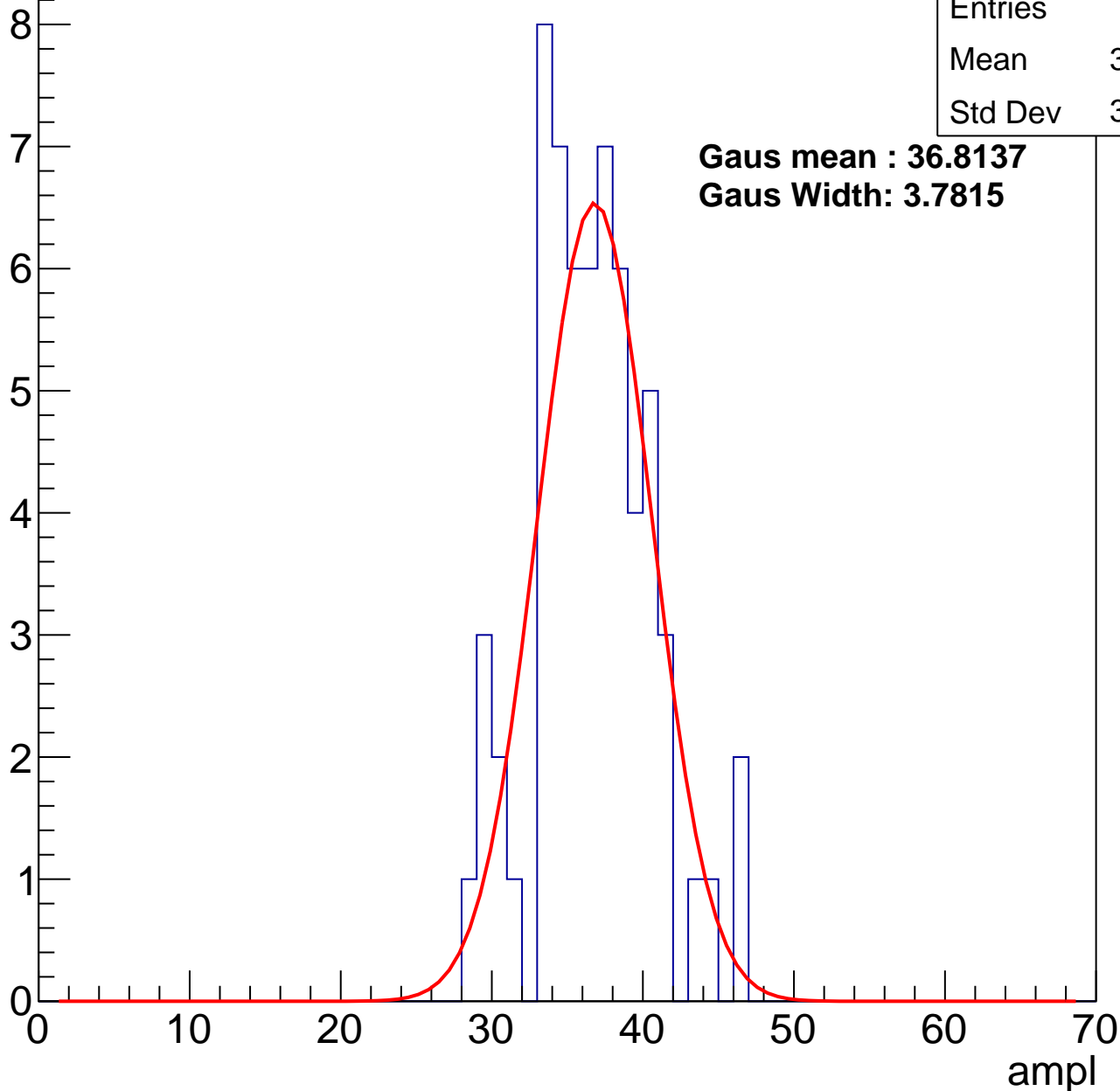
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	36.17
Std Dev	3.877

Gaus mean : 36.8137

Gaus Width: 3.7815



B1L103S, U14-ch107, adc2

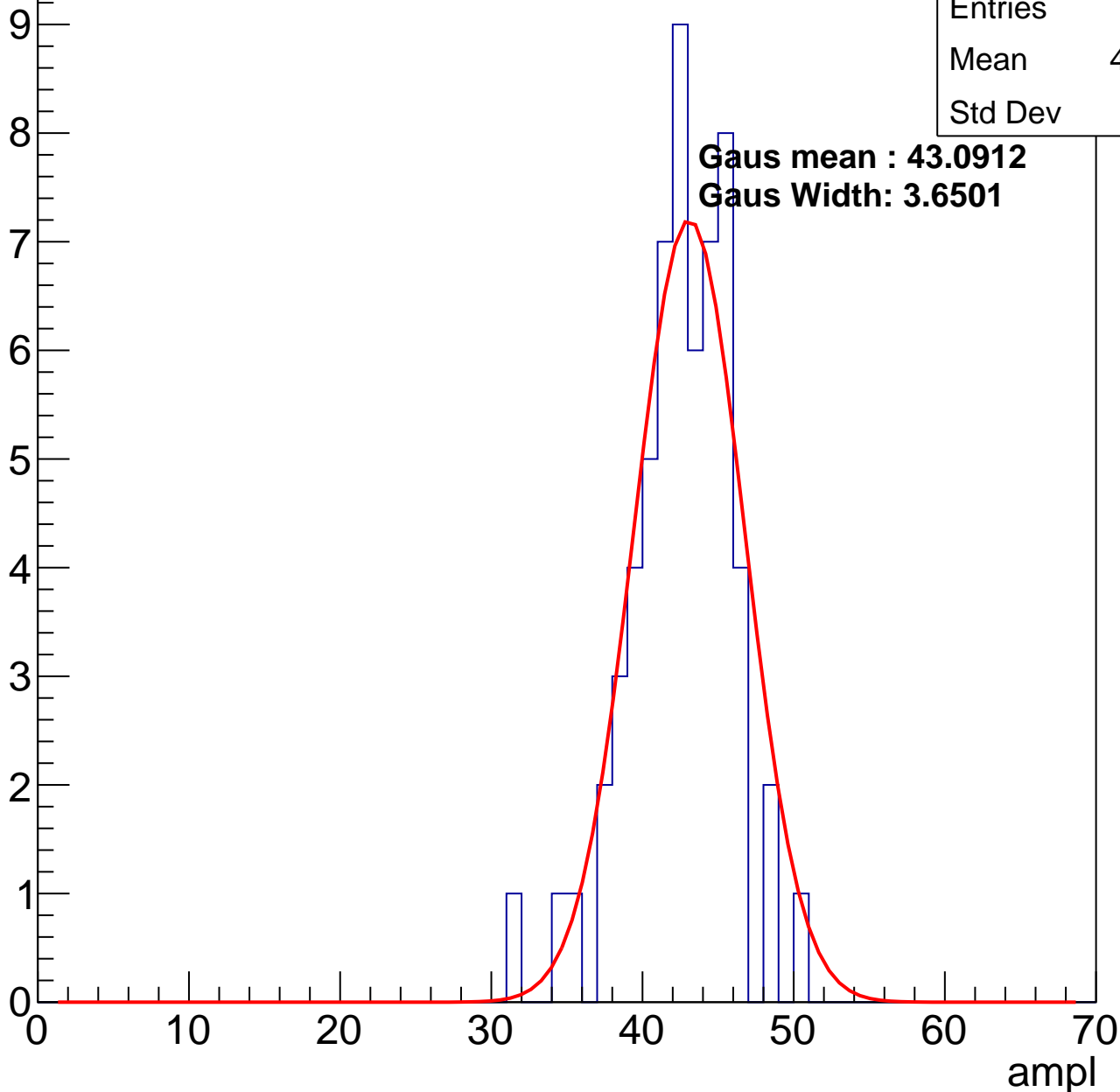
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.05
Std Dev	3.39

Gaus mean : 43.0912

Gaus Width: 3.6501

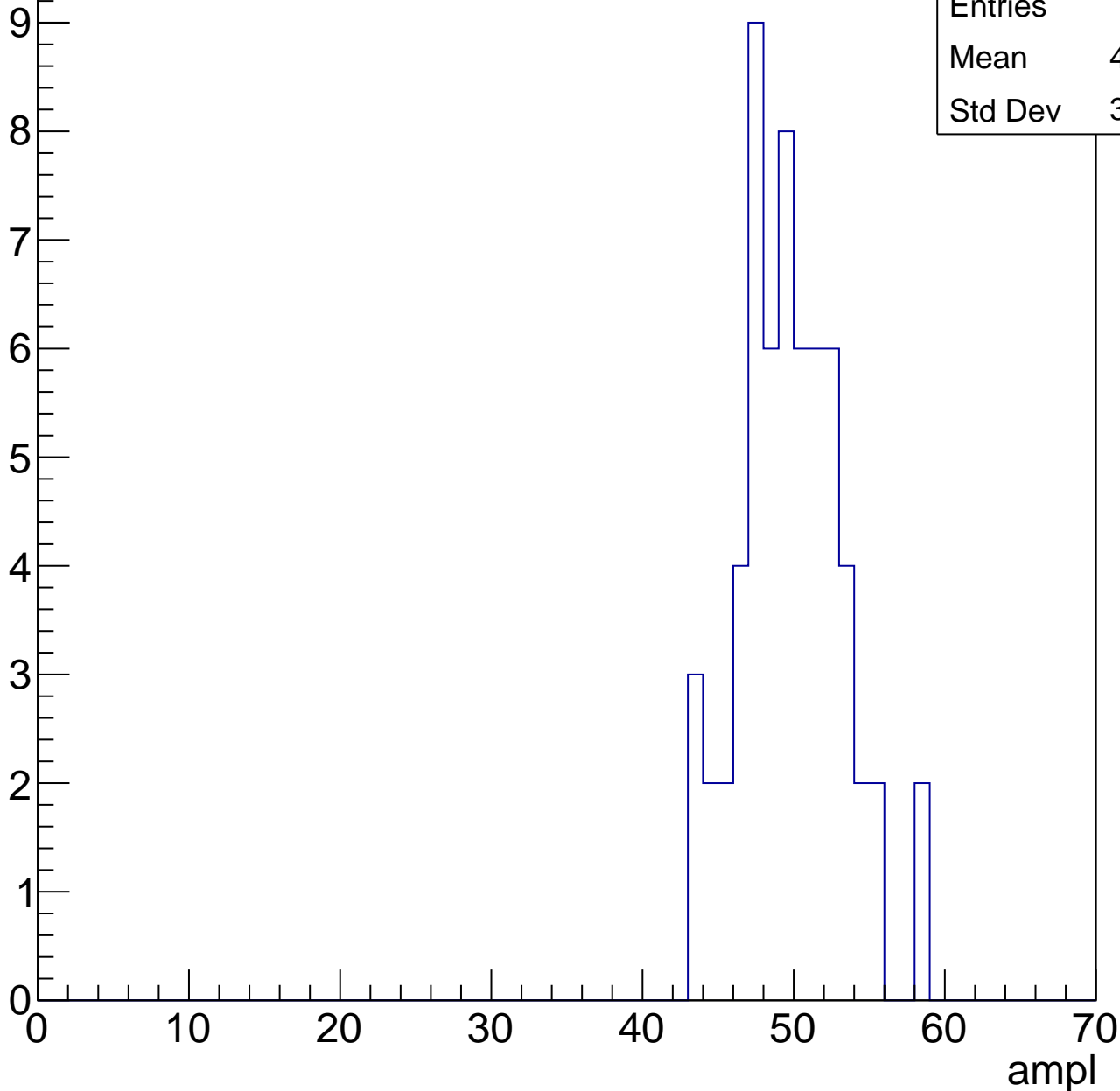


B1L103S, U14-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	49.32
Std Dev	3.345

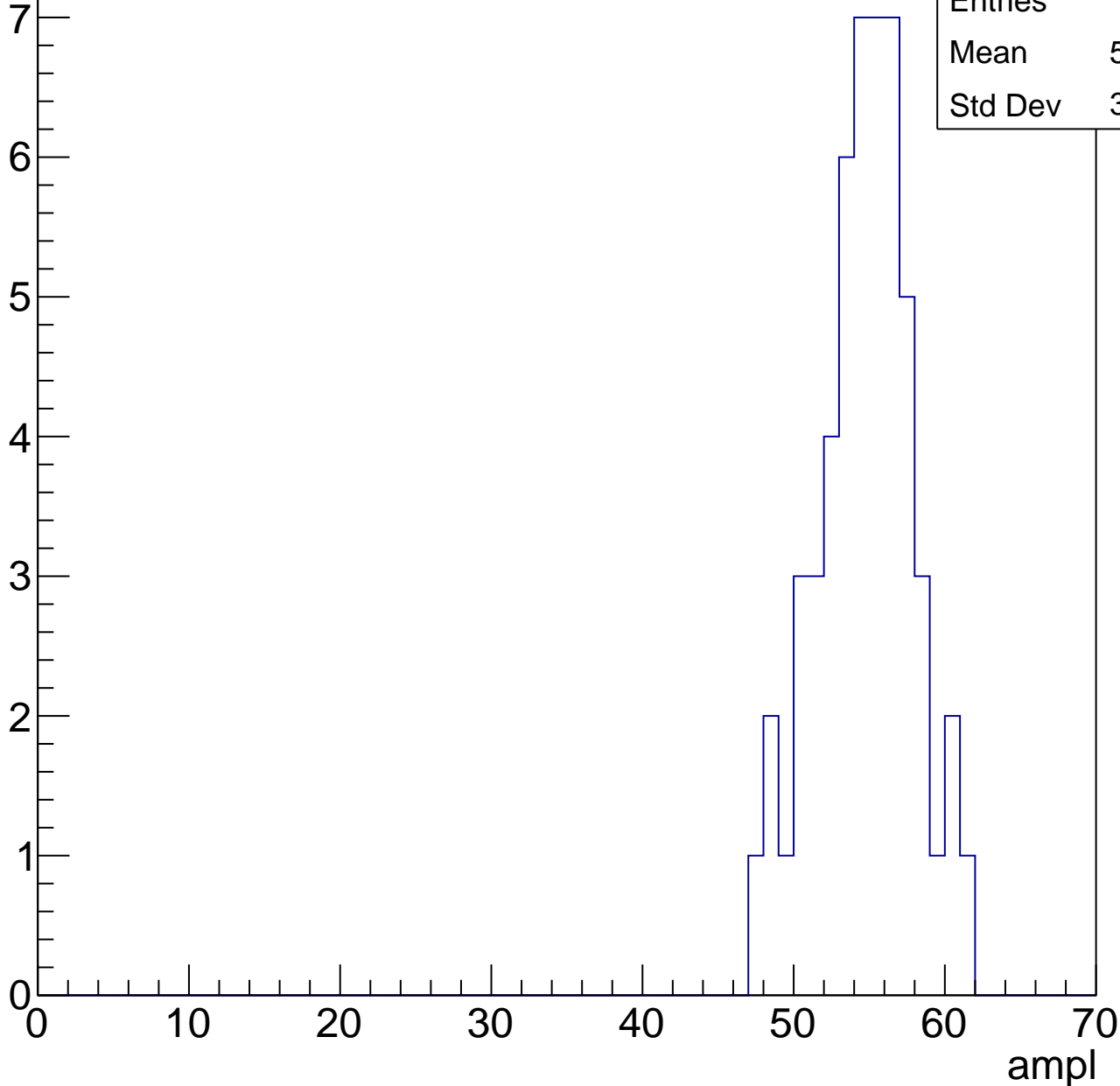


B1L103S, U14-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	54.25
Std Dev	3.114

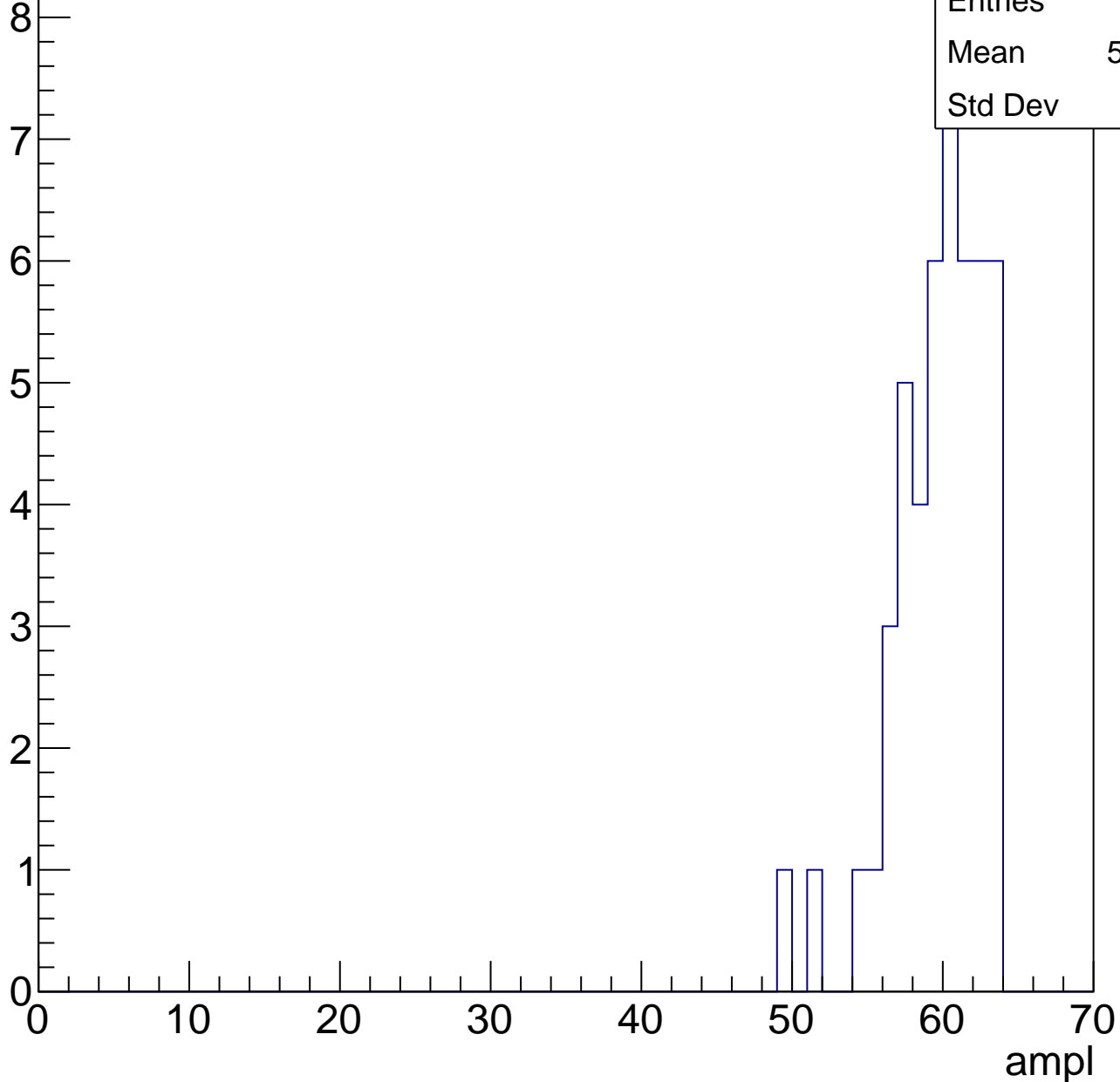


B1L103S, U14-ch107, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

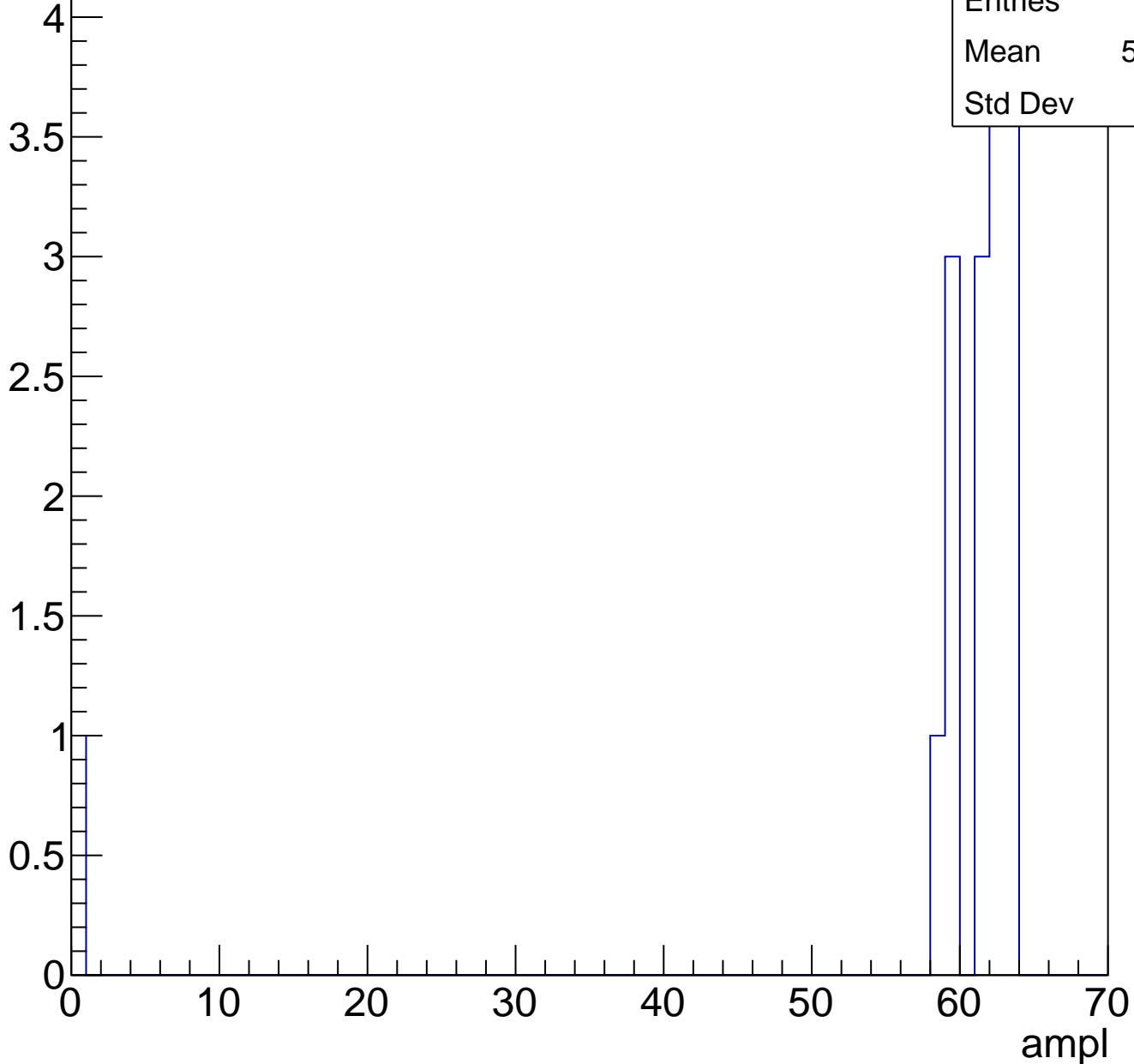
Entries	48
Mean	59.25
Std Dev	3.01



B1L103S, U14-ch107, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch108, adc0

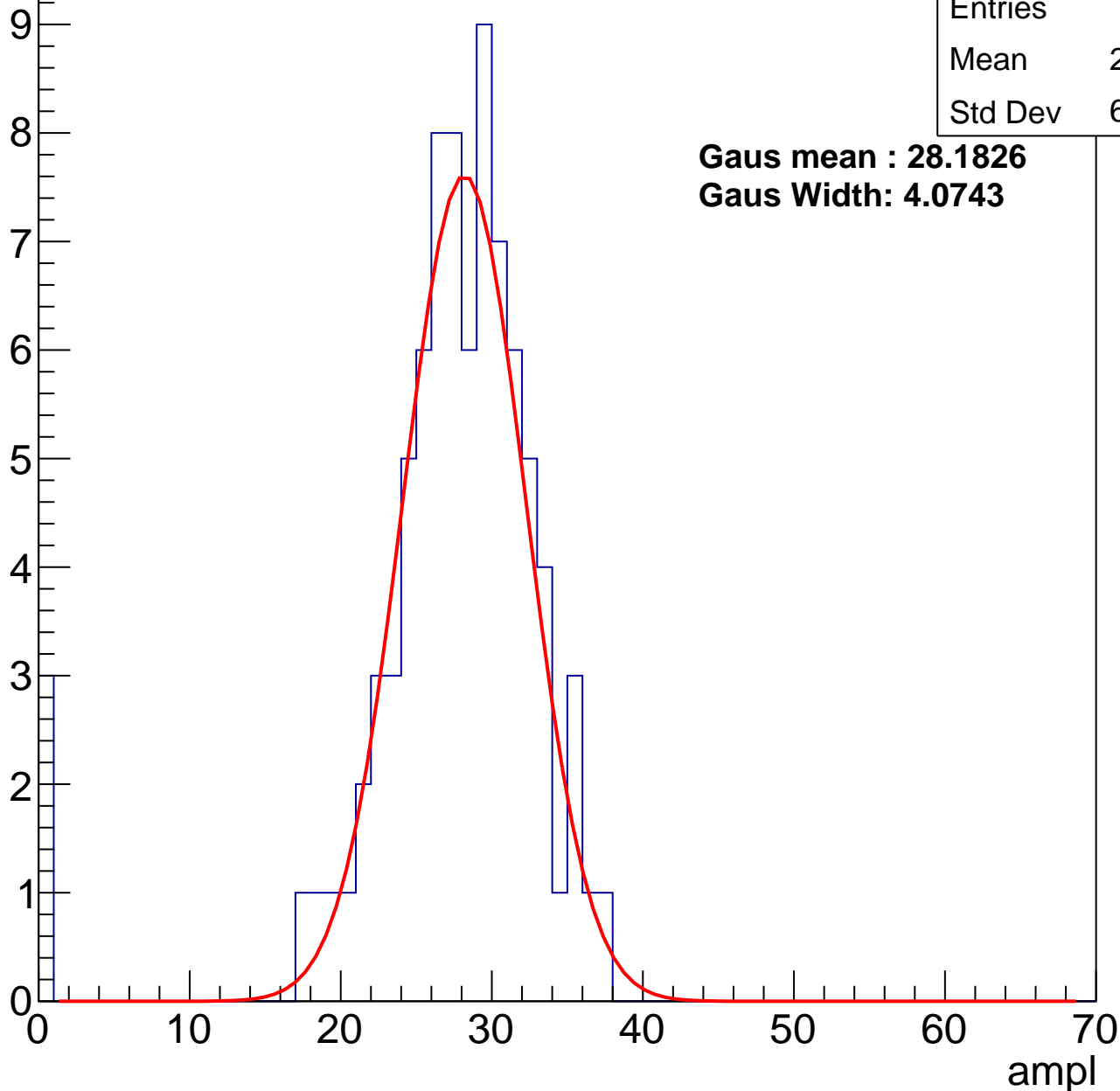
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	26.75
Std Dev	6.544

Gaus mean : 28.1826

Gaus Width: 4.0743



B1L103S, U14-ch108, adc1

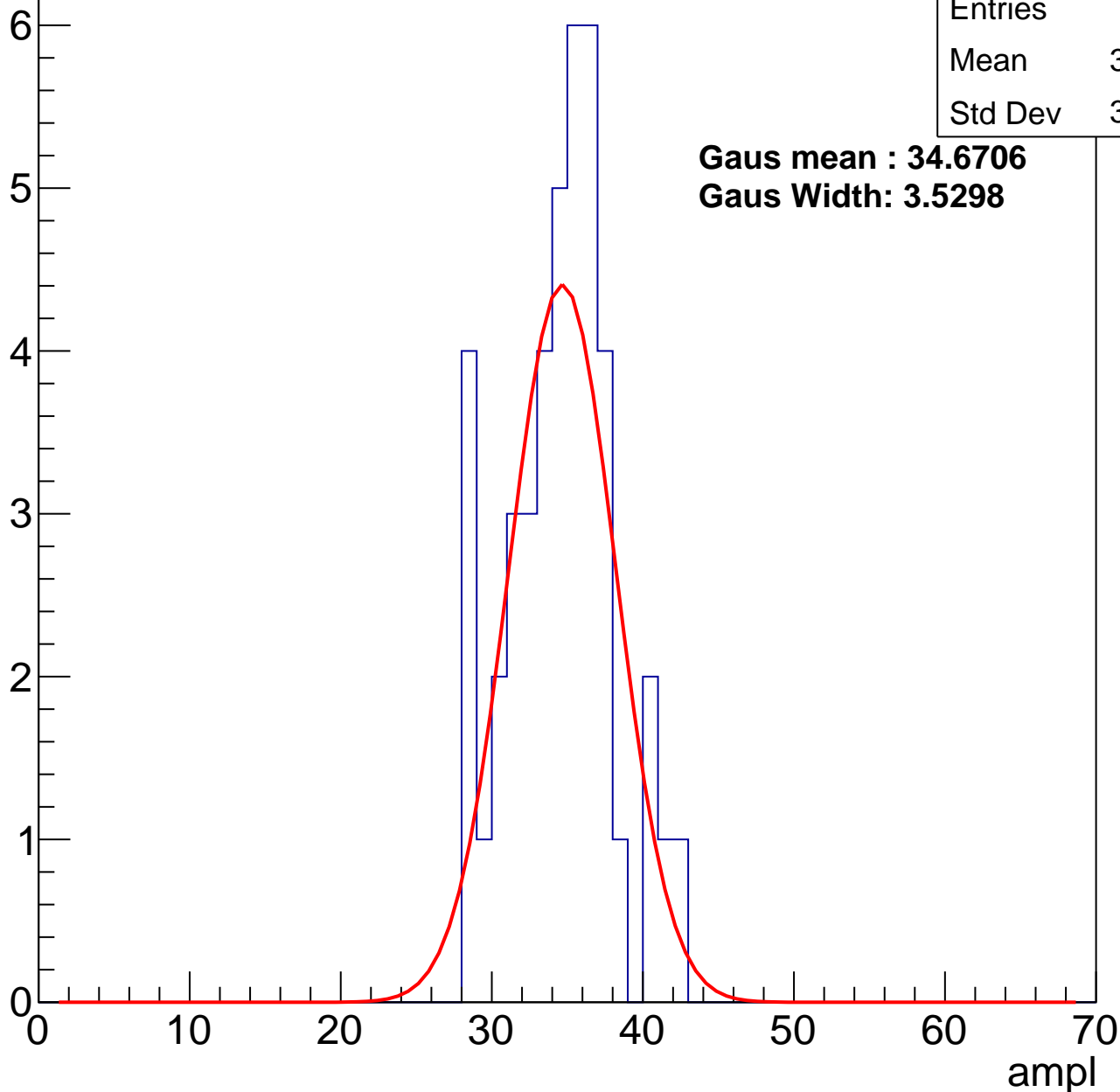
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	34.12
Std Dev	3.445

Gaus mean : 34.6706

Gaus Width: 3.5298



B1L103S, U14-ch108, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	39.1
Std Dev	3.68

Gaus mean : 39.9488

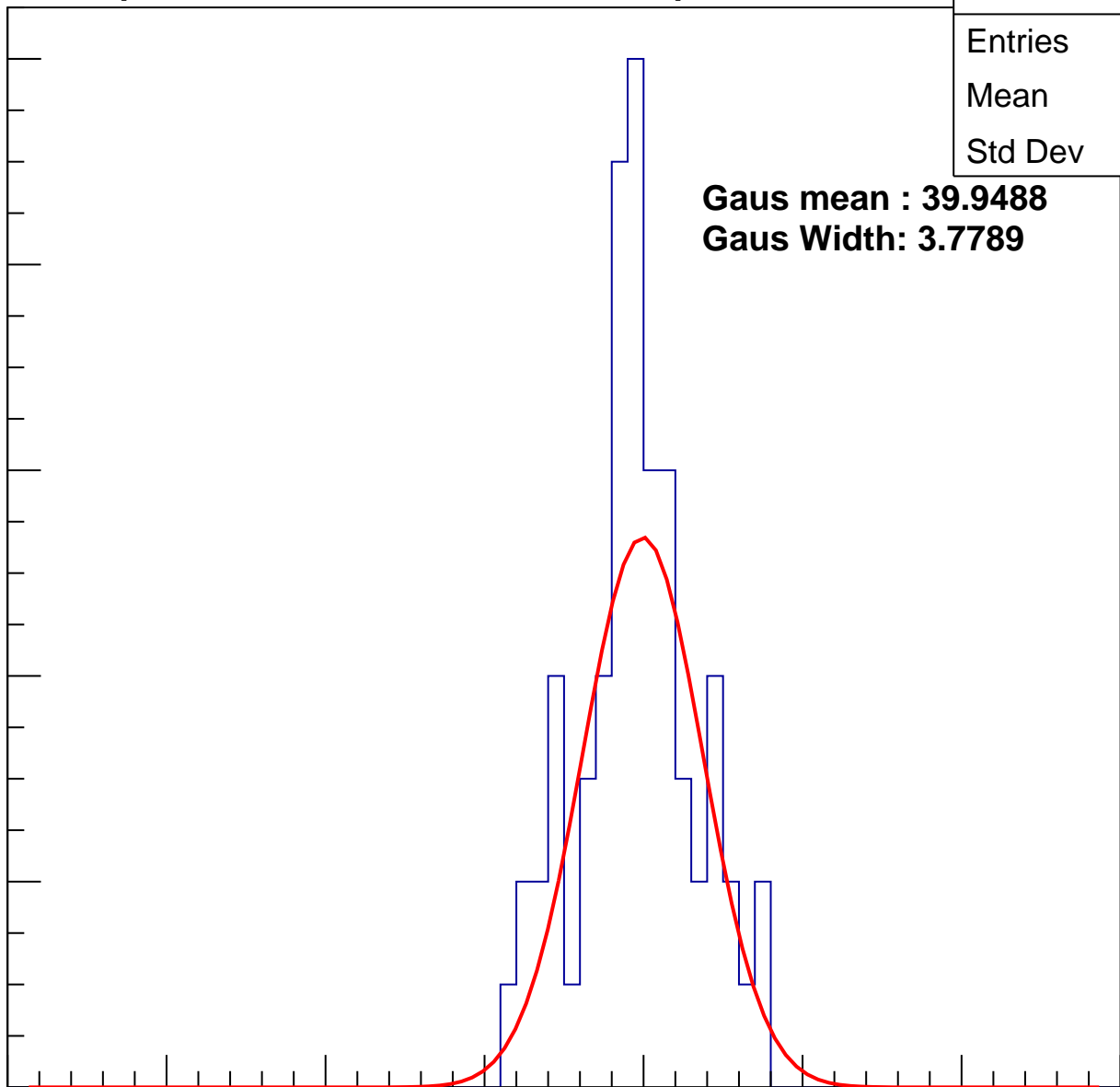
Gaus Width: 3.7789

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

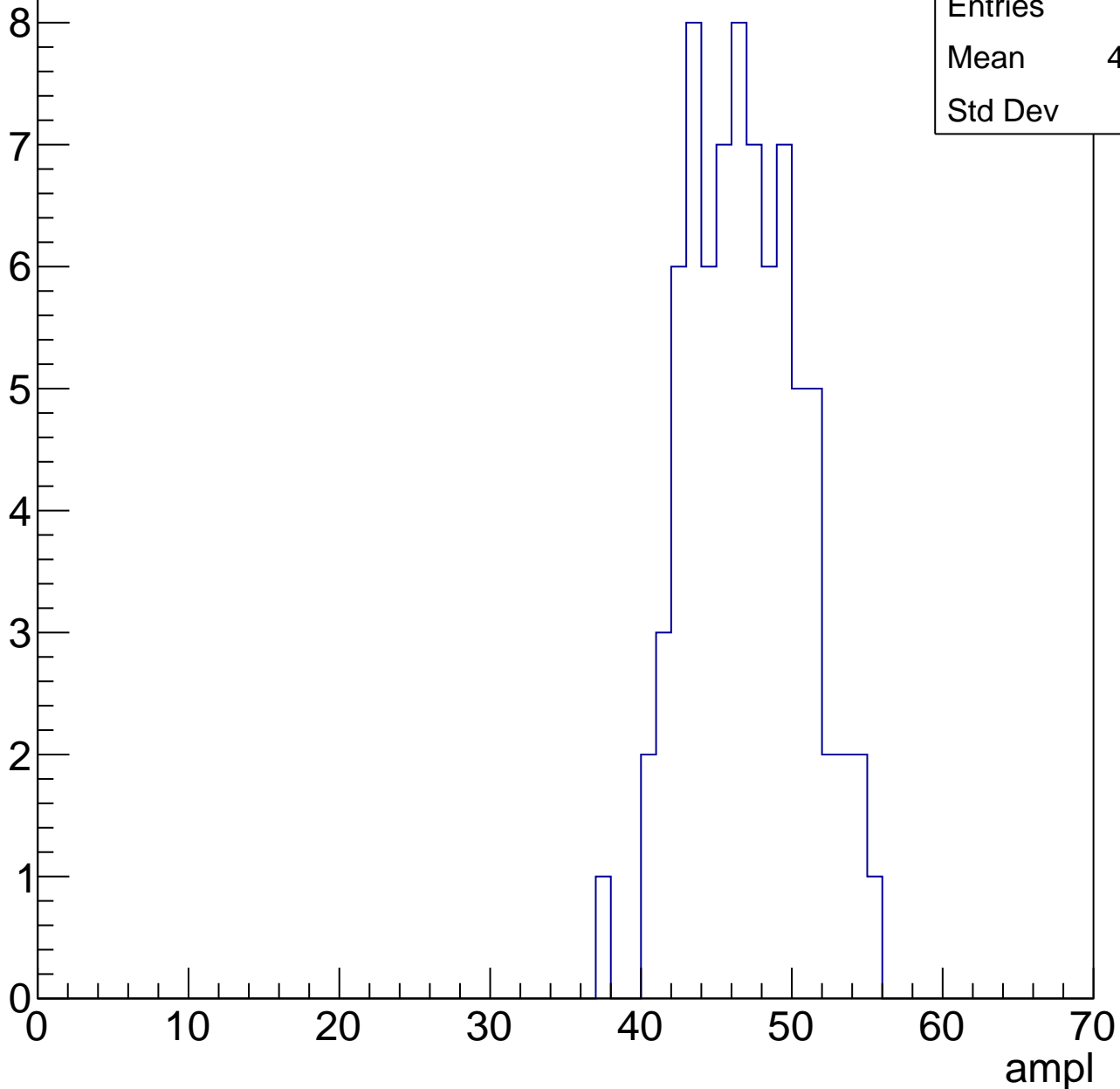


B1L103S, U14-ch108, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	46.42
Std Dev	3.74

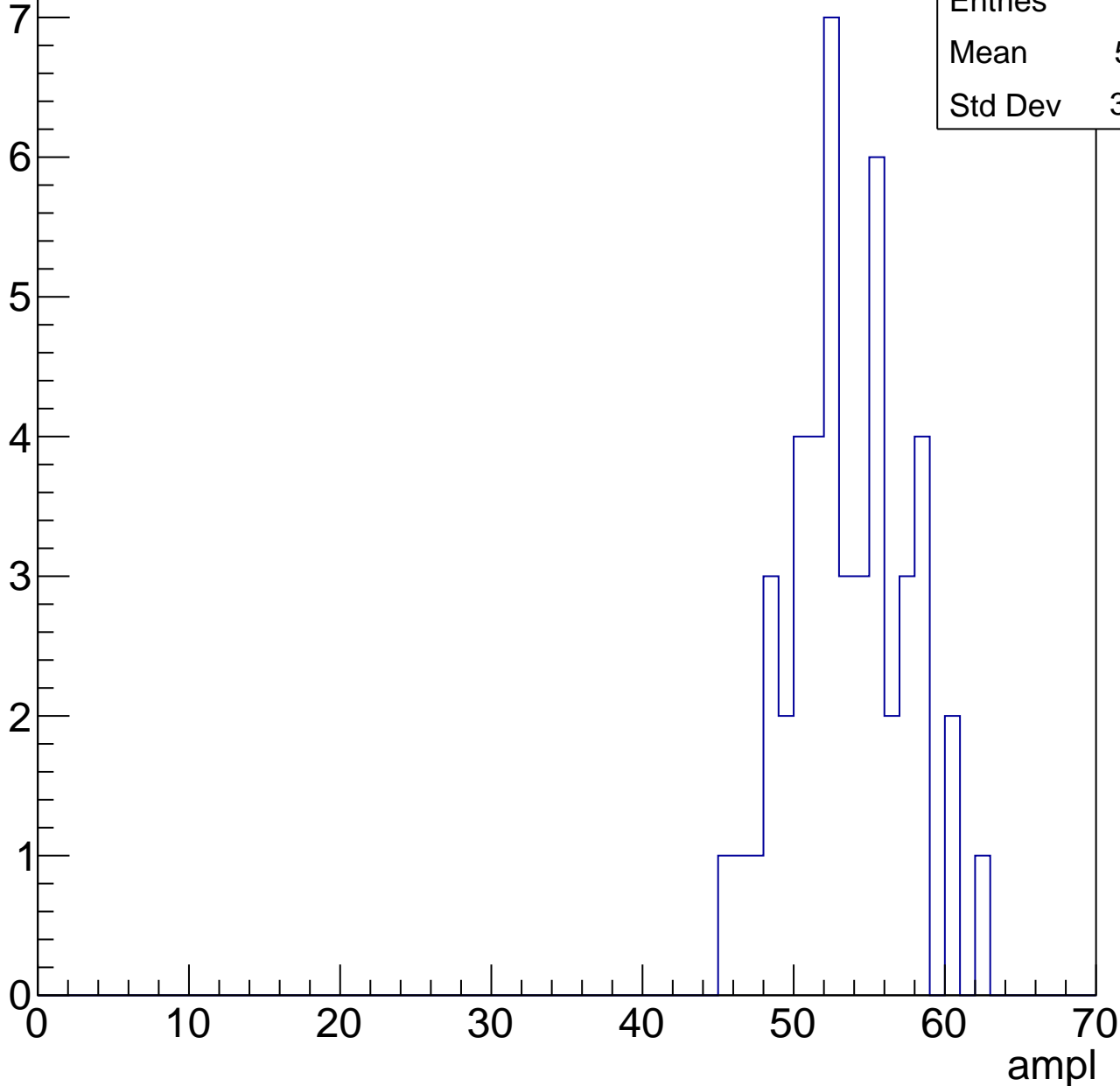


B1L103S, U14-ch108, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	53.11
Std Dev	3.822

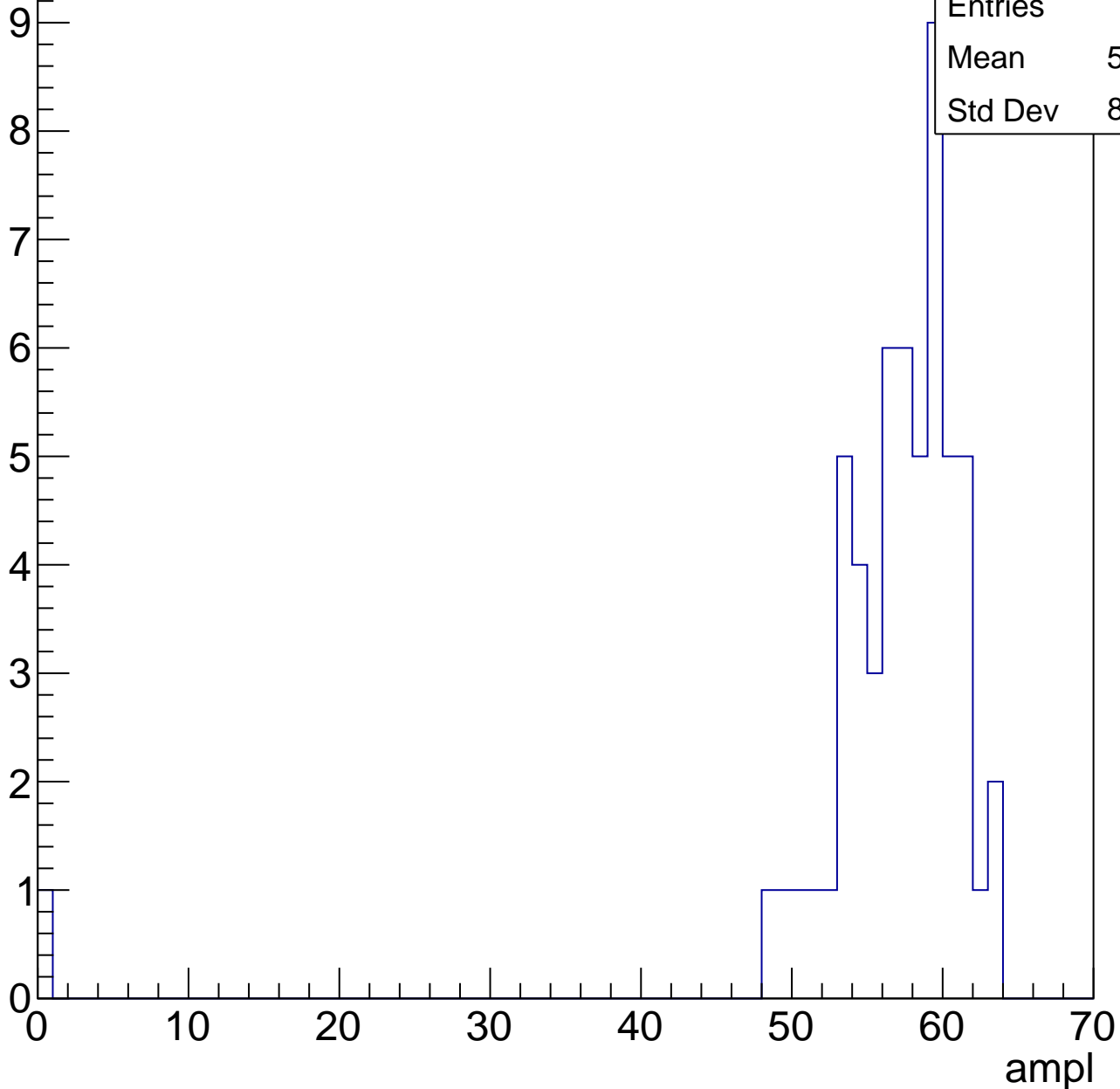


B1L103S, U14-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.93
Std Dev	8.203

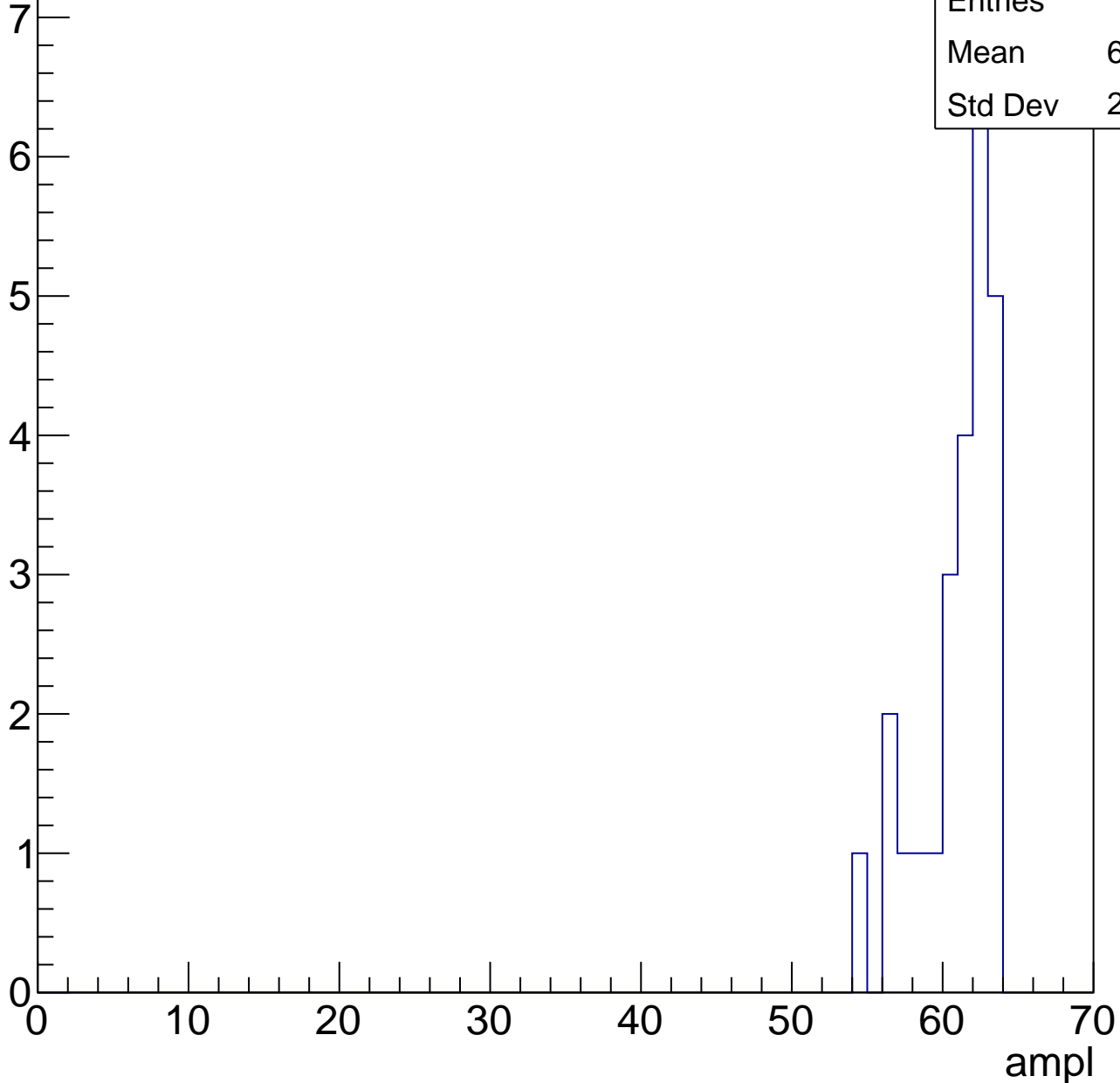


B1L103S, U14-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

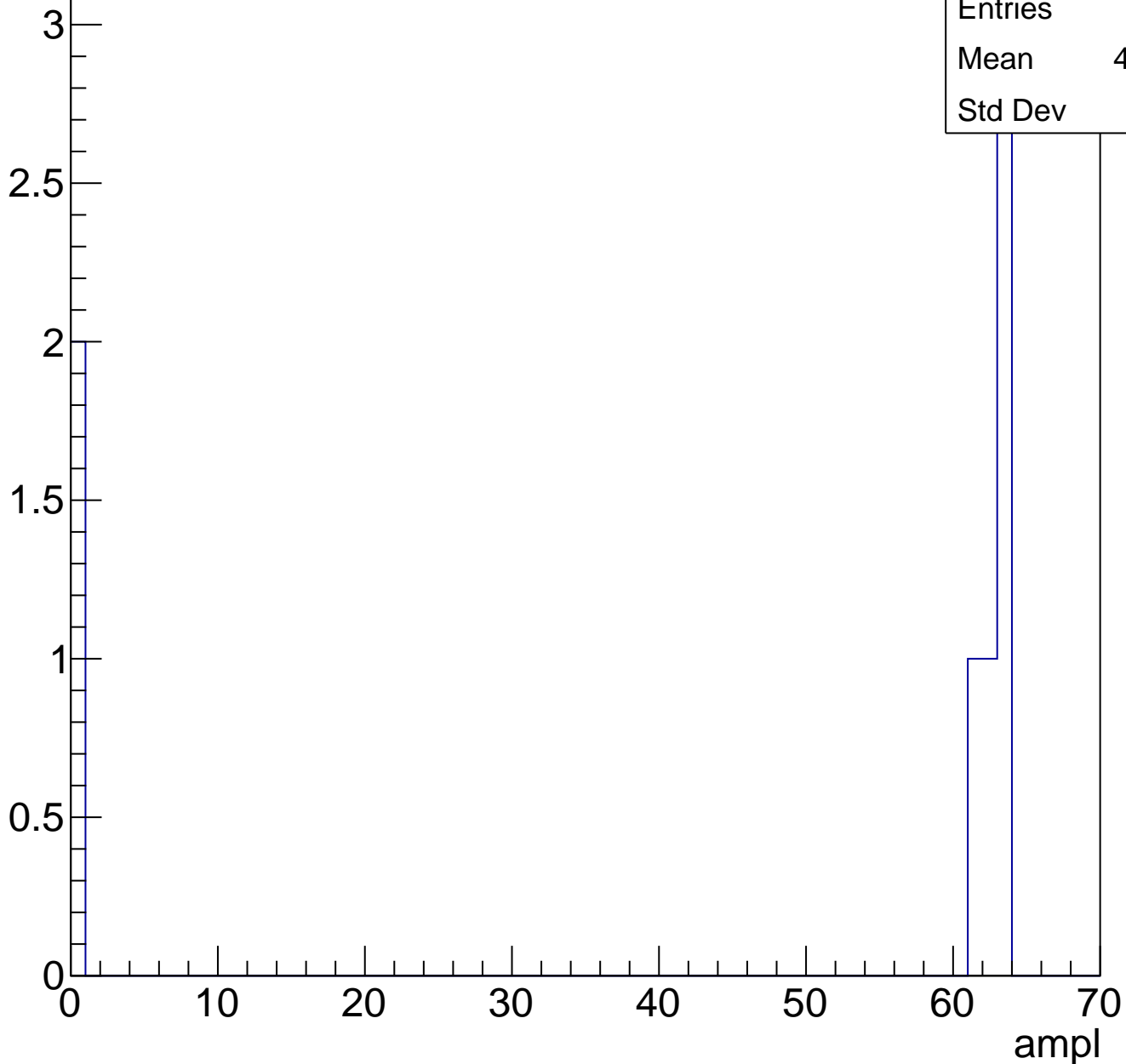
Entries	25
Mean	60.52
Std Dev	2.468



B1L103S, U14-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch109, adc0

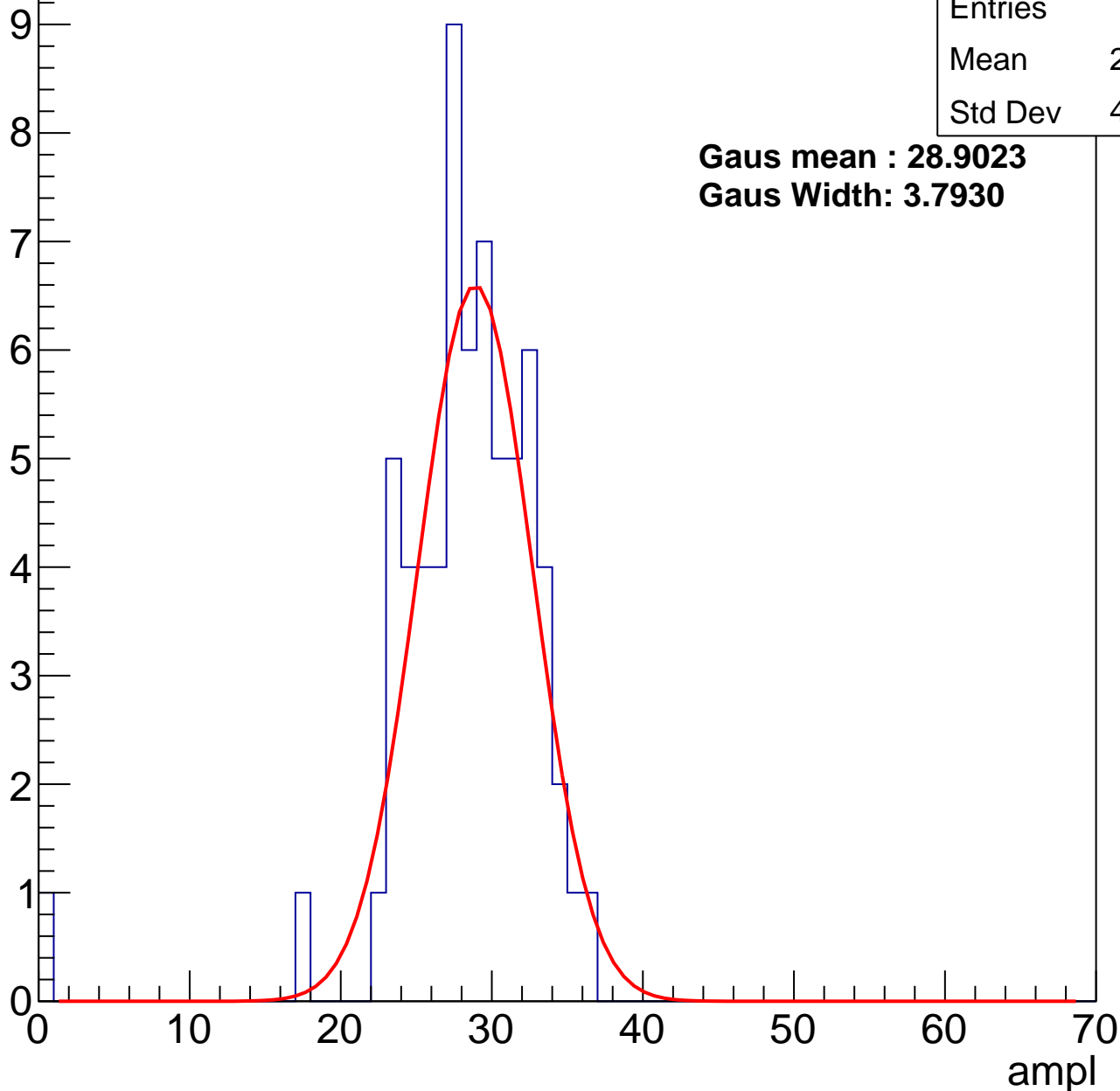
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	27.82
Std Dev	4.988

Gaus mean : 28.9023

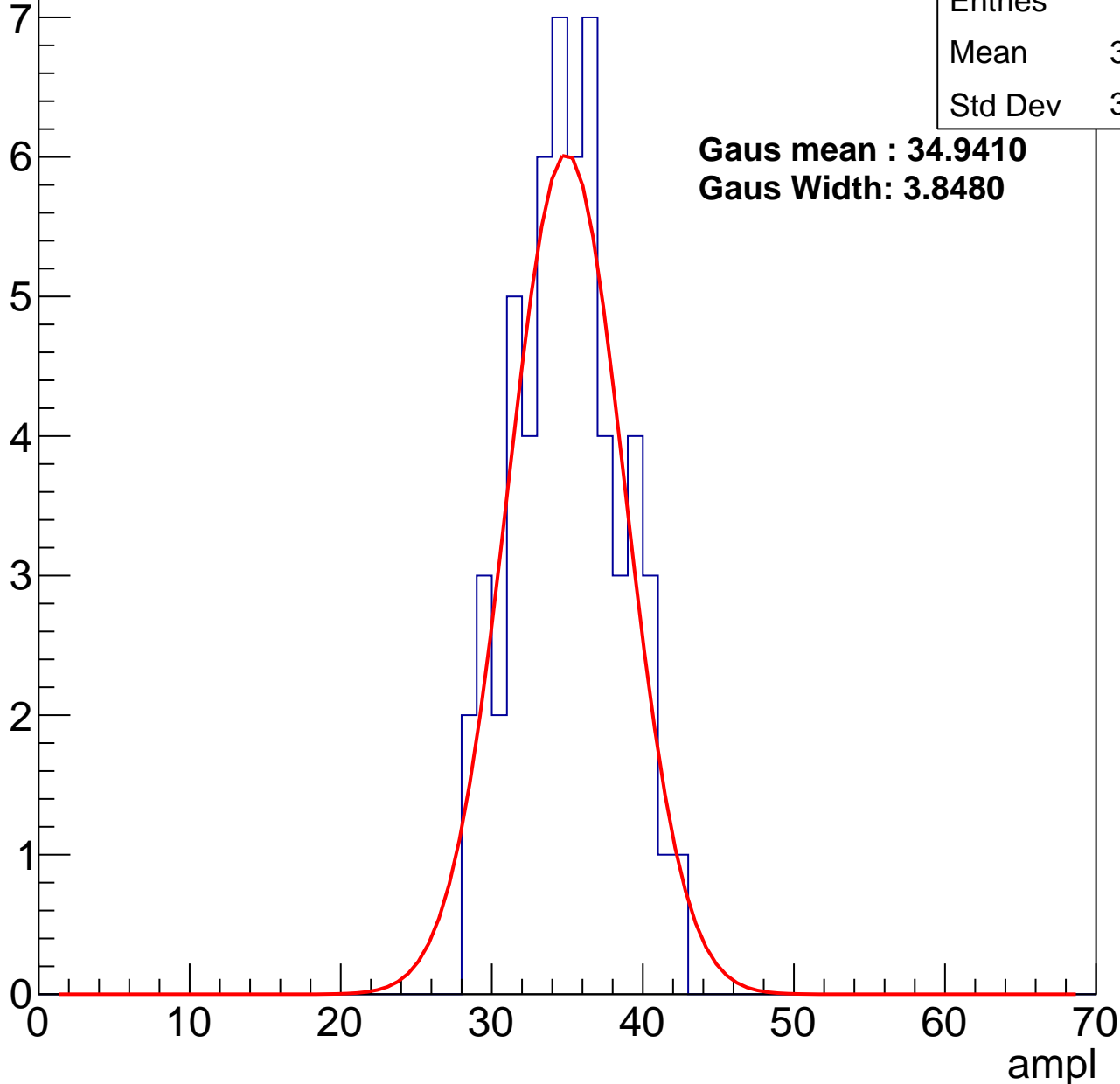
Gaus Width: 3.7930



B1L103S, U14-ch109, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch109, adc2

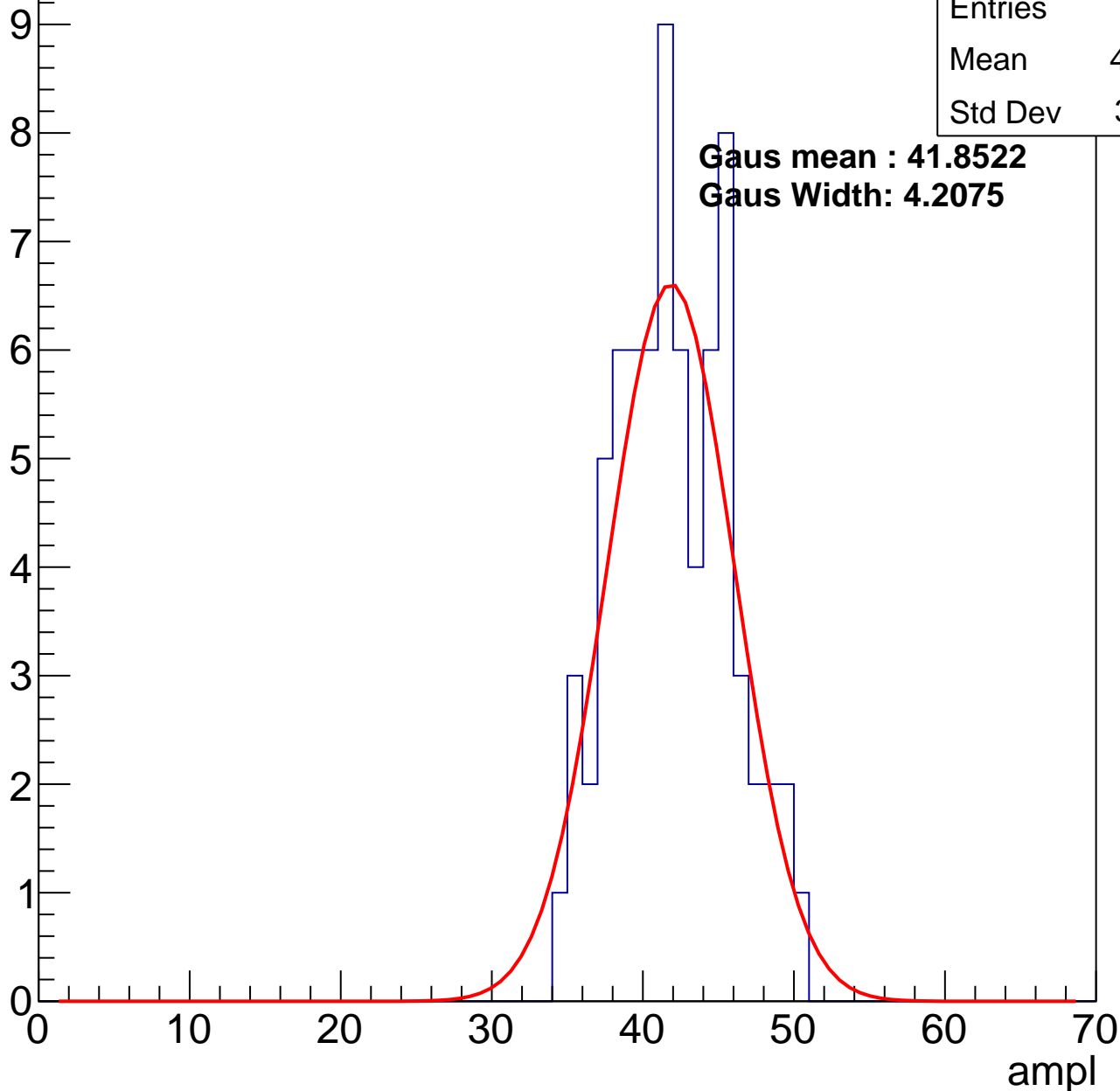
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	41.54
Std Dev	3.741

Gaus mean : 41.8522

Gaus Width: 4.2075

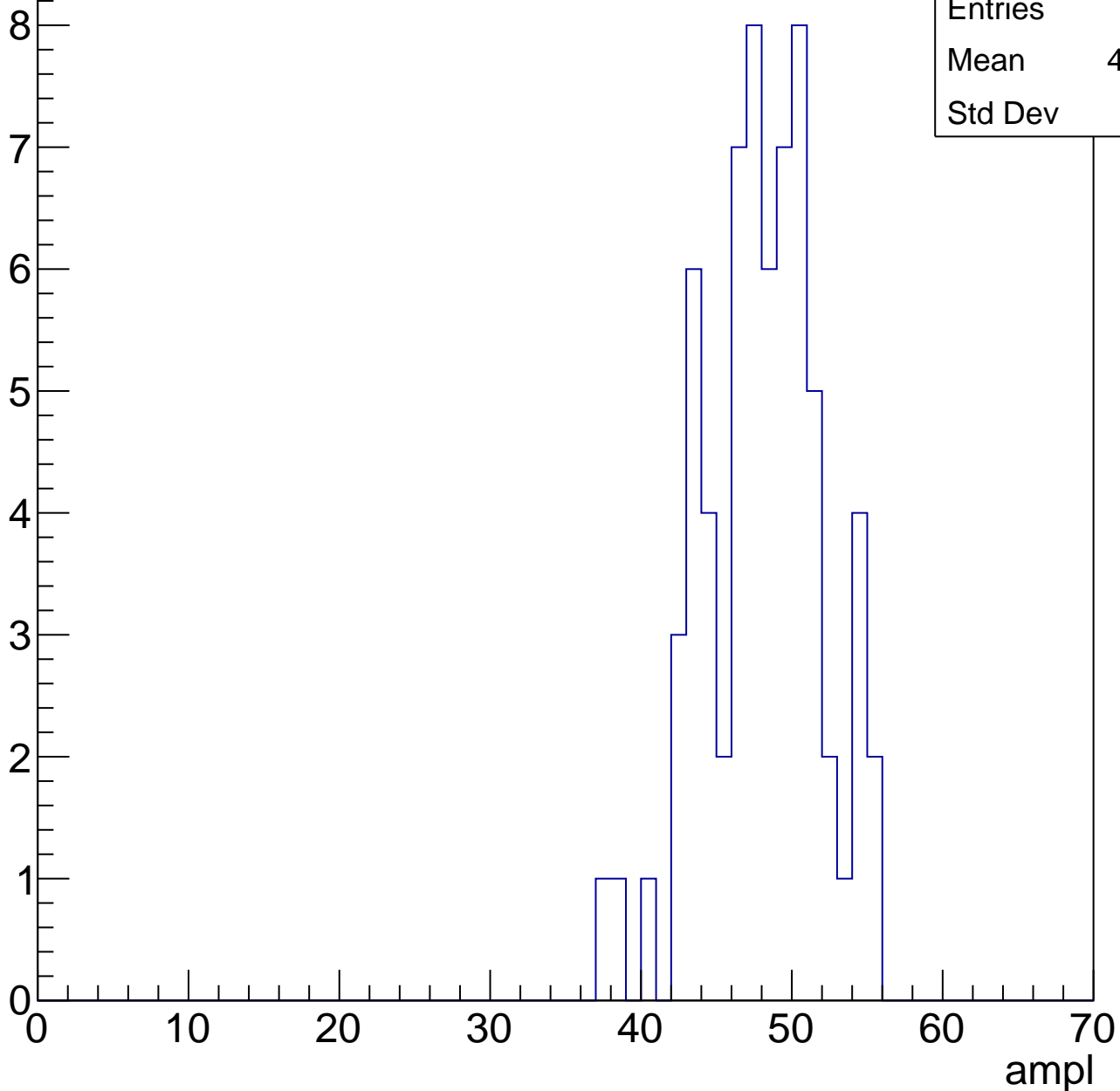


B1L103S, U14-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	47.53
Std Dev	3.89

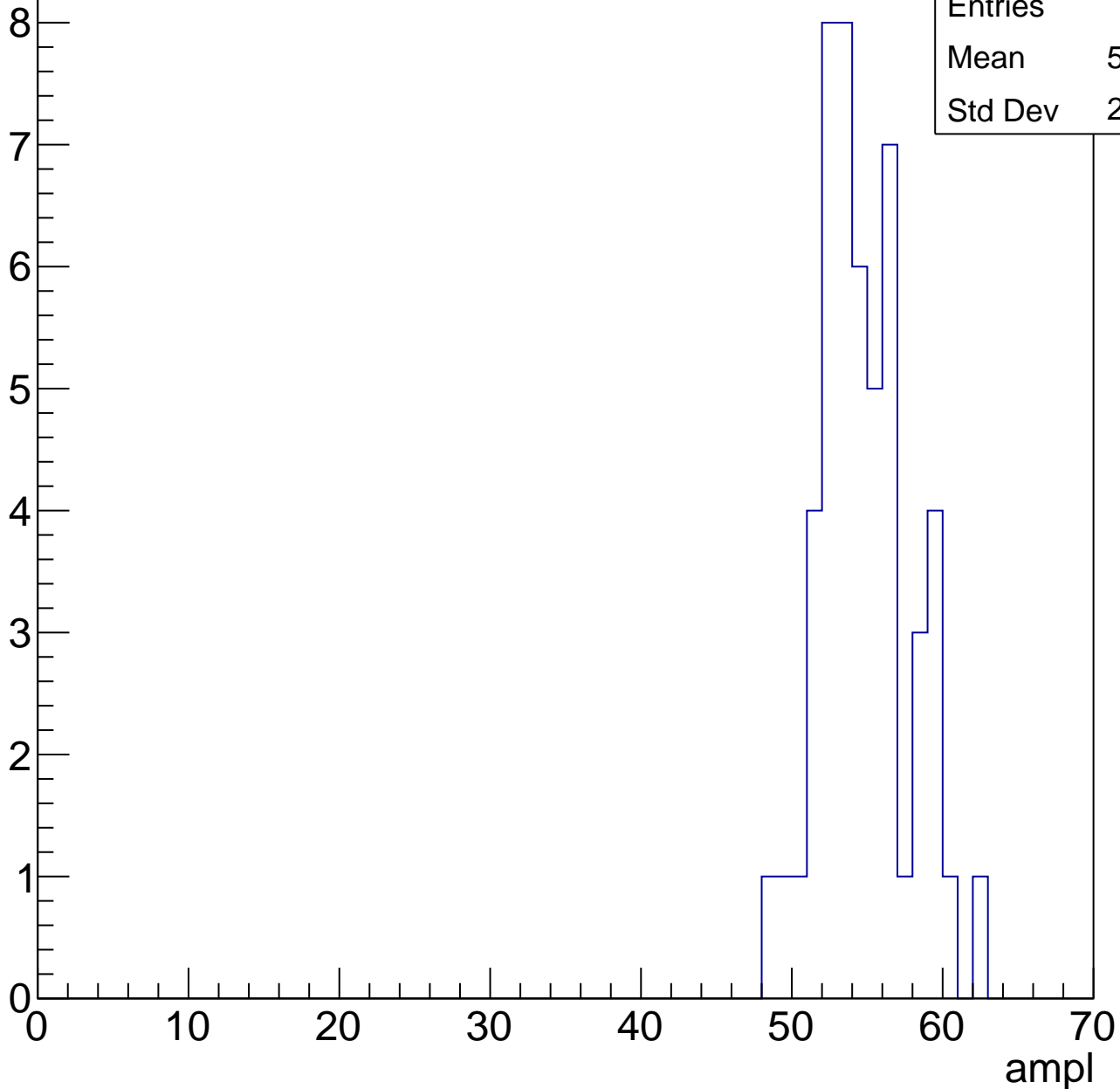


B1L103S, U14-ch109, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

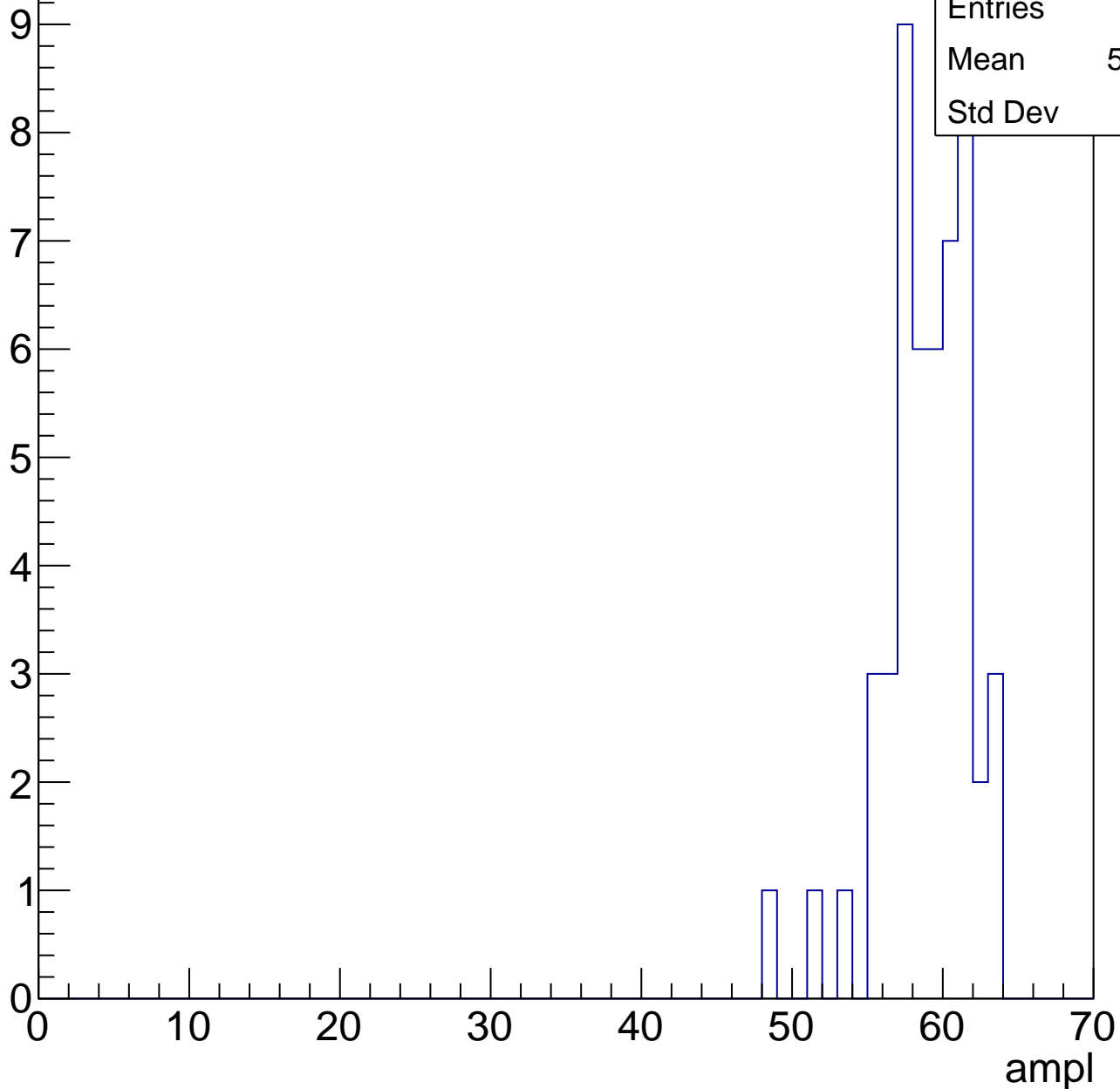
Entries	51
Mean	54.33
Std Dev	2.928



B1L103S, U14-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

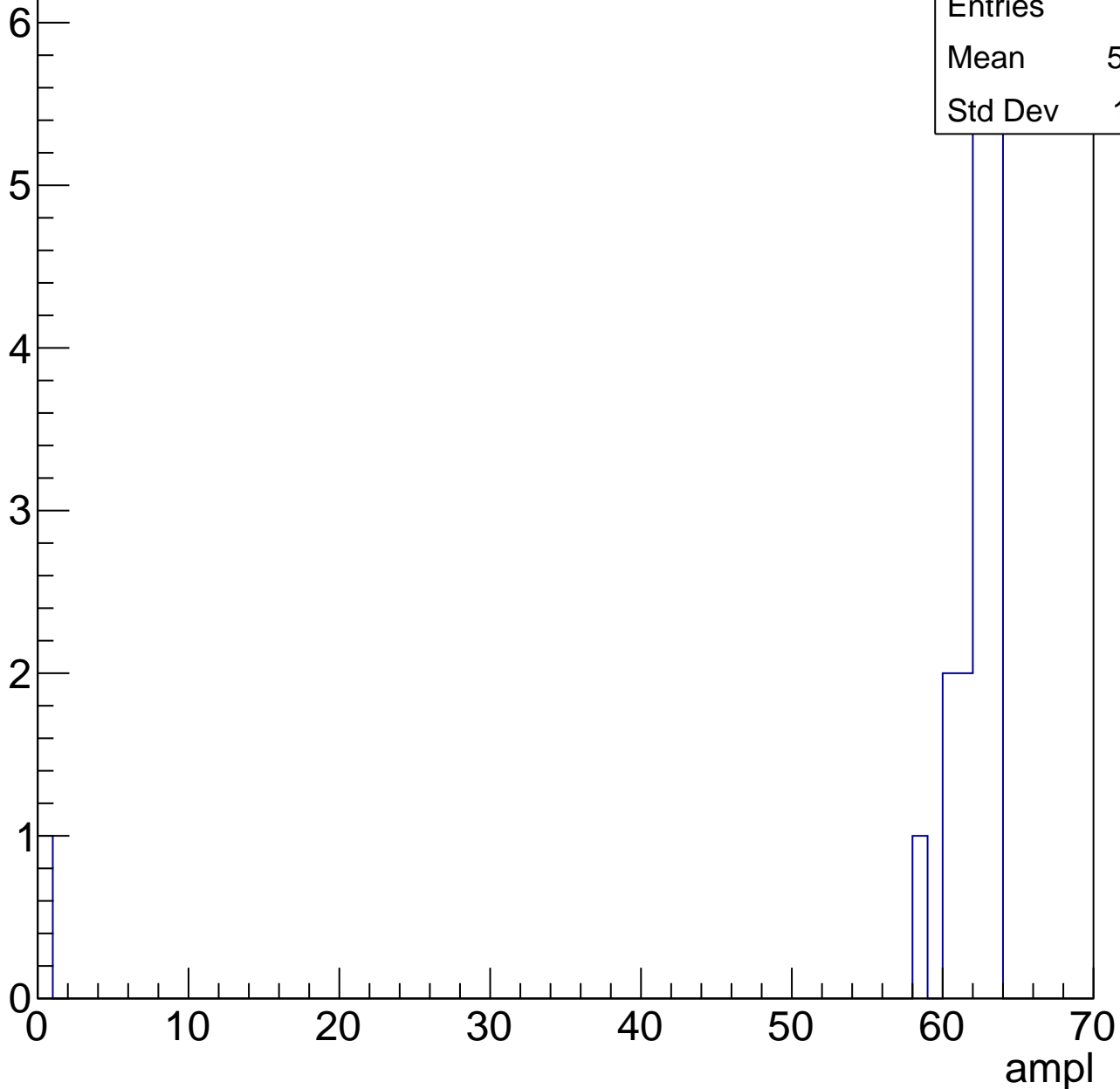


B1L103S, U14-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	18
Mean	58.33
Std Dev	14.21



B1L103S, U14-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch110, adc0

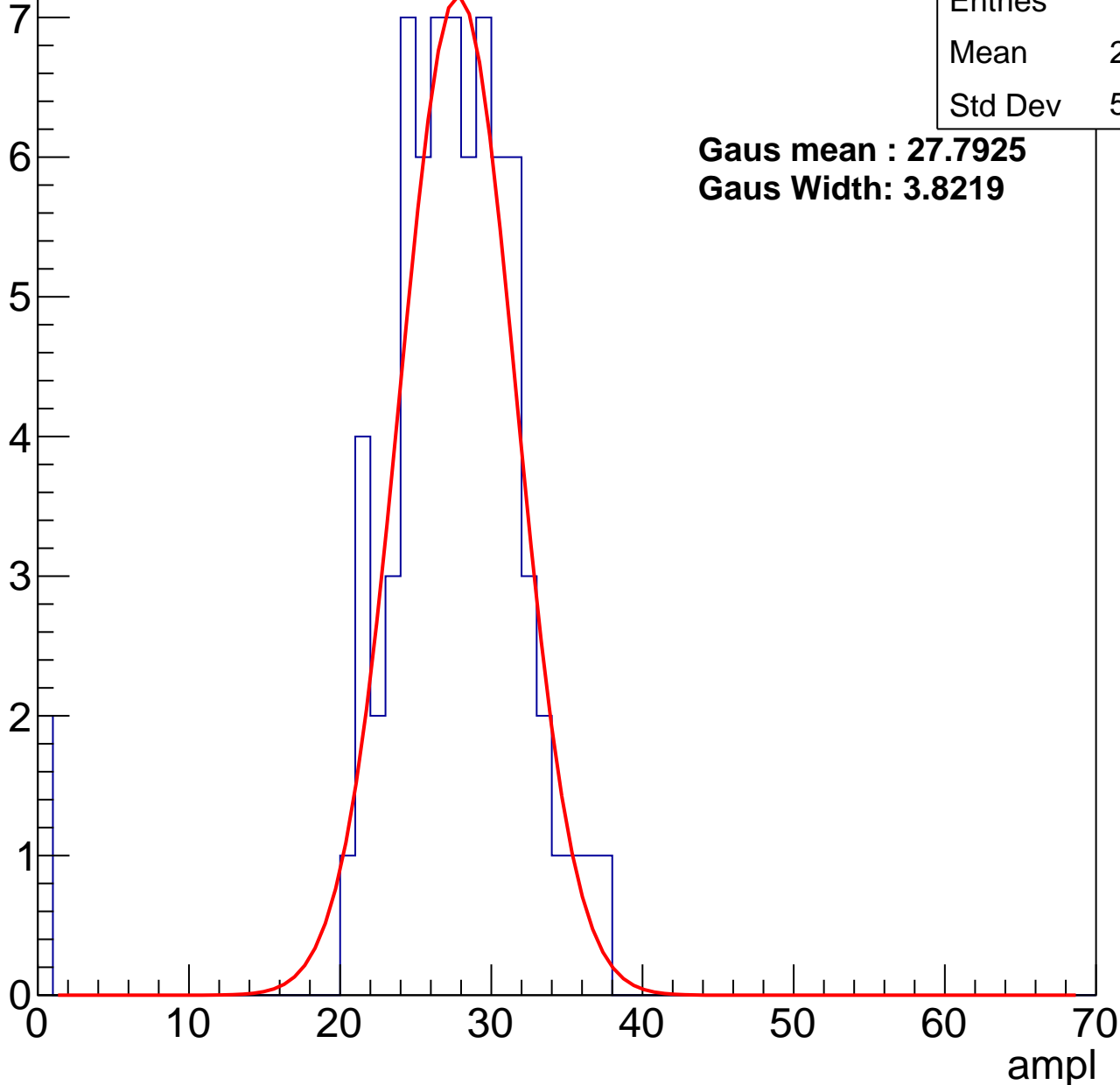
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	26.67
Std Dev	5.805

Gaus mean : 27.7925

Gaus Width: 3.8219



B1L103S, U14-ch110, adc1

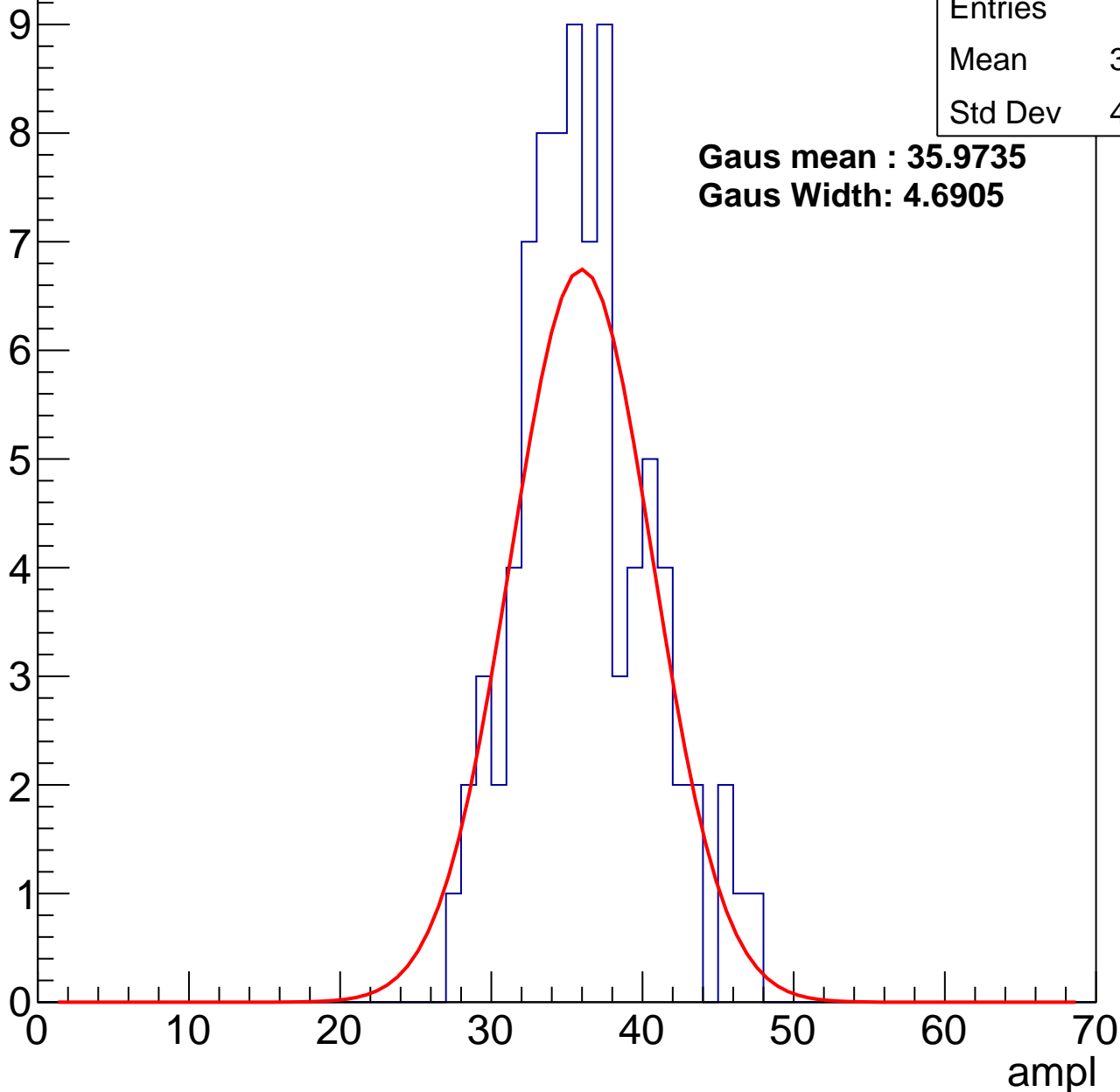
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	35.73
Std Dev	4.277

Gaus mean : 35.9735

Gaus Width: 4.6905



B1L103S, U14-ch110, adc2

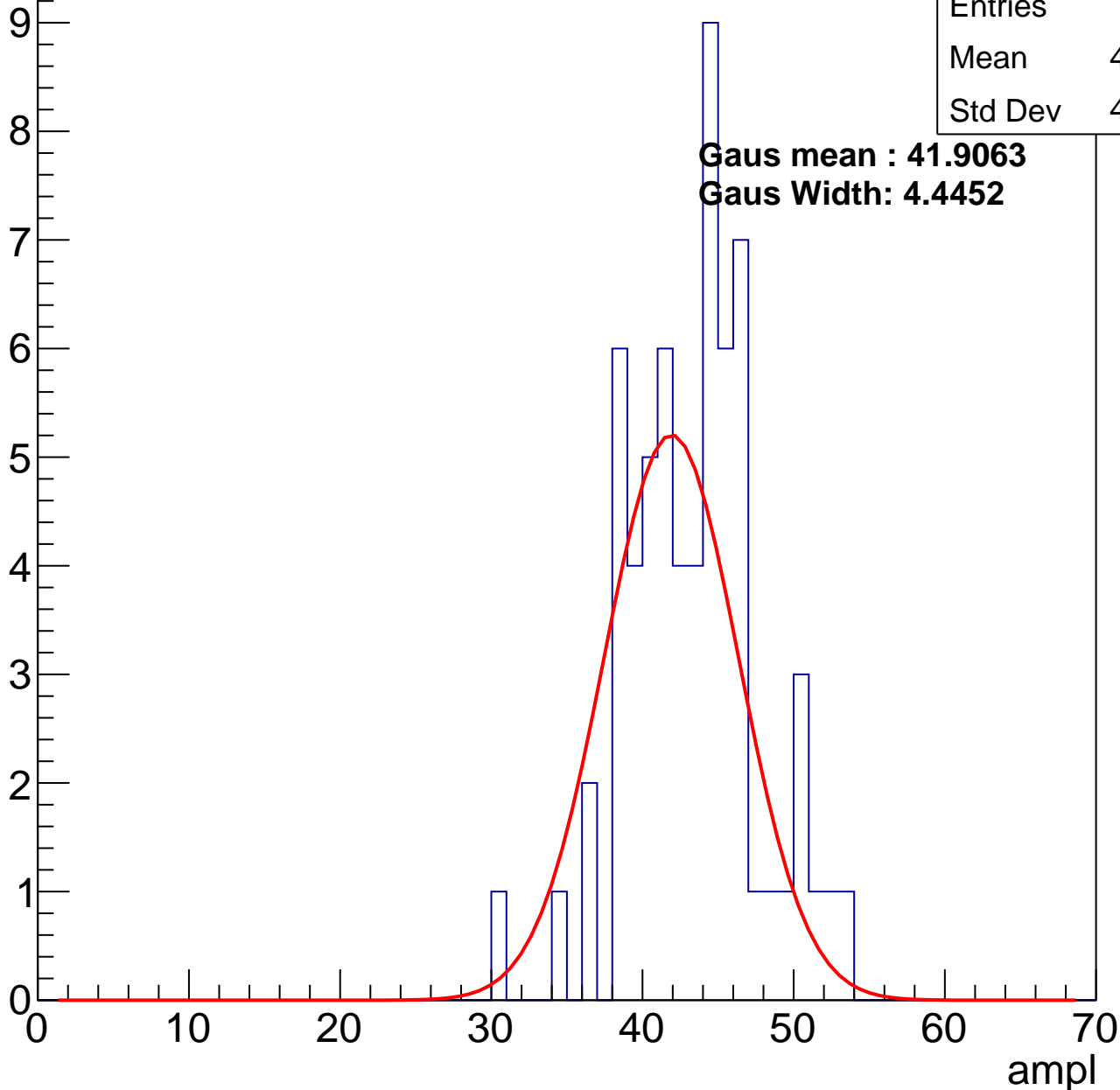
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.88
Std Dev	4.328

Gaus mean : 41.9063

Gaus Width: 4.4452

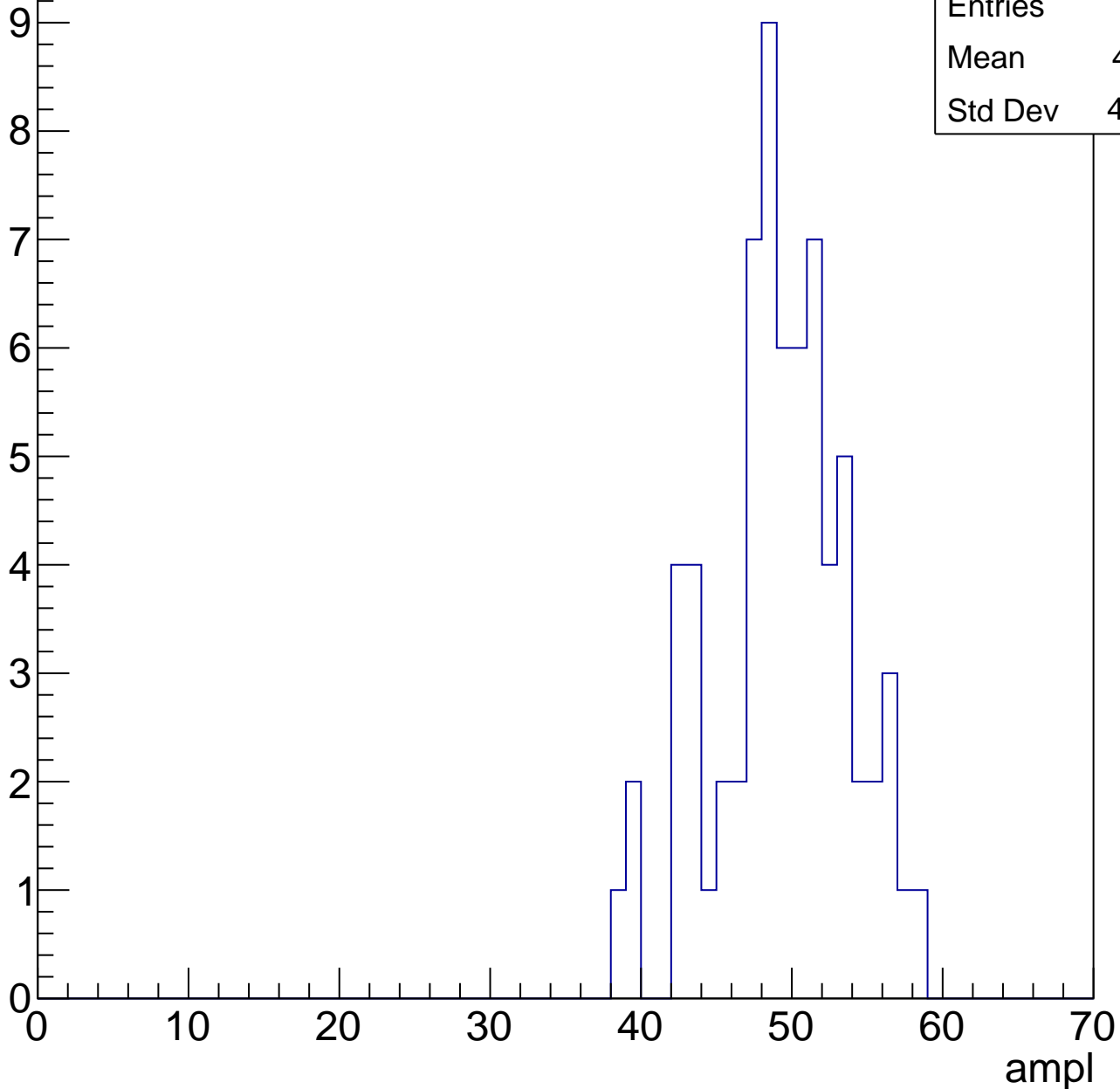


B1L103S, U14-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.81
Std Dev	4.398

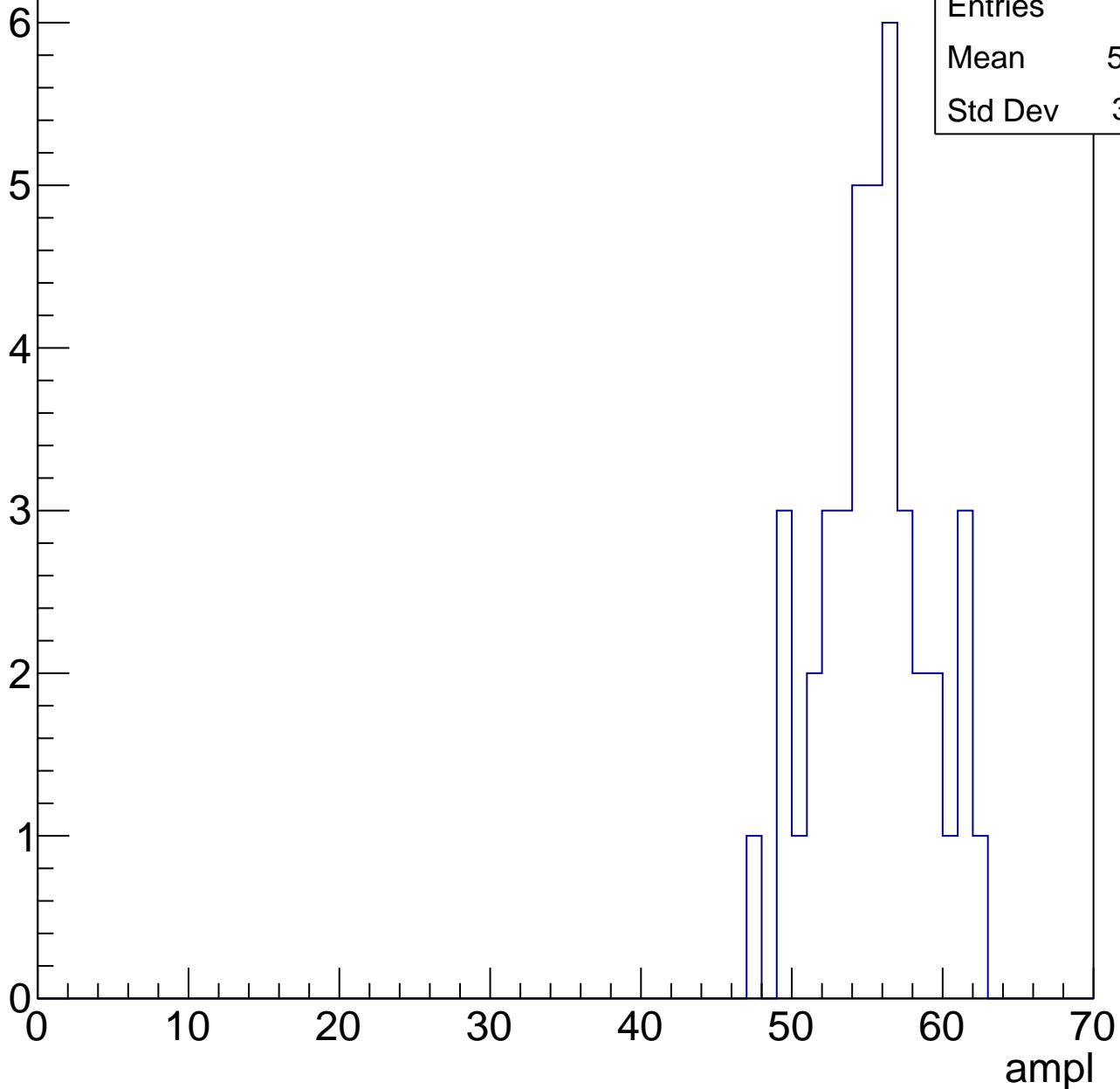


B1L103S, U14-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

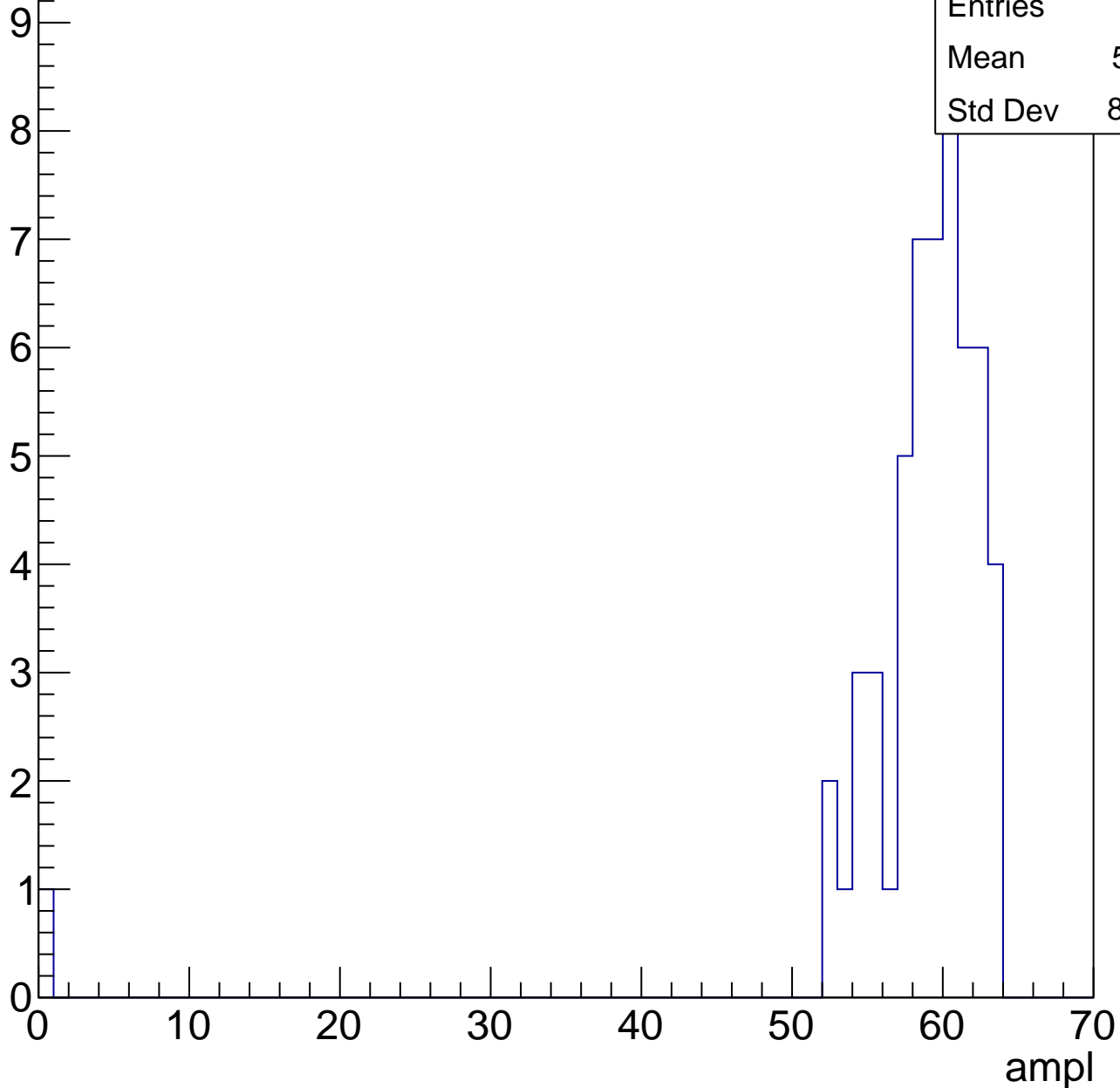
Entries	41
Mean	54.93
Std Dev	3.571



B1L103S, U14-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

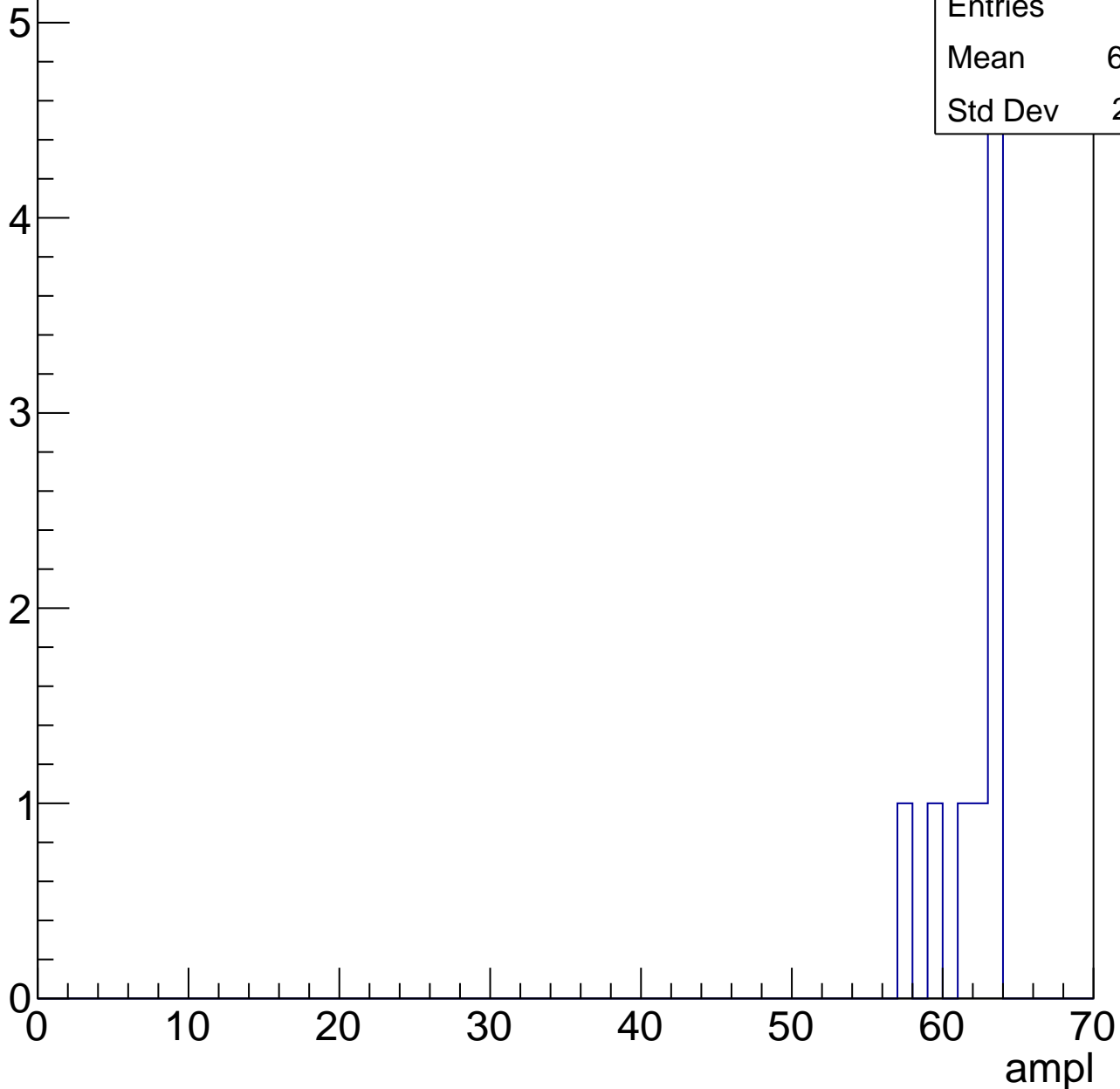


B1L103S, U14-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	61.56
Std Dev	2.061



B1L103S, U14-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

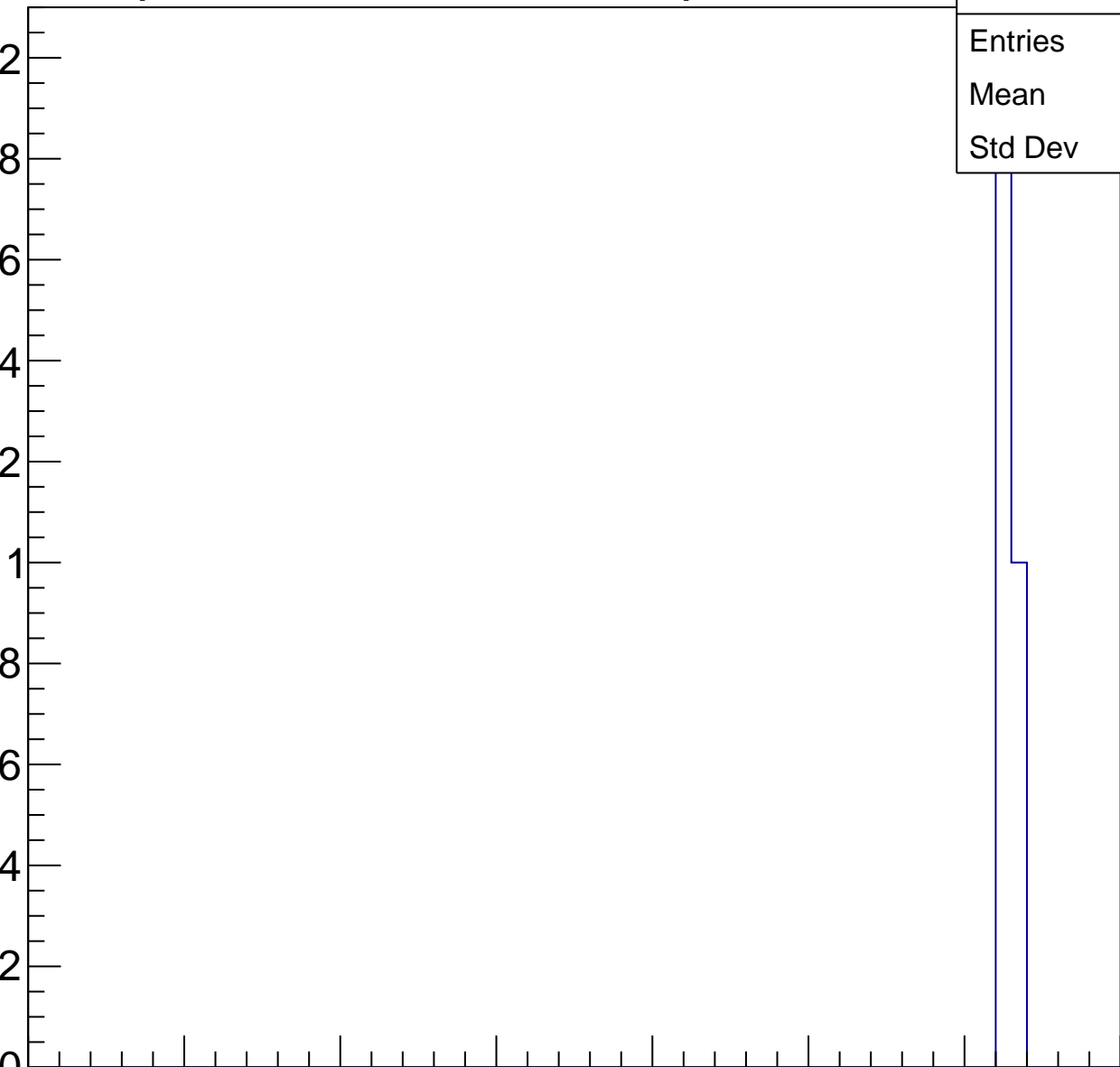
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L103S, U14-ch111, adc0

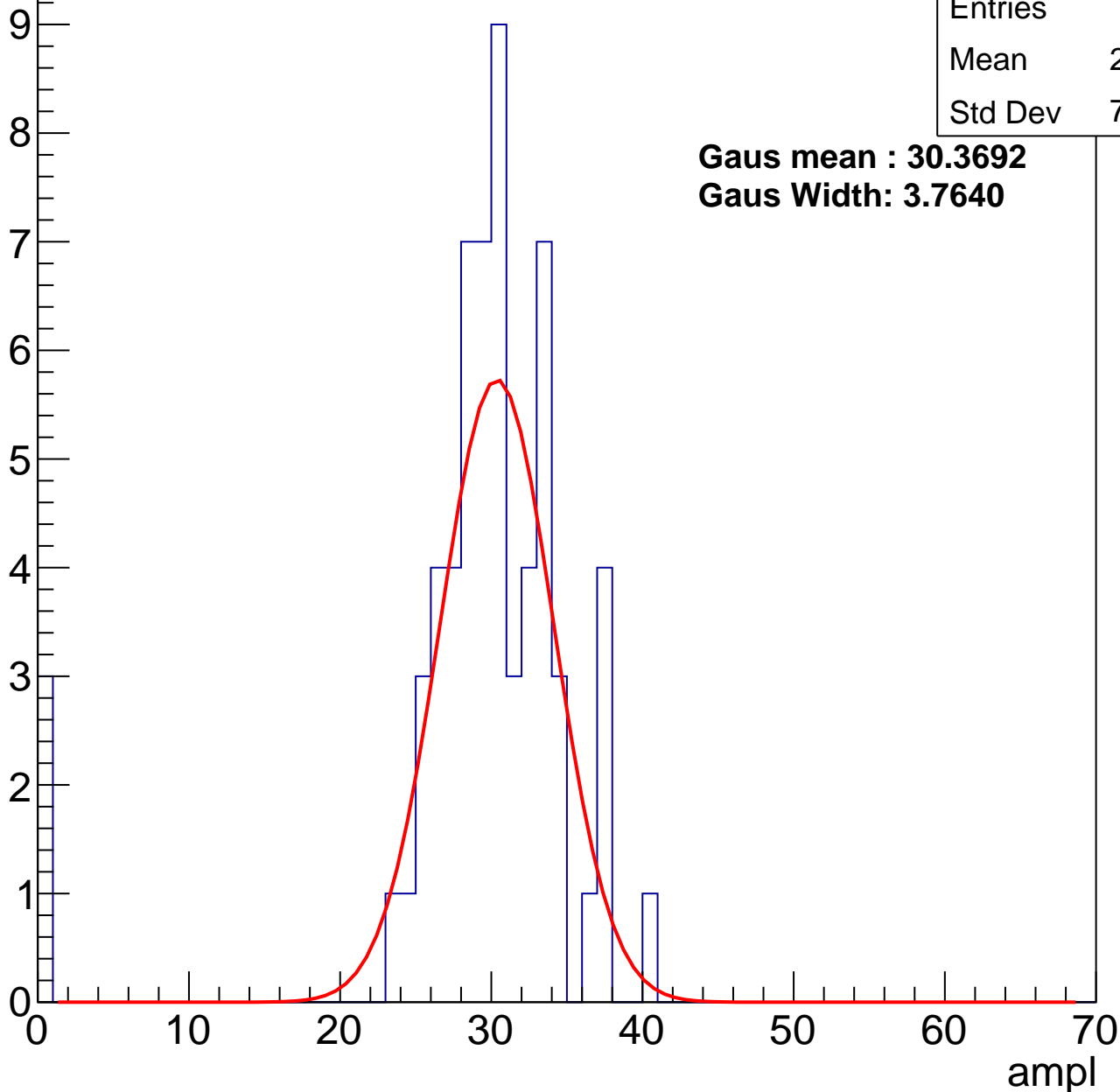
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.73
Std Dev	7.358

Gaus mean : 30.3692

Gaus Width: 3.7640



B1L103S, U14-ch111, adc1

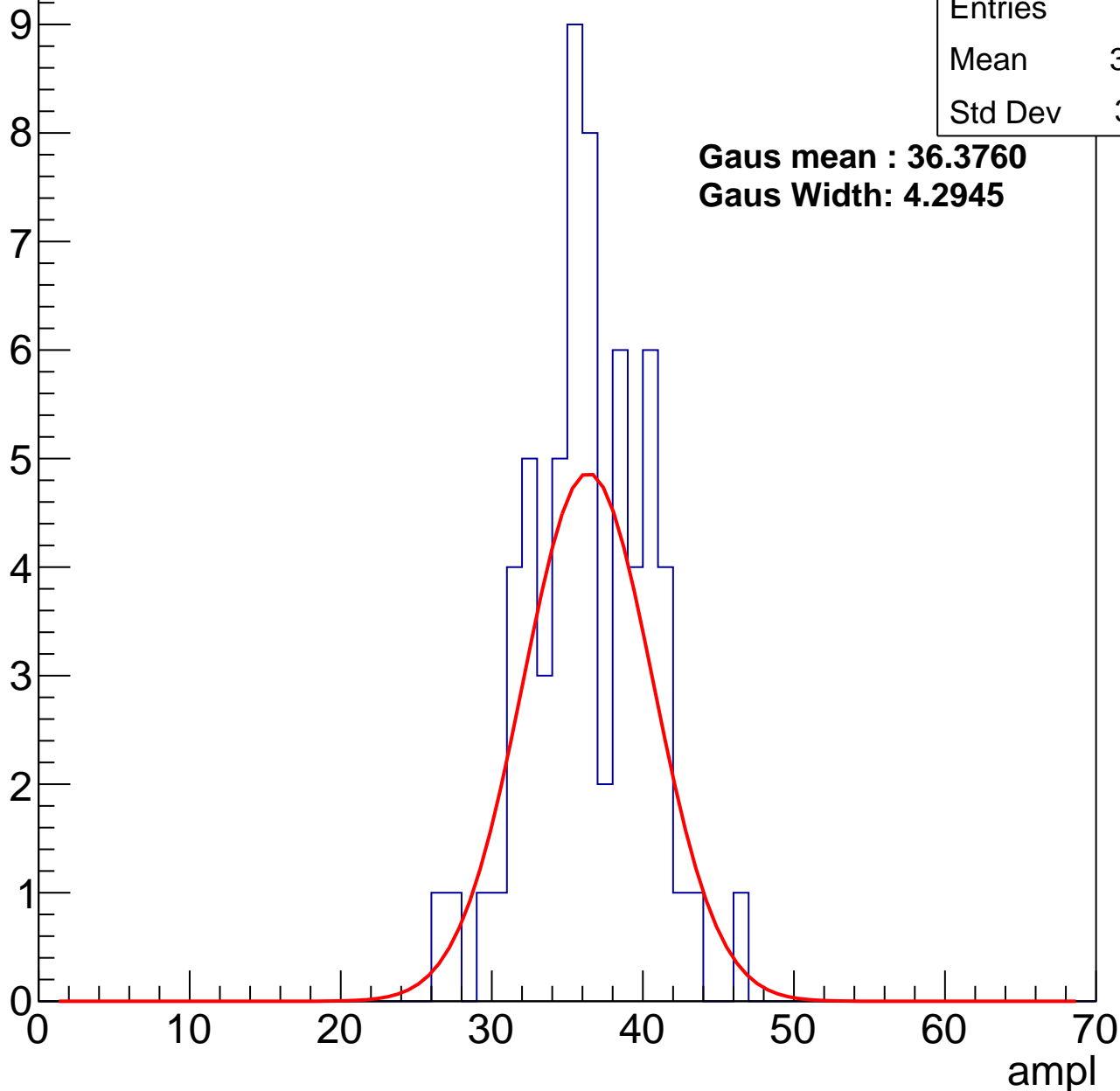
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.89
Std Dev	3.871

Gaus mean : 36.3760

Gaus Width: 4.2945



B1L103S, U14-ch111, adc2

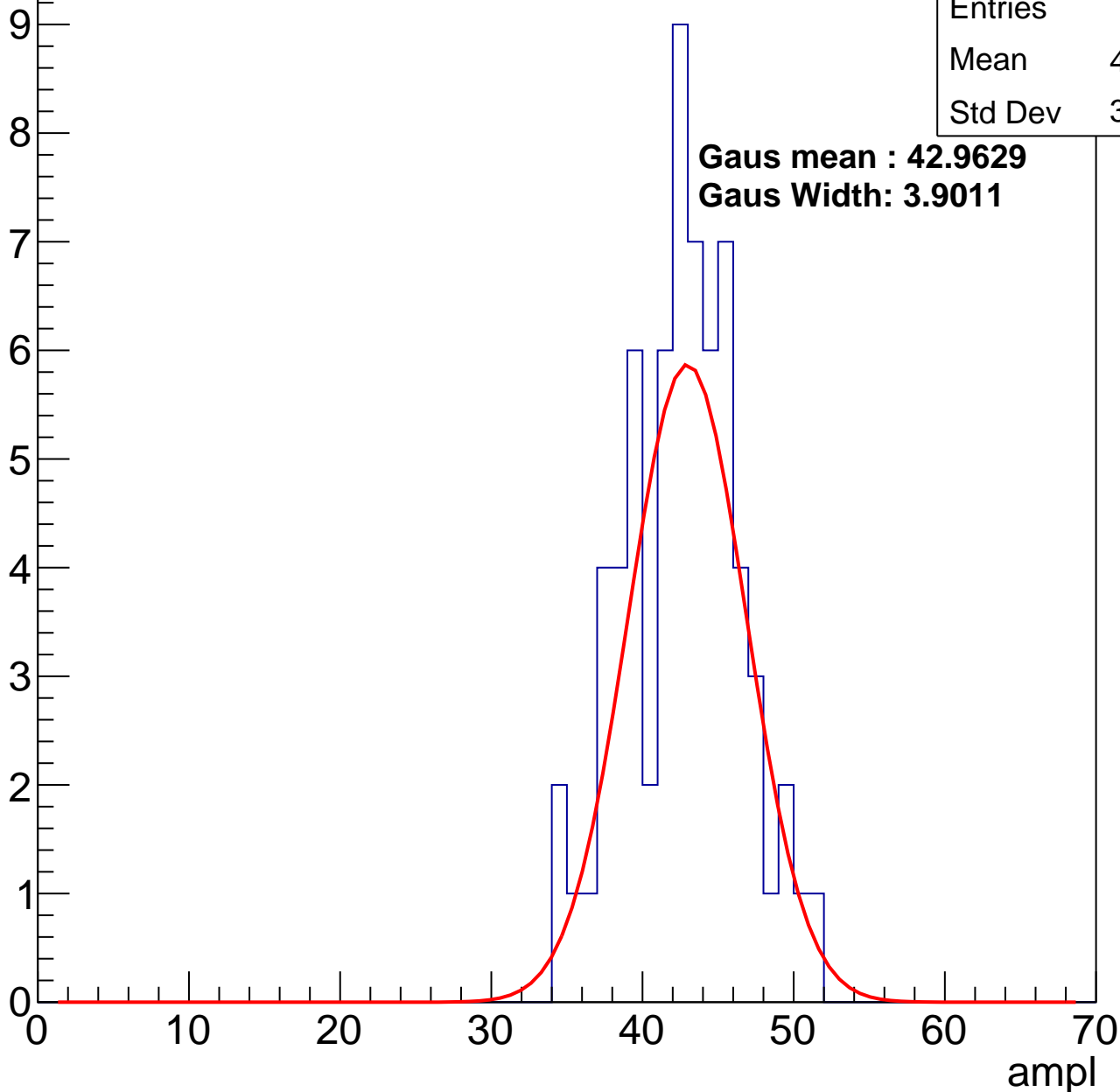
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.22
Std Dev	3.773

Gaus mean : 42.9629

Gaus Width: 3.9011

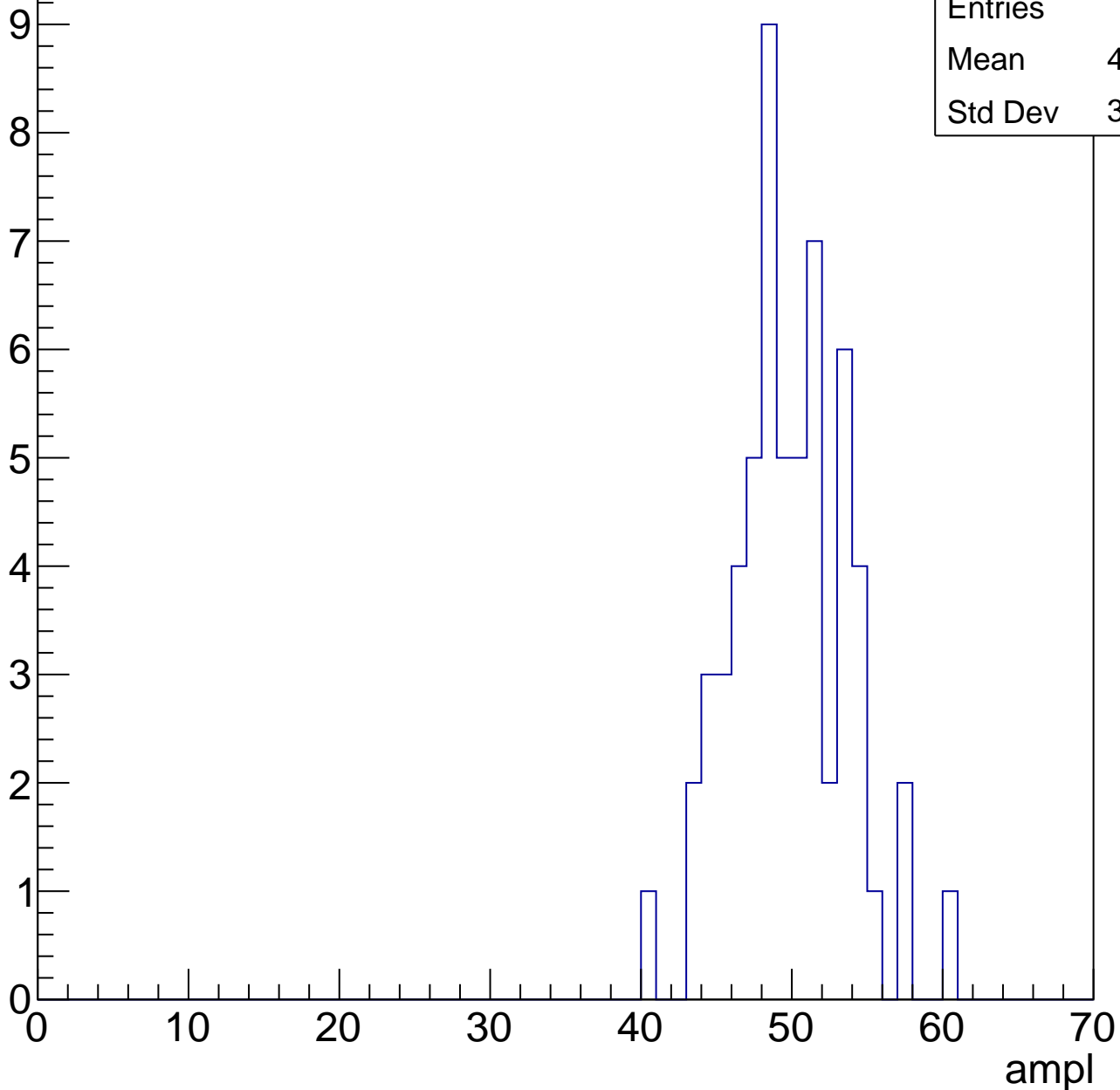


B1L103S, U14-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.38
Std Dev	3.795

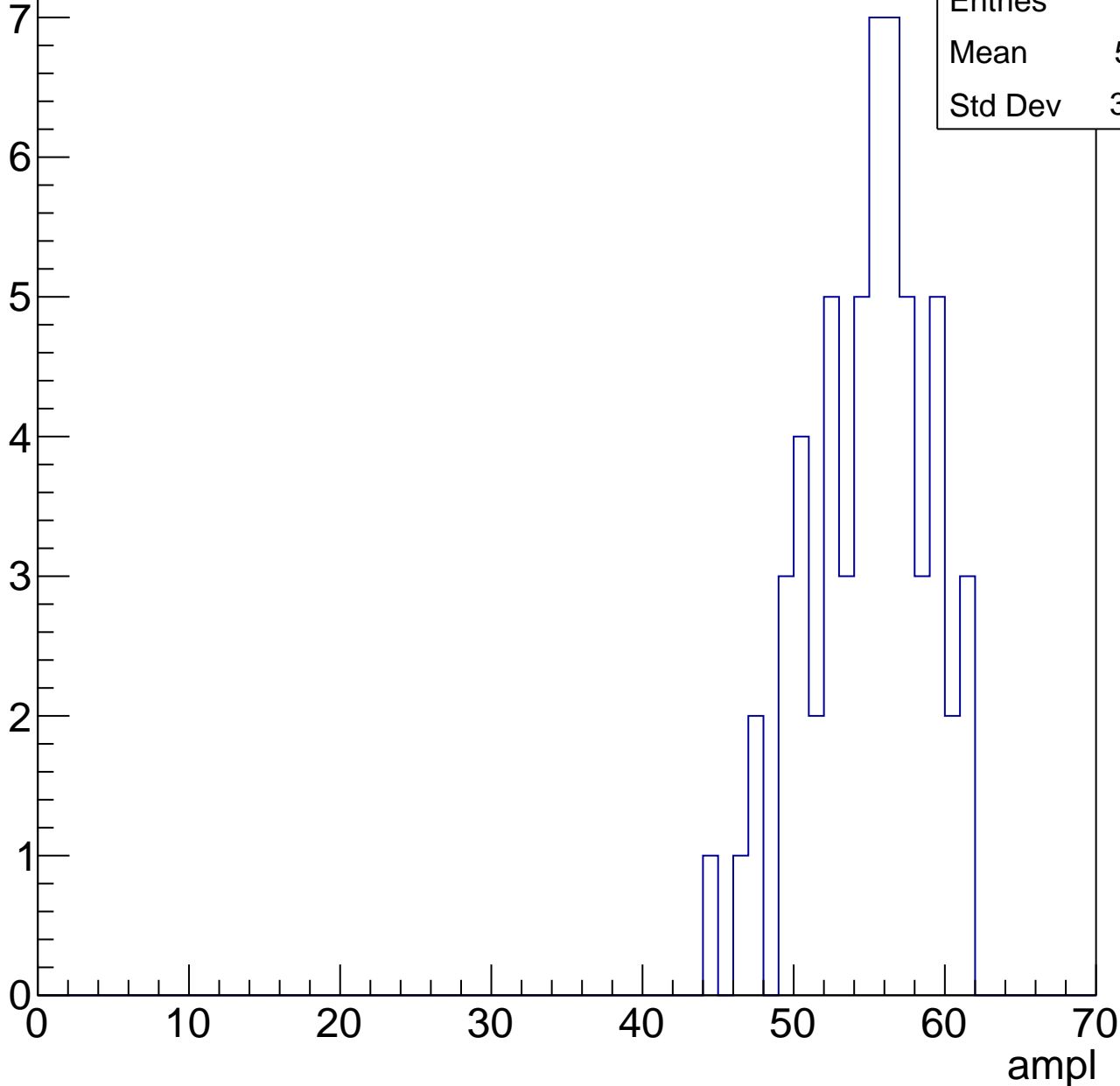


B1L103S, U14-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.41
Std Dev	3.948

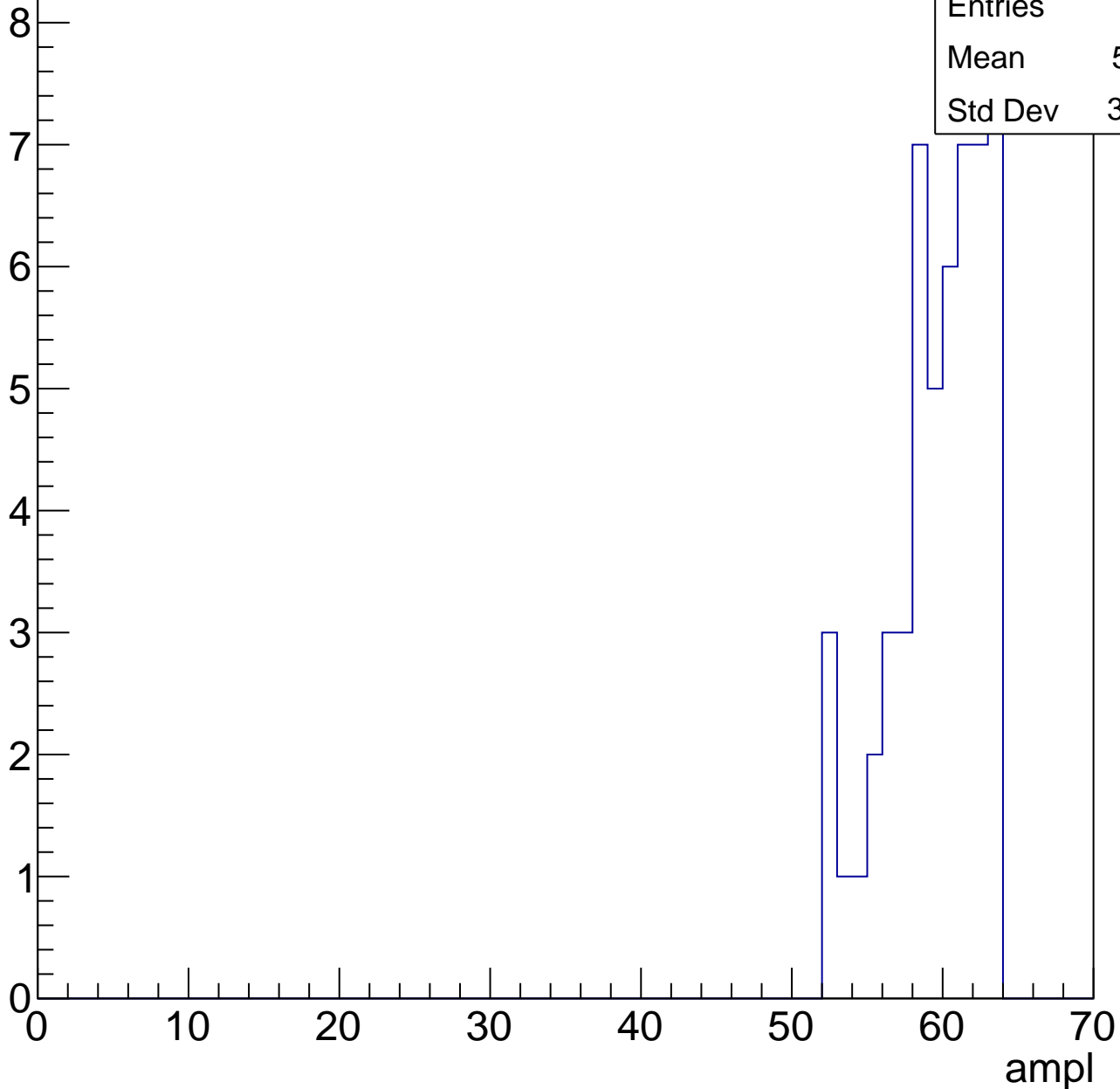


B1L103S, U14-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	59.21
Std Dev	3.098



B1L103S, U14-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

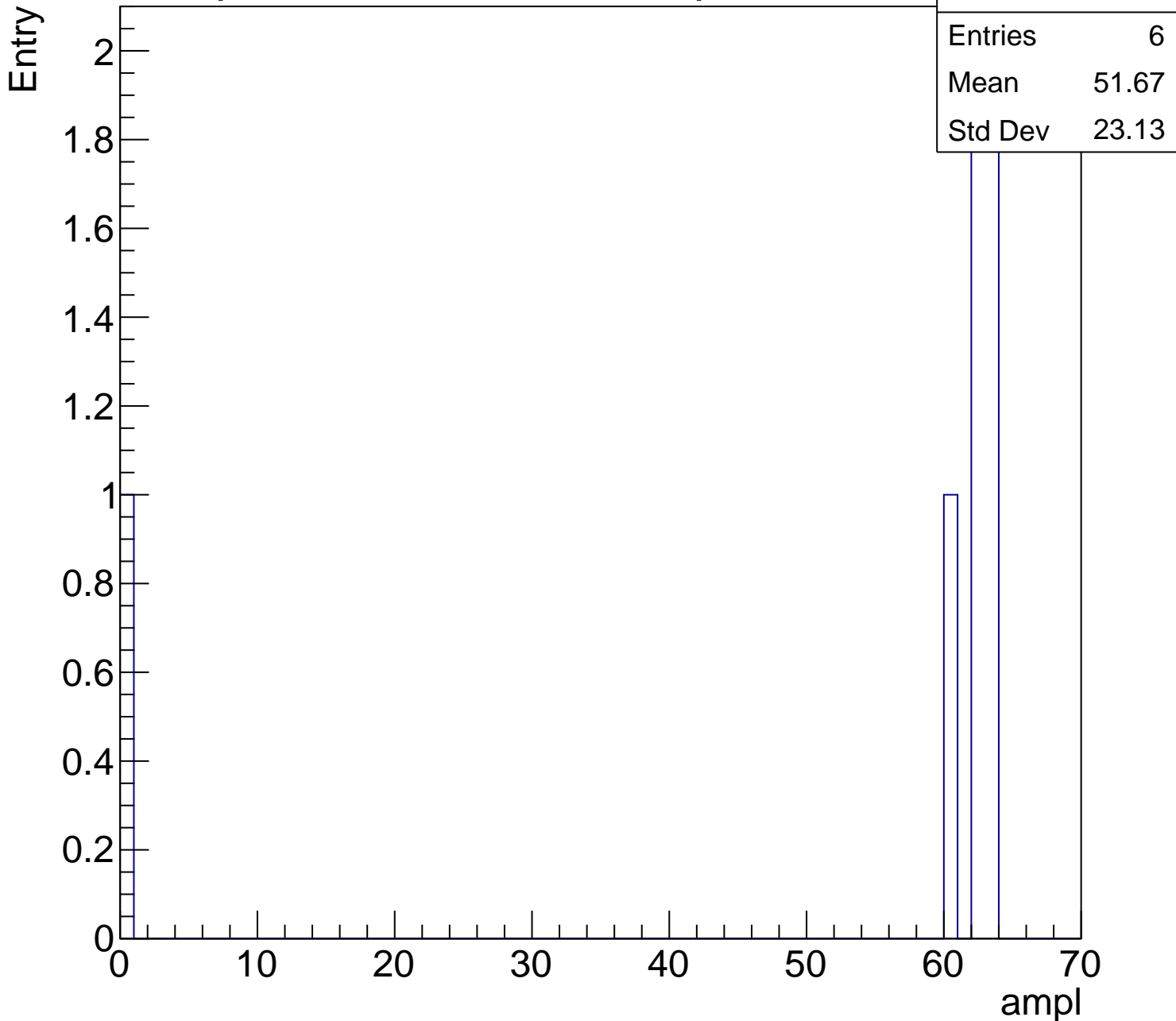
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.67
Std Dev	23.13

0 10 20 30 40 50 60 70

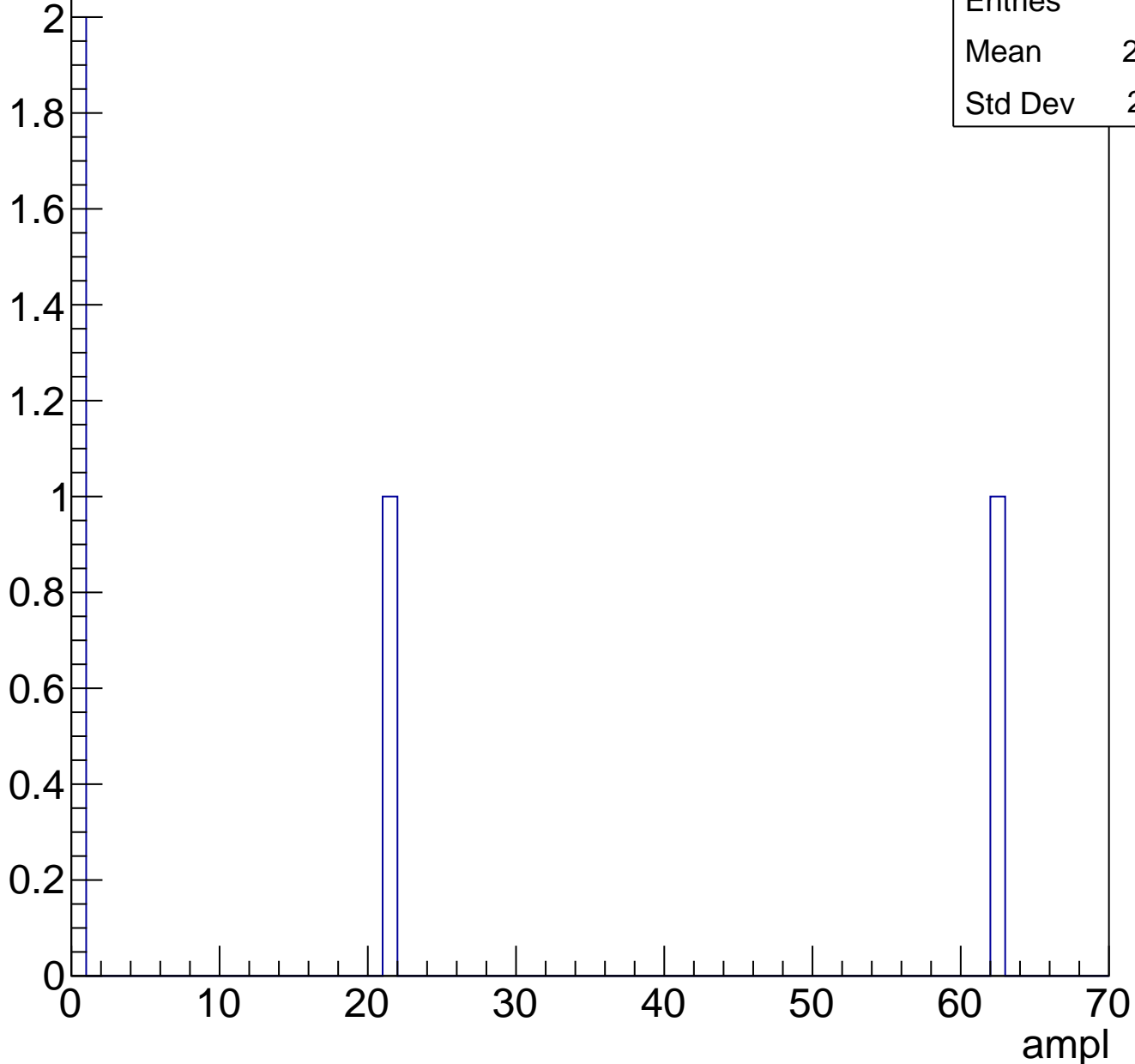
ampl



B1L103S, U14-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	20.75
Std Dev	25.31

B1L103S, U14-ch112, adc0

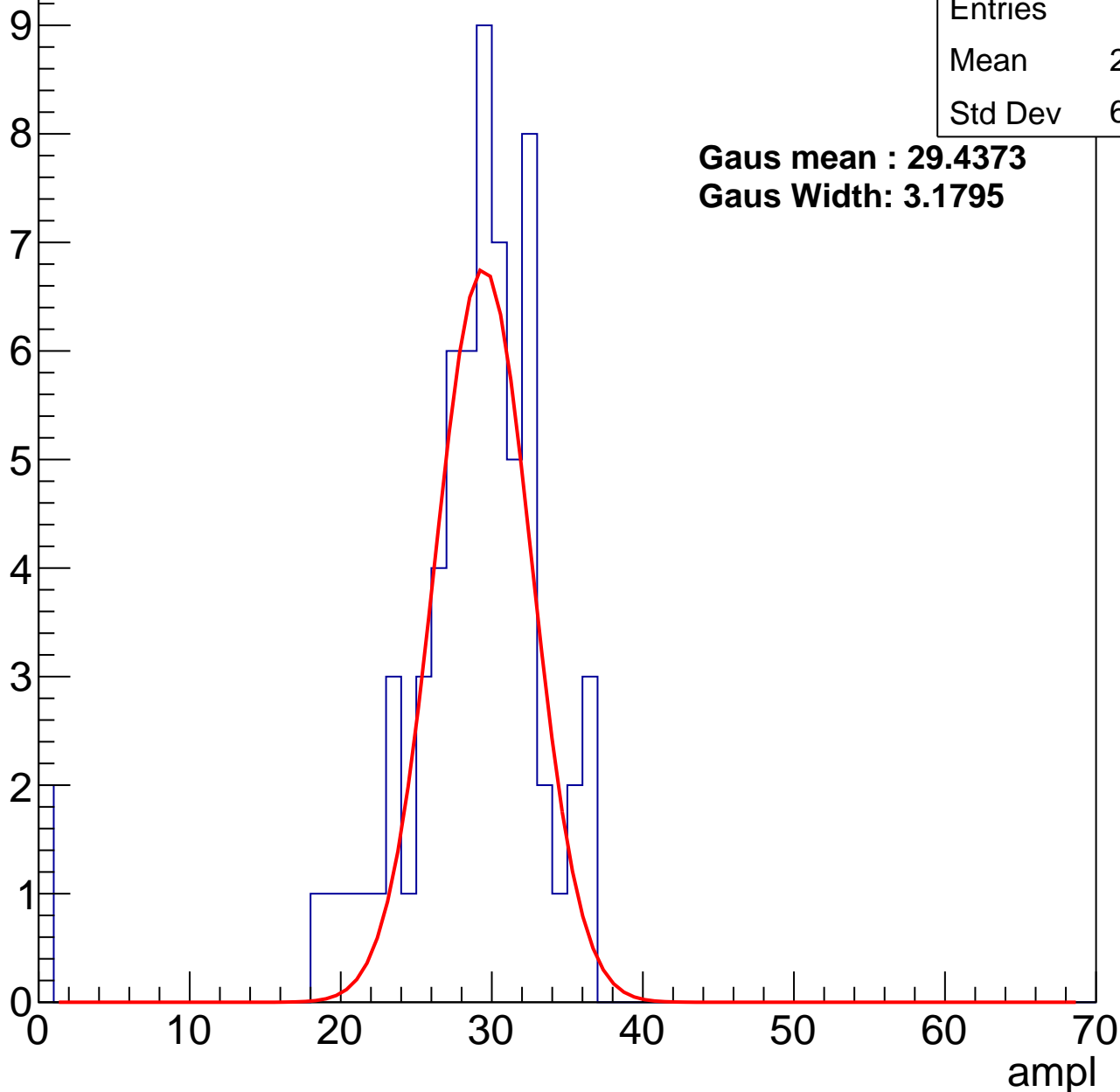
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	27.79
Std Dev	6.255

Gaus mean : 29.4373

Gaus Width: 3.1795



B1L103S, U14-ch112, adc1

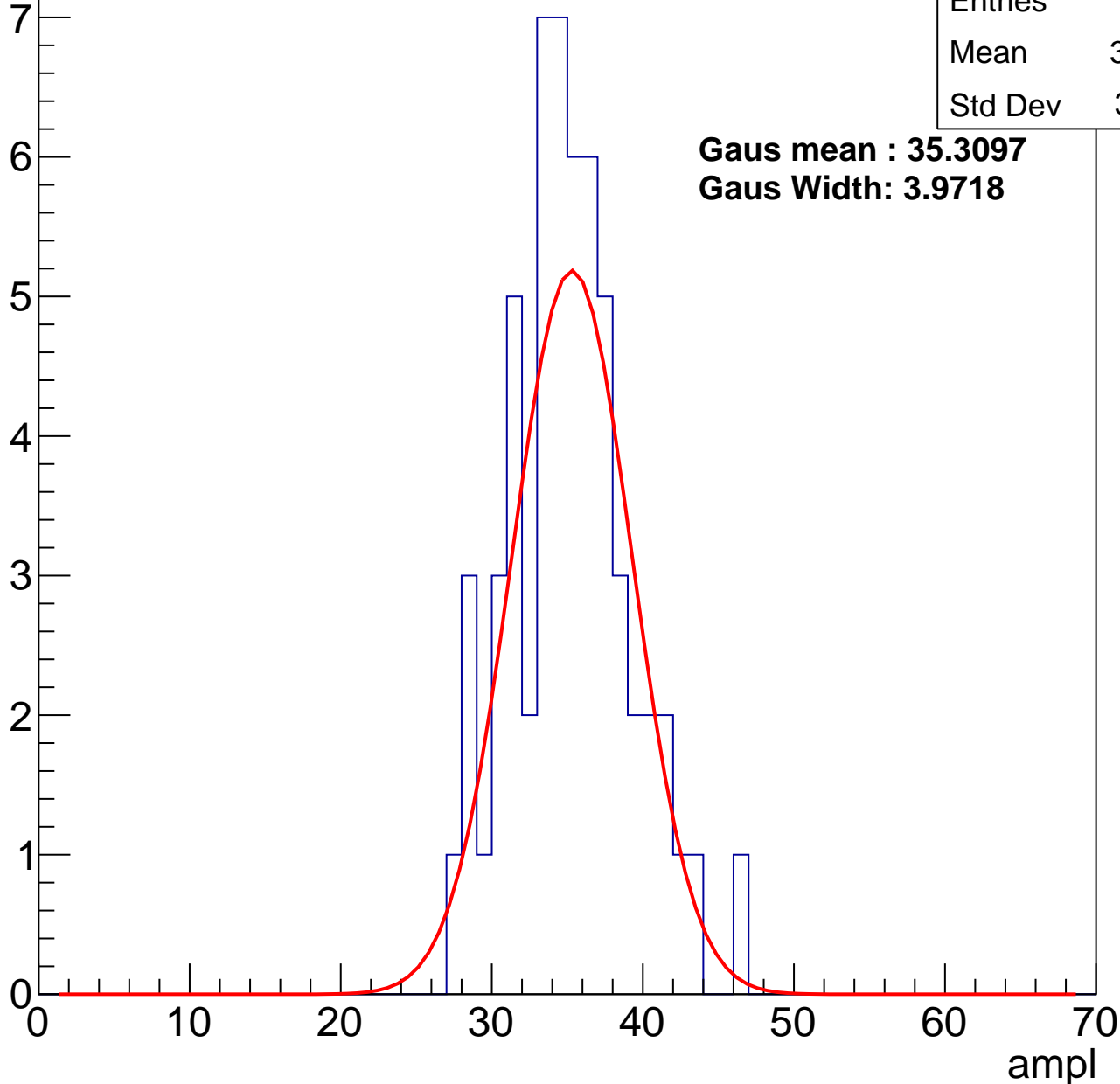
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	34.72
Std Dev	3.921

Gaus mean : 35.3097

Gaus Width: 3.9718



B1L103S, U14-ch112, adc2

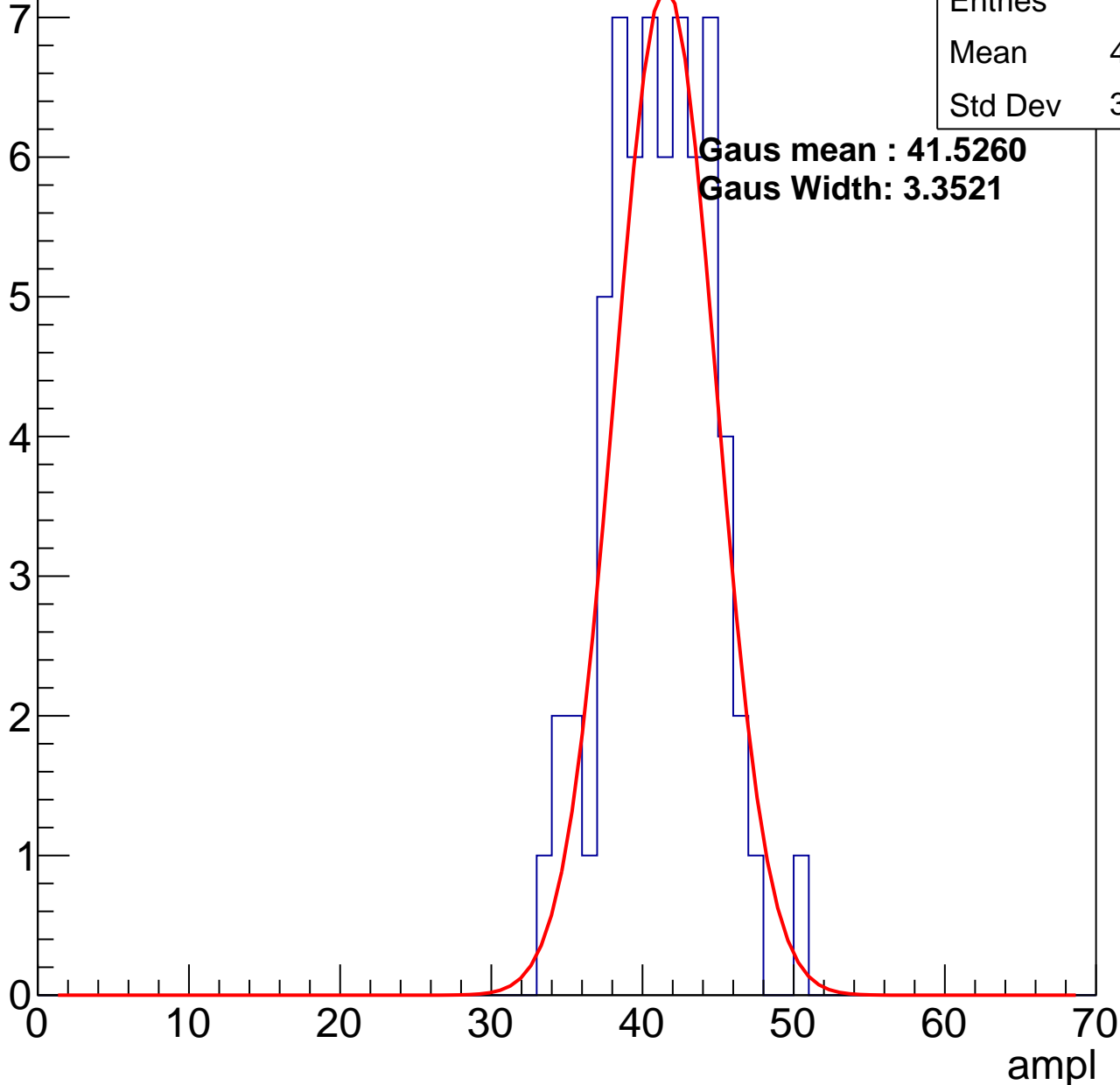
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	40.72
Std Dev	3.404

Gaus mean : 41.5260

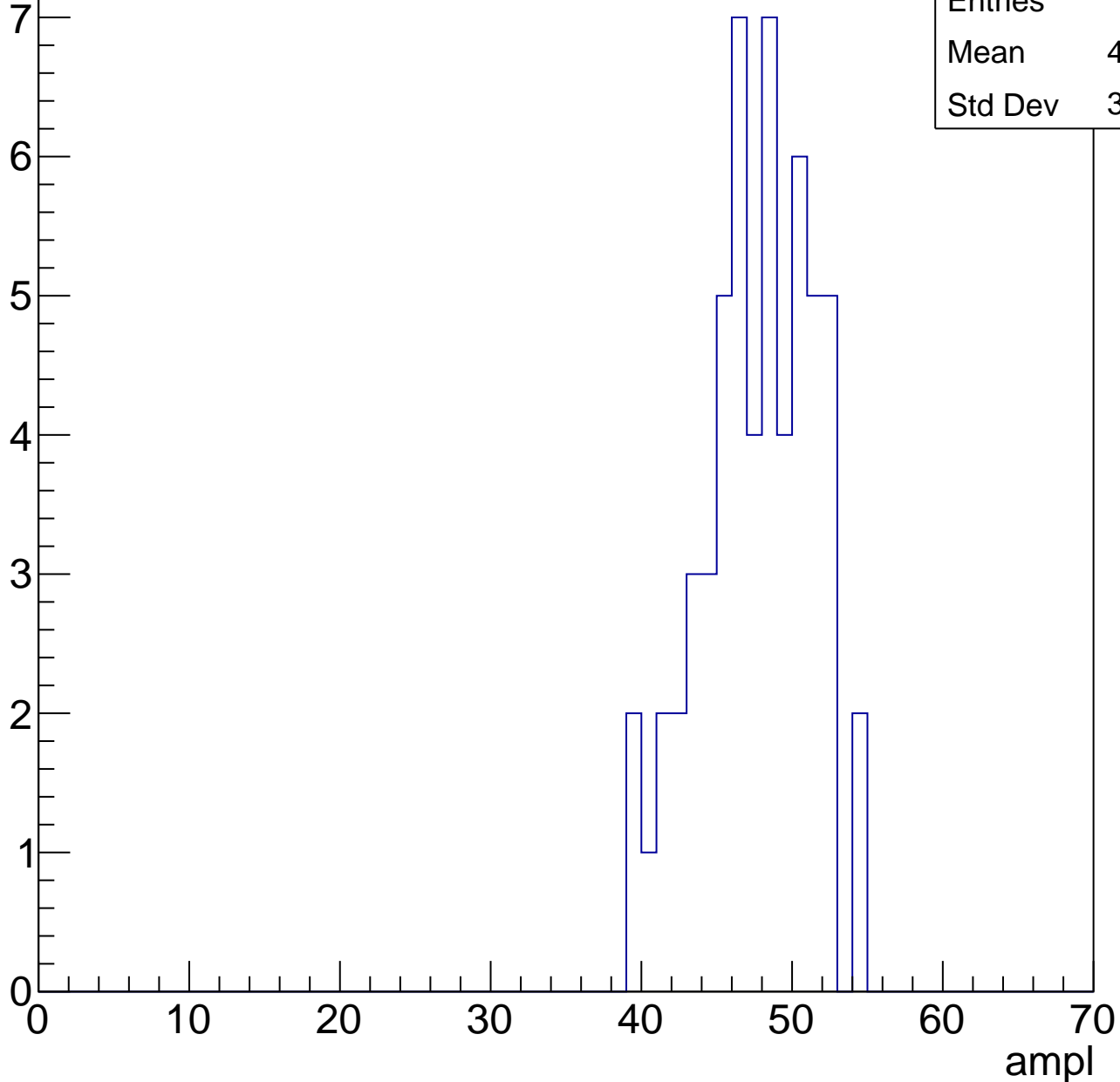
Gaus Width: 3.3521



B1L103S, U14-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

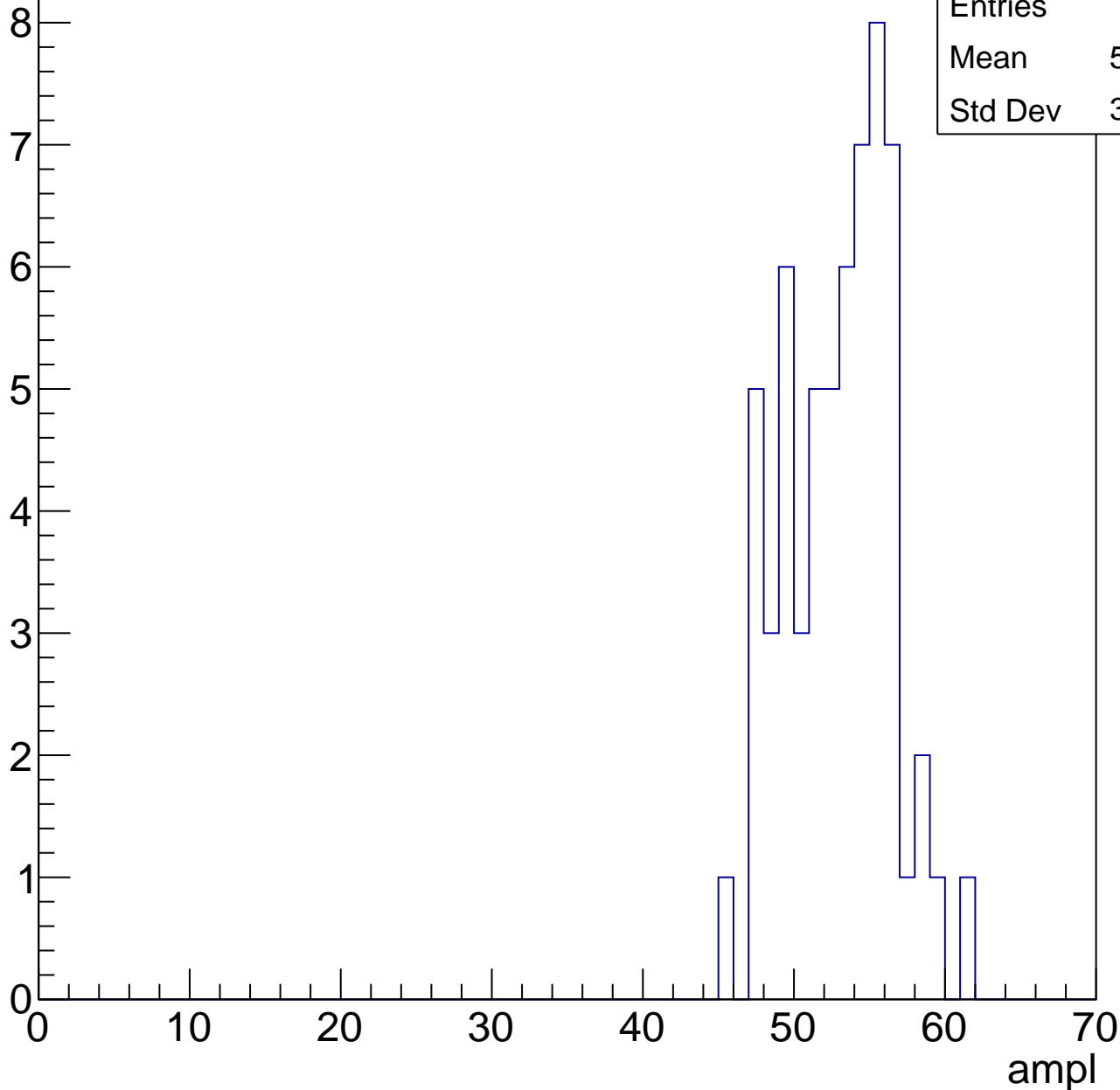


B1L103S, U14-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

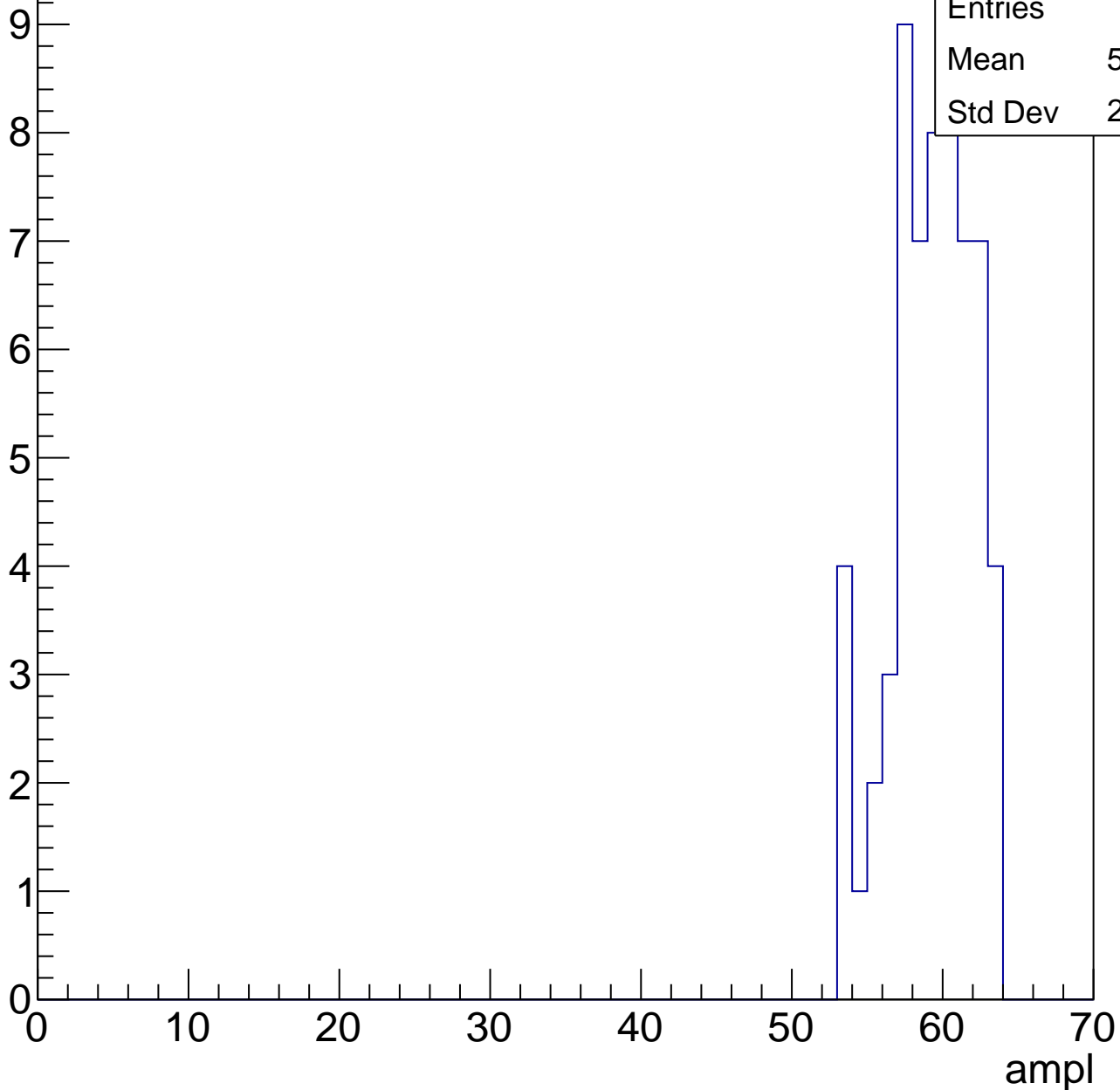
Entries	61
Mean	52.52
Std Dev	3.438



B1L103S, U14-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



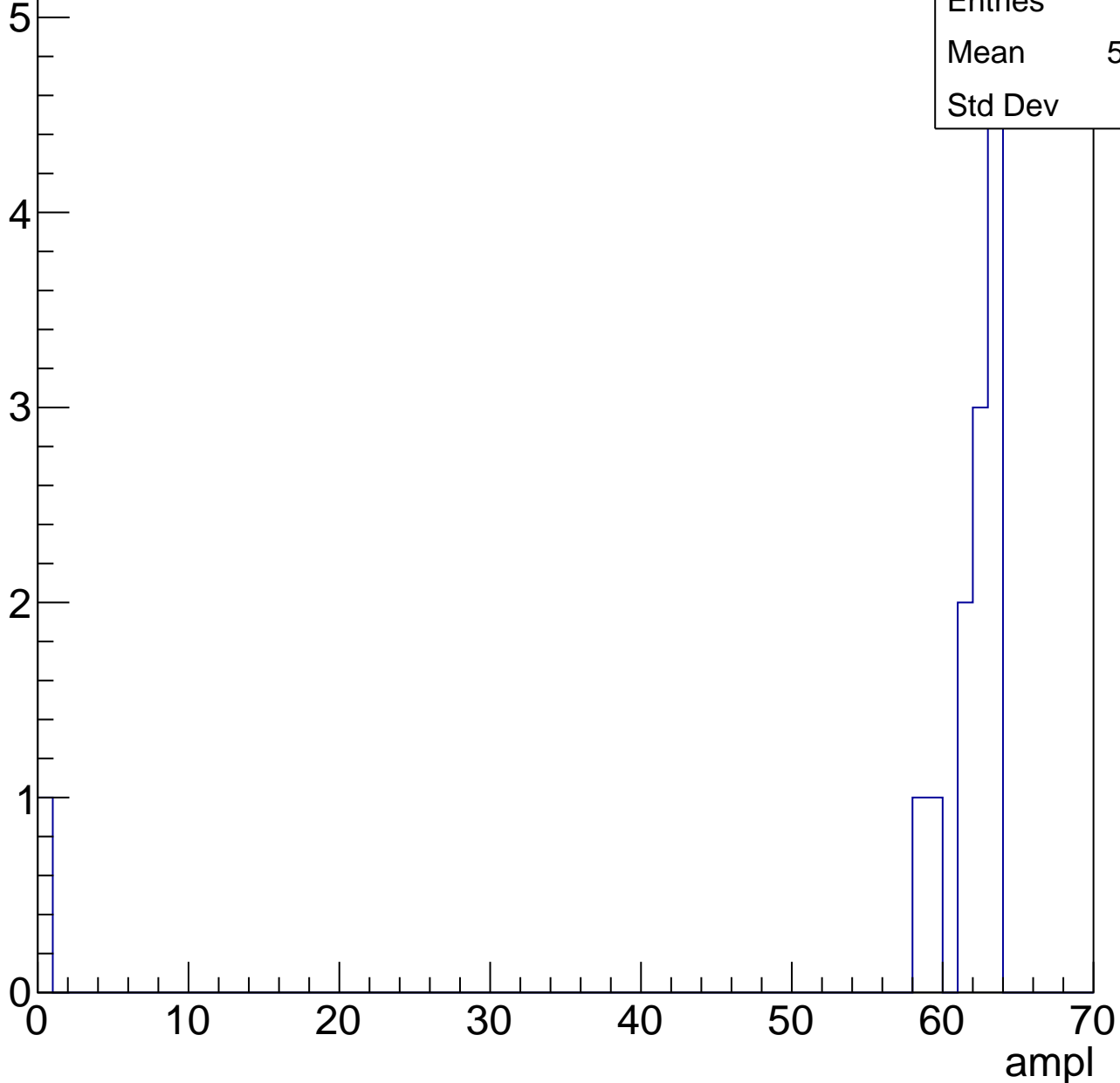
Entries	61
Mean	58.82
Std Dev	2.664

B1L103S, U14-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

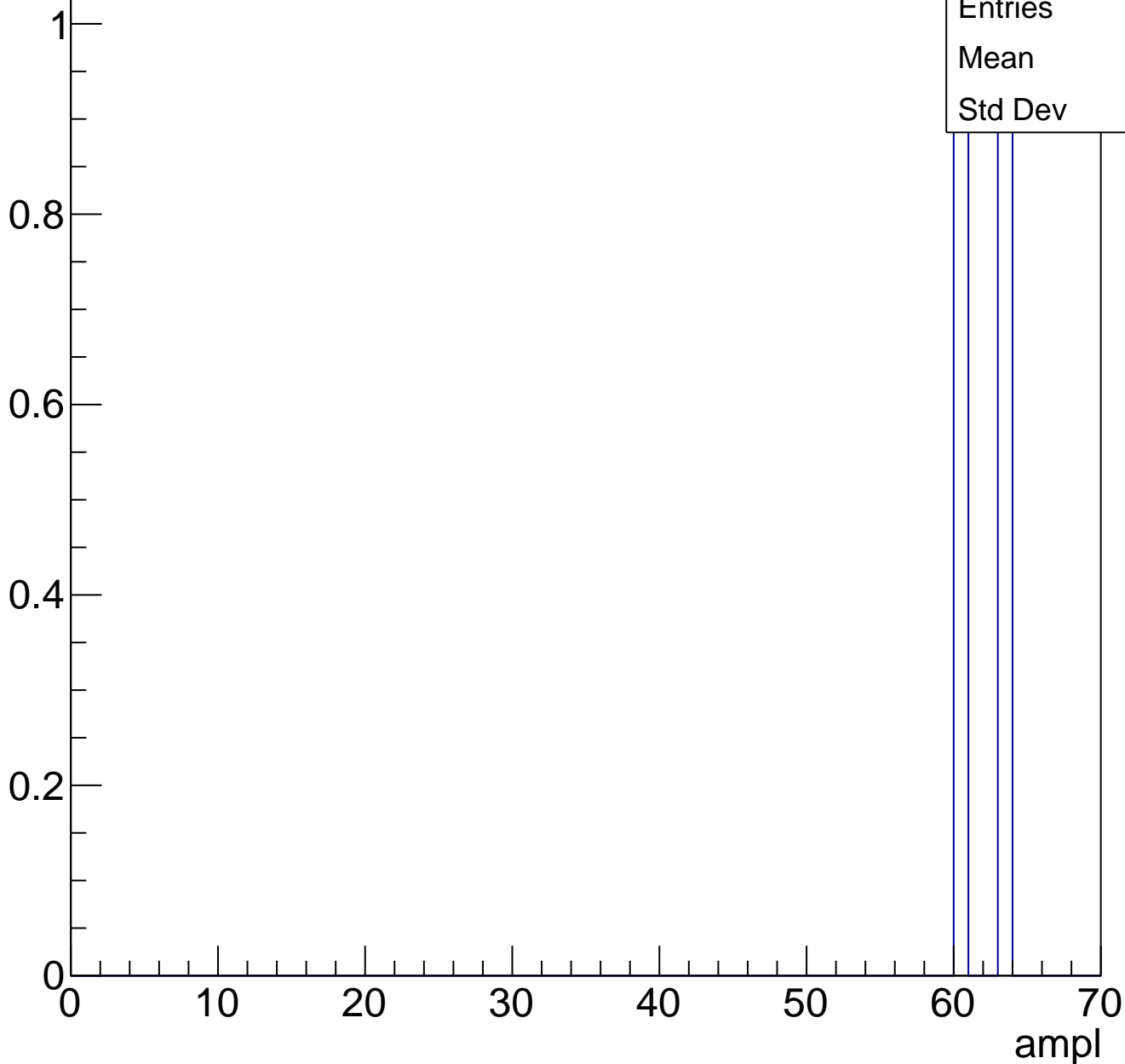
Entries	13
Mean	56.92
Std Dev	16.5



B1L103S, U14-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch113, adc0

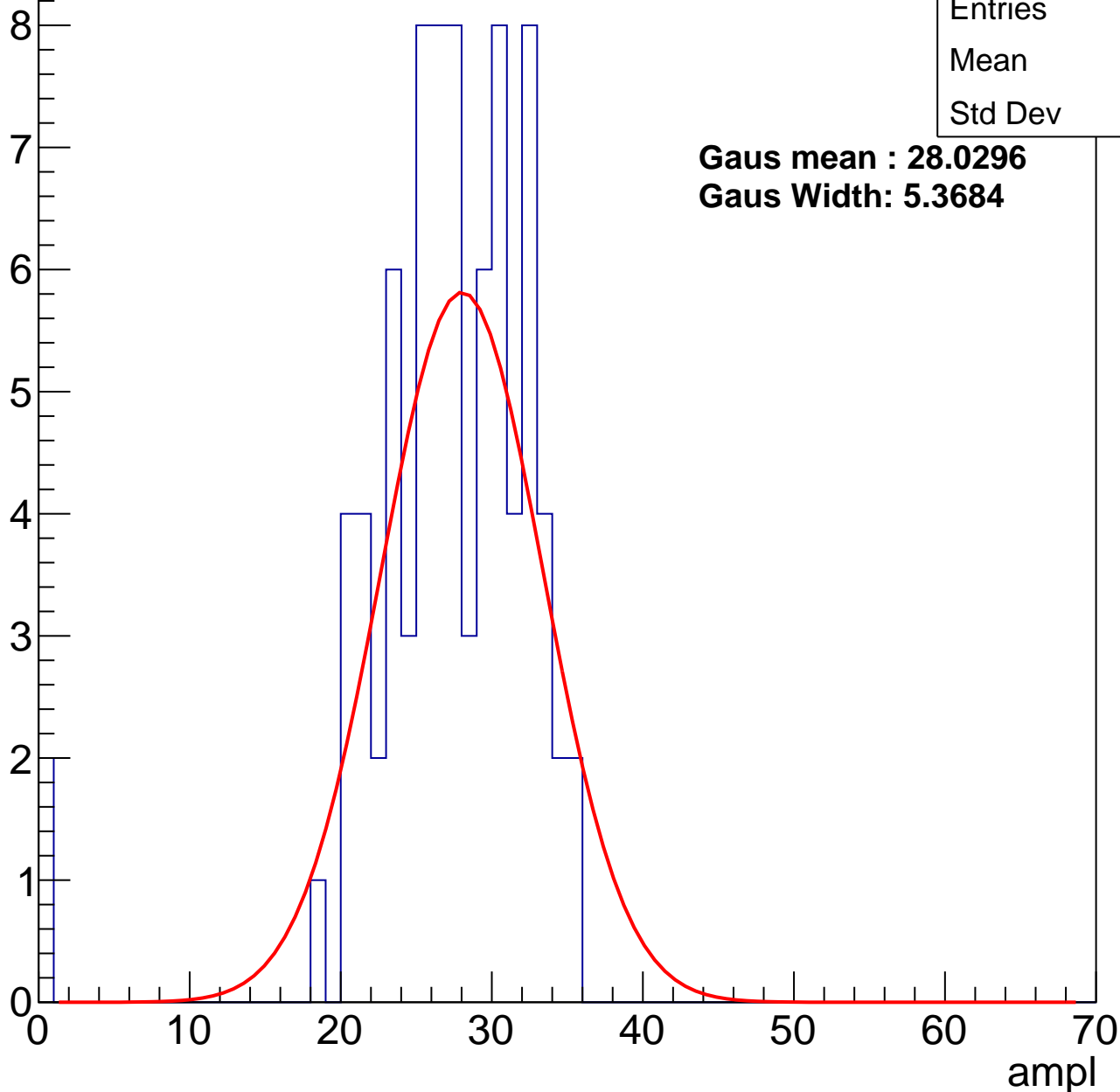
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	26.6
Std Dev	5.82

Gaus mean : 28.0296

Gaus Width: 5.3684



B1L103S, U14-ch113, adc1

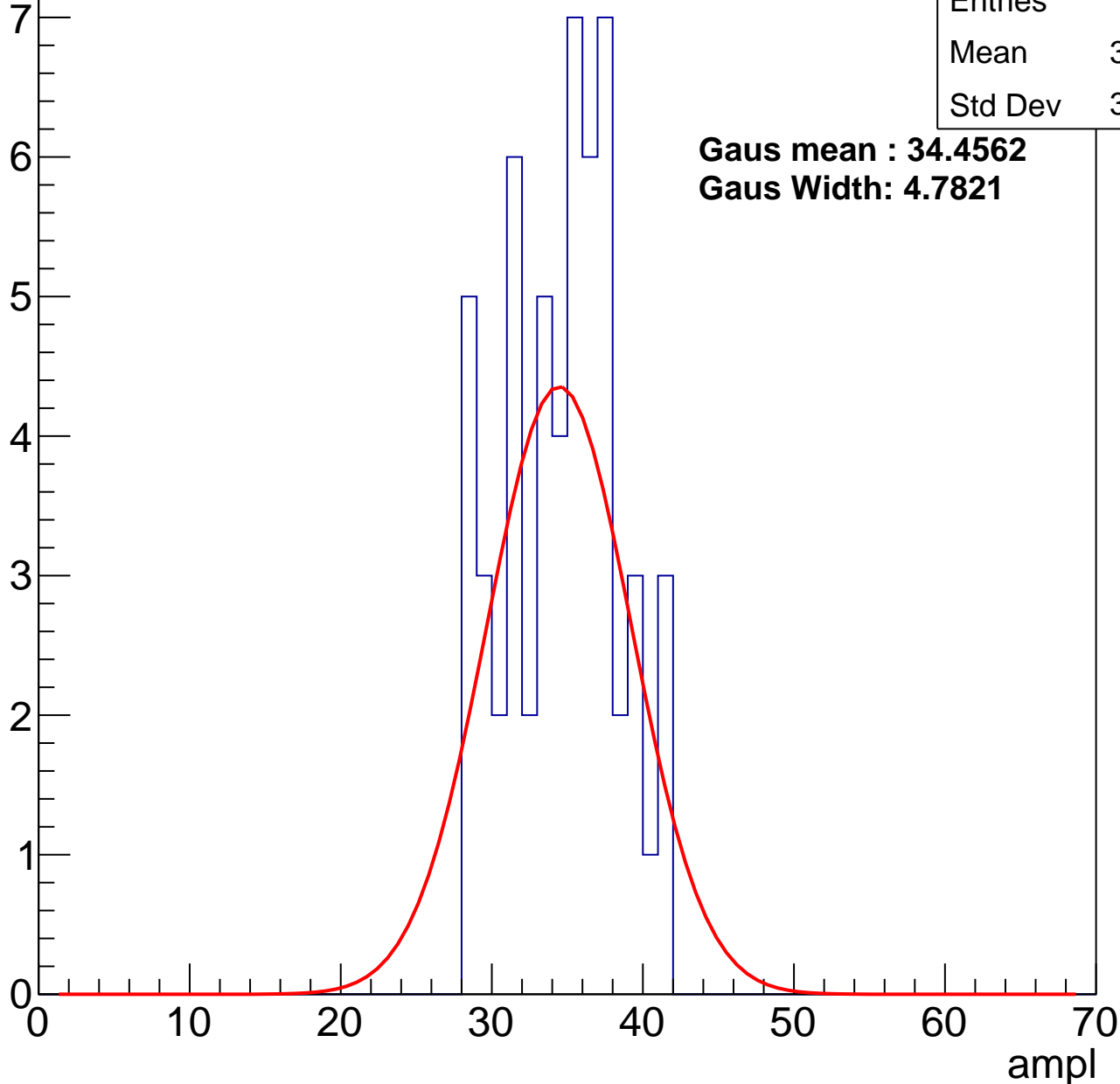
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	34.18
Std Dev	3.626

Gaus mean : 34.4562

Gaus Width: 4.7821



B1L103S, U14-ch113, adc2

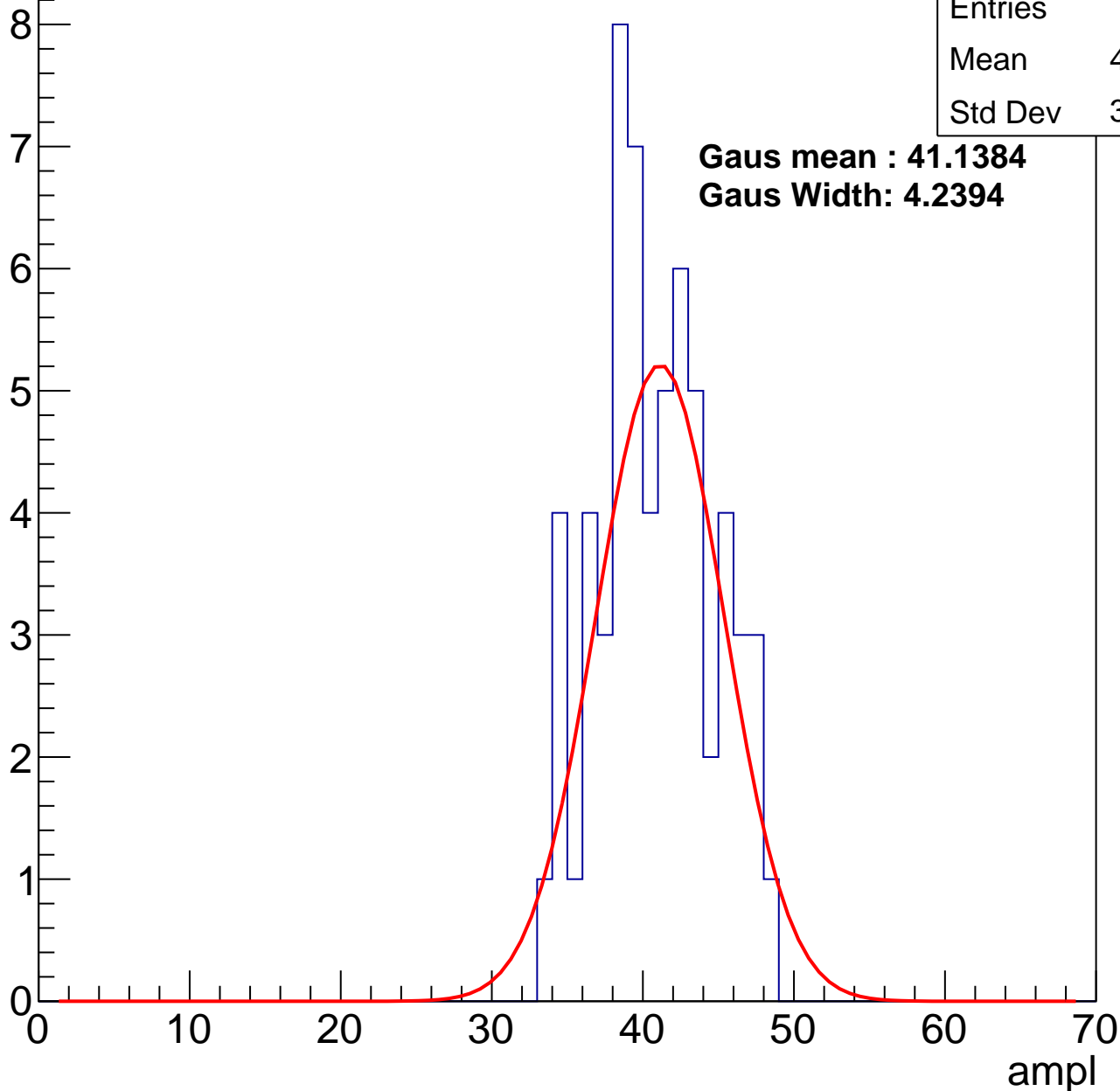
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	40.38
Std Dev	3.764

Gaus mean : 41.1384

Gaus Width: 4.2394

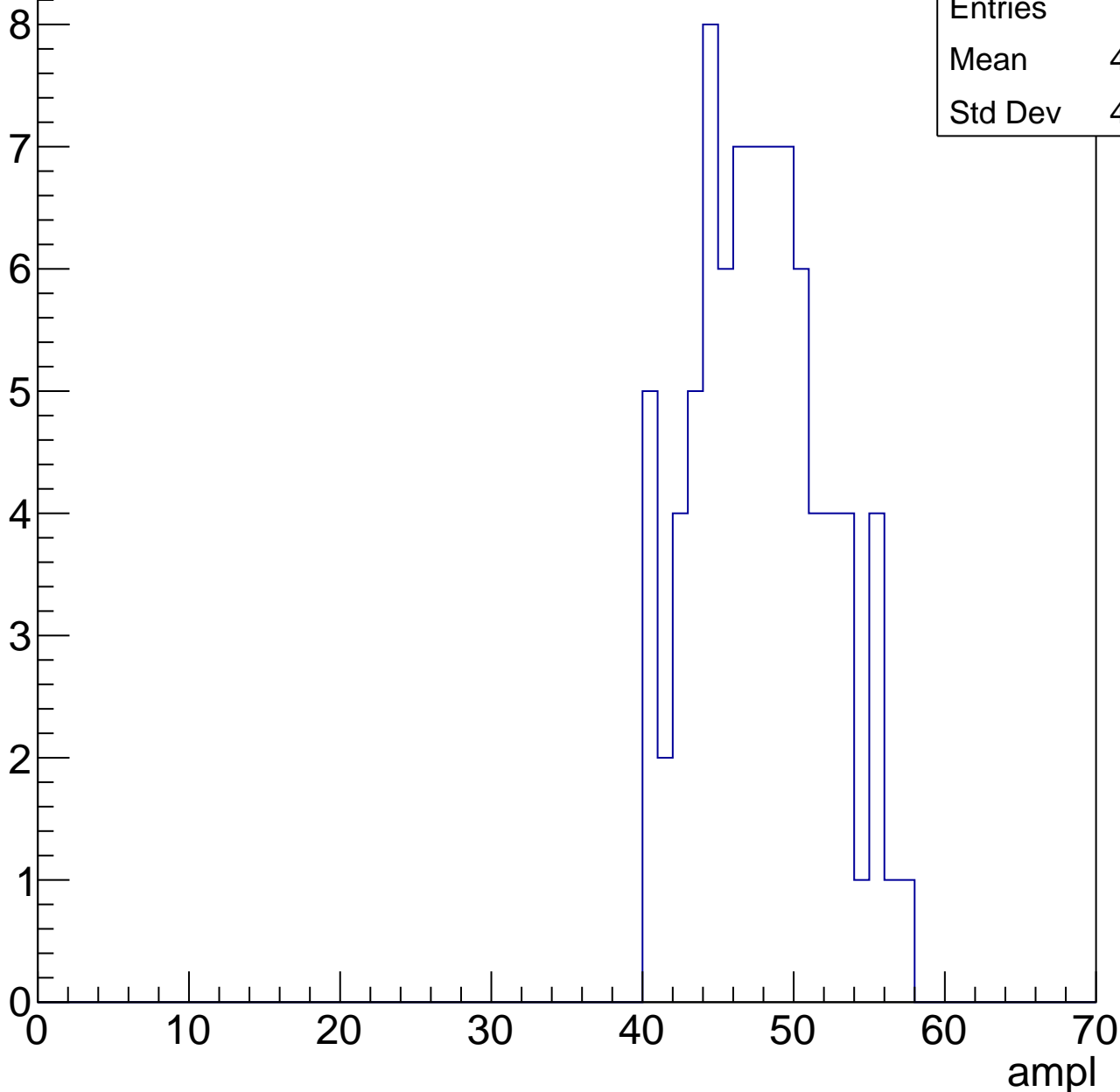


B1L103S, U14-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	47.33
Std Dev	4.214

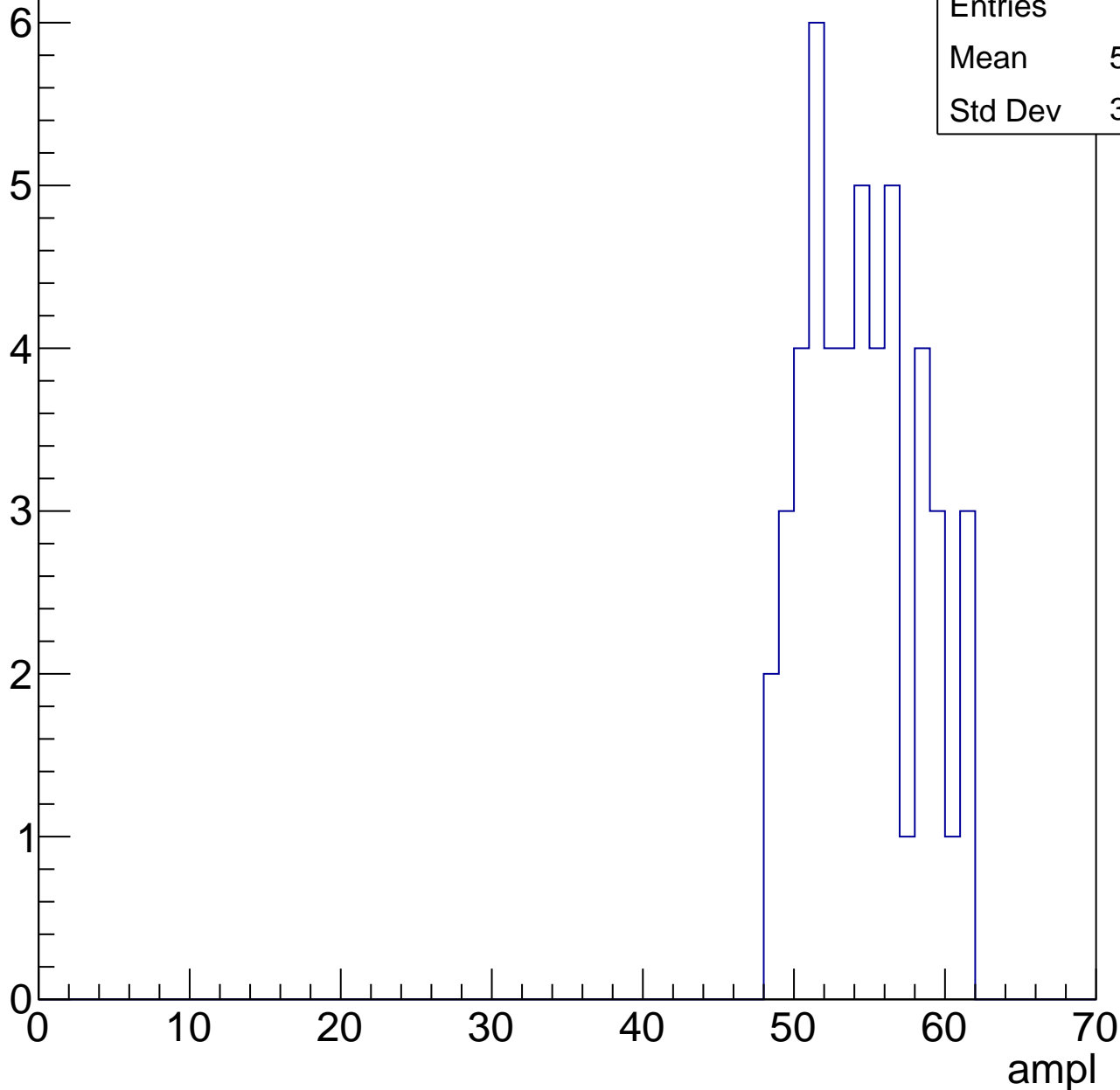


B1L103S, U14-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	54.04
Std Dev	3.614

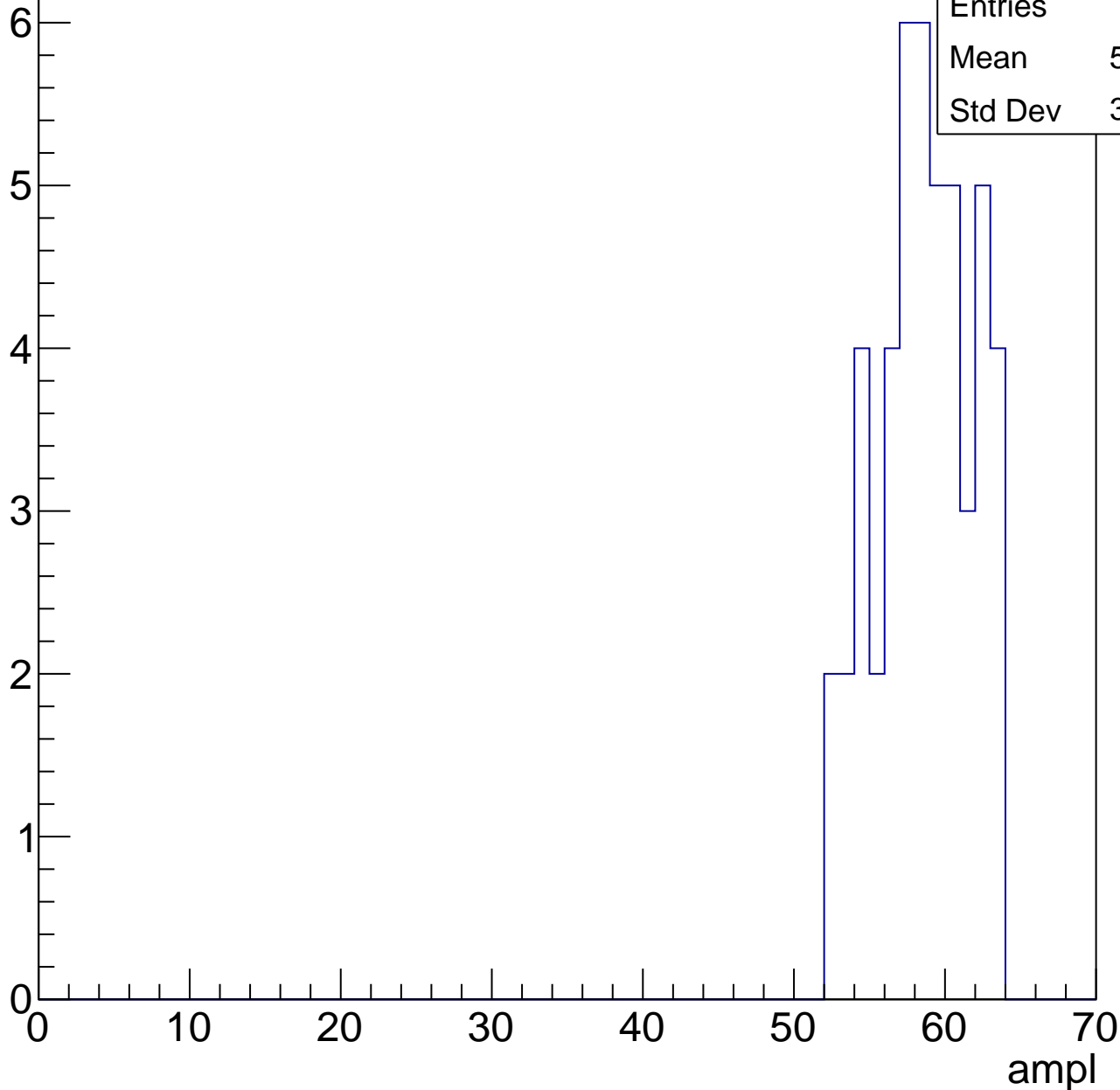


B1L103S, U14-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.12
Std Dev	3.086

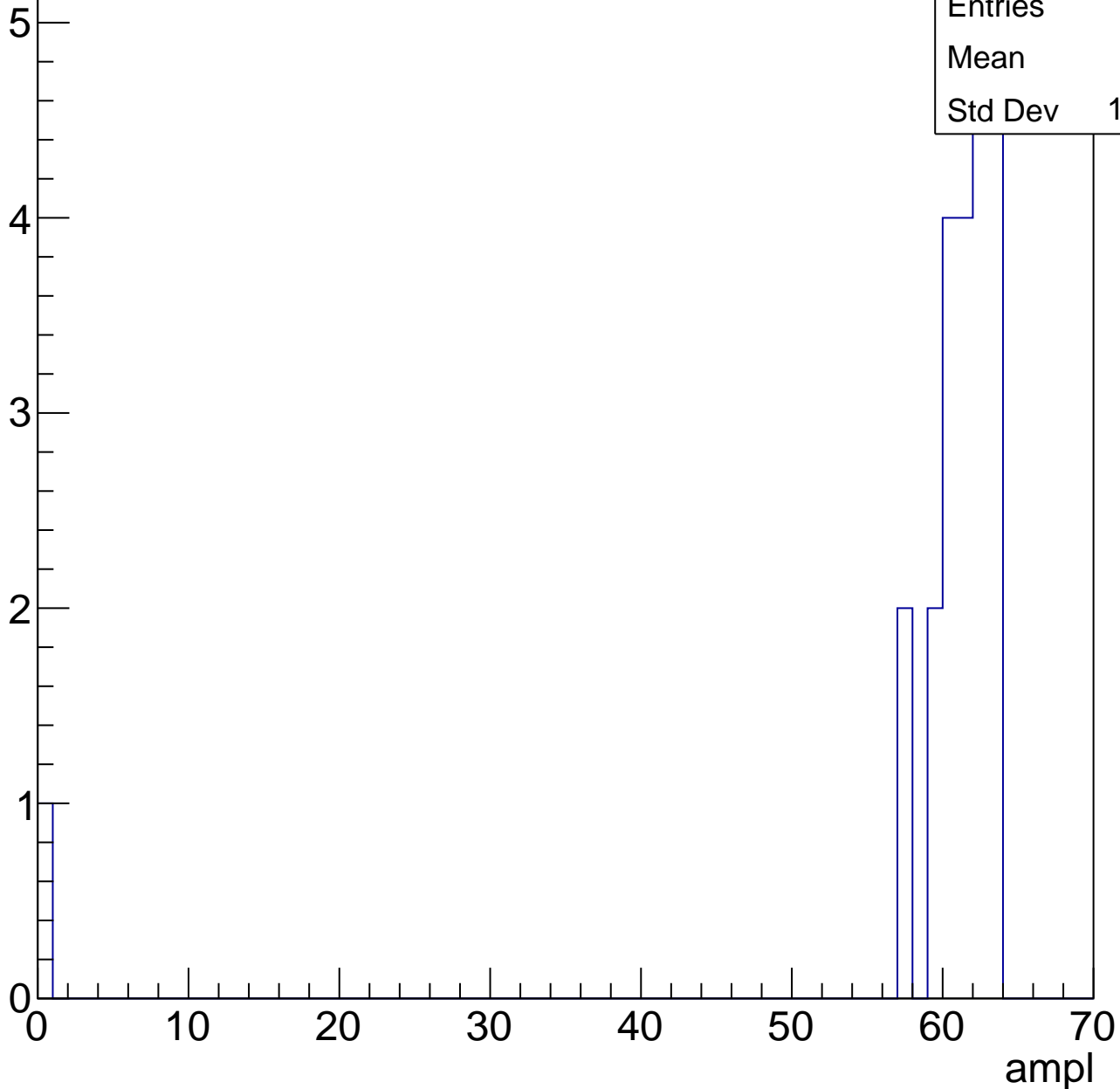


B1L103S, U14-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

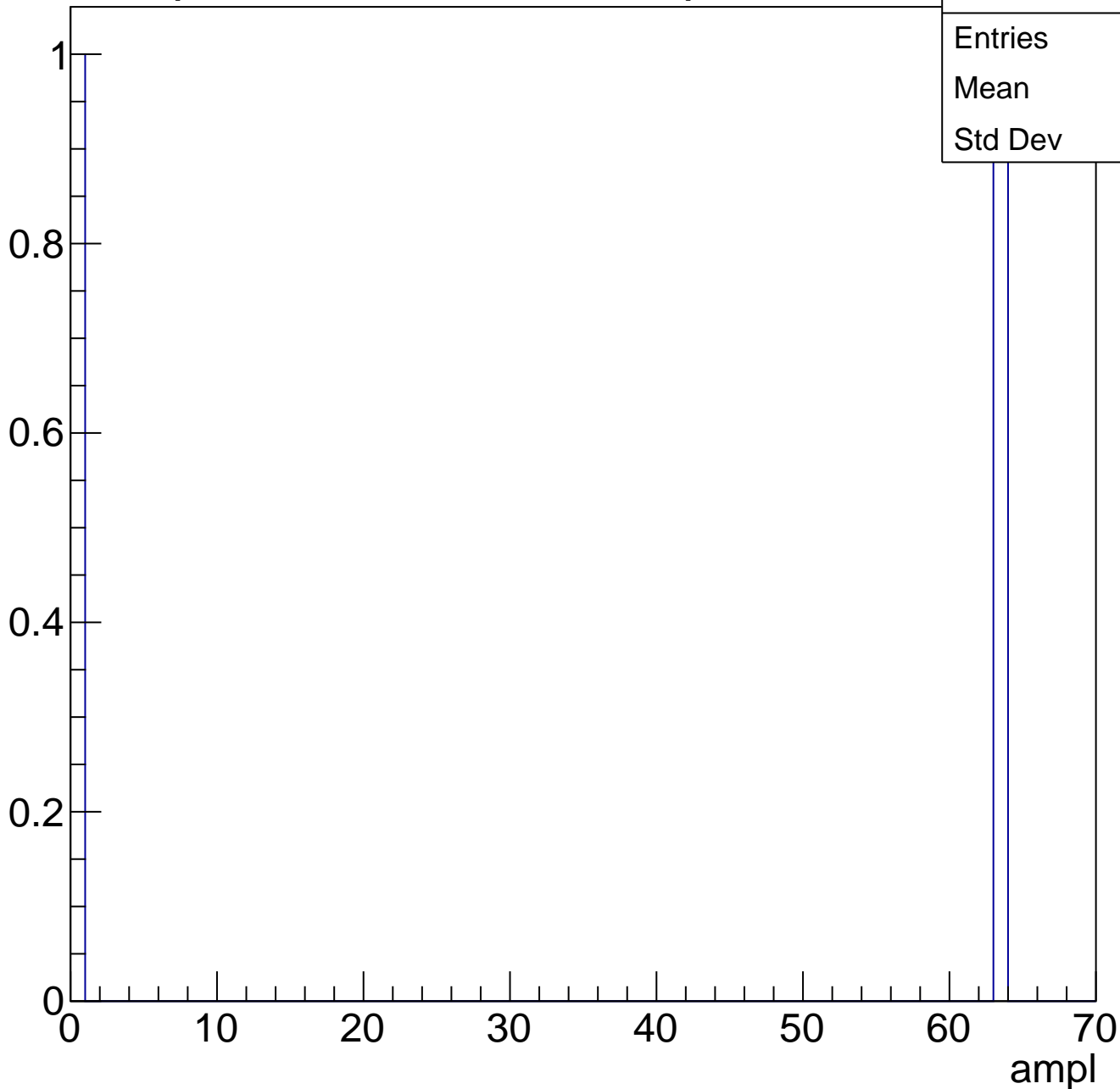
Entries	23
Mean	58.3
Std Dev	12.55



B1L103S, U14-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch114, adc0

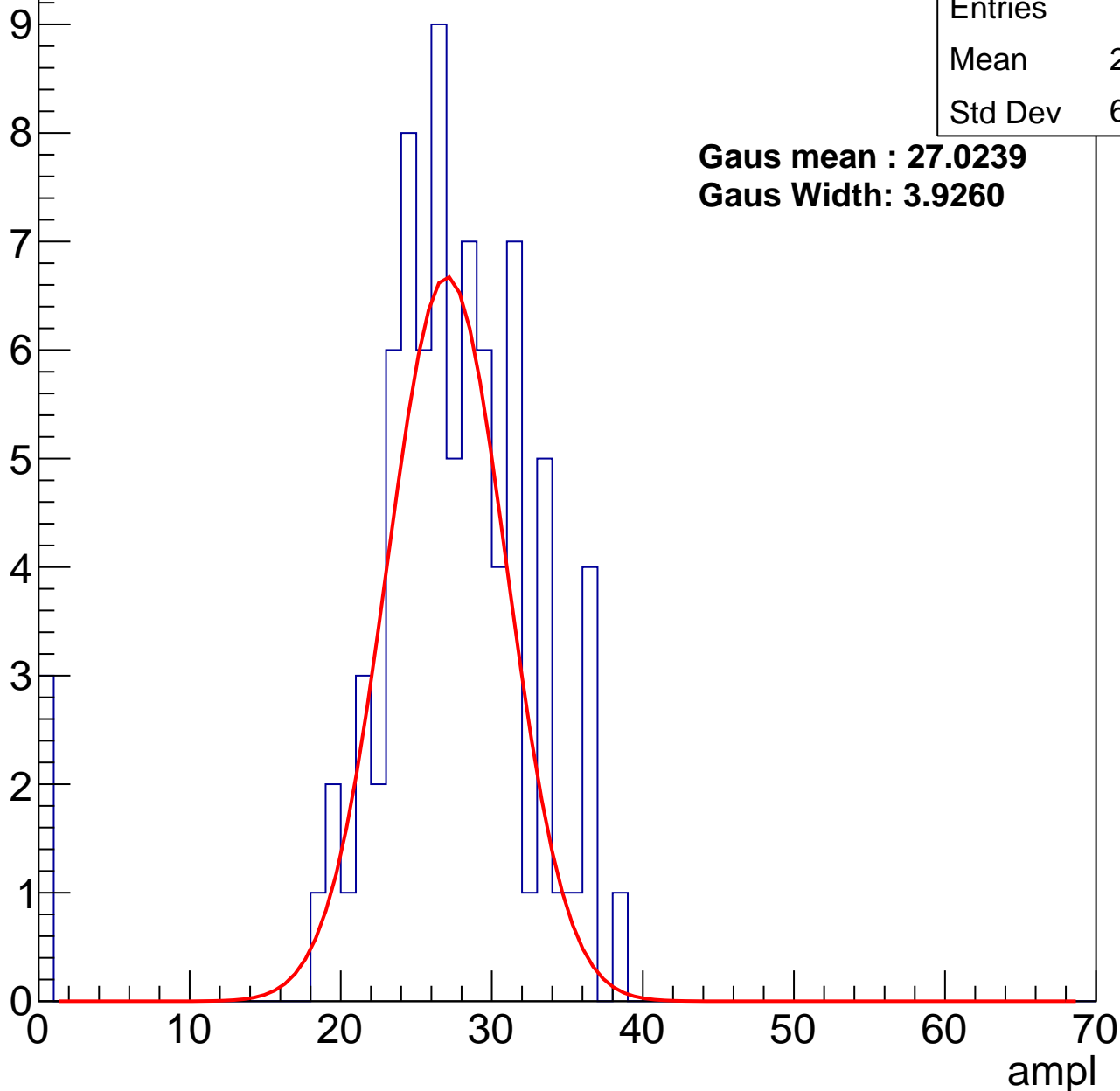
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	26.35
Std Dev	6.686

Gaus mean : 27.0239

Gaus Width: 3.9260



B1L103S, U14-ch114, adc1

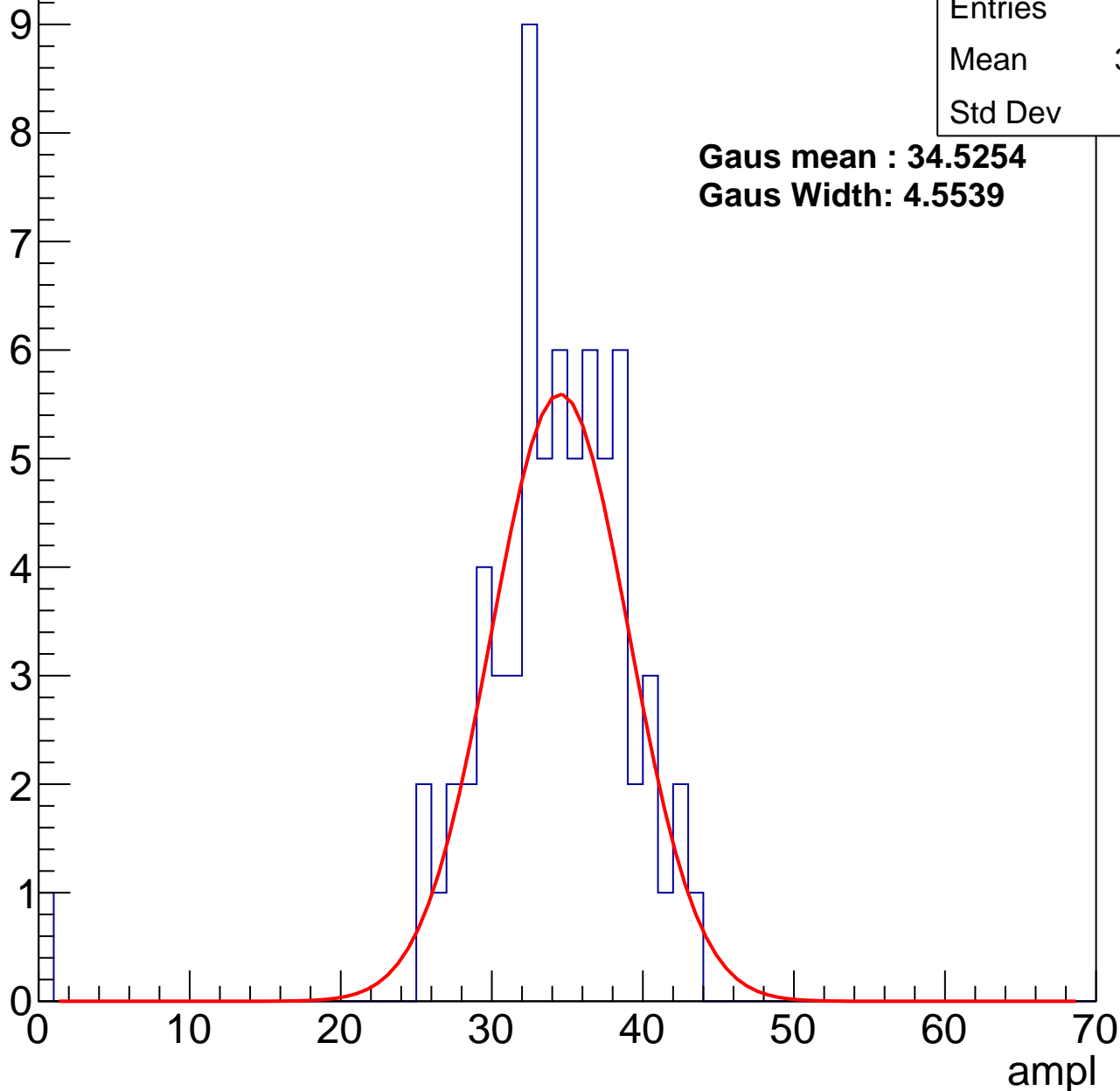
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	33.51
Std Dev	5.82

Gaus mean : 34.5254

Gaus Width: 4.5539



B1L103S, U14-ch114, adc2

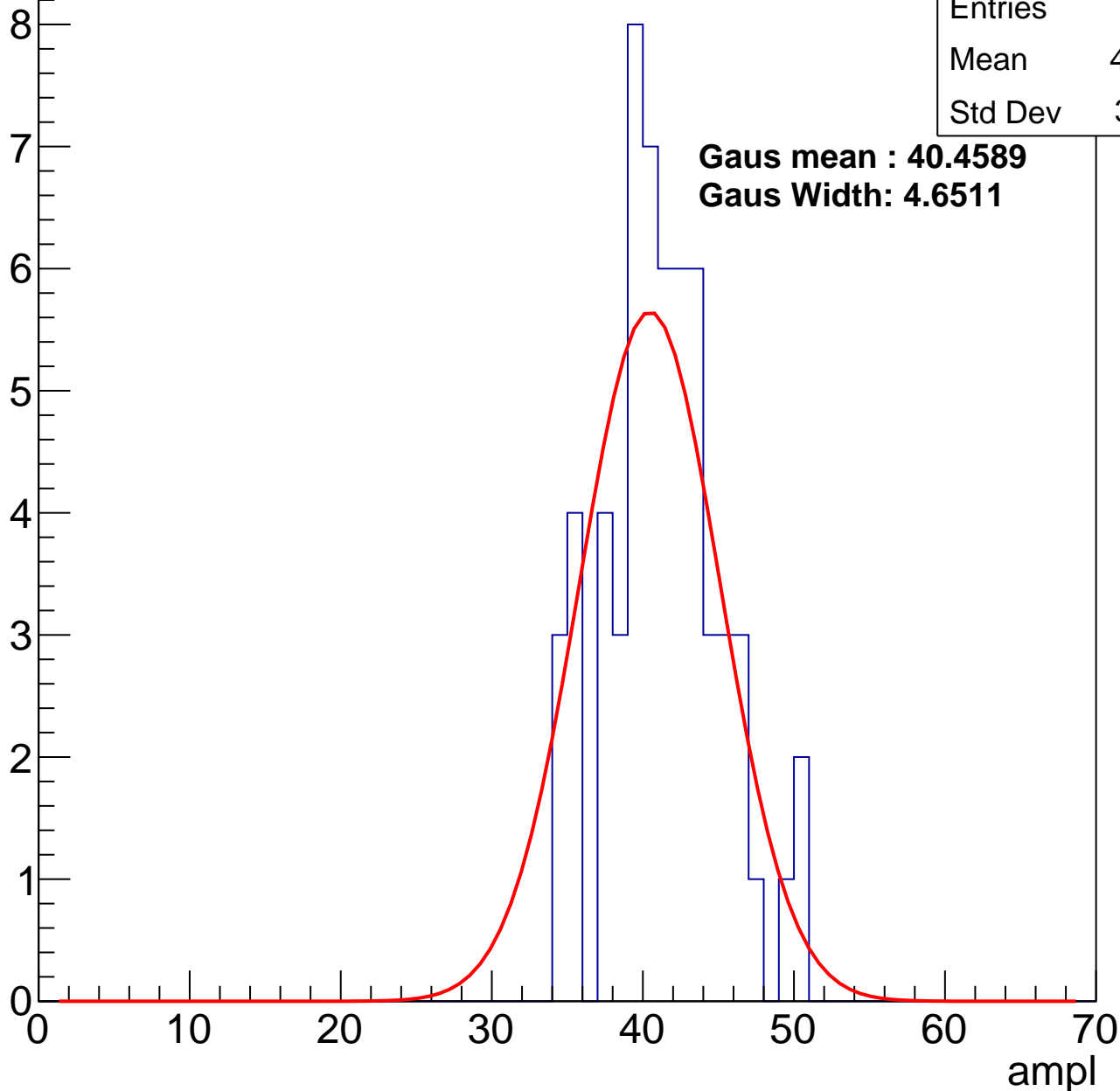
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	40.88
Std Dev	3.791

Gaus mean : 40.4589

Gaus Width: 4.6511

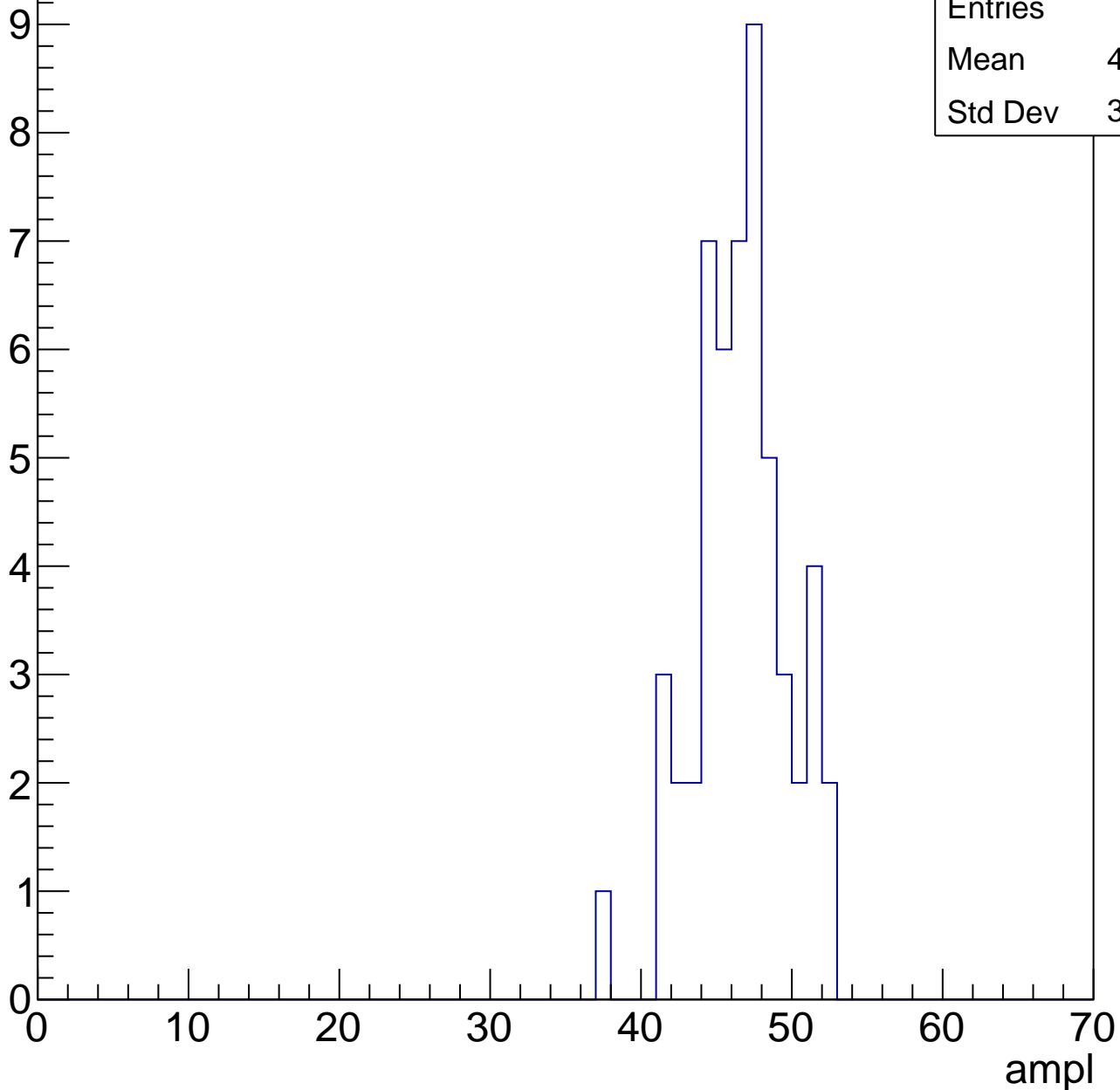


B1L103S, U14-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	46.19
Std Dev	3.066

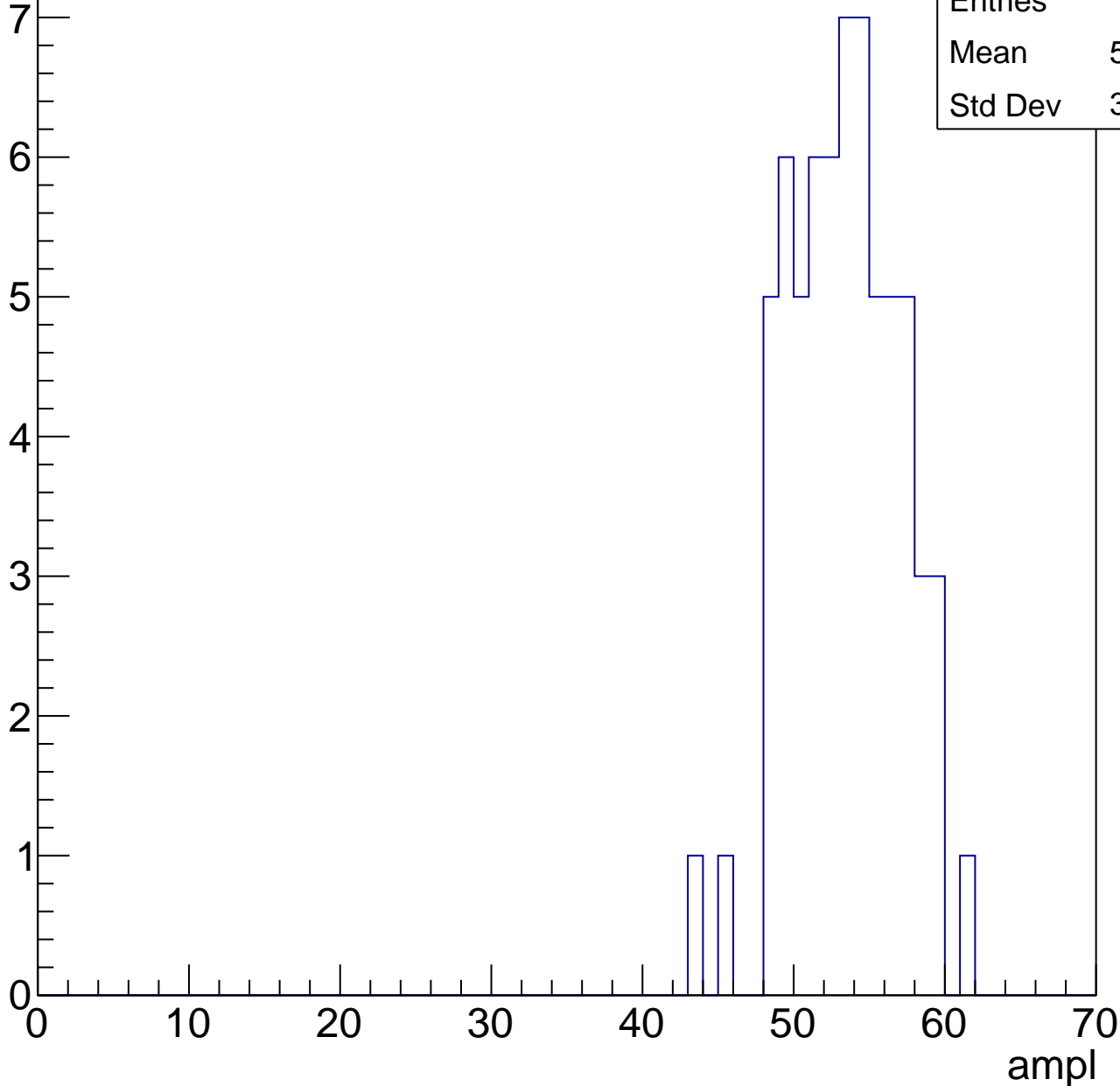


B1L103S, U14-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	52.89
Std Dev	3.606

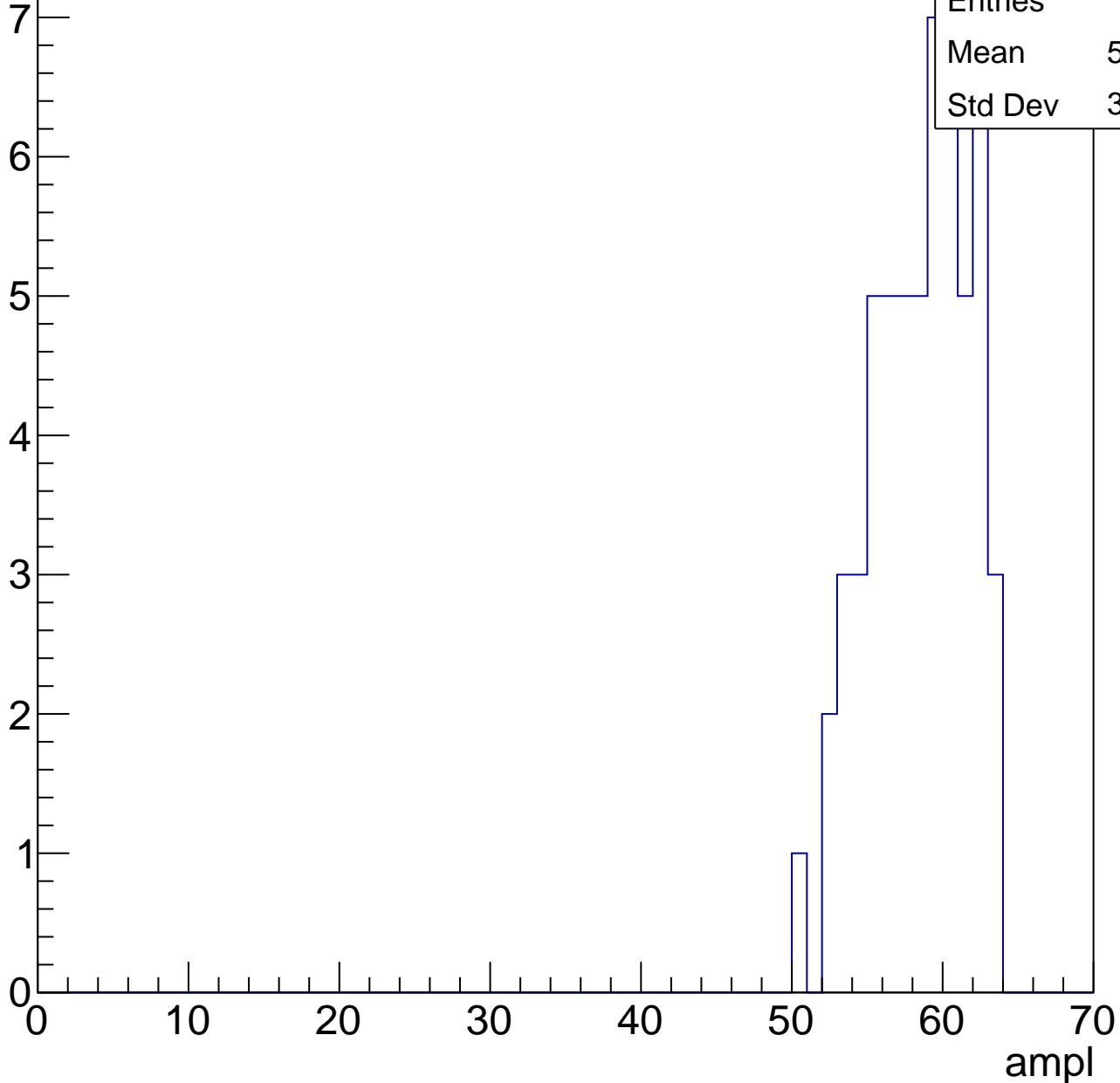


B1L103S, U14-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	58.03
Std Dev	3.205

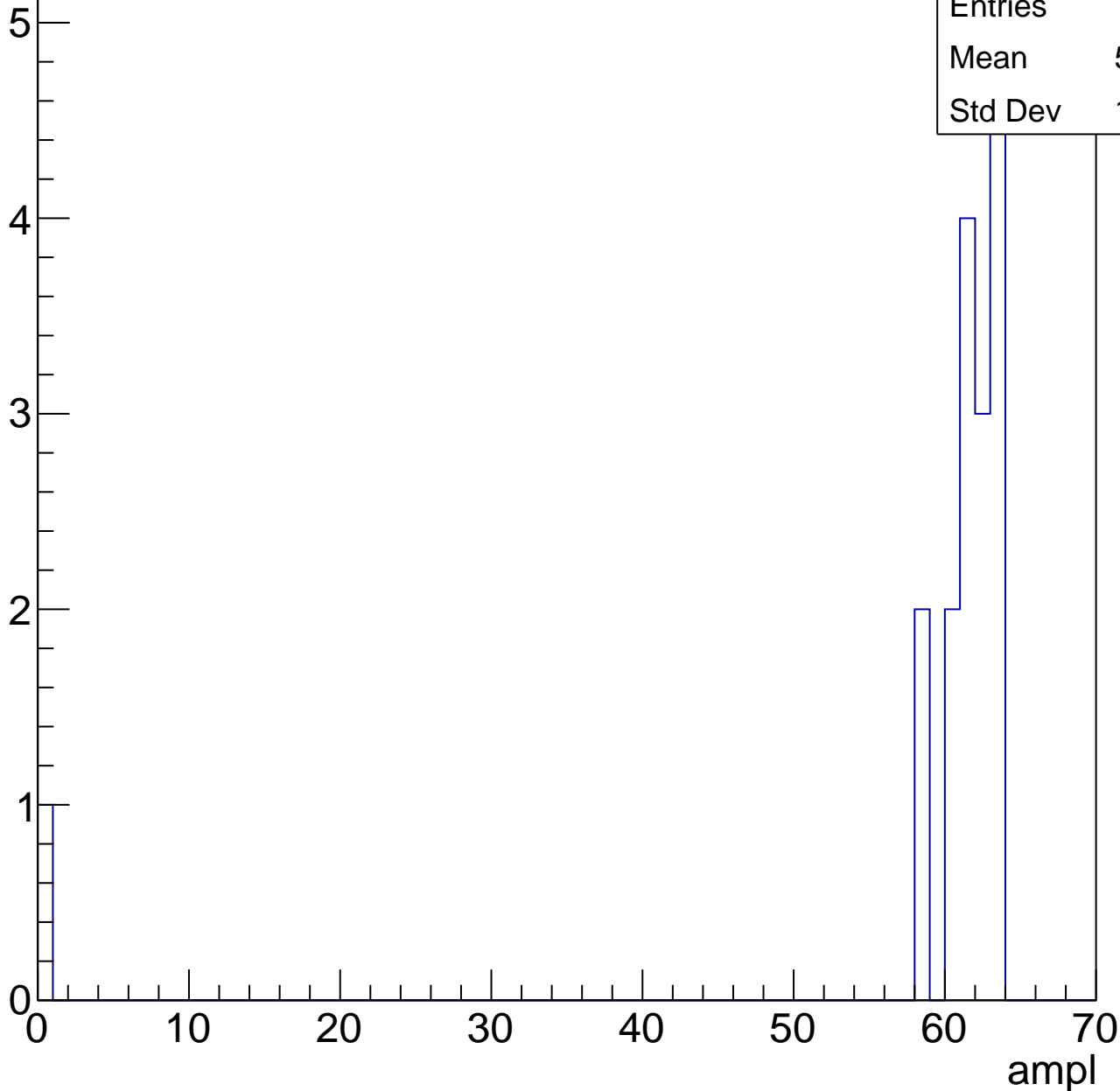


B1L103S, U14-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

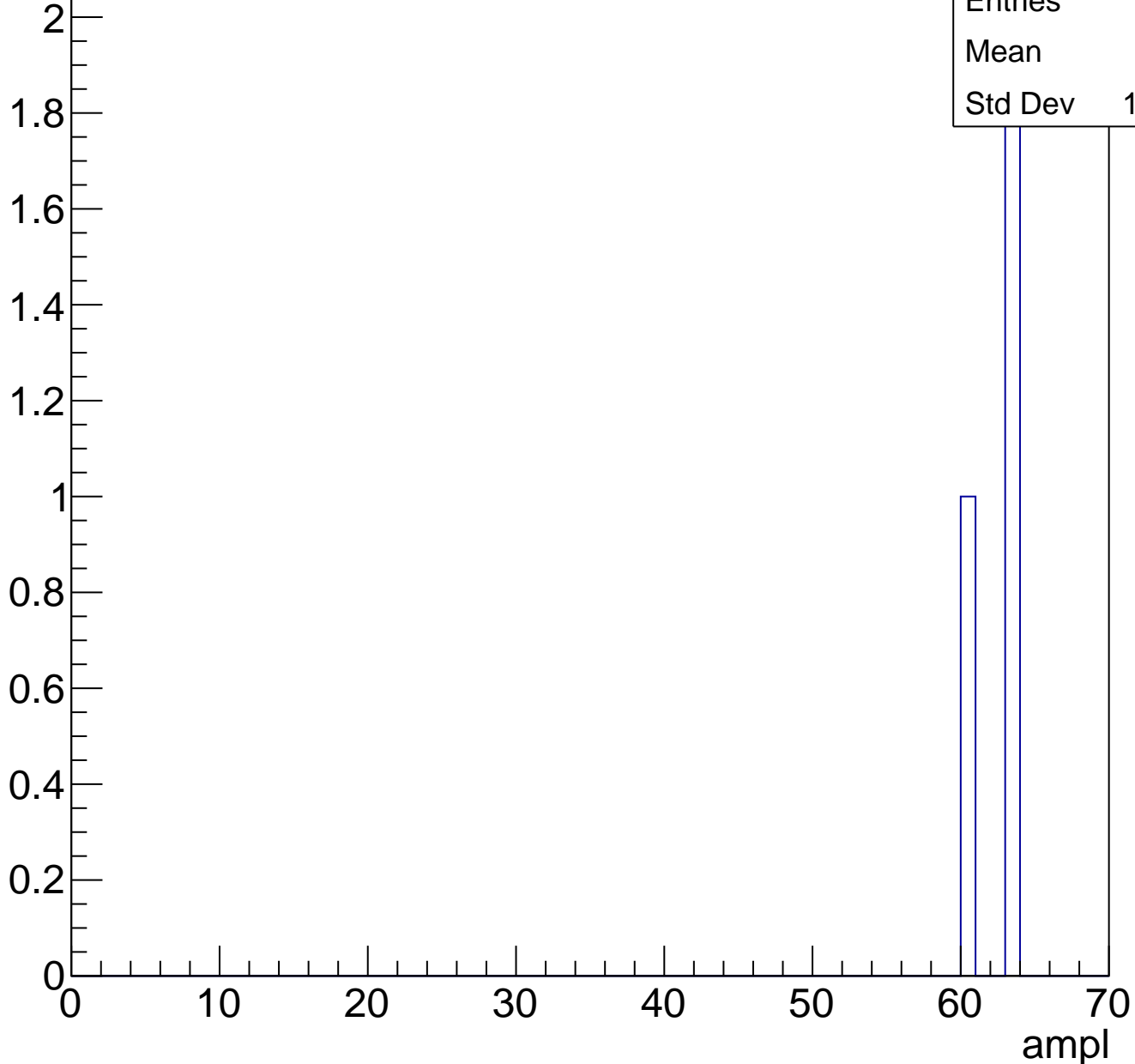
Entries	17
Mean	57.71
Std Dev	14.51



B1L103S, U14-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch115, adc0

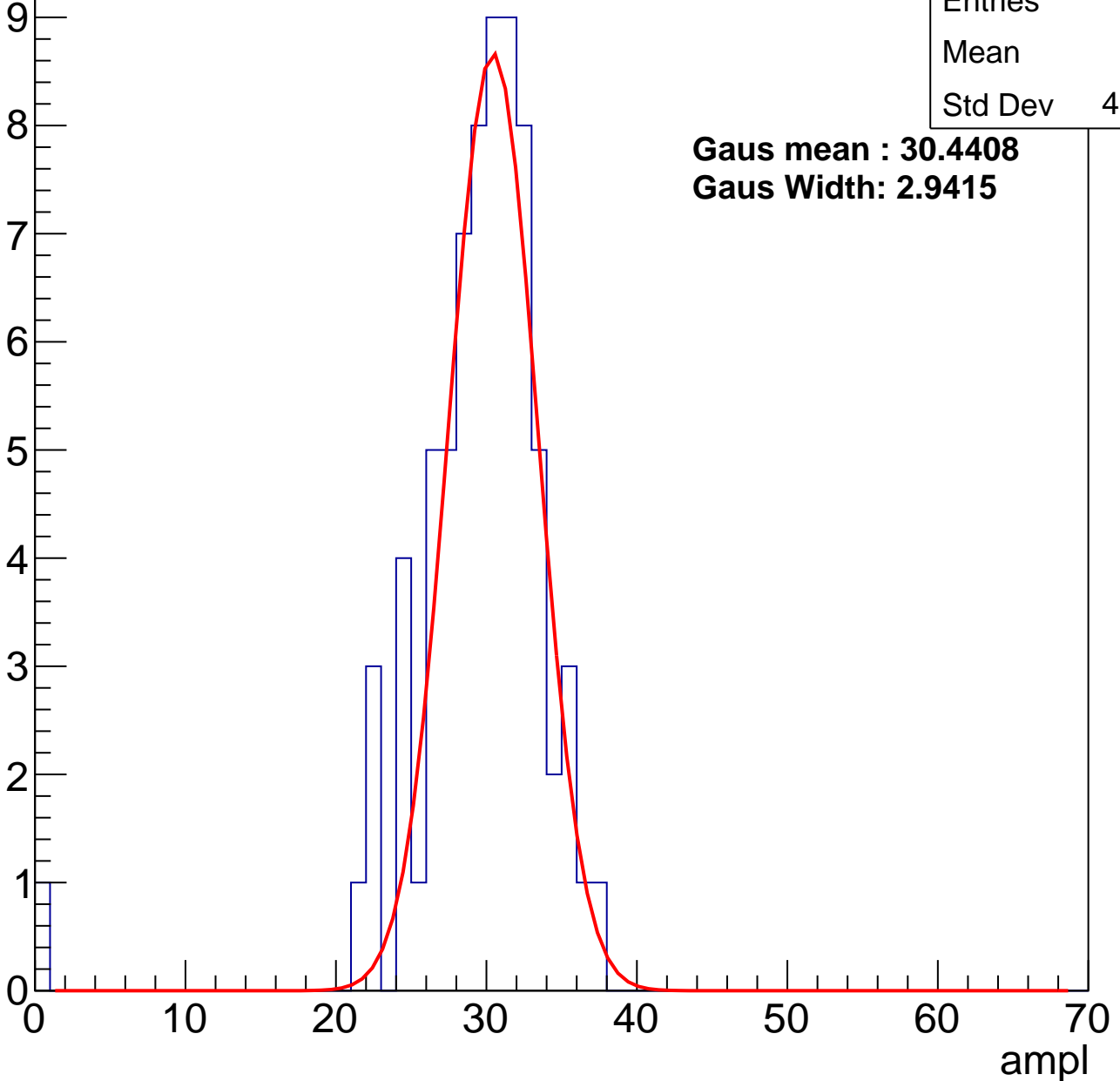
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	29
Std Dev	4.837

Gaus mean : 30.4408

Gaus Width: 2.9415



B1L103S, U14-ch115, adc1

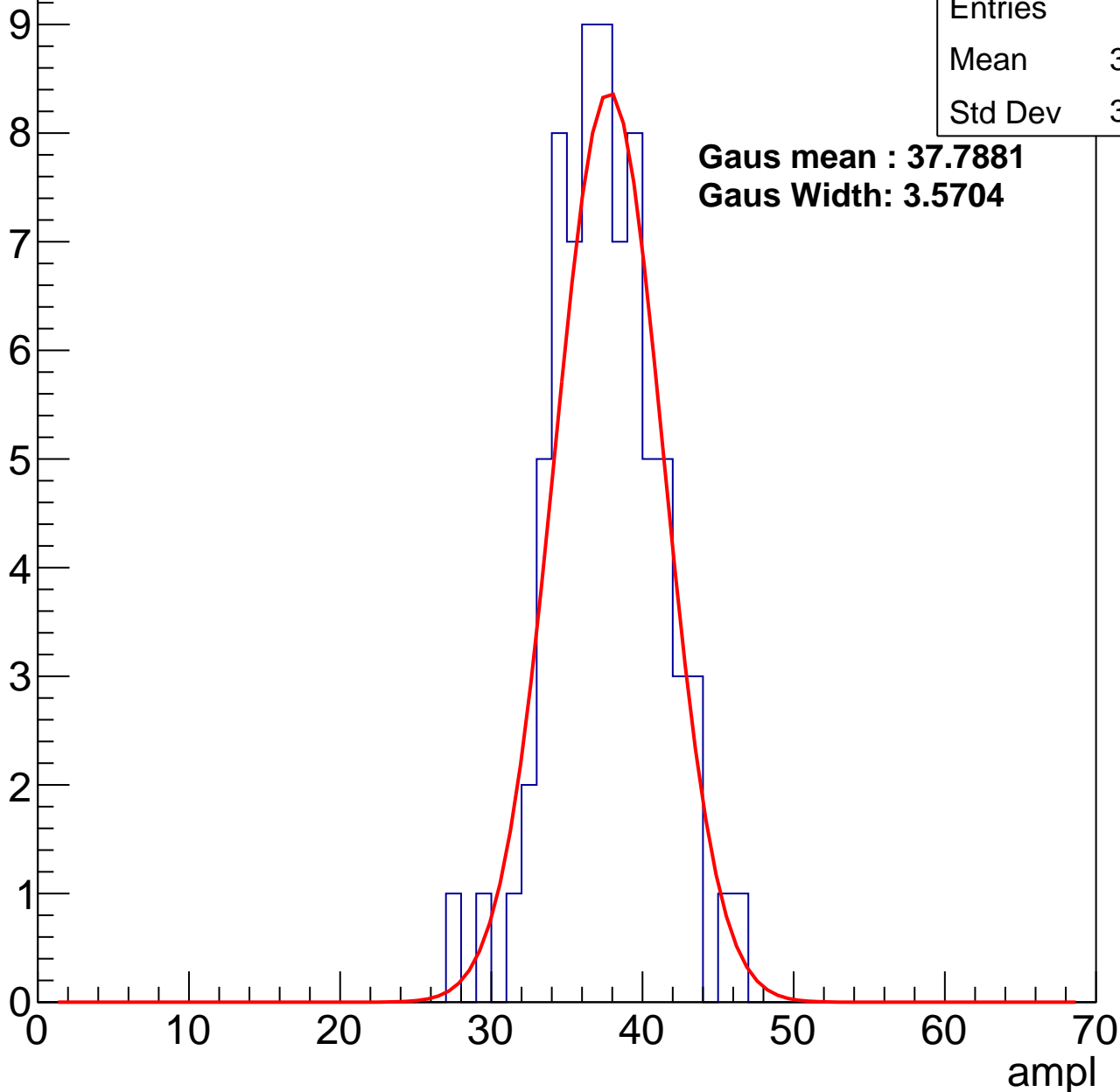
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	37.09
Std Dev	3.499

Gaus mean : 37.7881

Gaus Width: 3.5704



B1L103S, U14-ch115, adc2

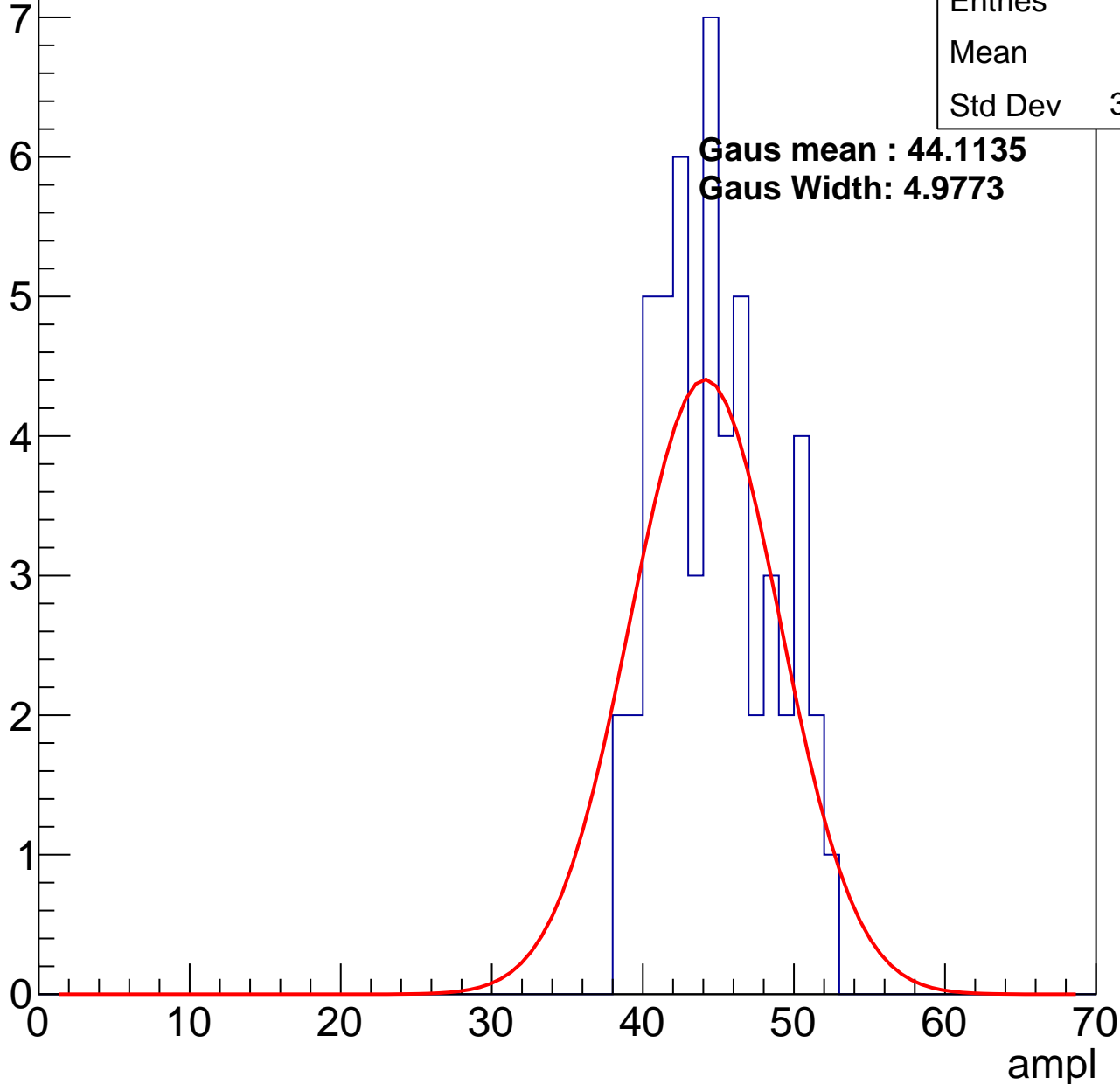
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	44.3
Std Dev	3.673

Gaus mean : 44.1135

Gaus Width: 4.9773

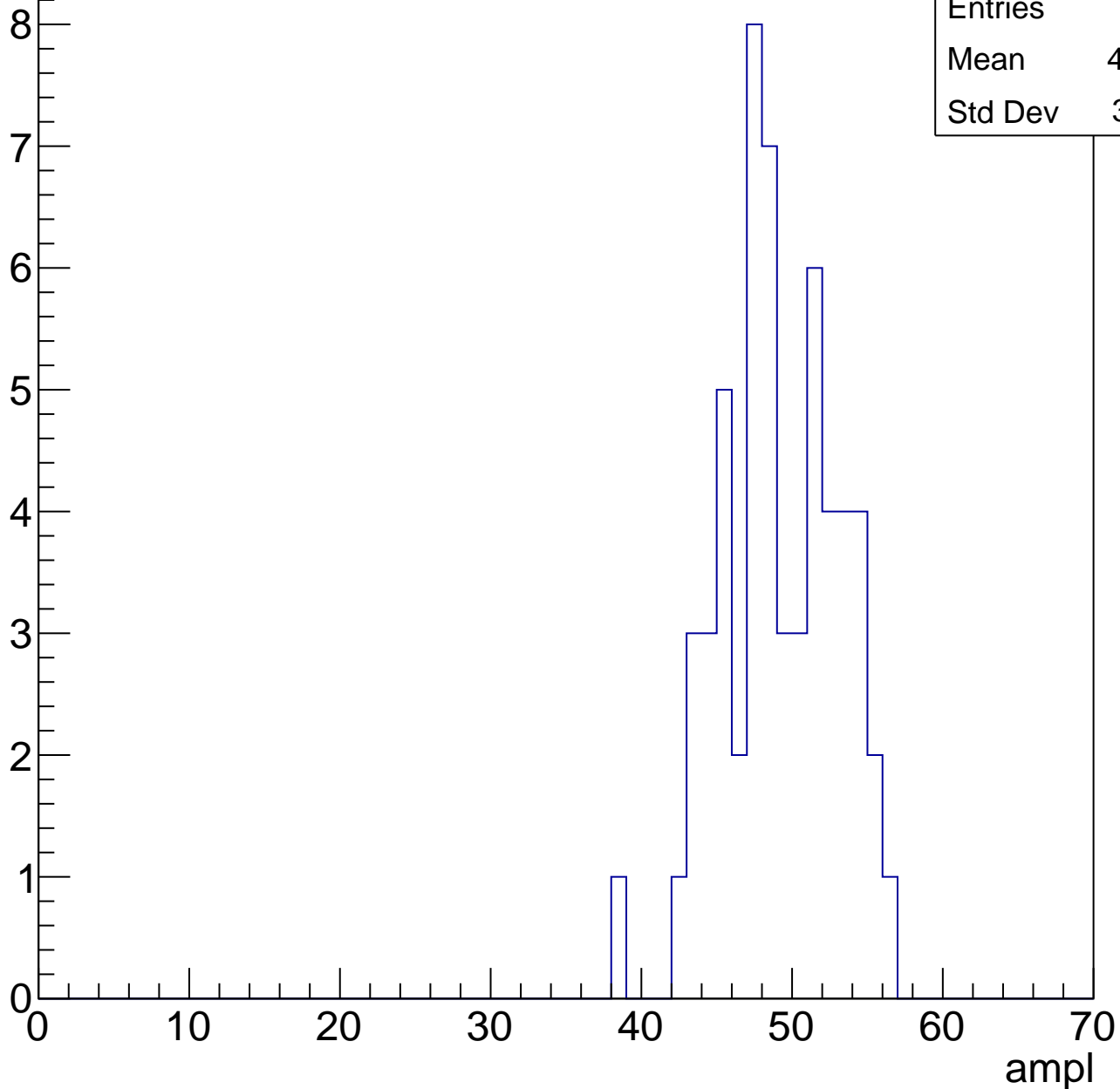


B1L103S, U14-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	48.68
Std Dev	3.821

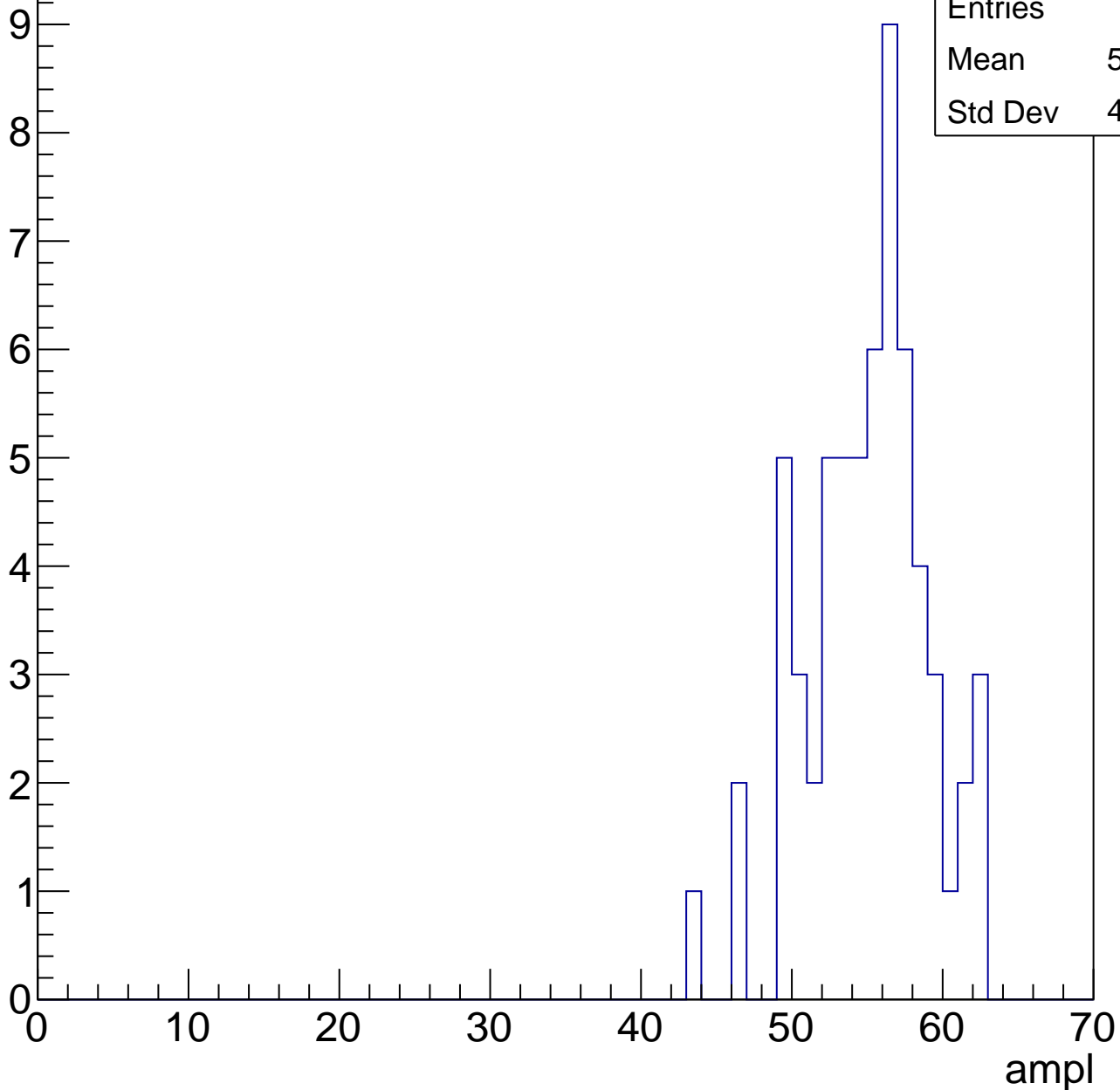


B1L103S, U14-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	54.52
Std Dev	4.035

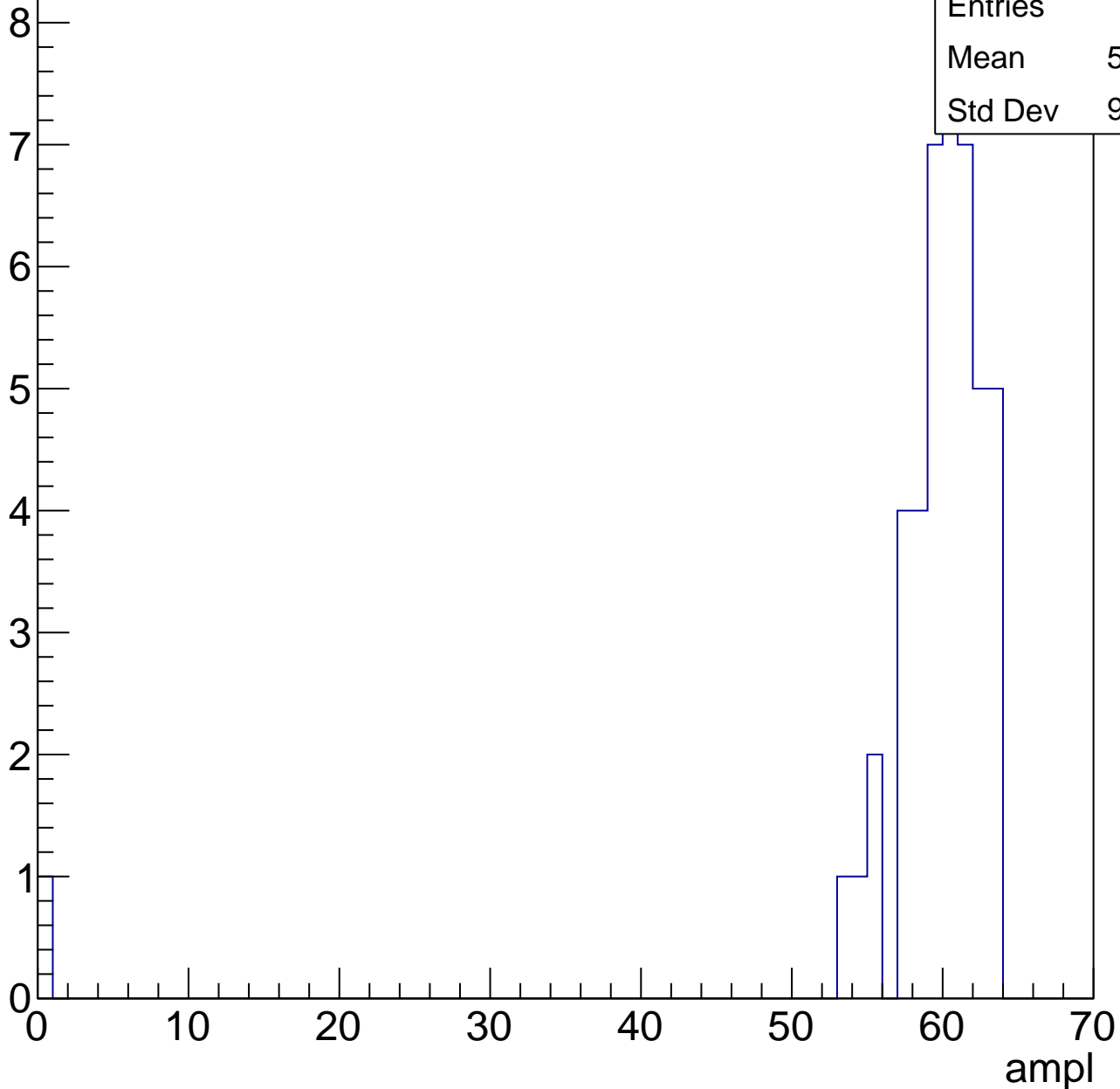


B1L103S, U14-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	58.27
Std Dev	9.105

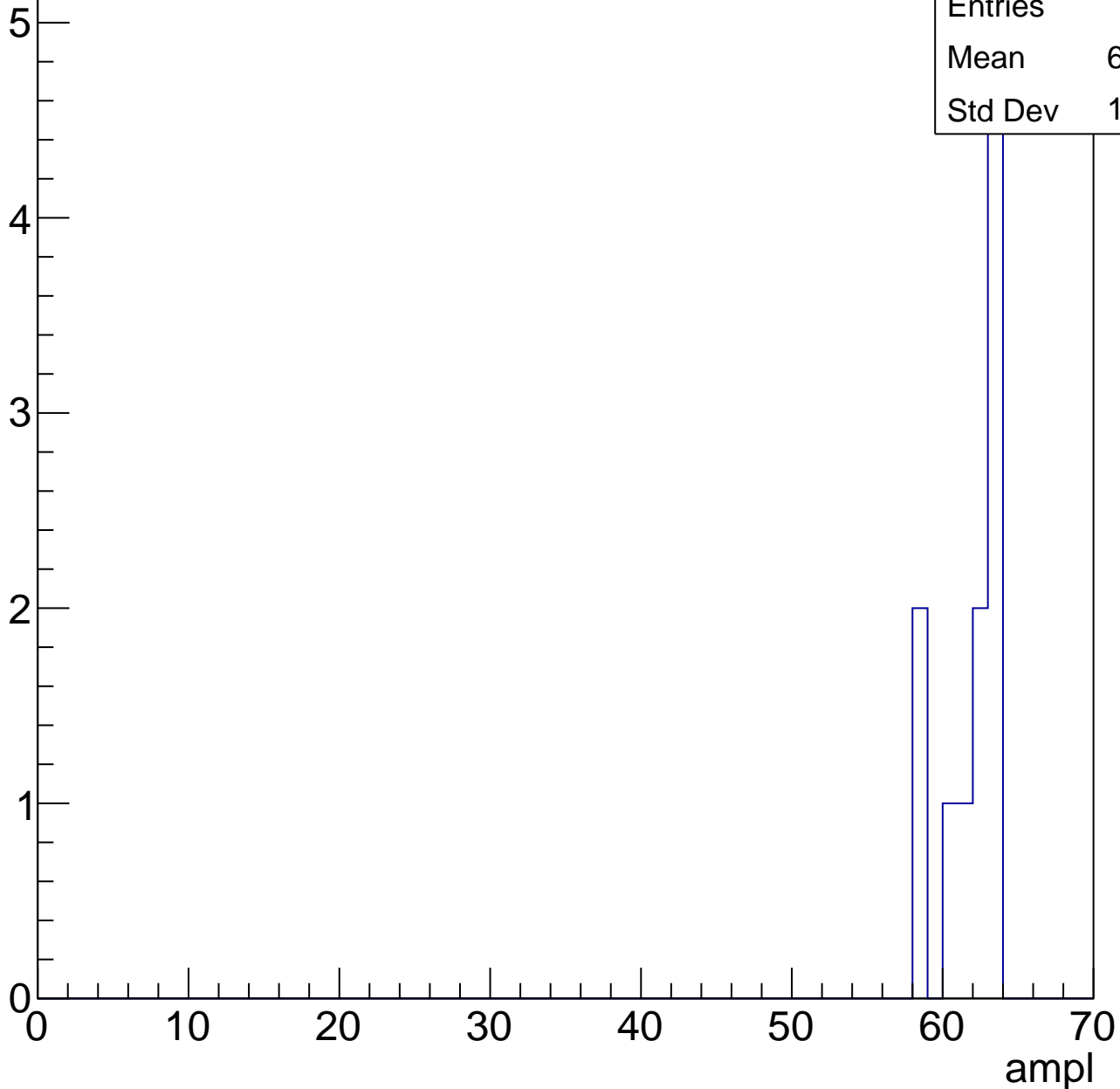


B1L103S, U14-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	61.45
Std Dev	1.876



B1L103S, U14-ch115, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch116, adc0

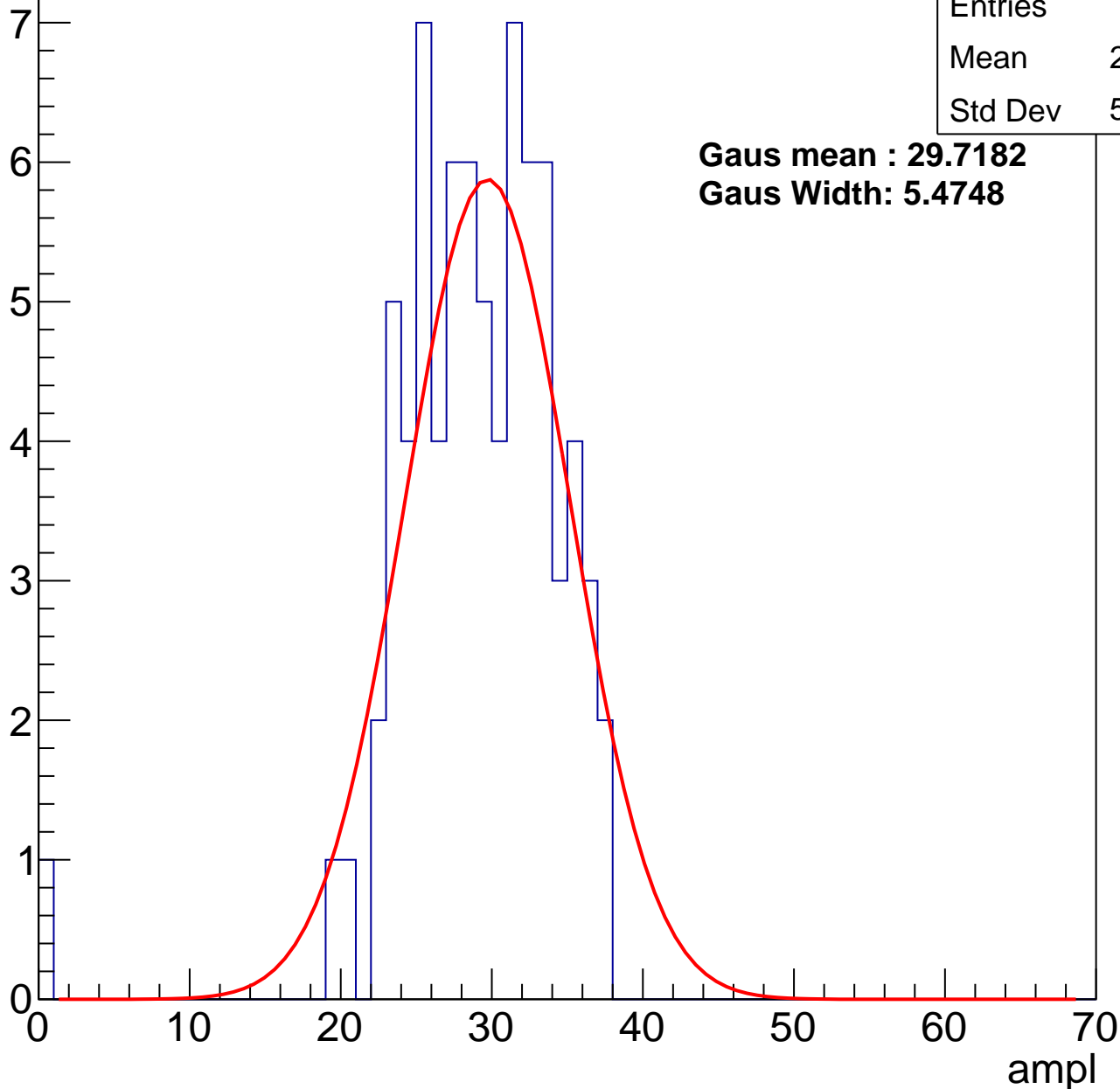
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	28.56
Std Dev	5.392

Gaus mean : 29.7182

Gaus Width: 5.4748



B1L103S, U14-ch116, adc1

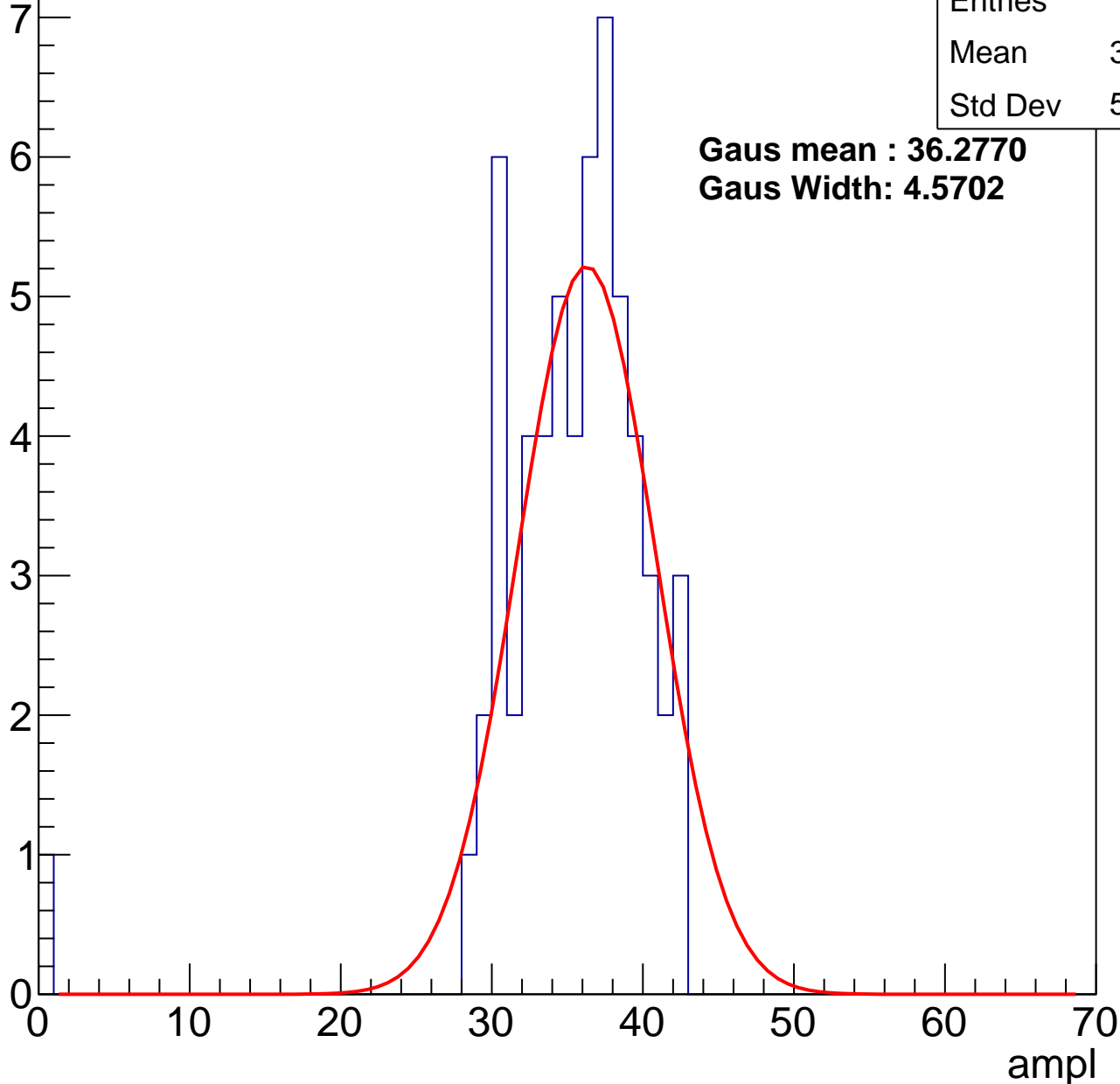
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	34.69
Std Dev	5.846

Gaus mean : 36.2770

Gaus Width: 4.5702

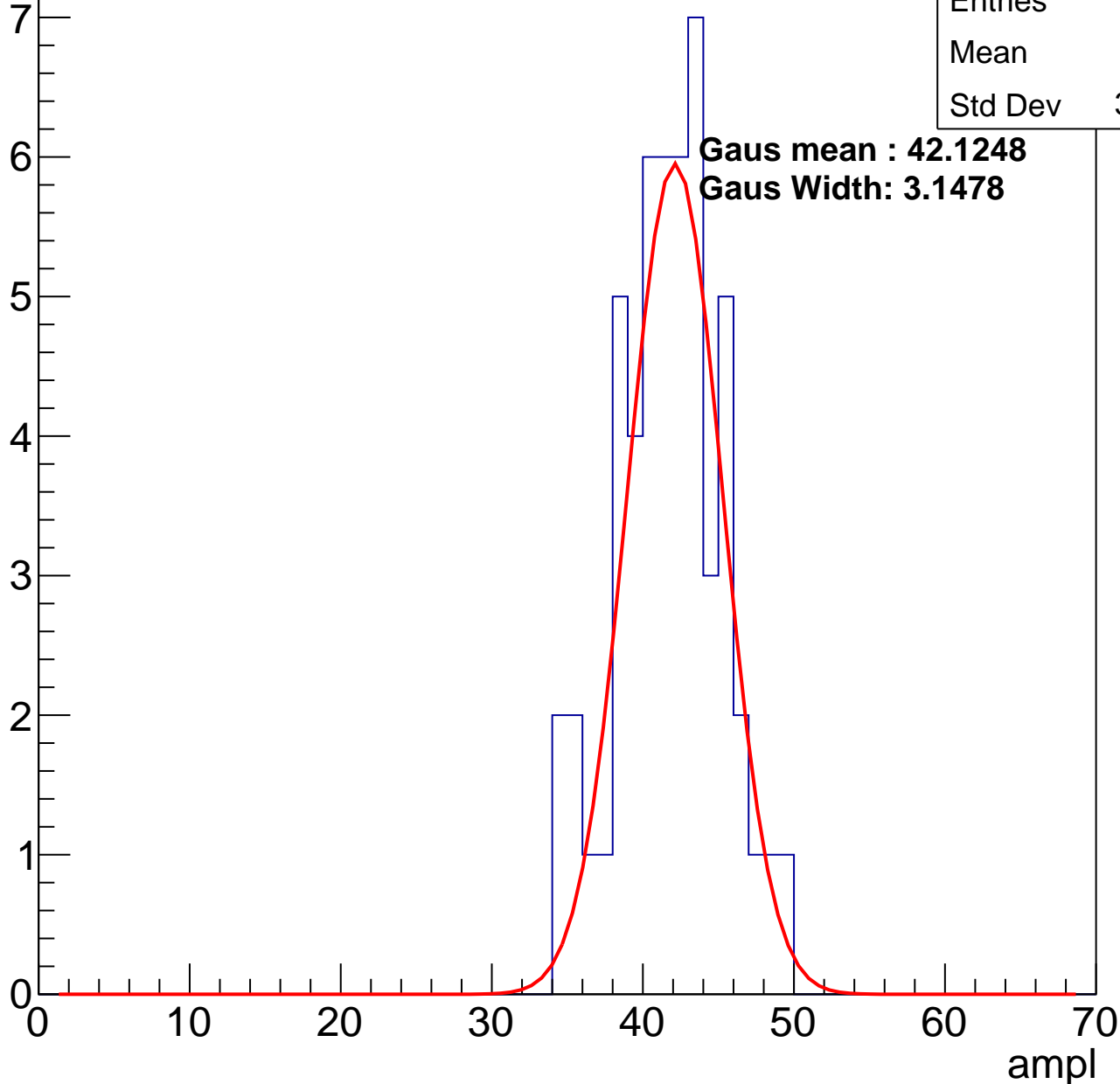


B1L103S, U14-ch116, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	41.3
Std Dev	3.401

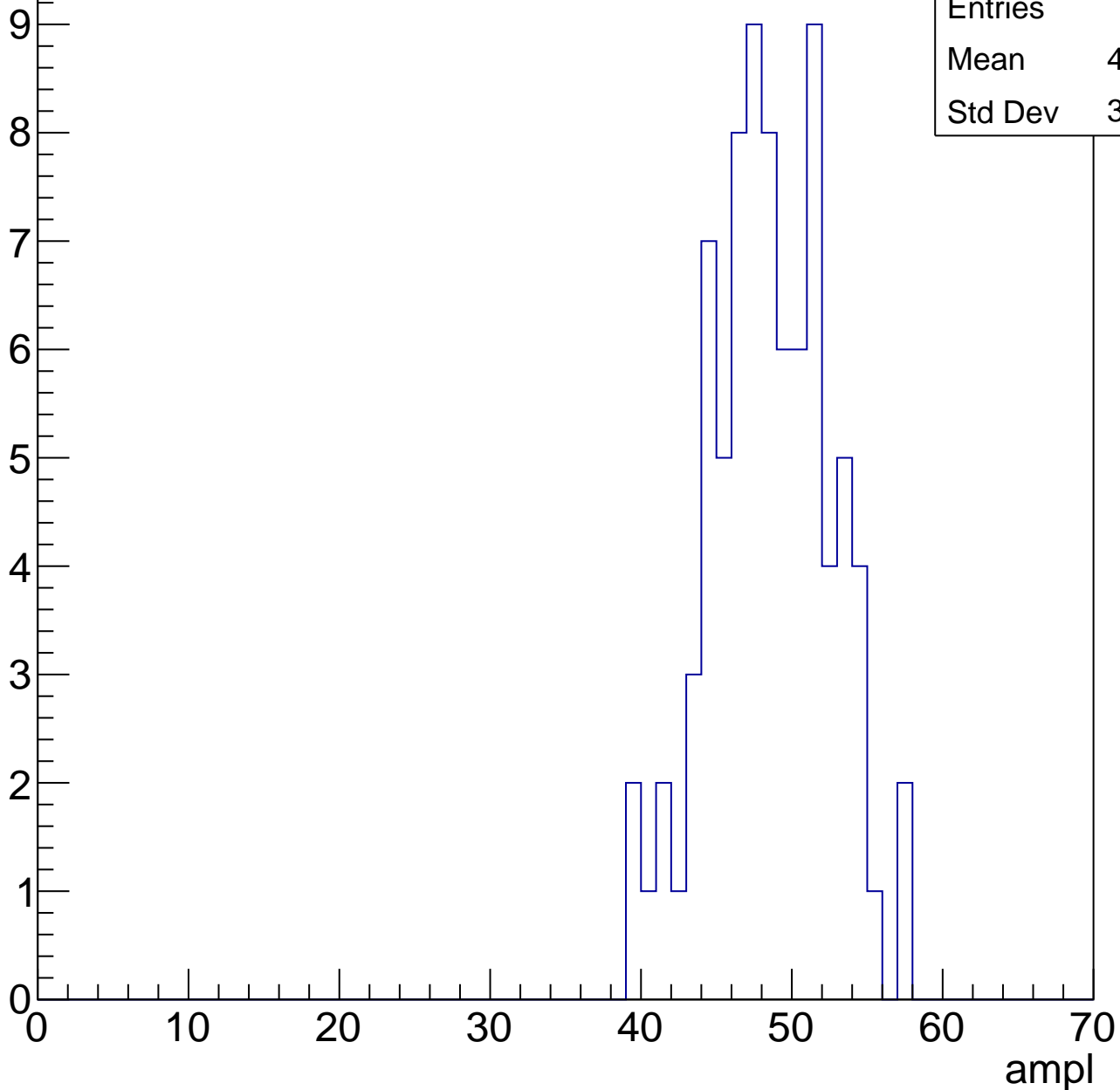


B1L103S, U14-ch116, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	48.07
Std Dev	3.939

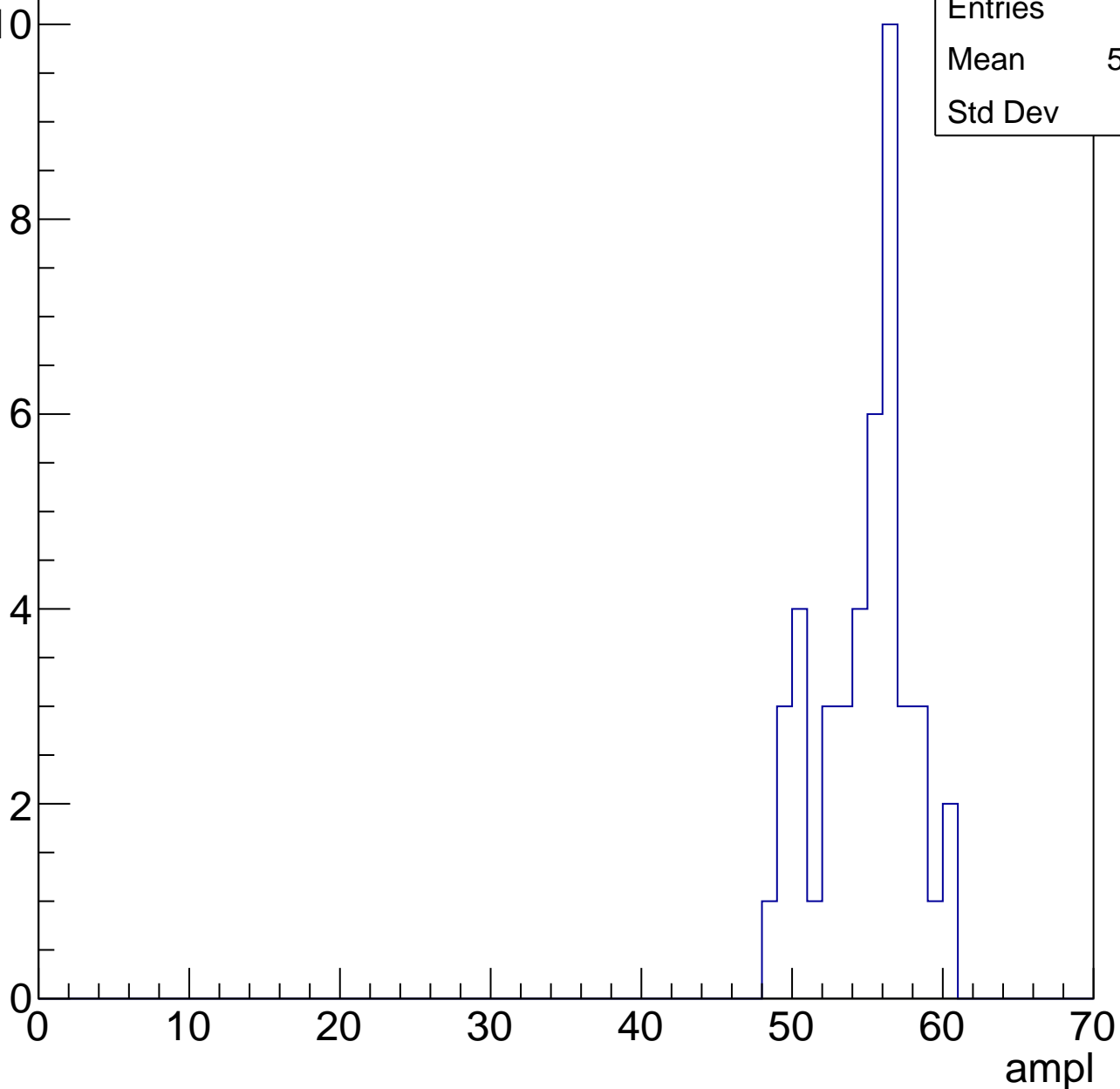


B1L103S, U14-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	54.34
Std Dev	3.06

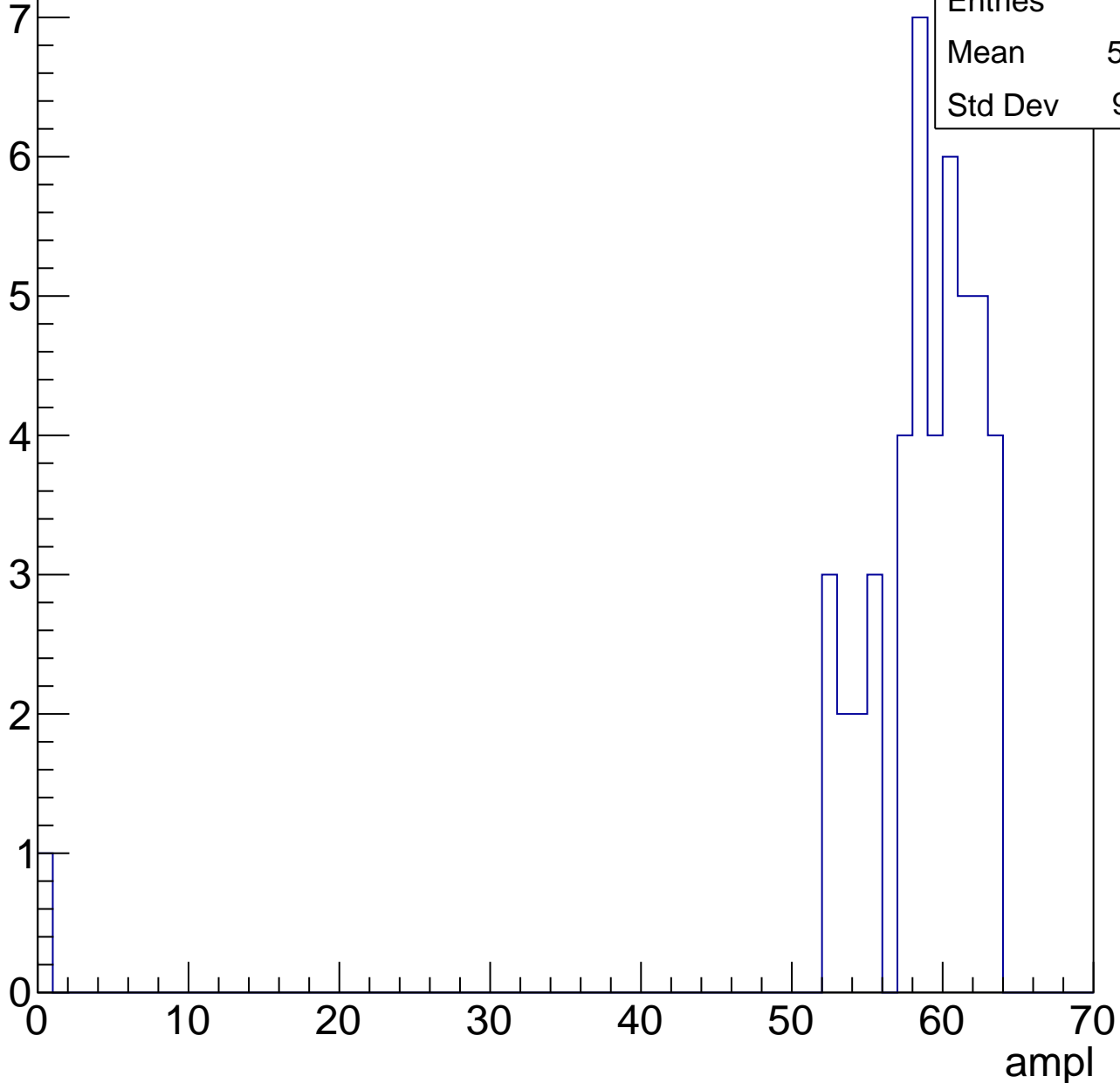


B1L103S, U14-ch116, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

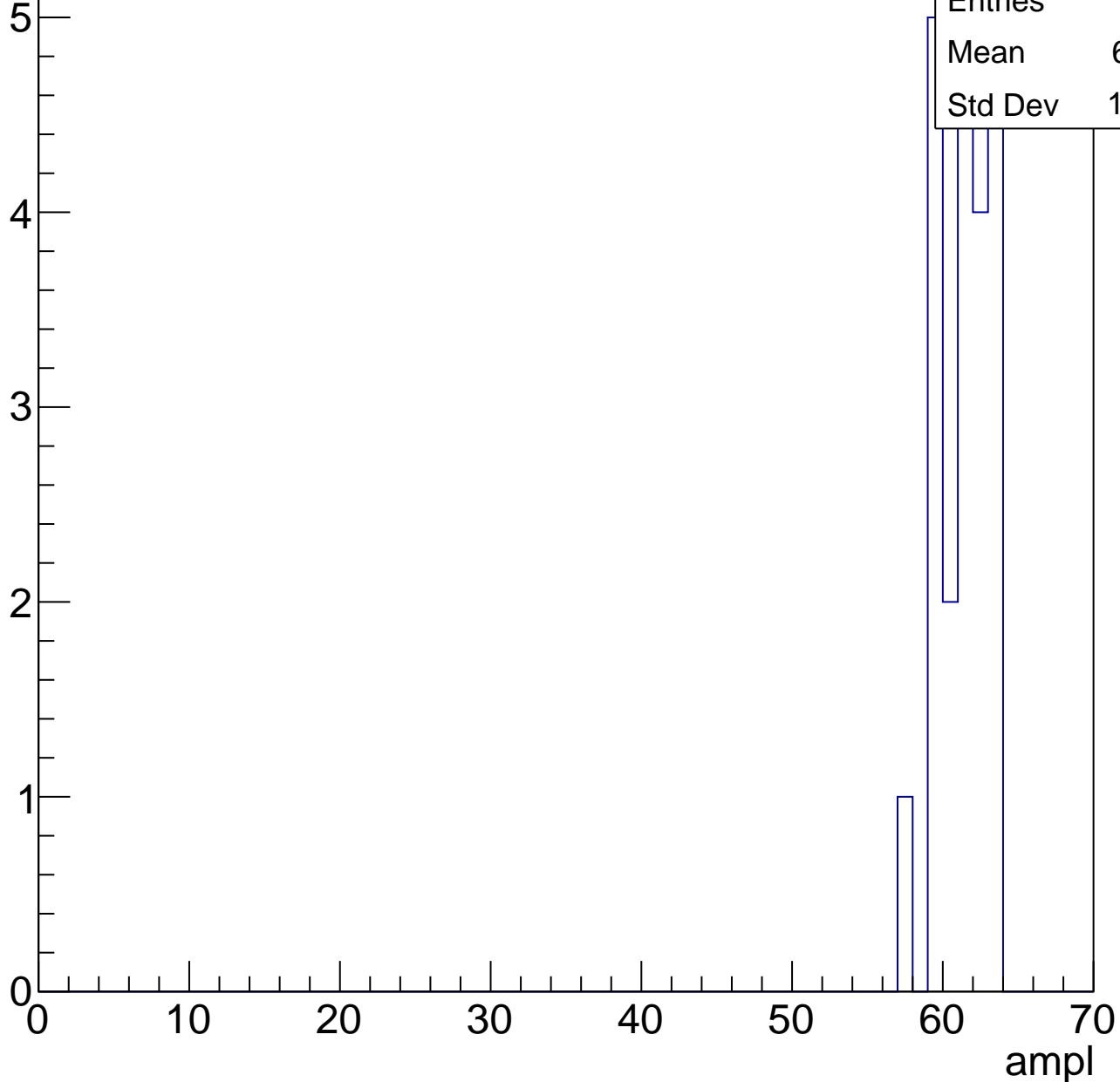
Entries	46
Mean	57.22
Std Dev	9.101



B1L103S, U14-ch116, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

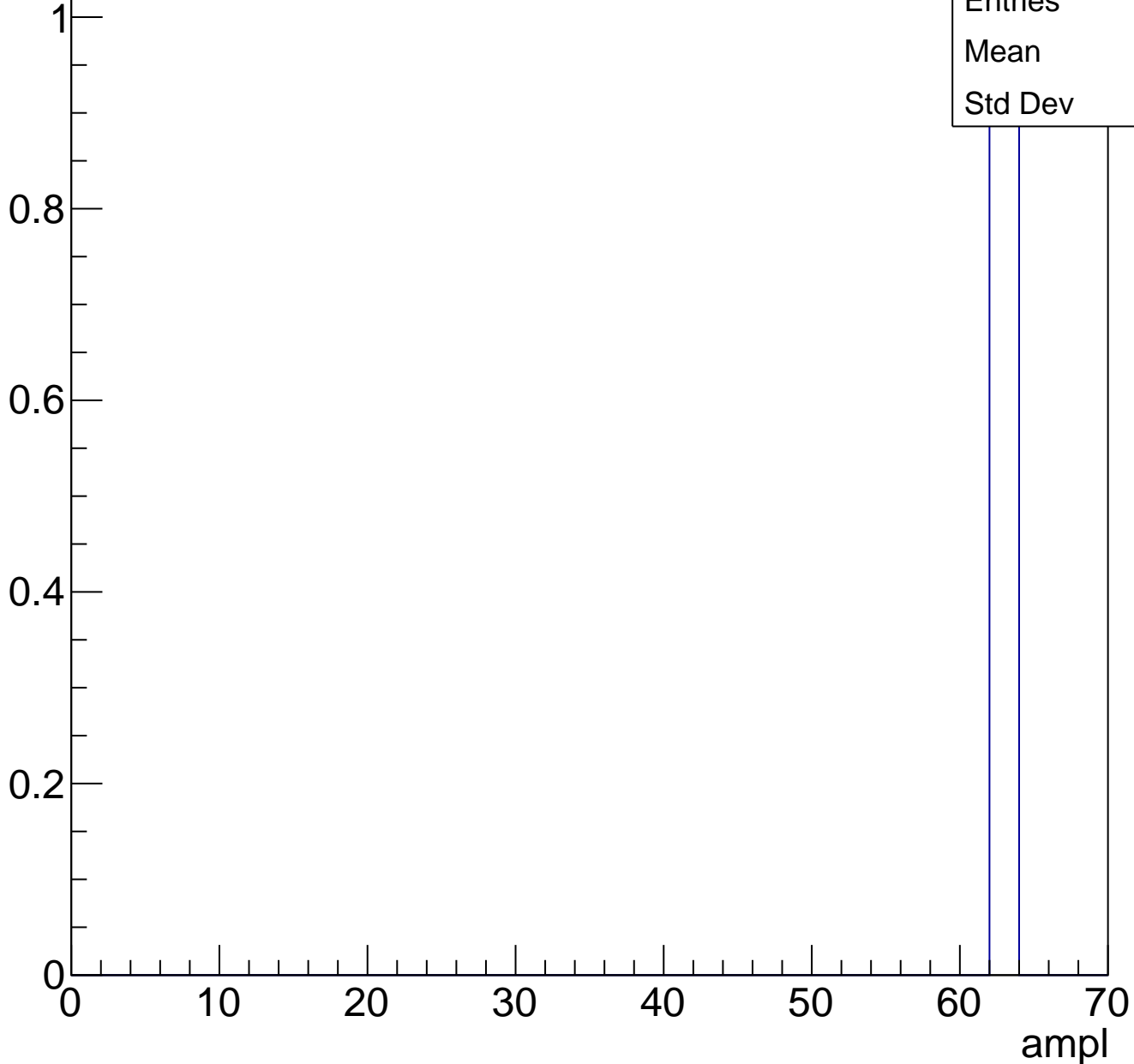


Entries	22
Mean	60.91
Std Dev	1.676

B1L103S, U14-ch116, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch117, adc0

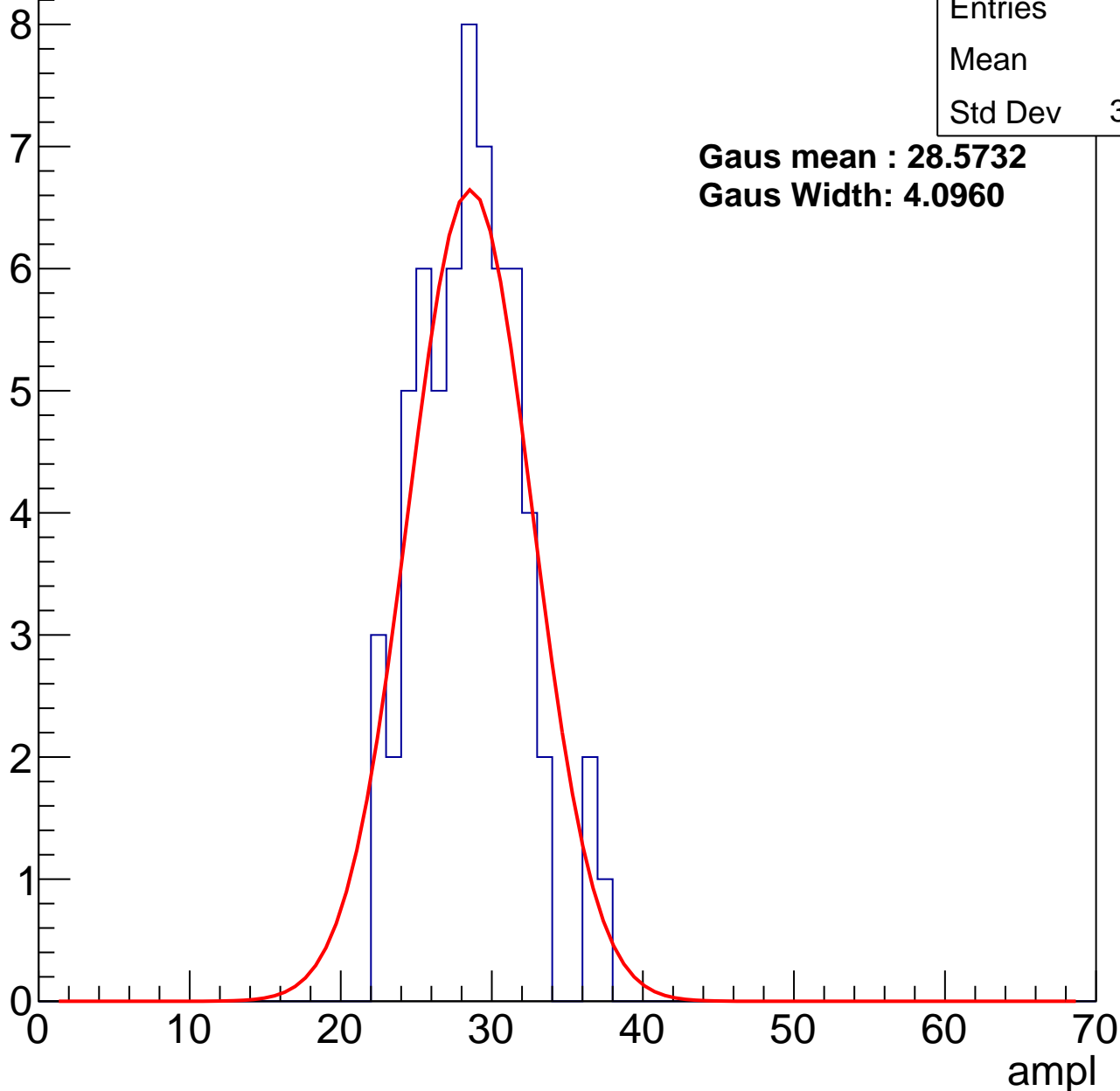
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.1
Std Dev	3.393

Gaus mean : 28.5732

Gaus Width: 4.0960



B1L103S, U14-ch117, adc1

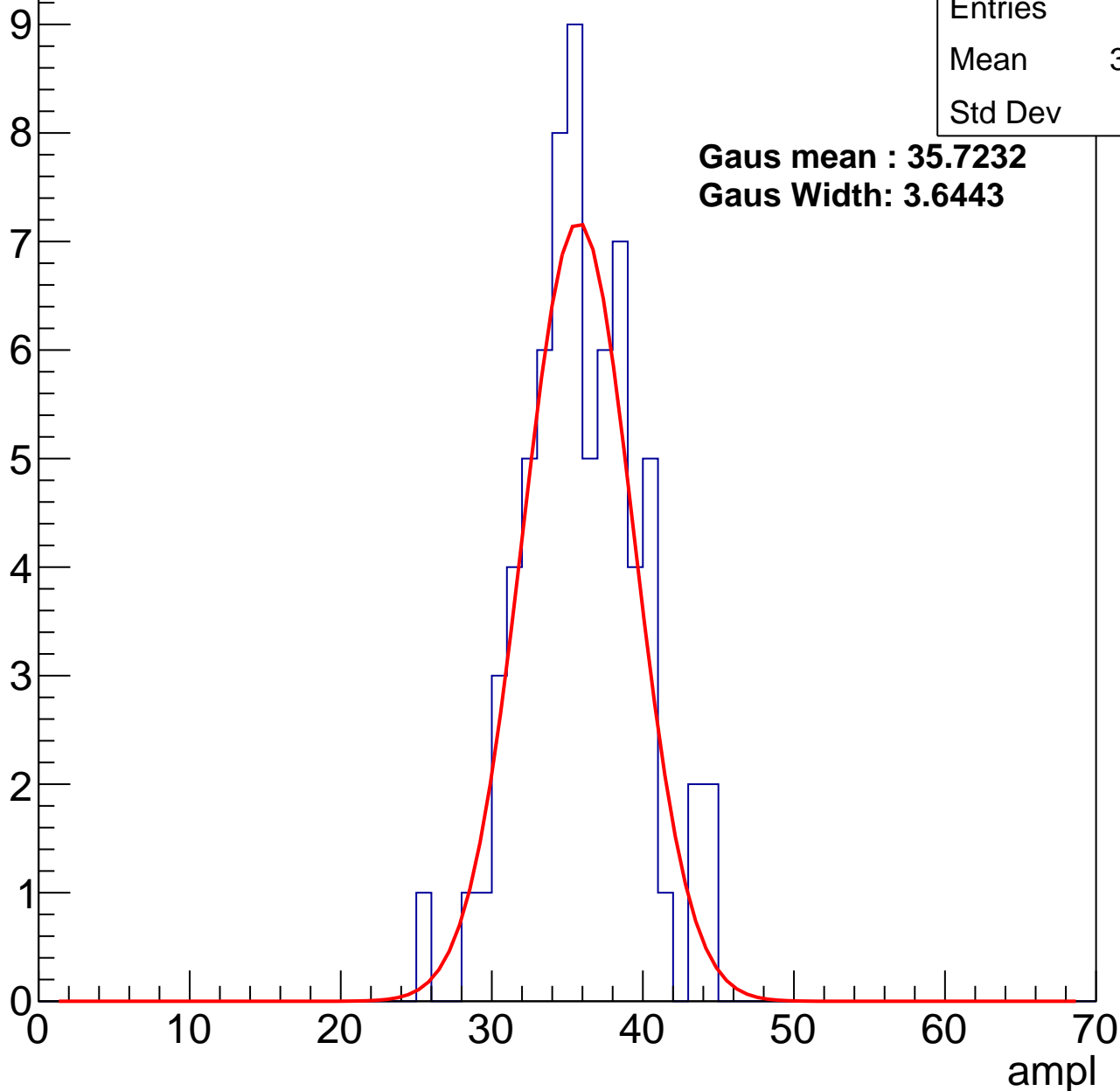
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	35.43
Std Dev	3.77

Gaus mean : 35.7232

Gaus Width: 3.6443



B1L103S, U14-ch117, adc2

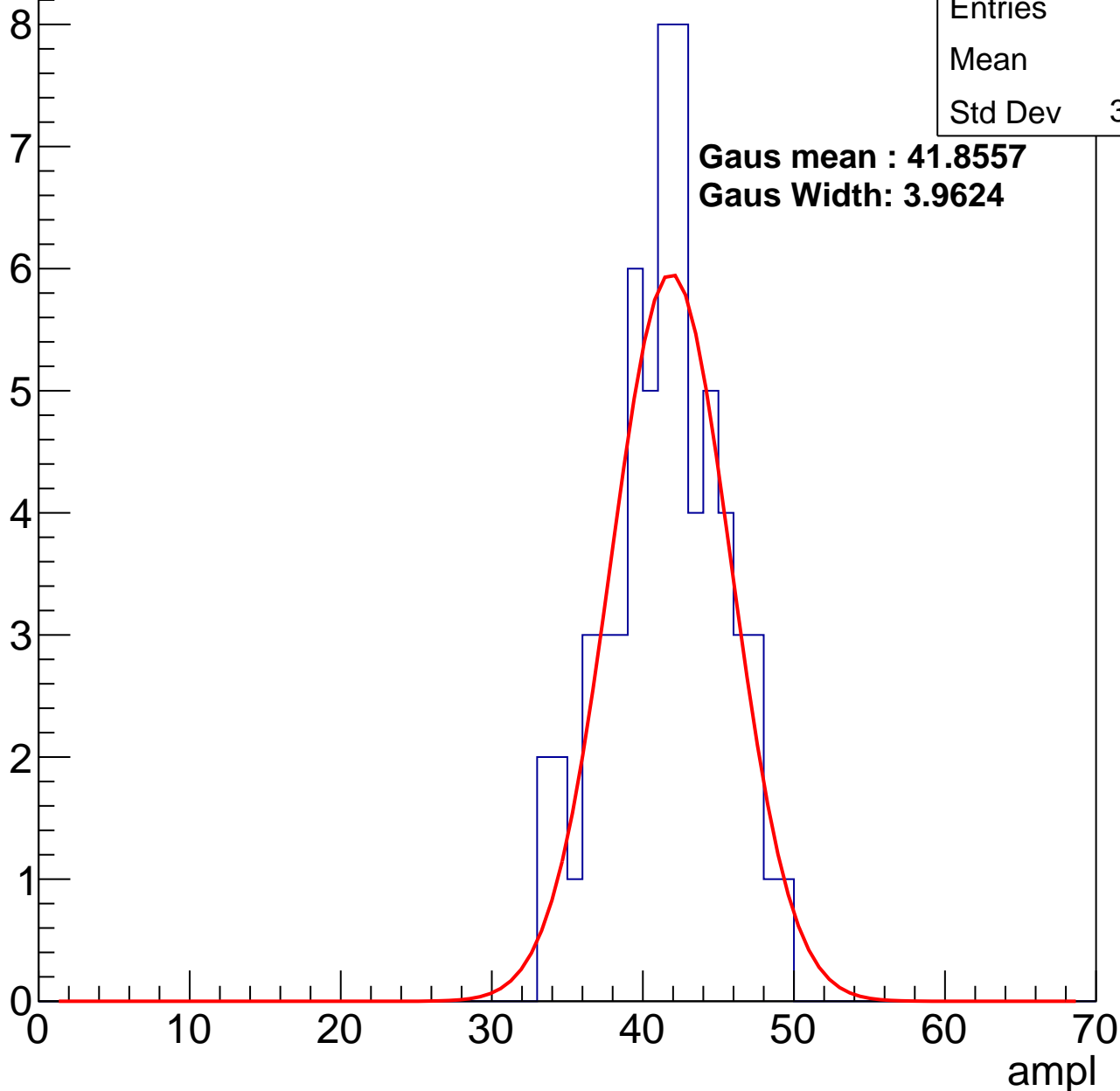
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.1
Std Dev	3.745

Gaus mean : 41.8557

Gaus Width: 3.9624

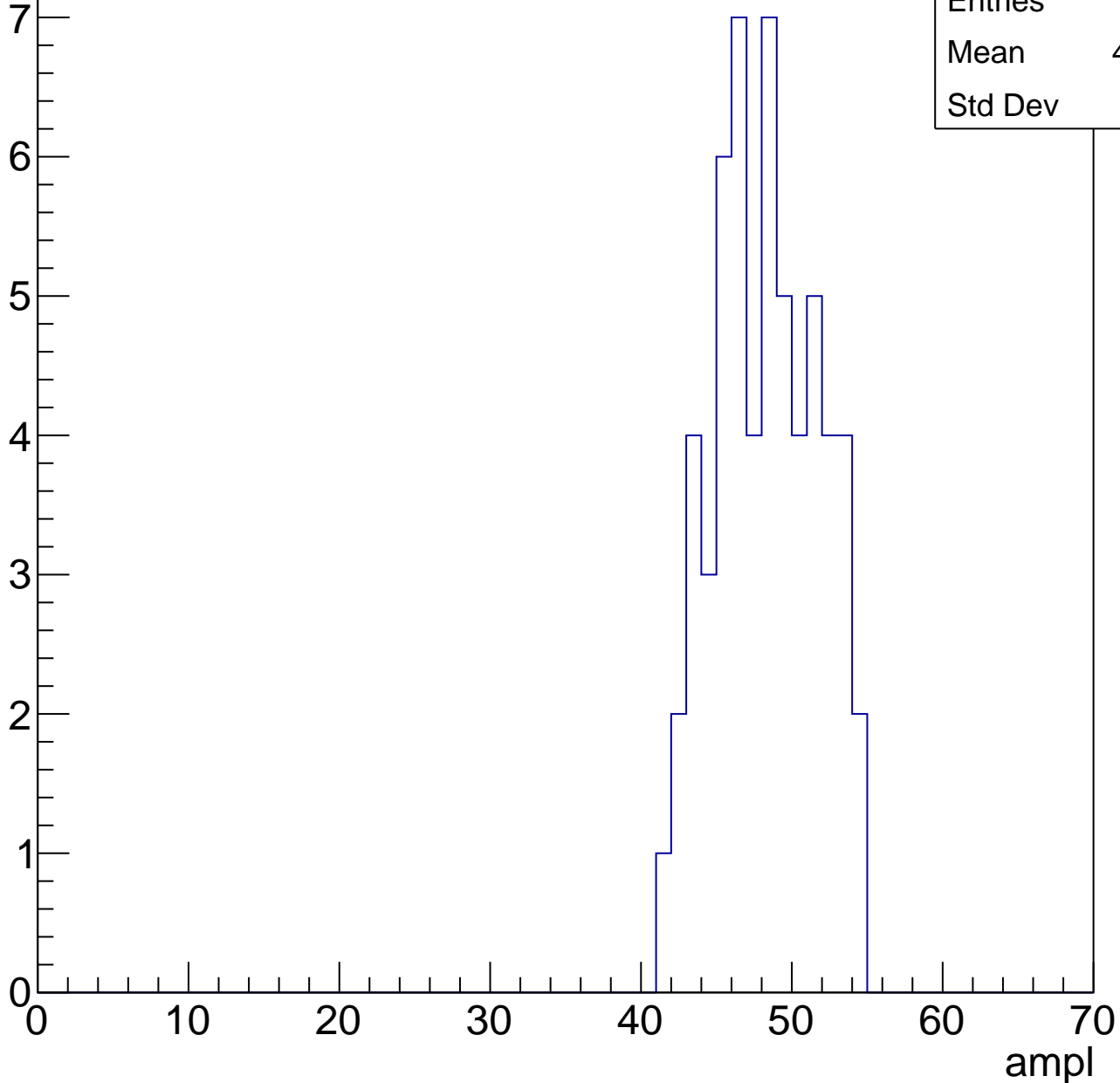


B1L103S, U14-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	47.81
Std Dev	3.36

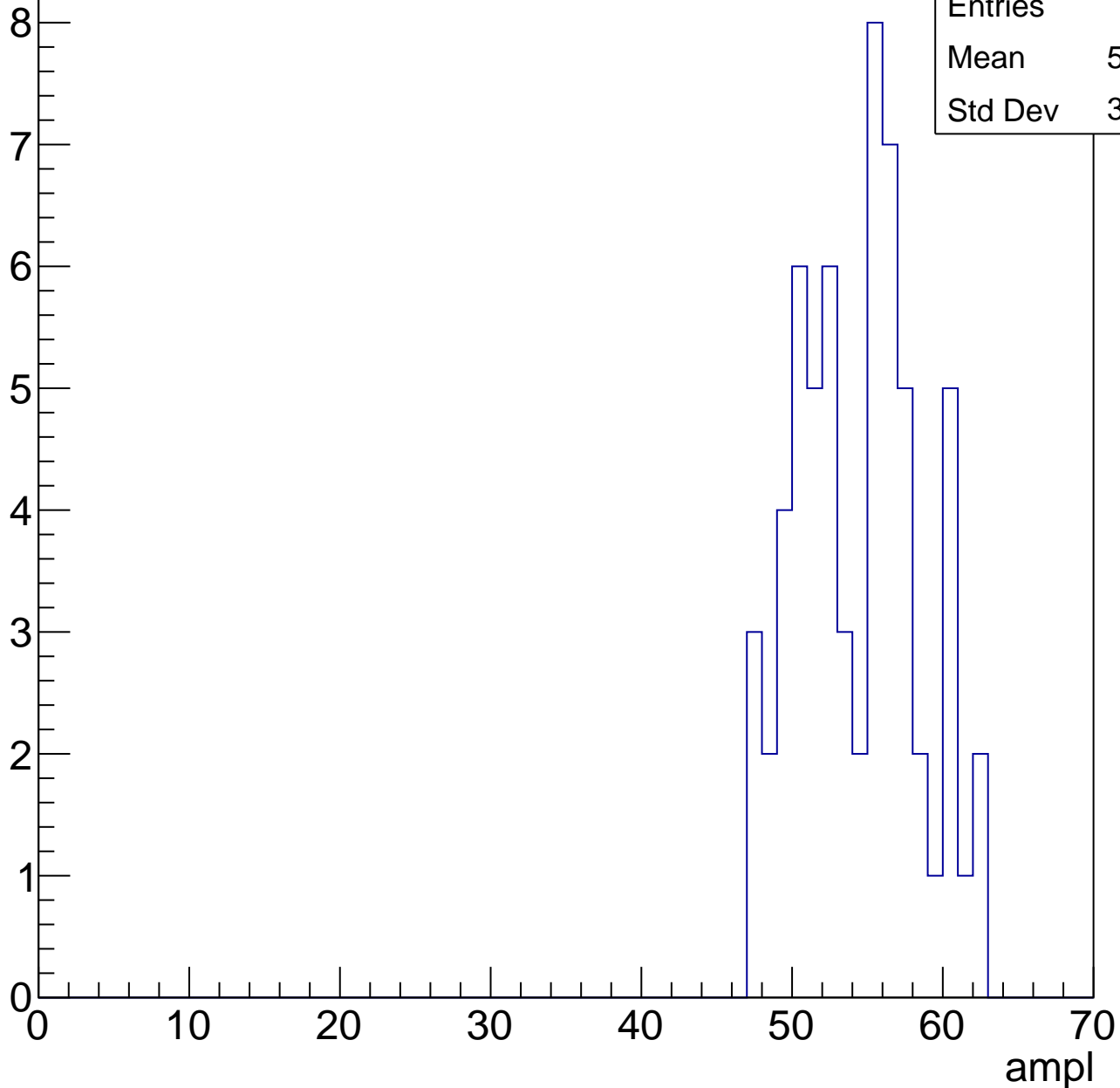


B1L103S, U14-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

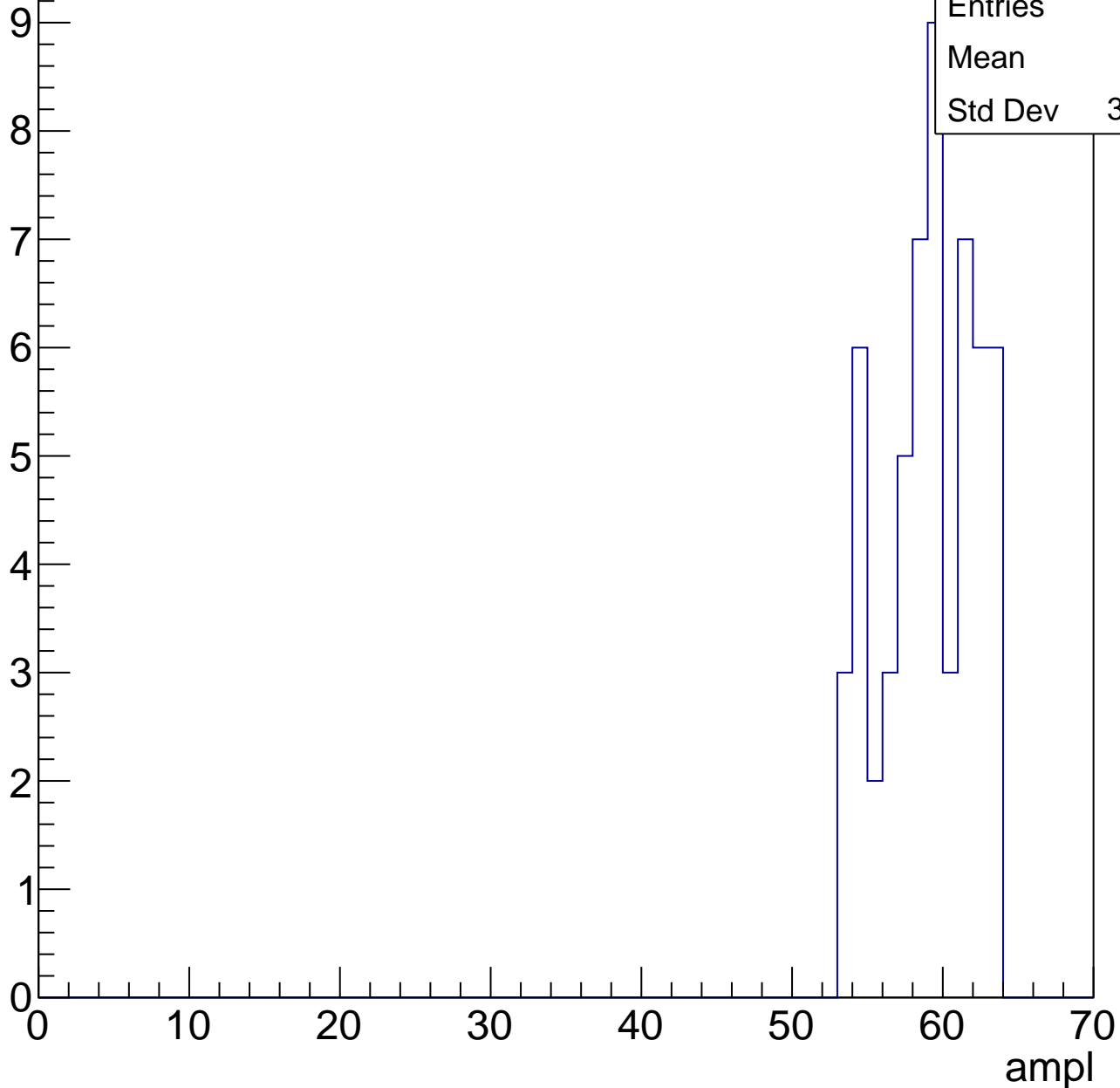
Entries	62
Mean	53.94
Std Dev	3.979



B1L103S, U14-ch117, adc5

calib_packv5_042523_0143.root, FC#7, port C2

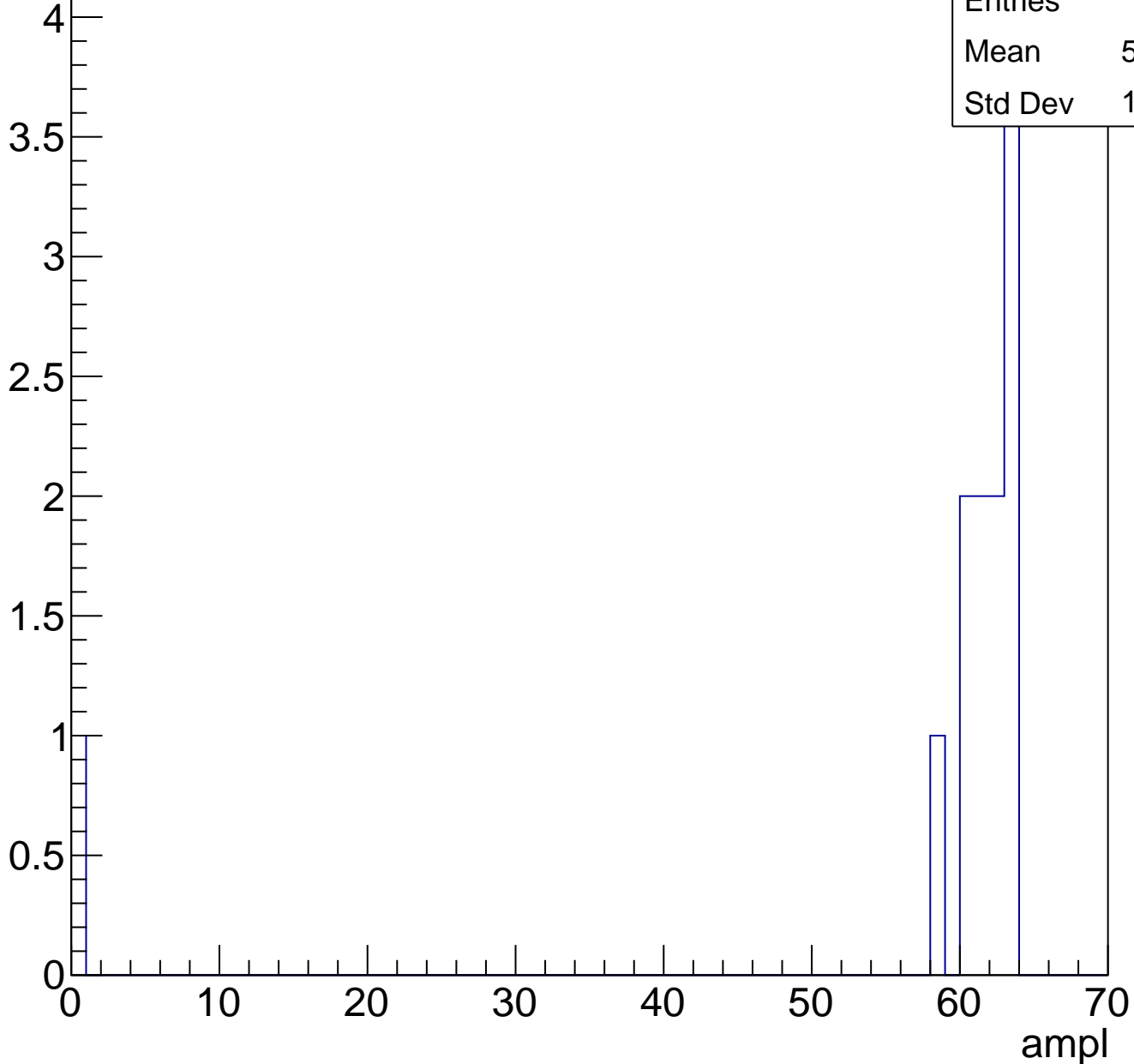
Entry



B1L103S, U14-ch117, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U14-ch118, adc0

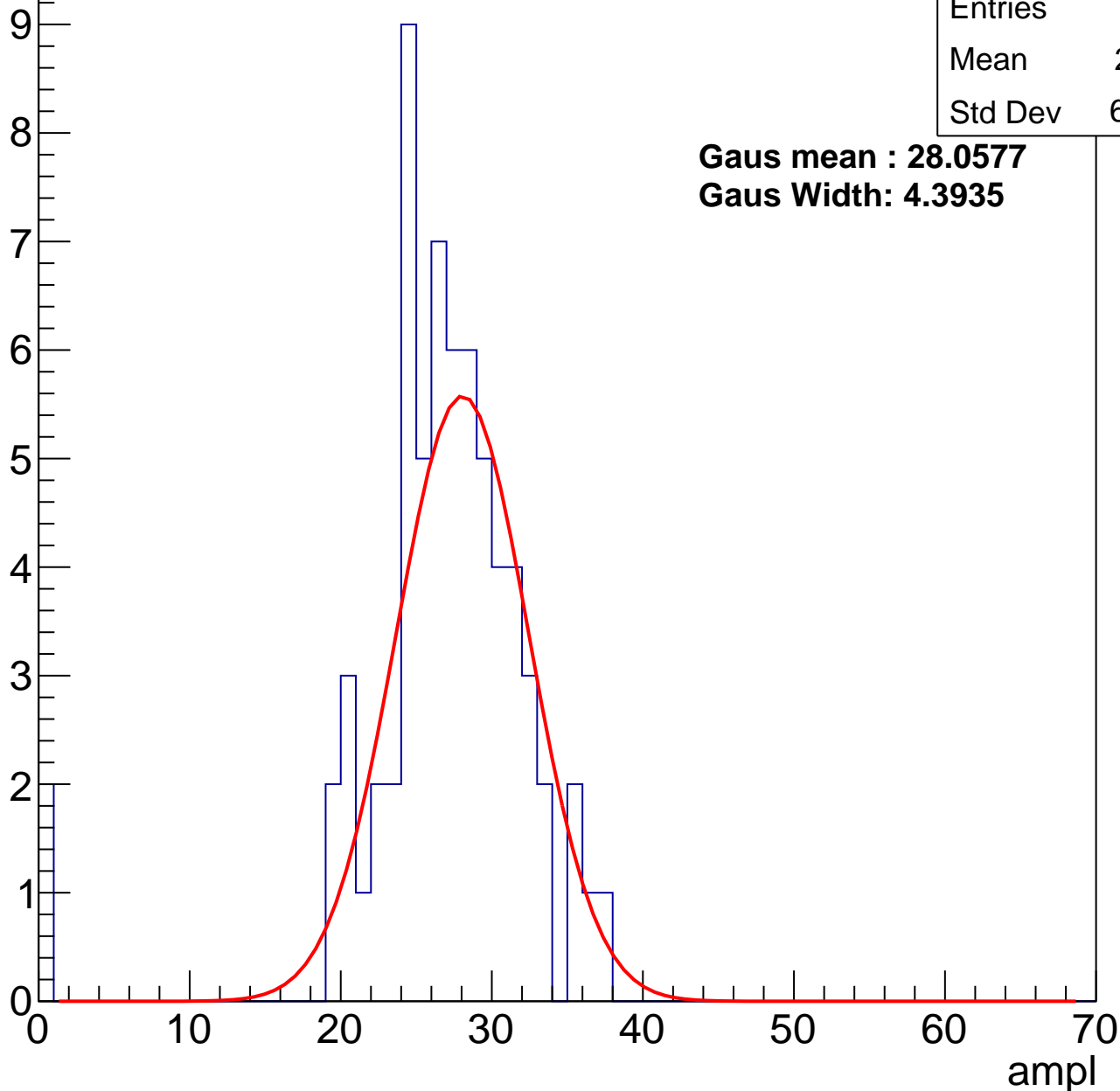
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	26.21
Std Dev	6.107

Gaus mean : 28.0577

Gaus Width: 4.3935



B1L103S, U14-ch118, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	34.54
Std Dev	3.978

Gaus mean : 34.3201

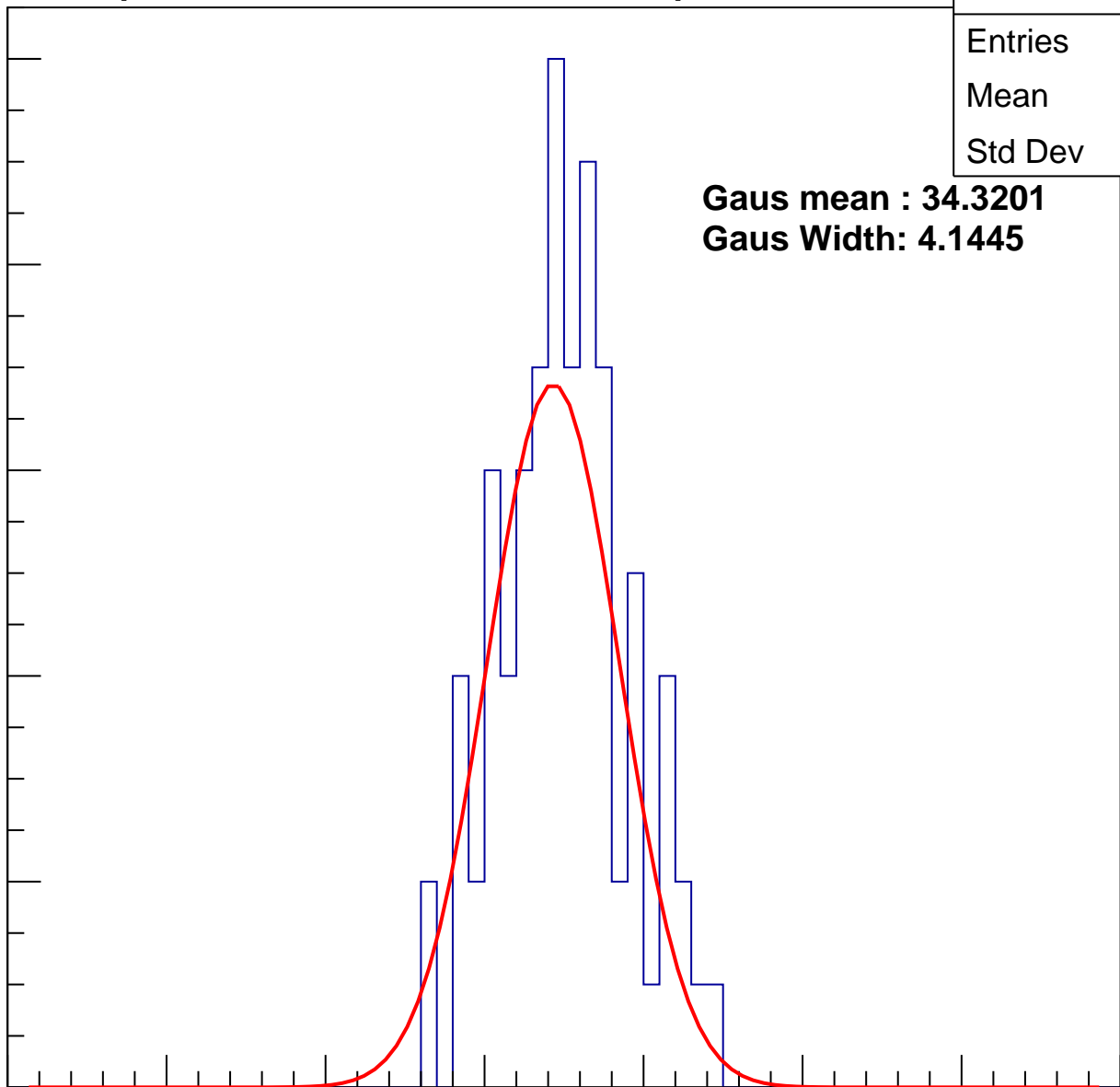
Gaus Width: 4.1445

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch118, adc2

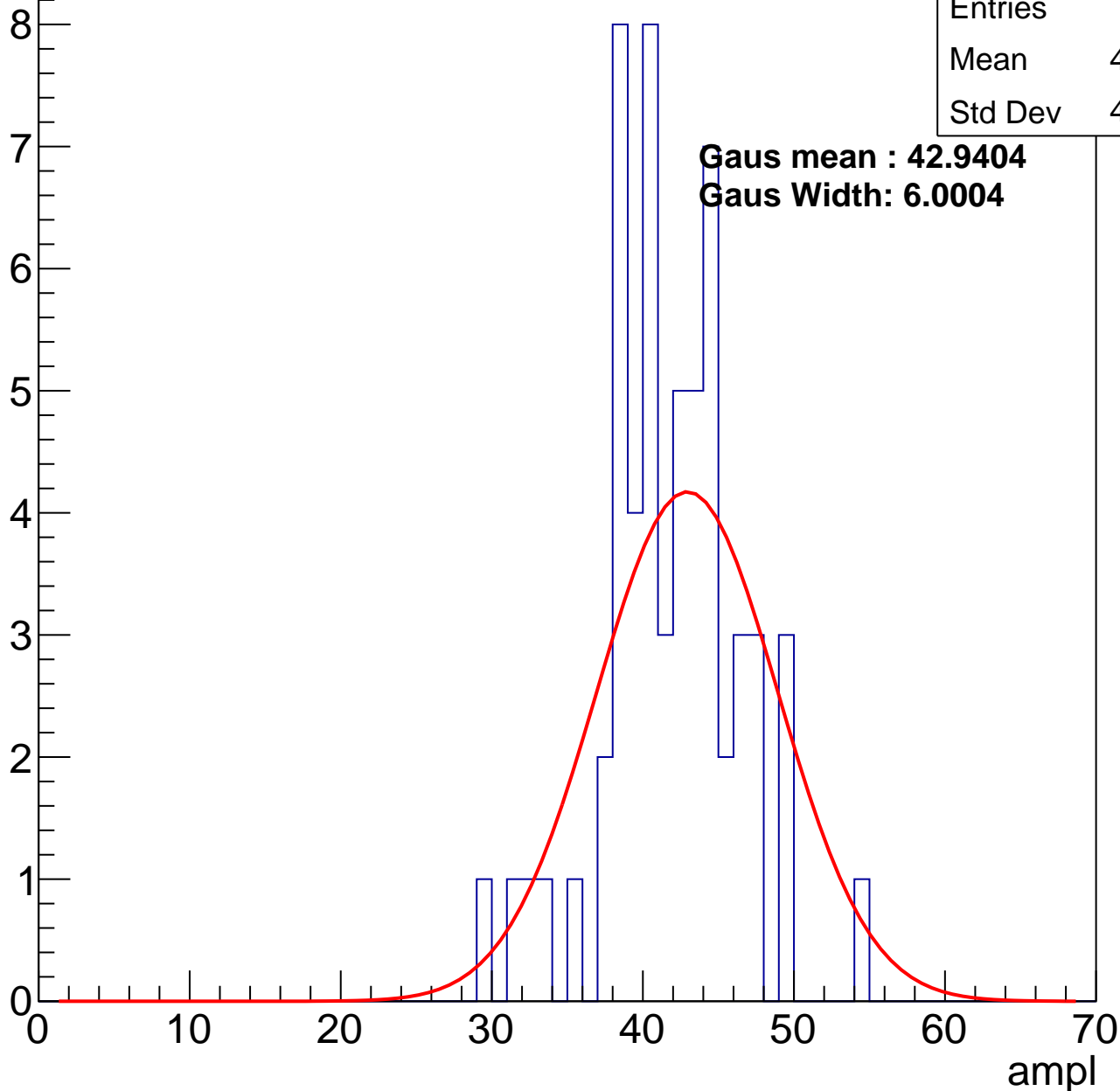
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	41.36
Std Dev	4.528

Gaus mean : 42.9404

Gaus Width: 6.0004

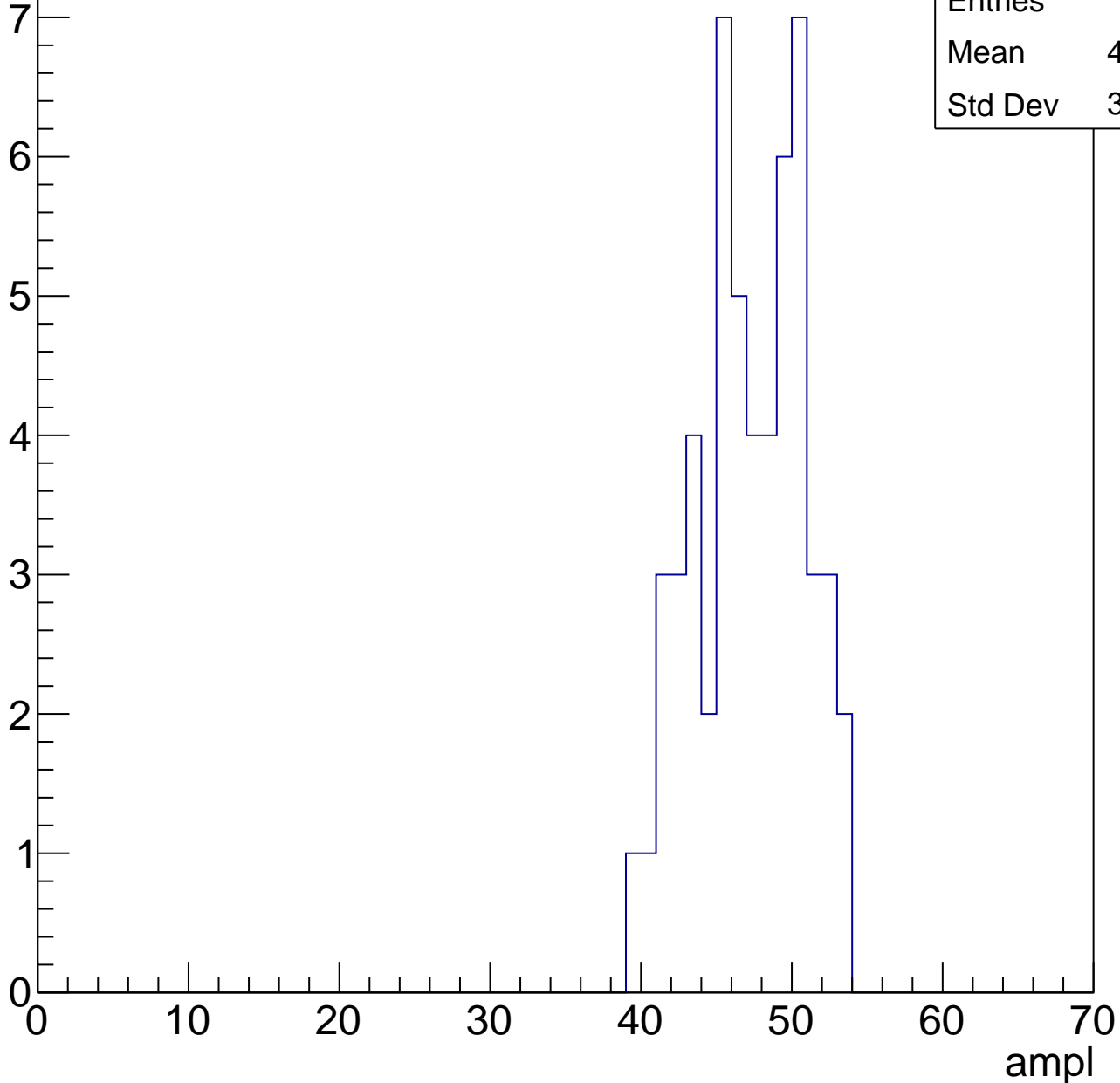


B1L103S, U14-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	46.76
Std Dev	3.552

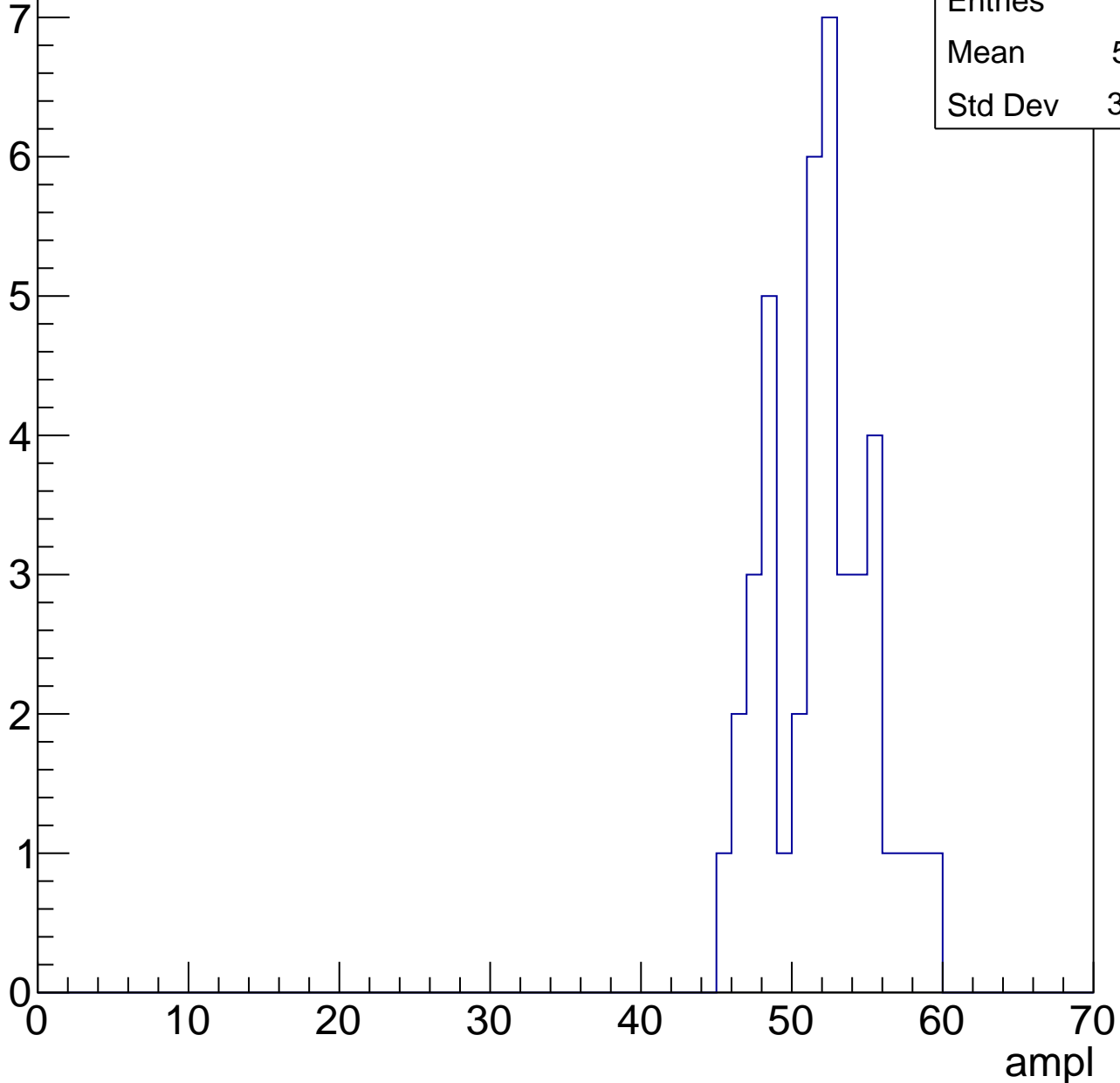


B1L103S, U14-ch118, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

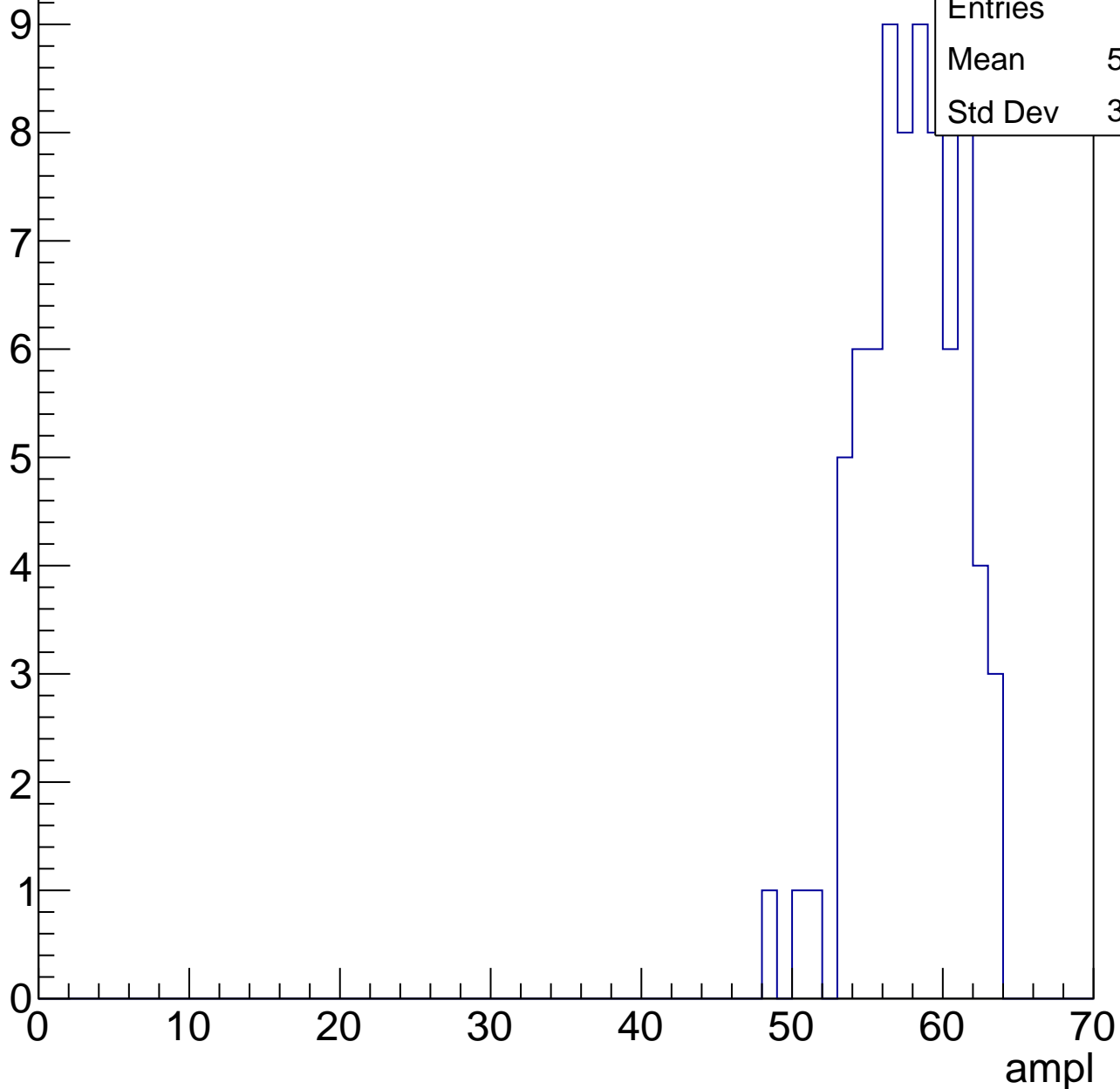
Entries	41
Mean	51.41
Std Dev	3.357



B1L103S, U14-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

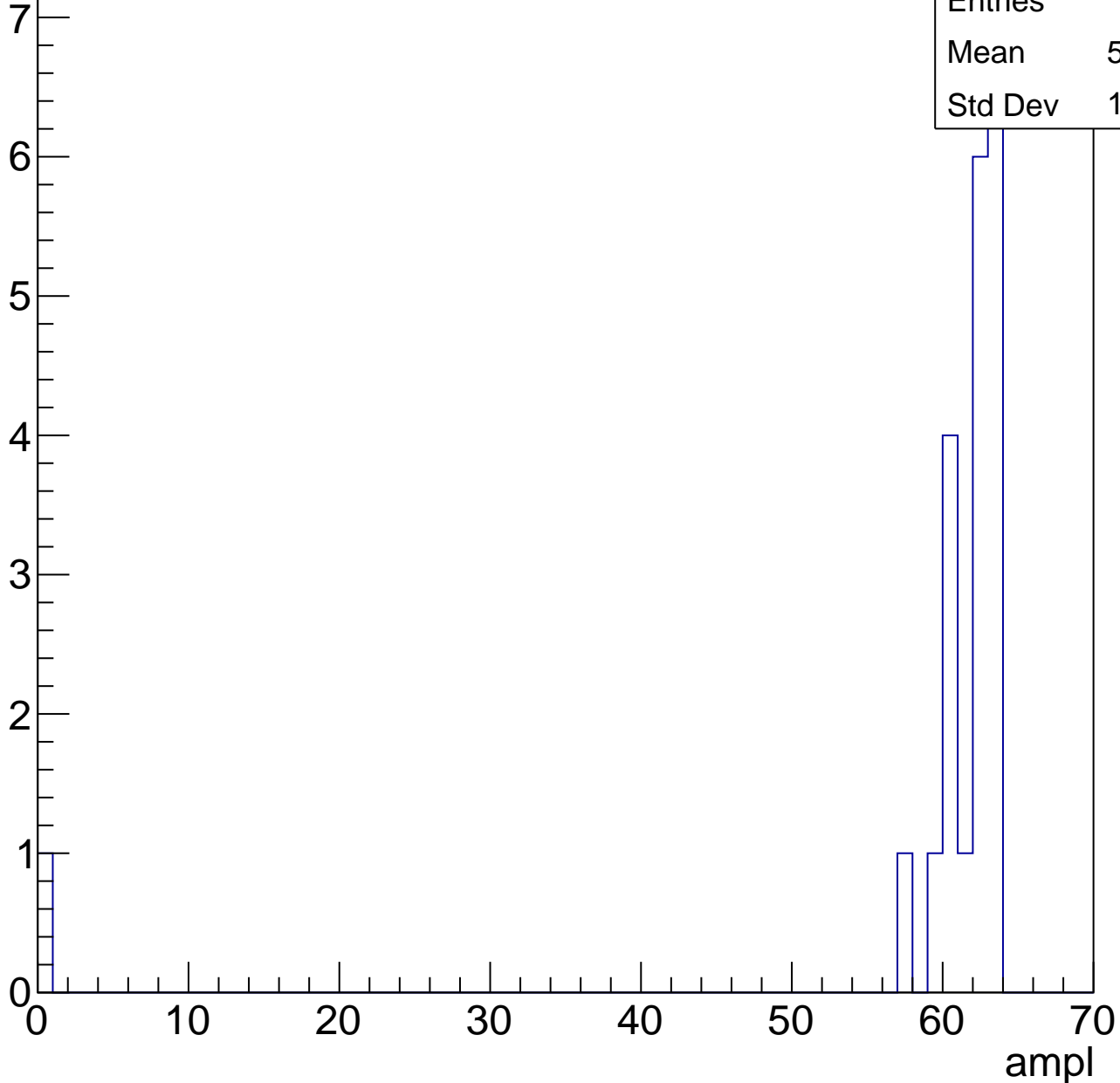


B1L103S, U14-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	58.57
Std Dev	13.19



B1L103S, U14-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U14-ch119, adc0

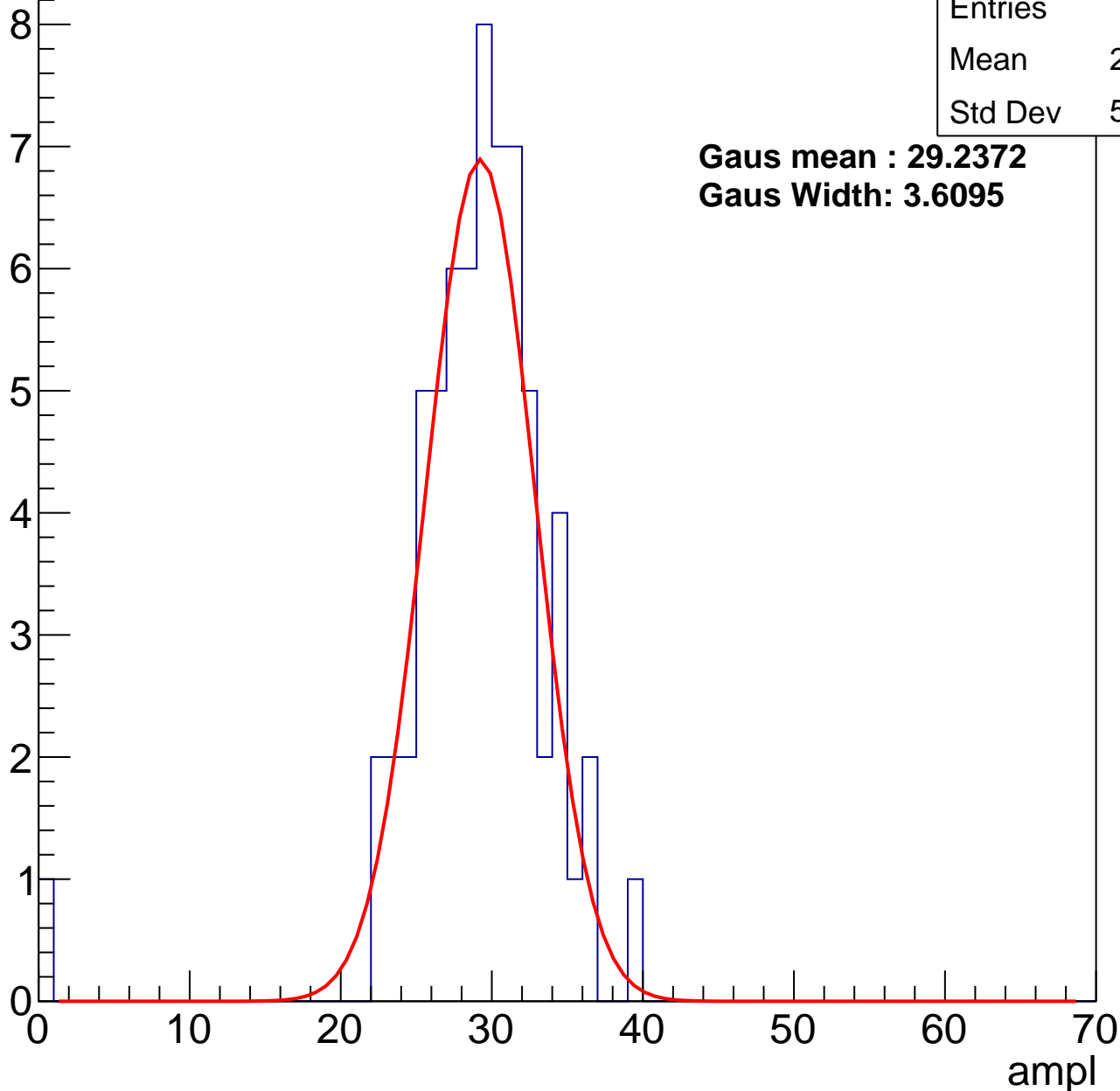
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.64
Std Dev	5.005

Gaus mean : 29.2372

Gaus Width: 3.6095



B1L103S, U14-ch119, adc1

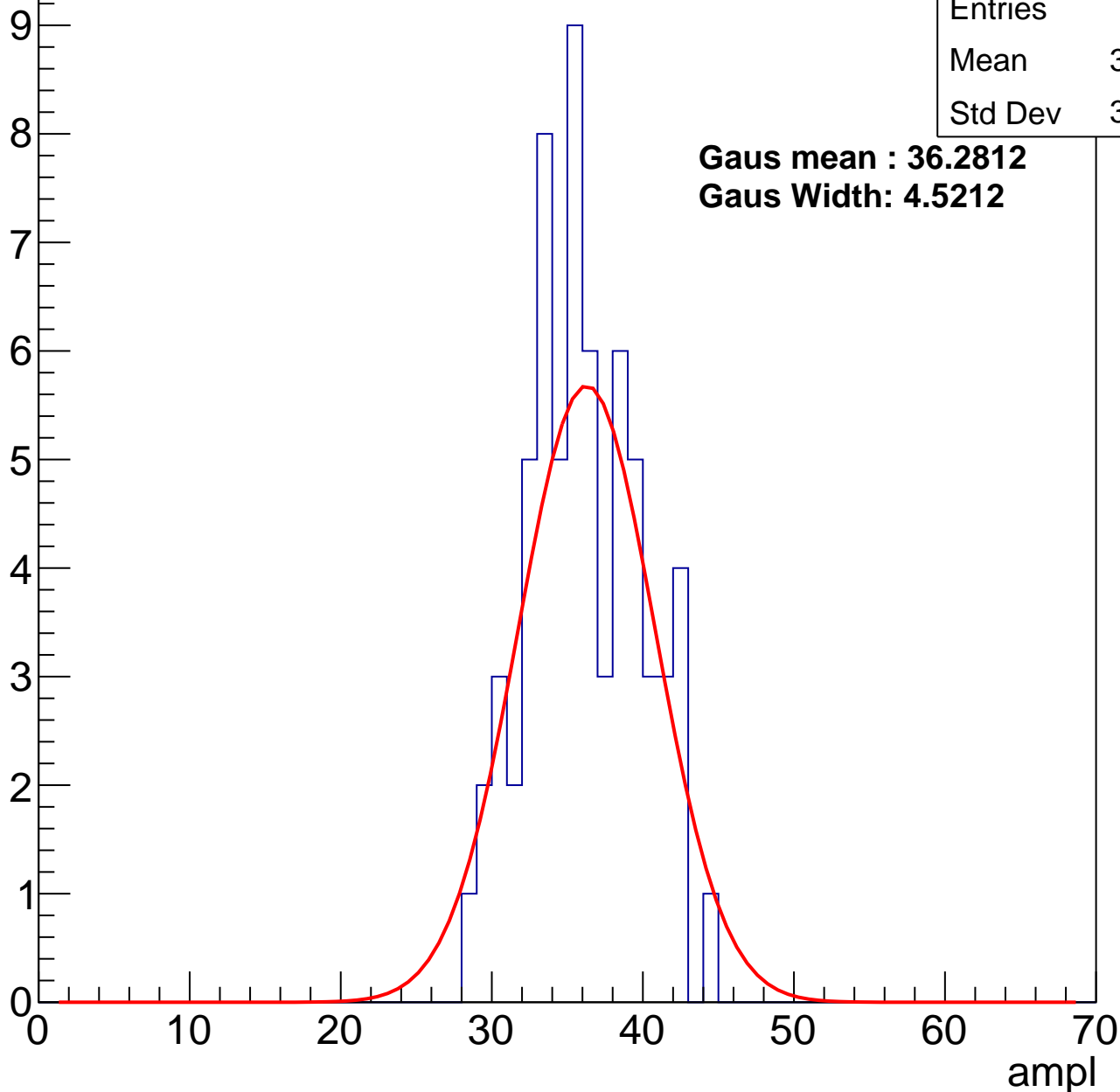
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	35.64
Std Dev	3.683

Gaus mean : 36.2812

Gaus Width: 4.5212

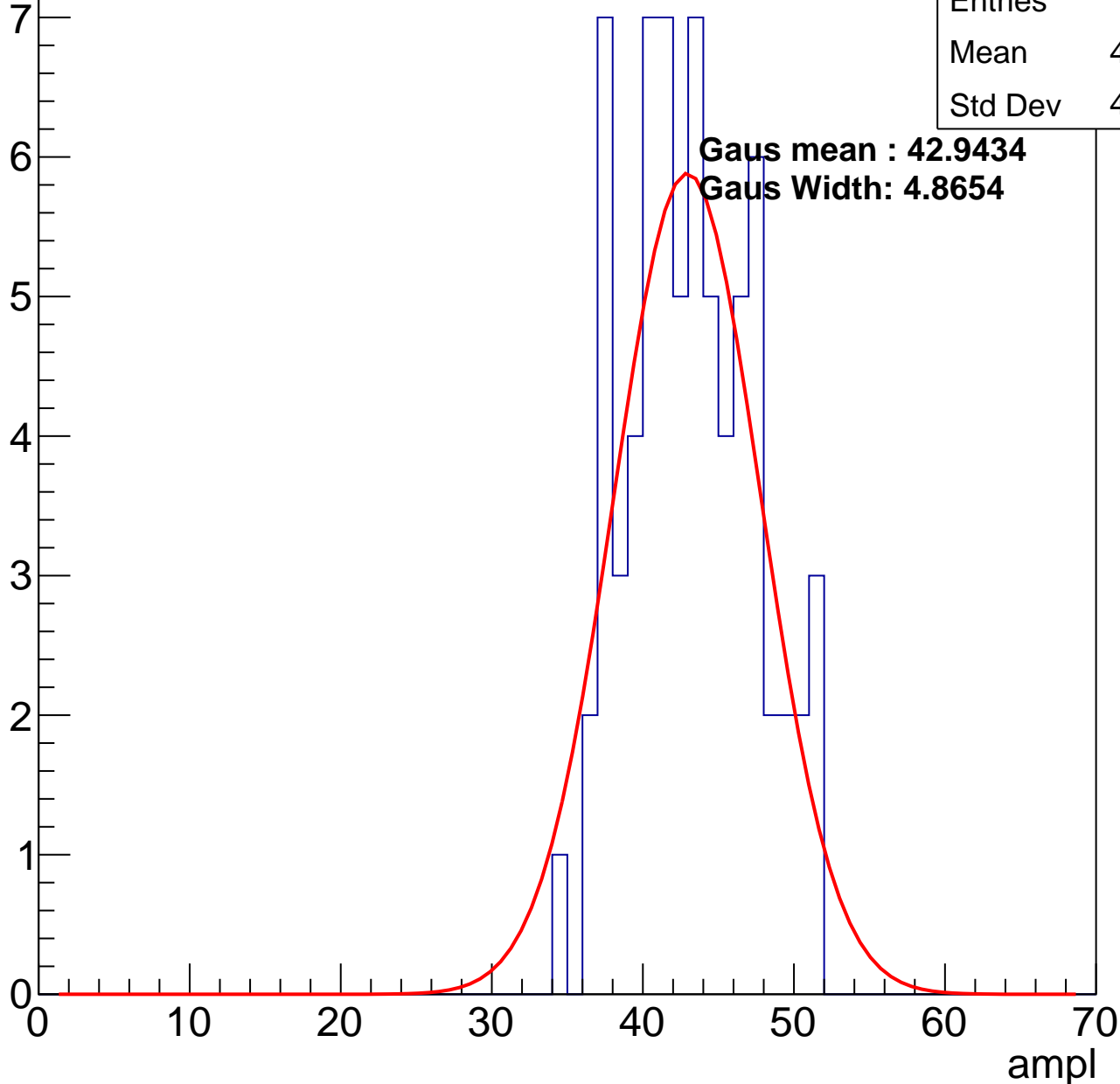


B1L103S, U14-ch119, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.67
Std Dev	4.137

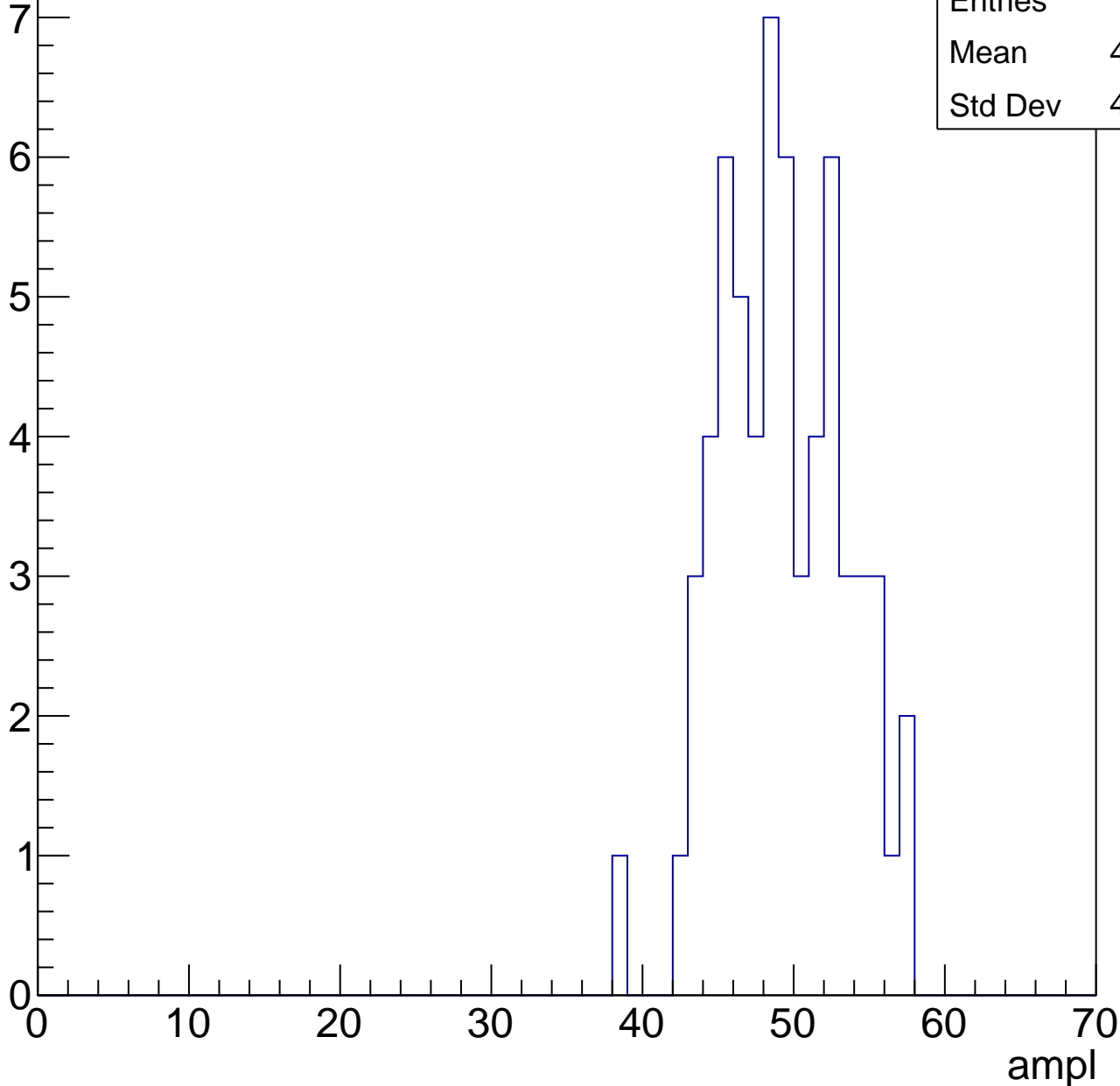


B1L103S, U14-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	48.79
Std Dev	4.057

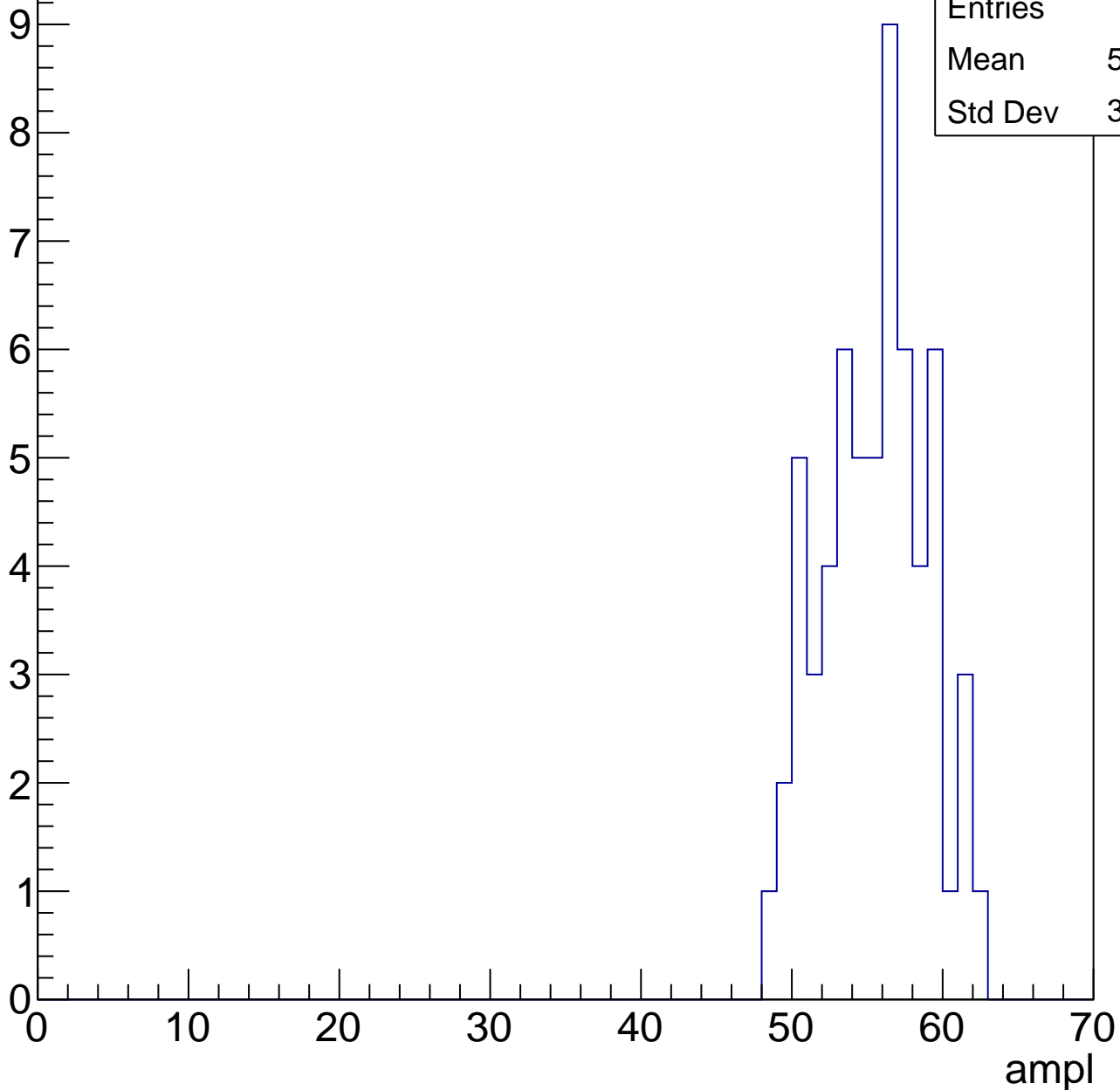


B1L103S, U14-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.03
Std Dev	3.402

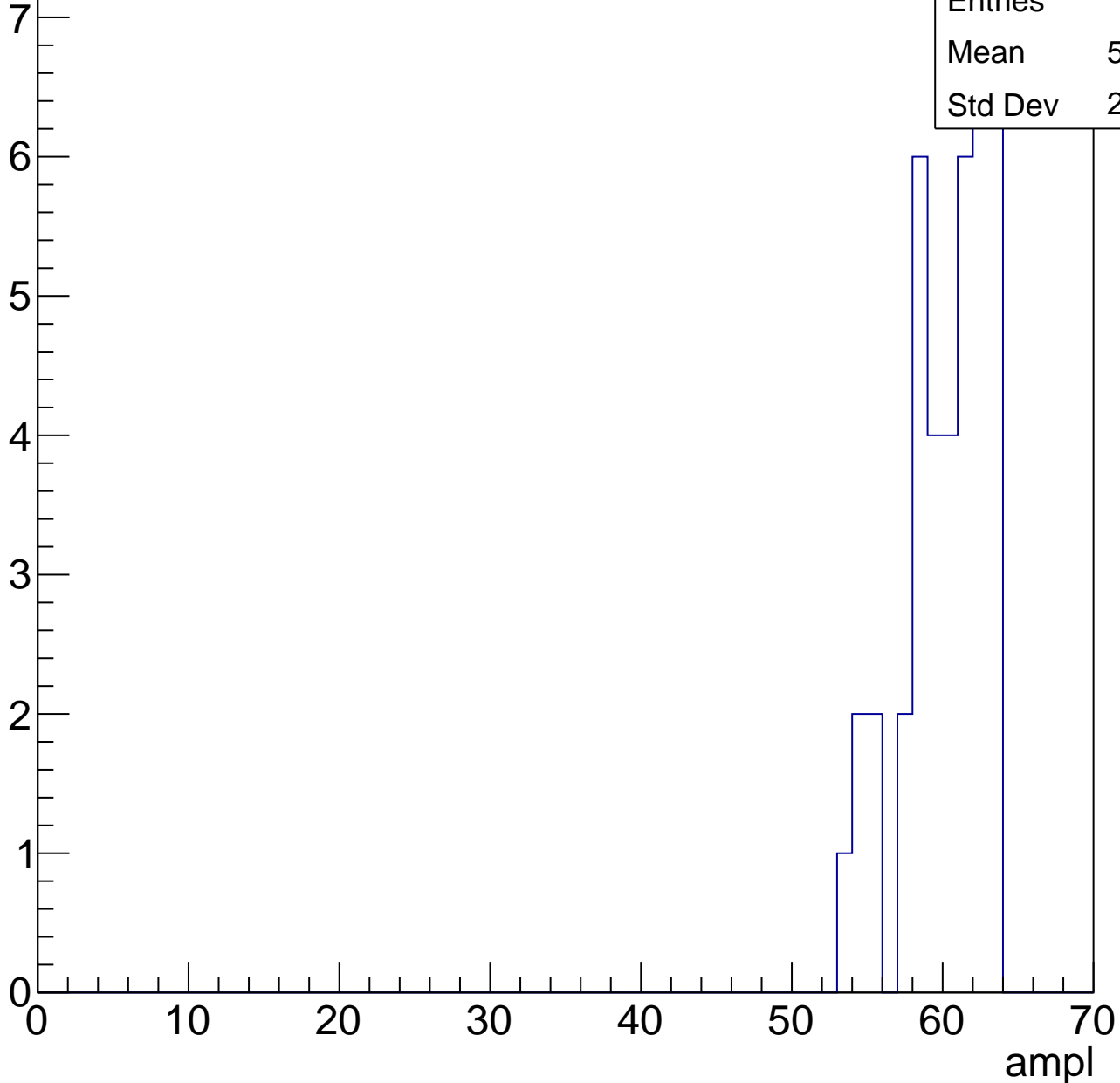


B1L103S, U14-ch119, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	59.76
Std Dev	2.757

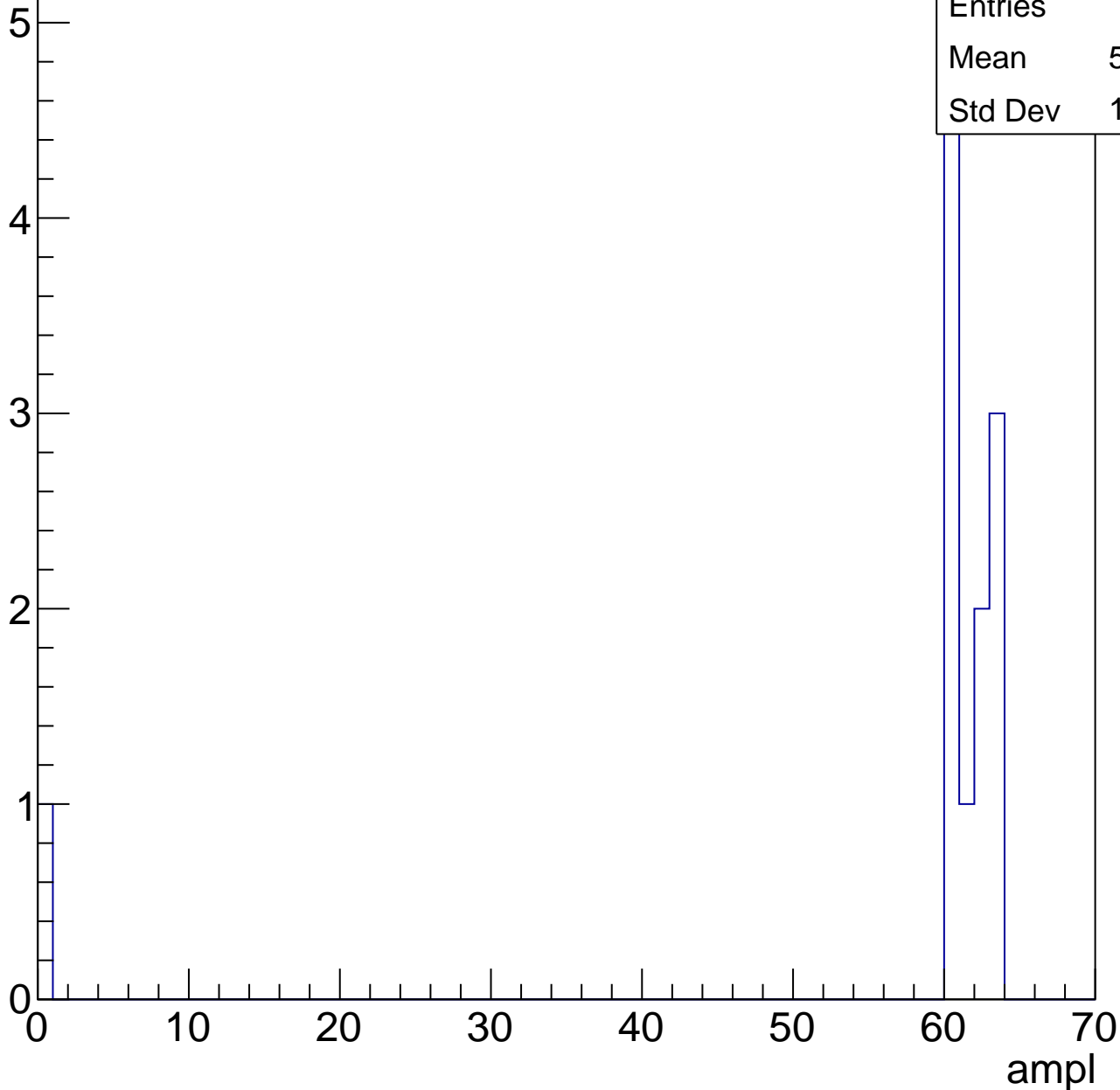


B1L103S, U14-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	56.17
Std Dev	16.98



B1L103S, U14-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch120, adc0

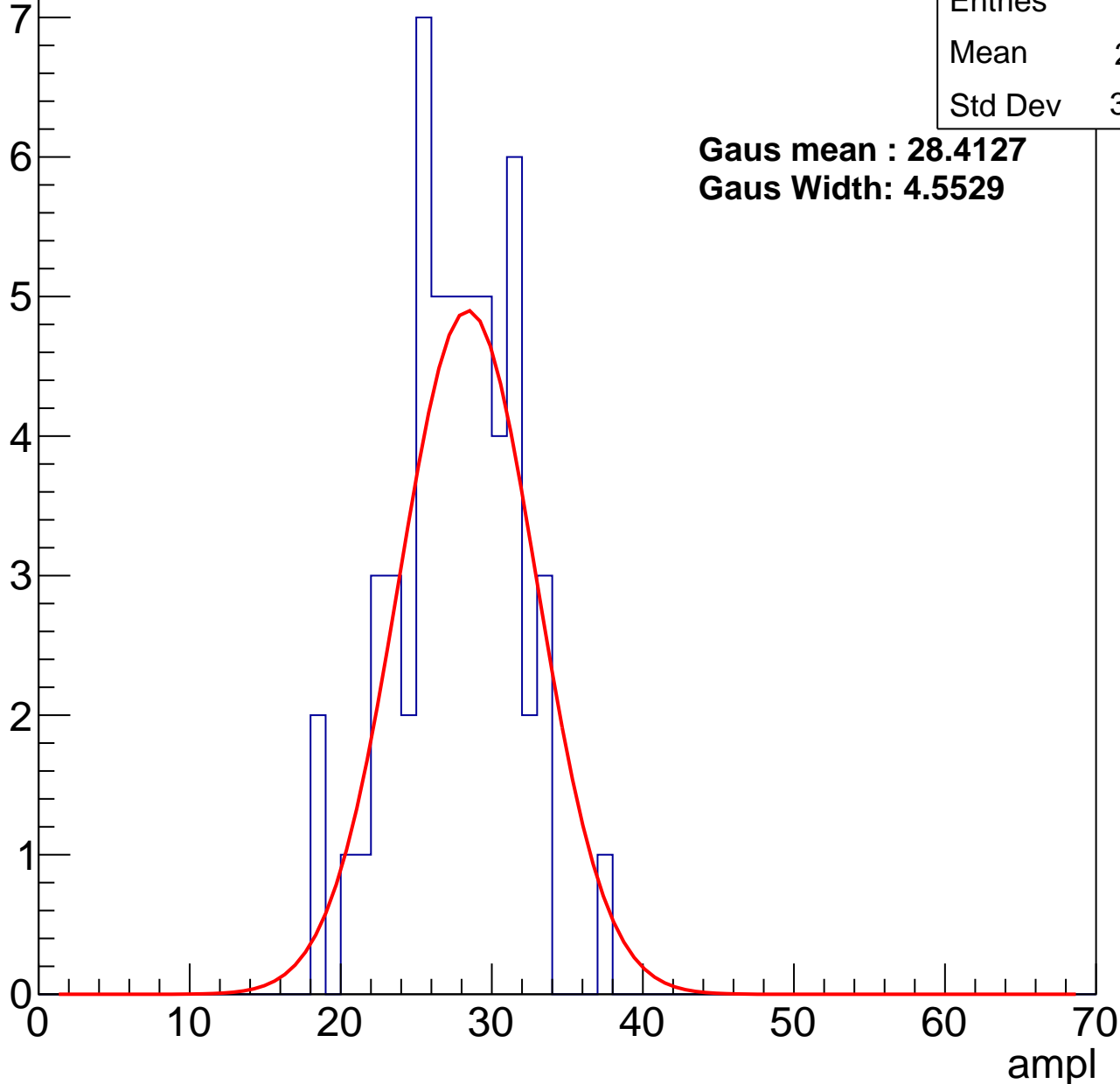
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	27.11
Std Dev	3.897

Gaus mean : 28.4127

Gaus Width: 4.5529



B1L103S, U14-ch120, adc1

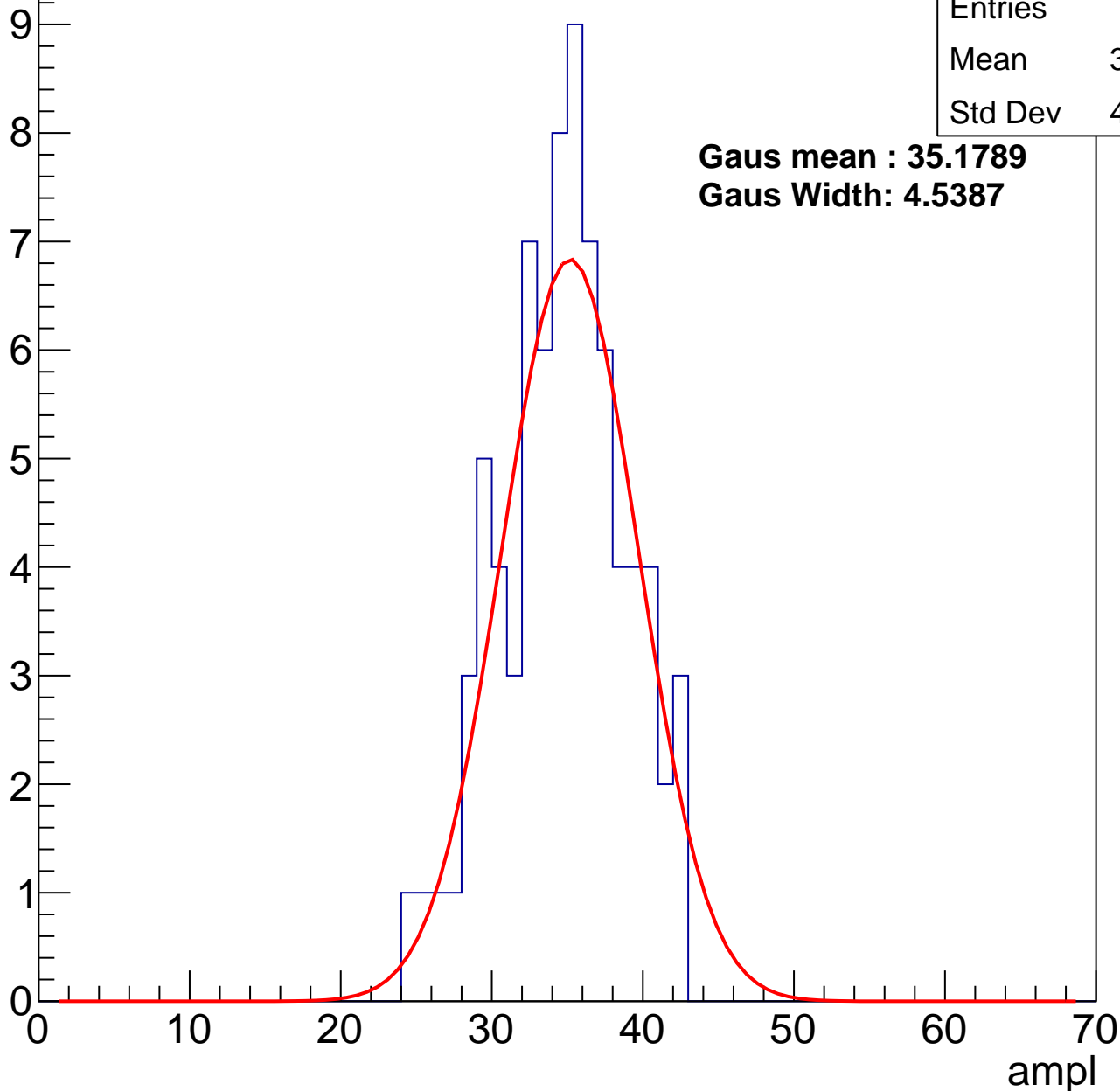
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	34.22
Std Dev	4.114

Gaus mean : 35.1789

Gaus Width: 4.5387



B1L103S, U14-ch120, adc2

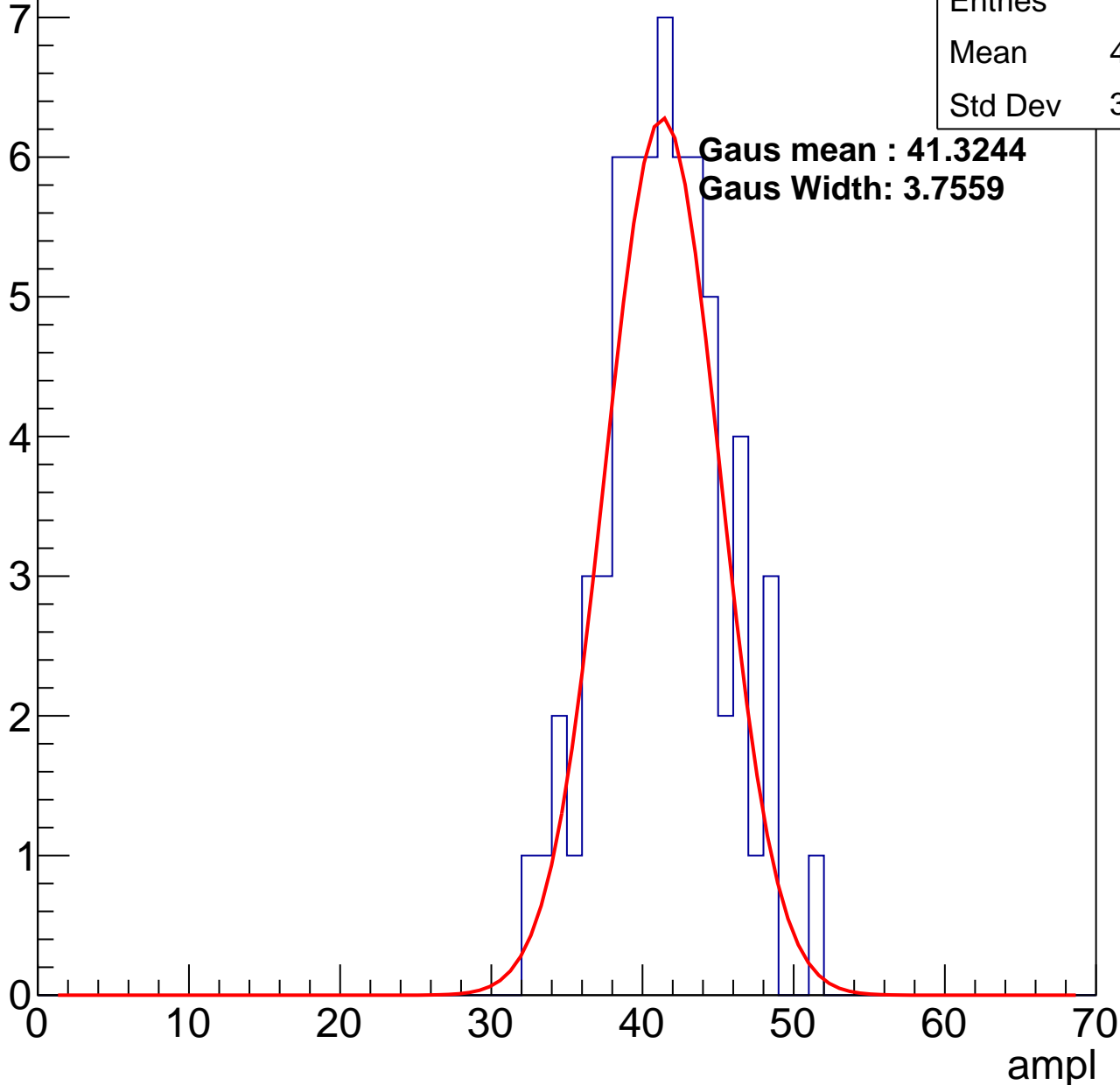
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	40.97
Std Dev	3.905

Gaus mean : 41.3244

Gaus Width: 3.7559

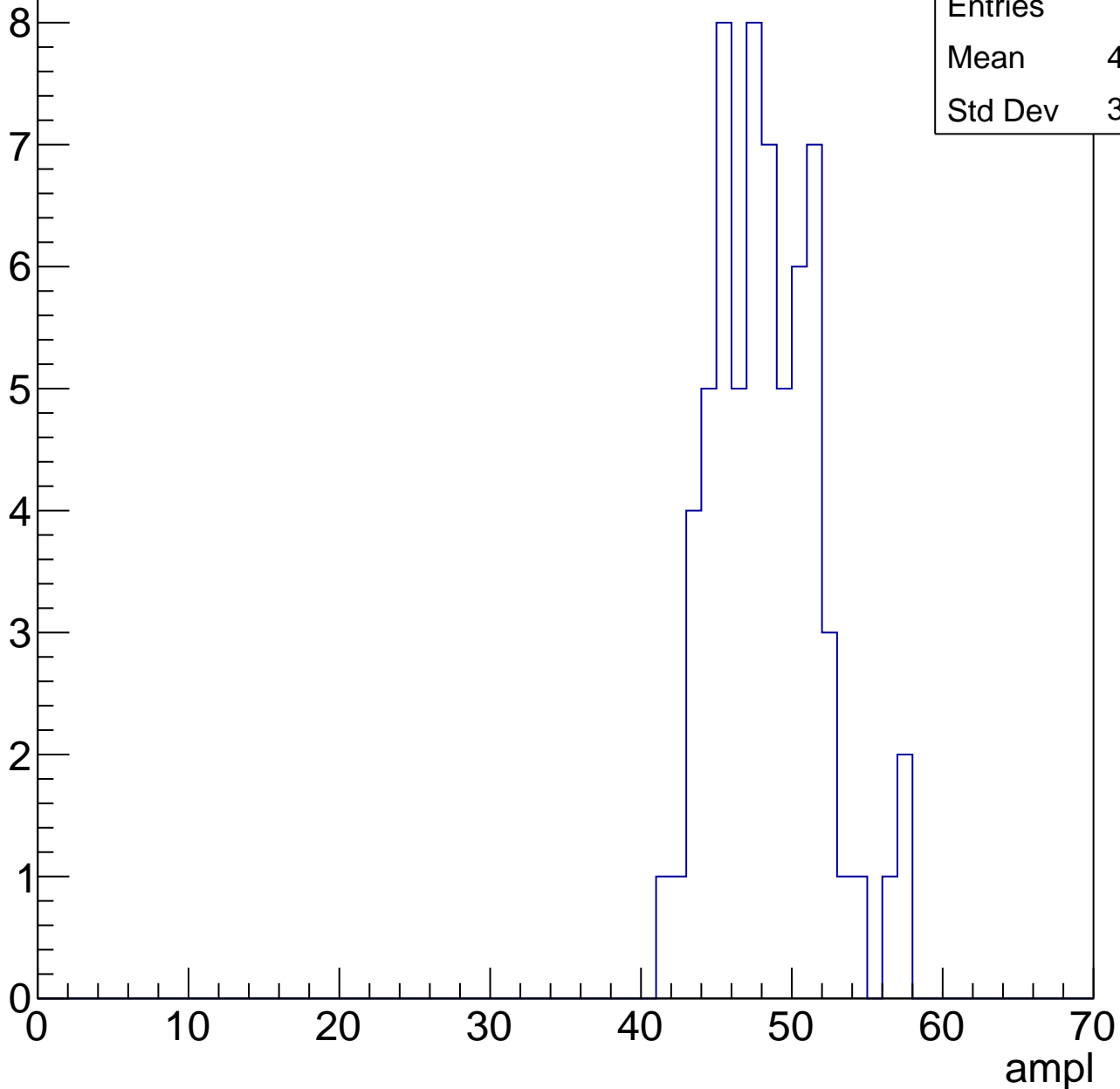


B1L103S, U14-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	47.88
Std Dev	3.488

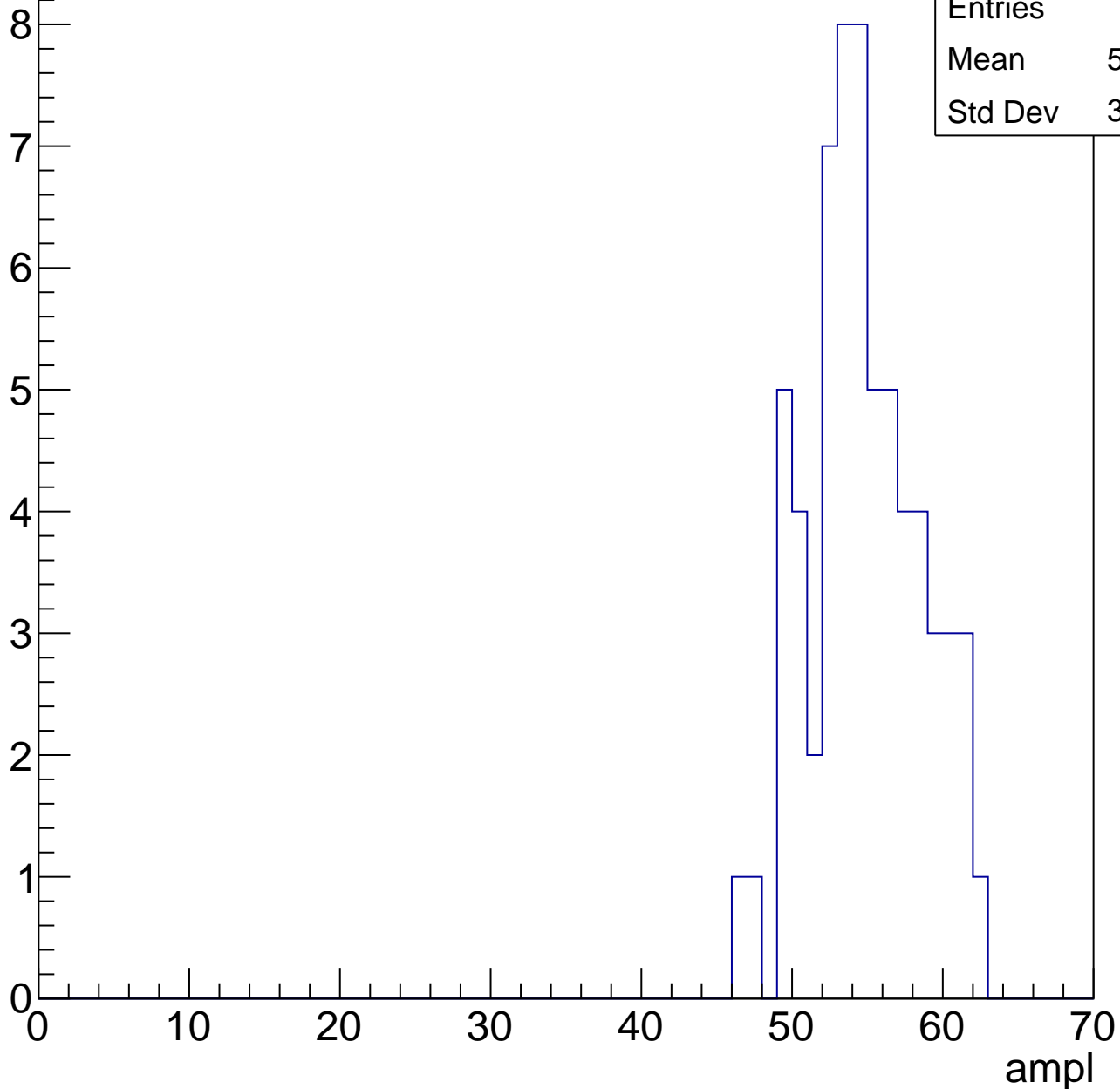


B1L103S, U14-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	54.33
Std Dev	3.683

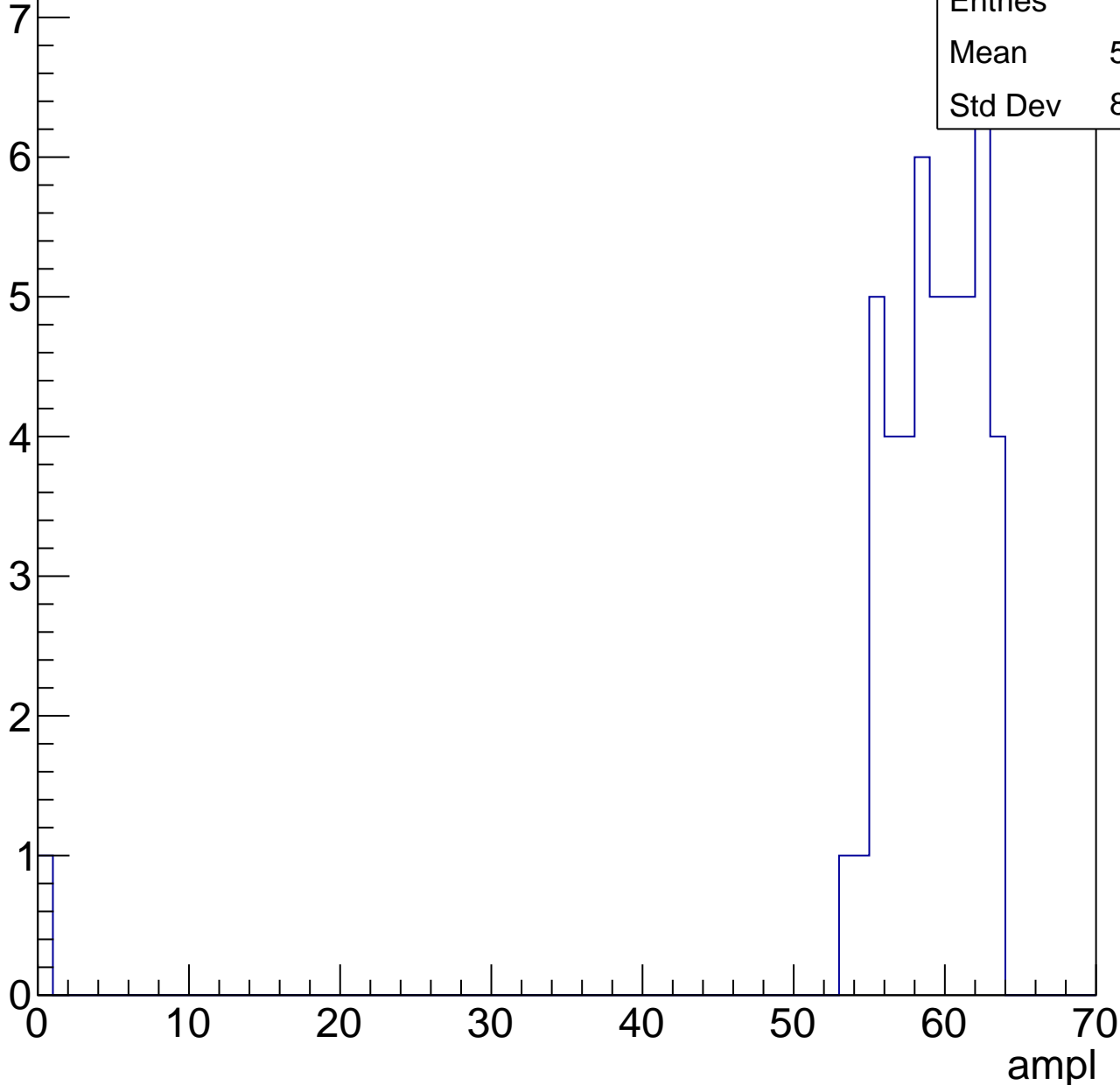


B1L103S, U14-ch120, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	57.67
Std Dev	8.835

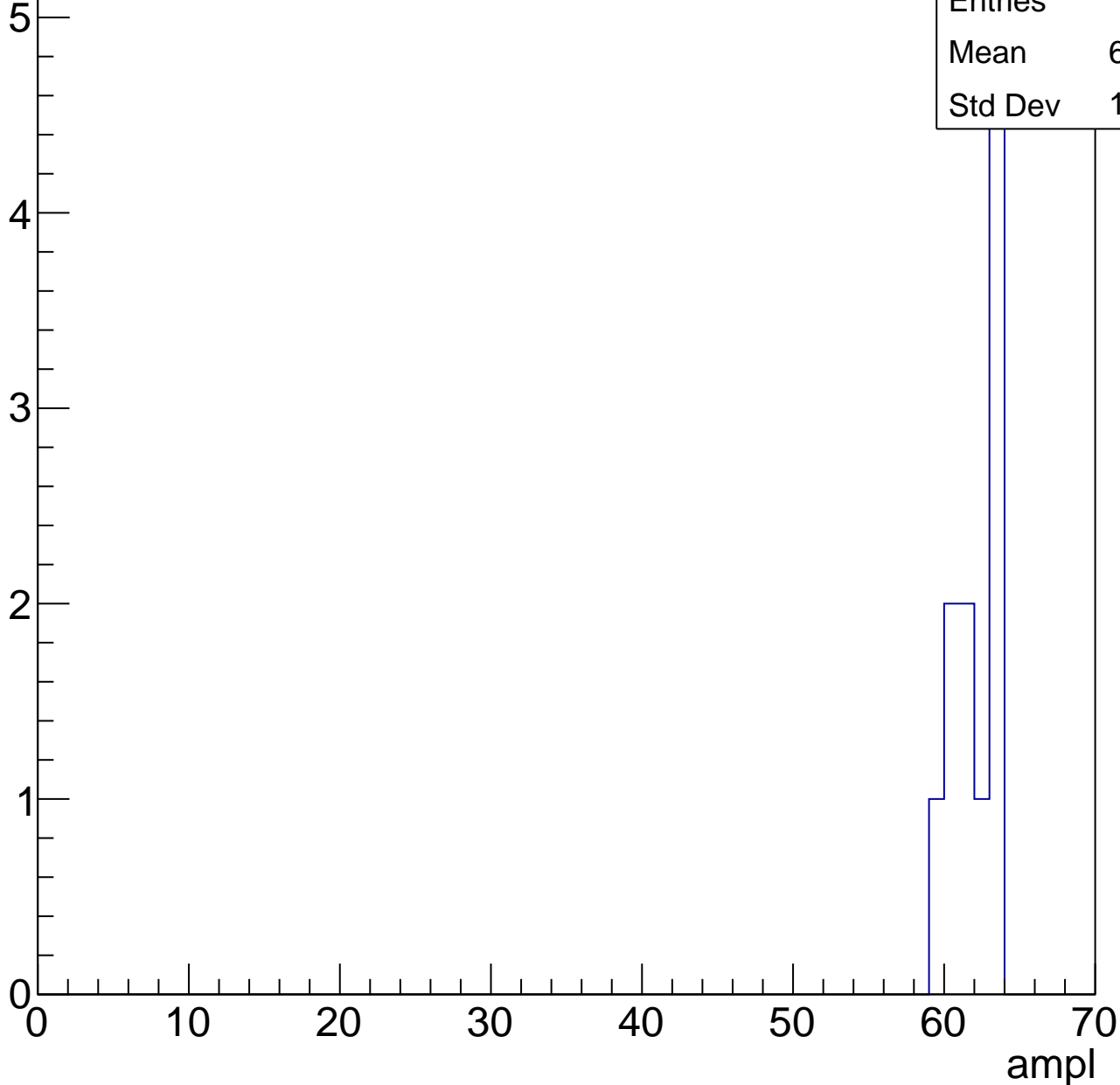


B1L103S, U14-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

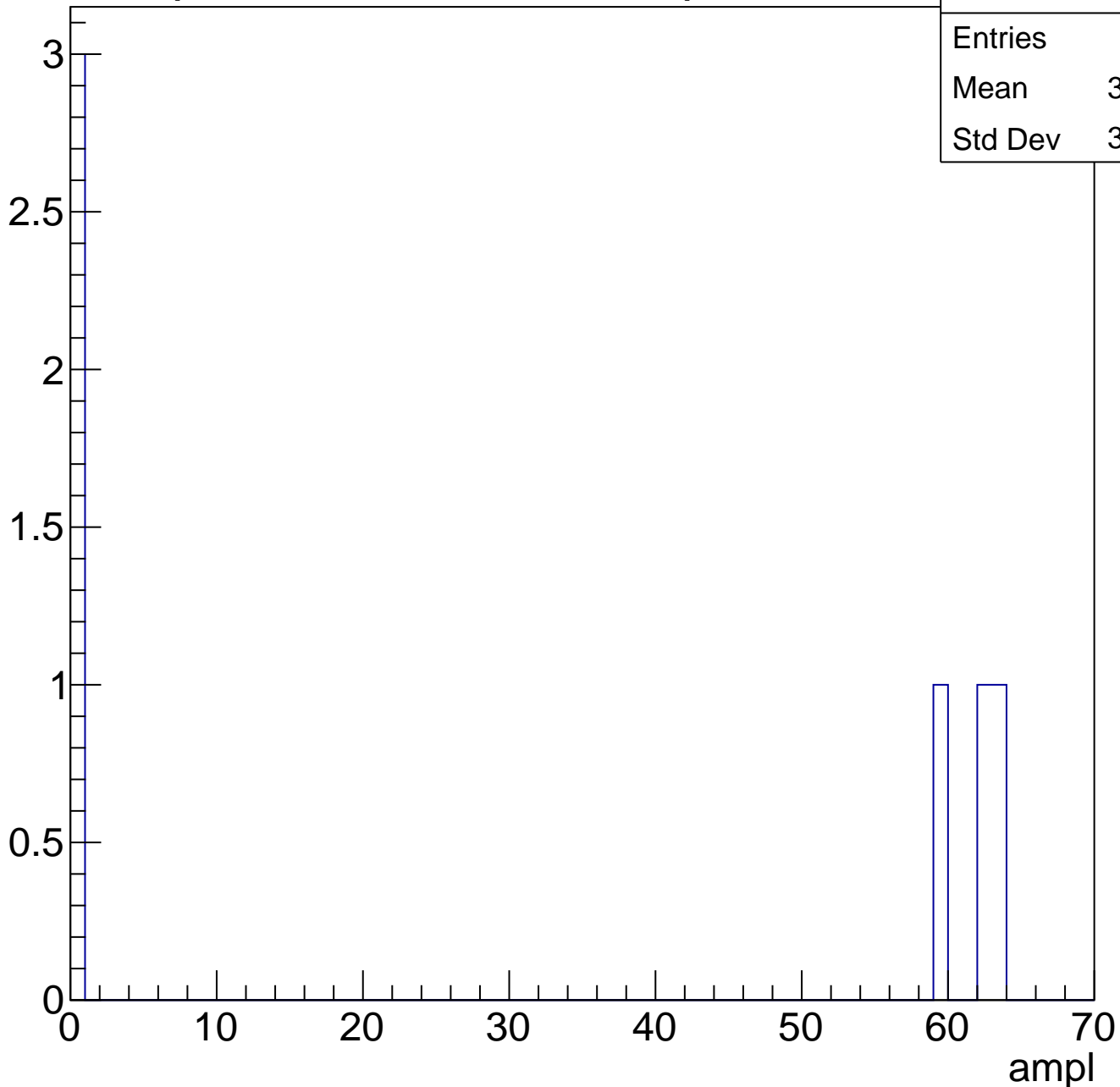
Entries	11
Mean	61.64
Std Dev	1.432



B1L103S, U14-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	30.67
Std Dev	30.69

B1L103S, U14-ch121, adc0

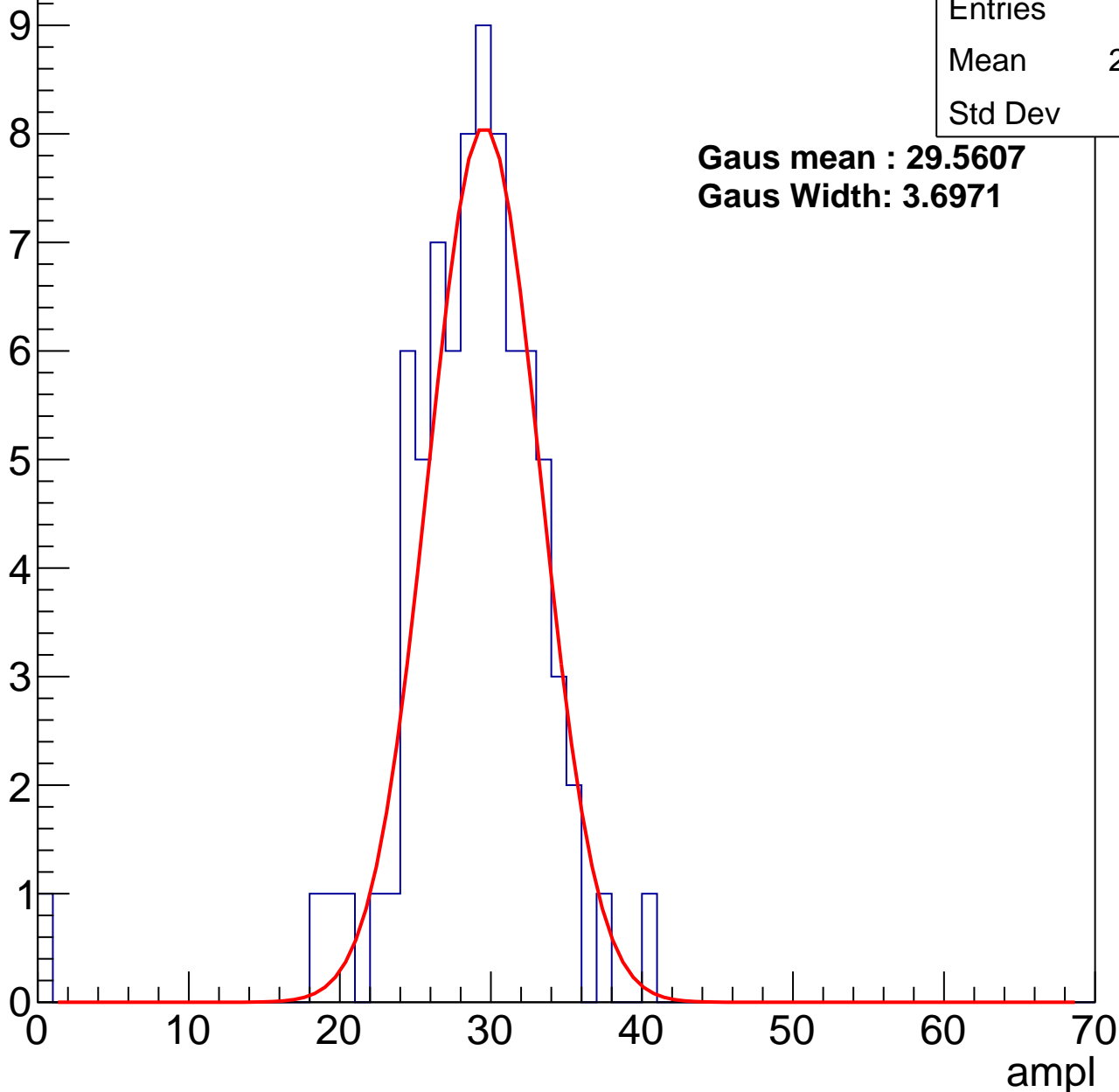
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	28.25
Std Dev	5.04

Gaus mean : 29.5607

Gaus Width: 3.6971



B1L103S, U14-ch121, adc1

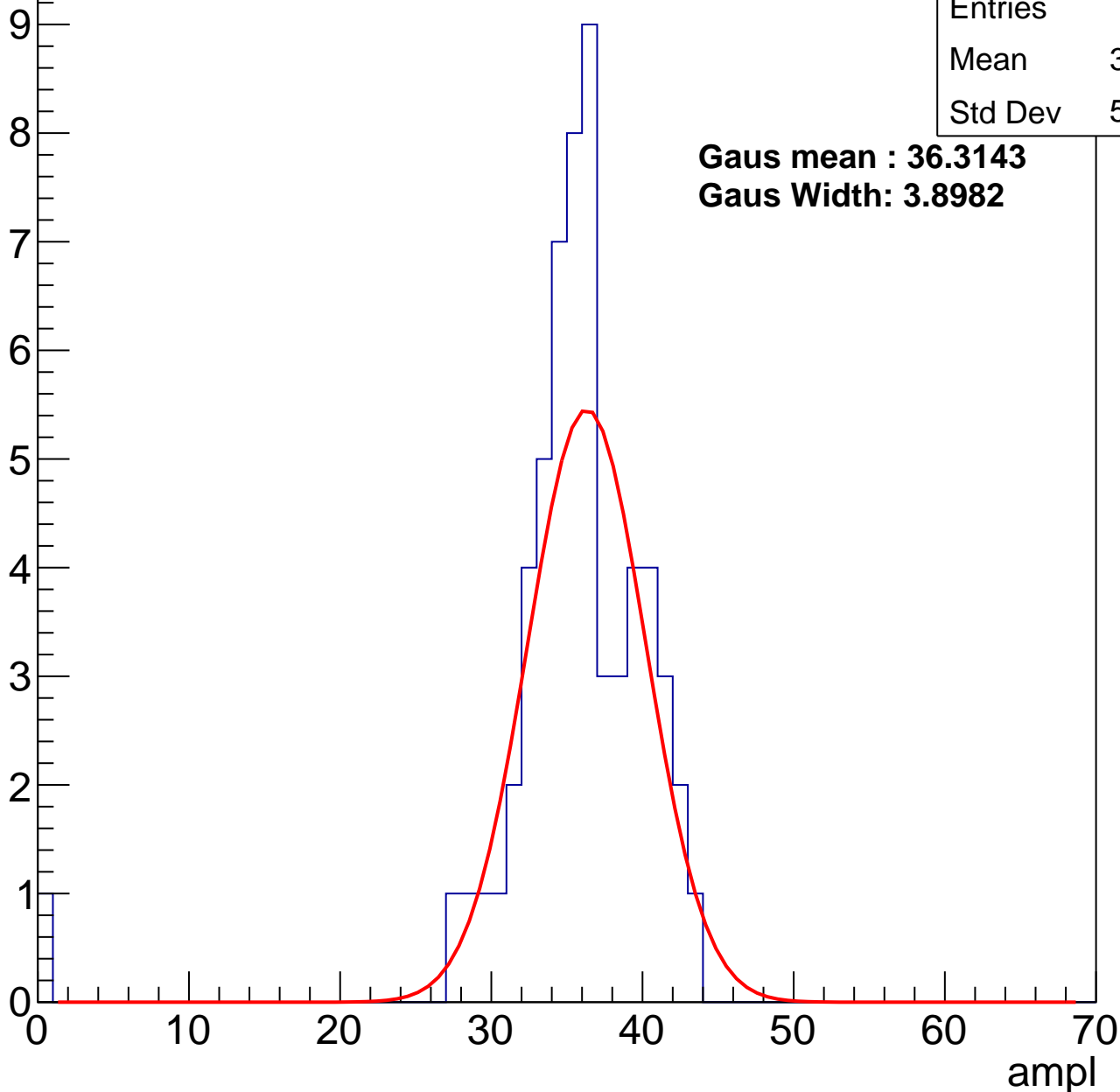
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.03
Std Dev	5.739

Gaus mean : 36.3143

Gaus Width: 3.8982



B1L103S, U14-ch121, adc2

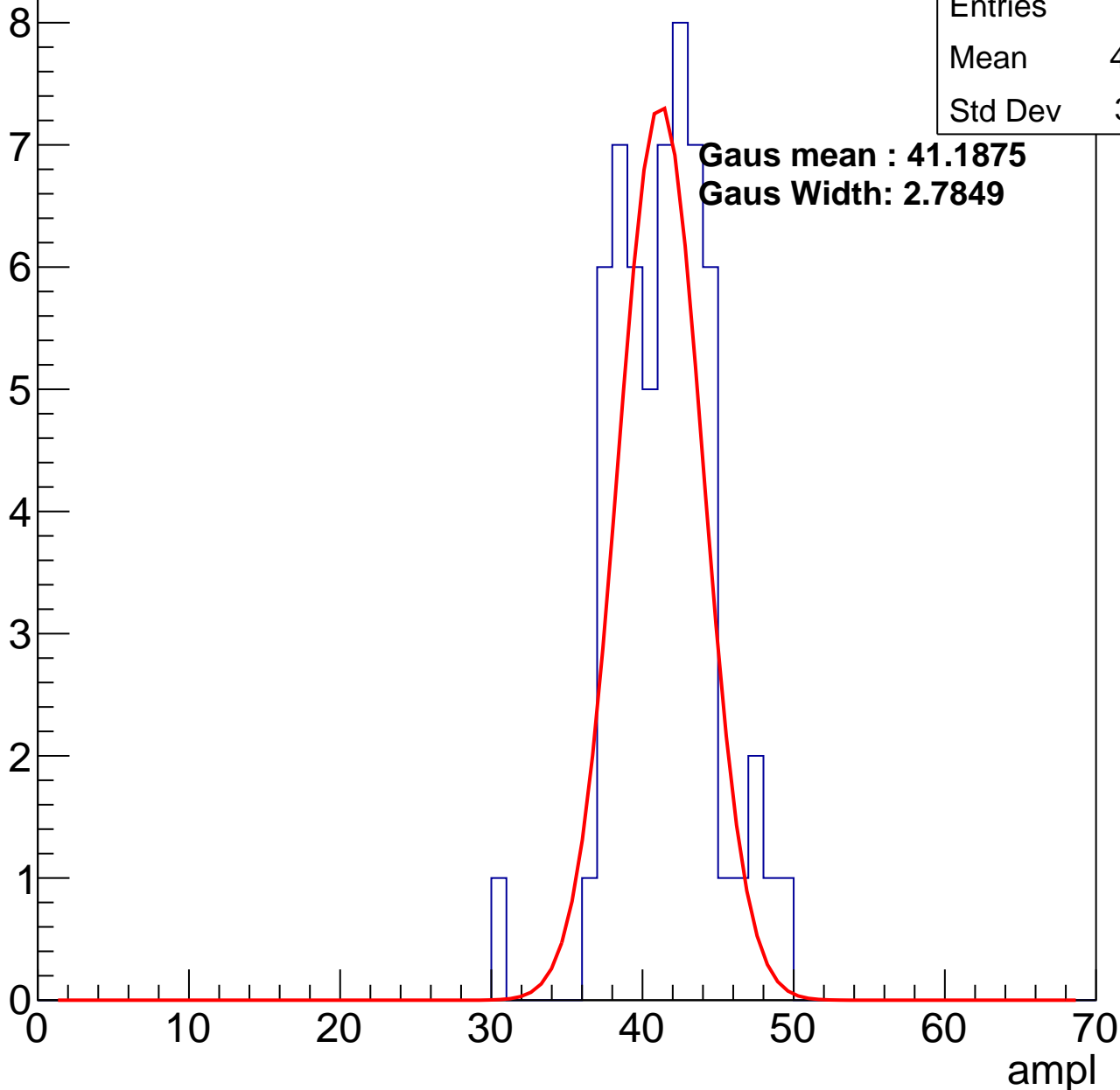
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	40.97
Std Dev	3.291

Gaus mean : 41.1875

Gaus Width: 2.7849

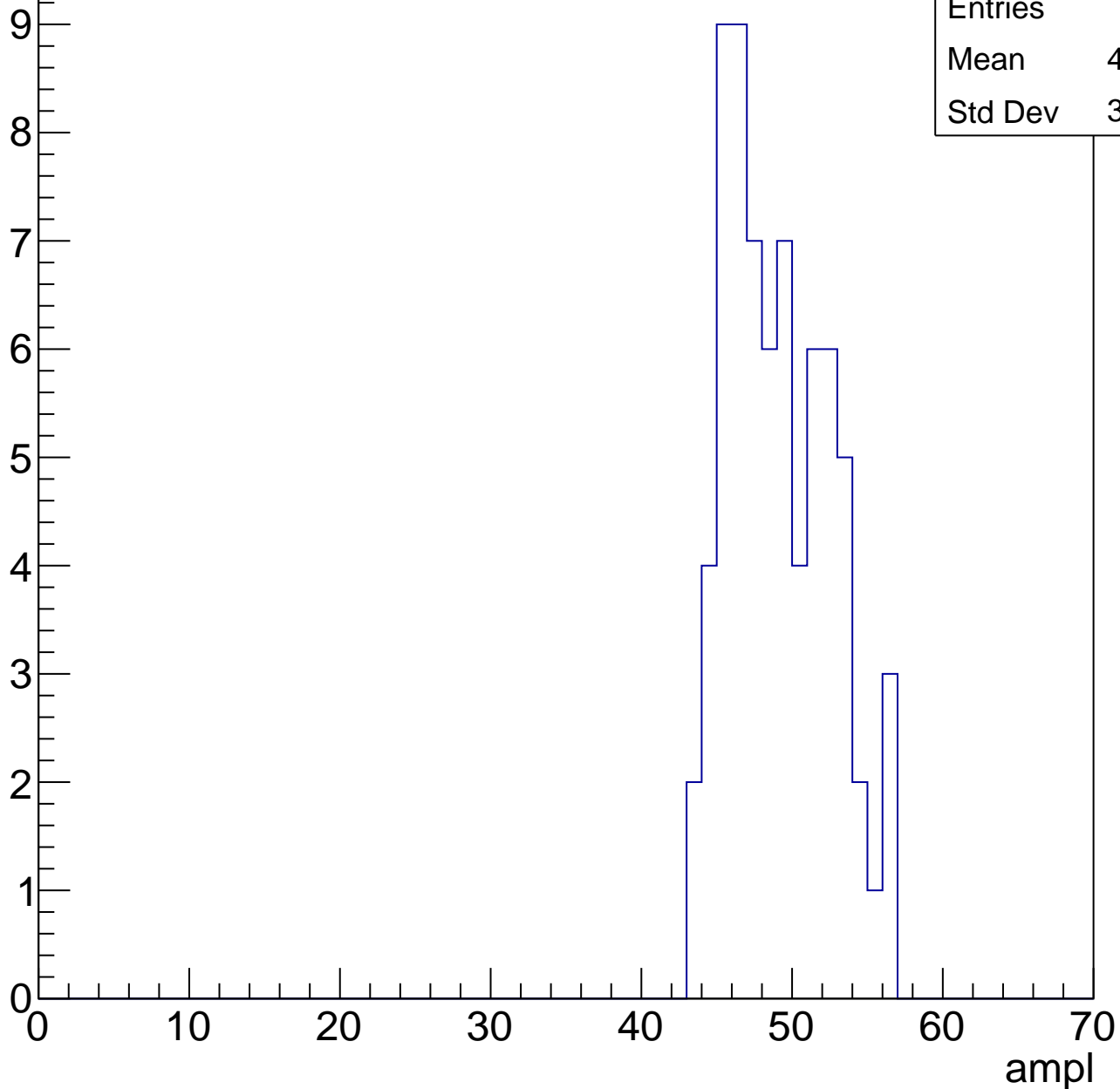


B1L103S, U14-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.66
Std Dev	3.394

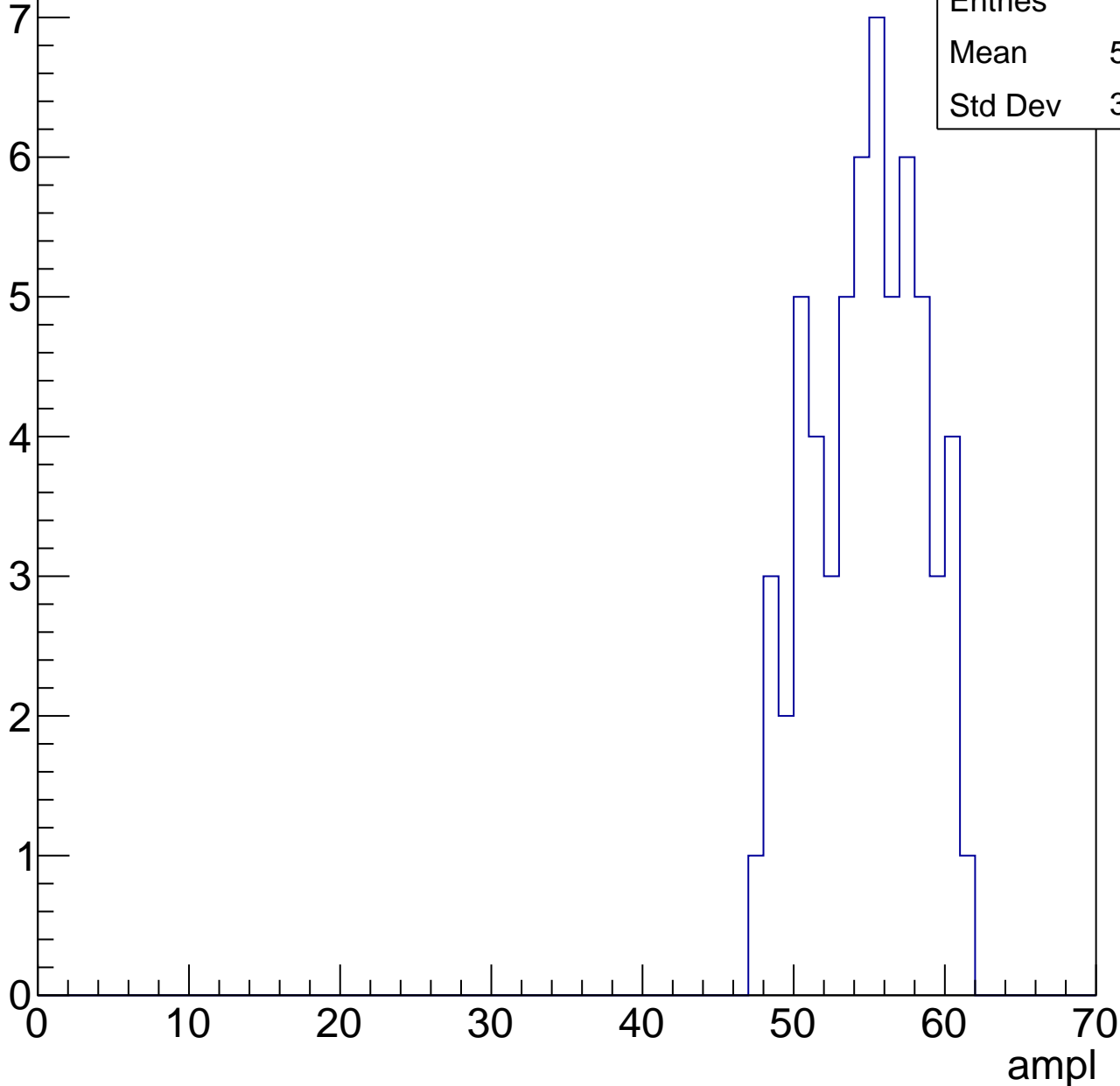


B1L103S, U14-ch121, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.38
Std Dev	3.559

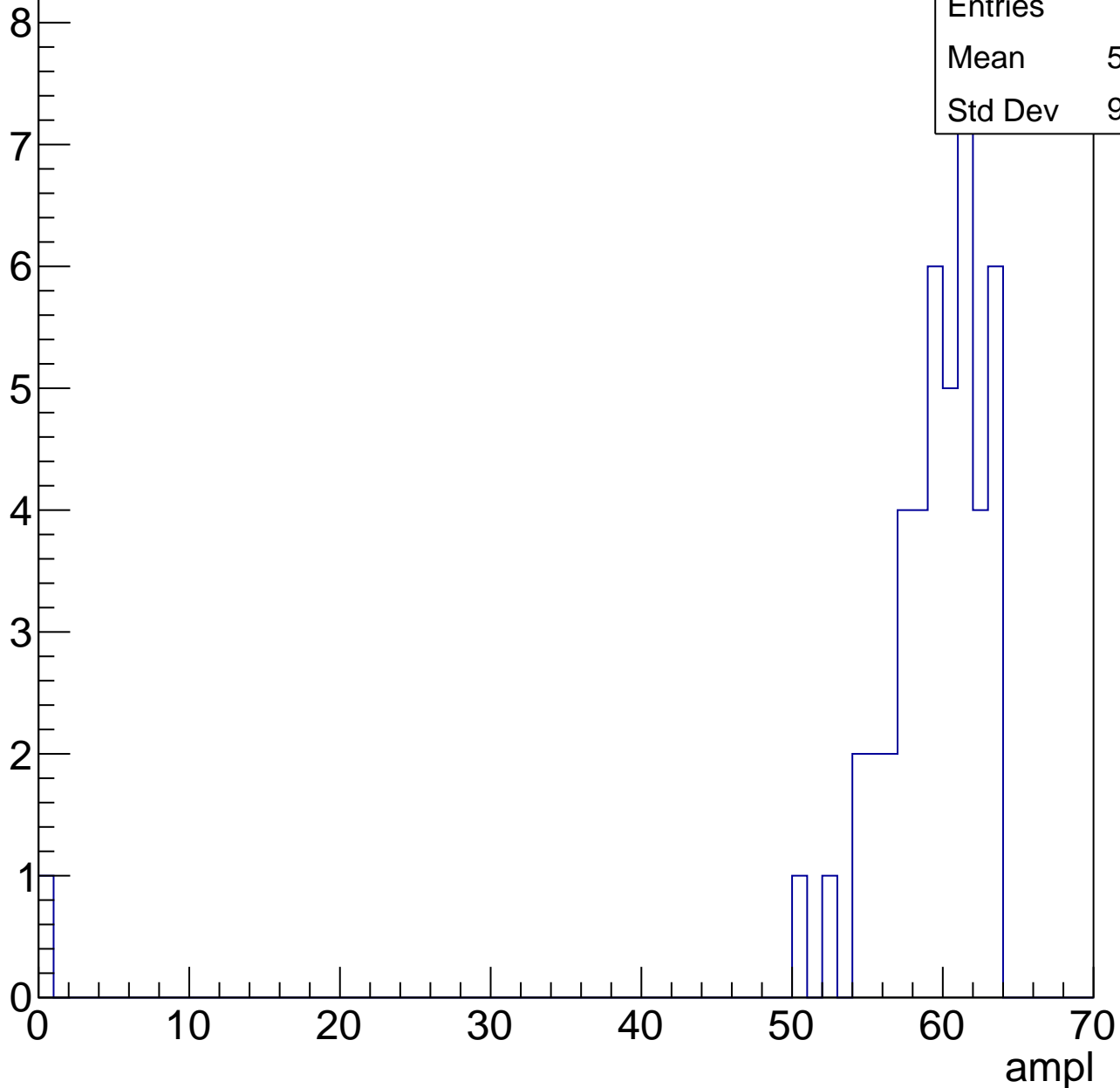


B1L103S, U14-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

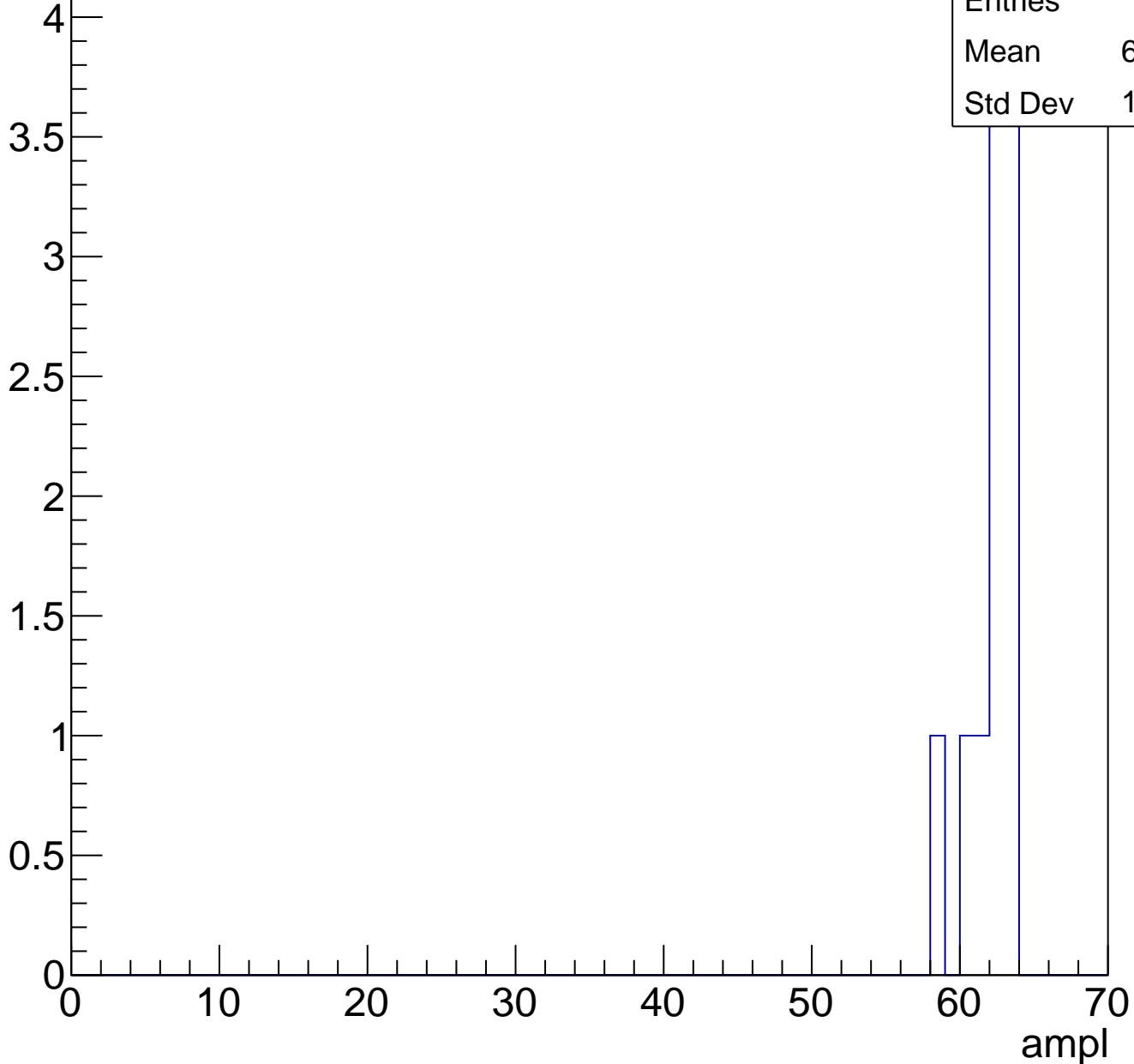
Entries	46
Mean	57.83
Std Dev	9.133



B1L103S, U14-ch121, adc6

calib_packv5_042523_0143.root, FC#7, port C2

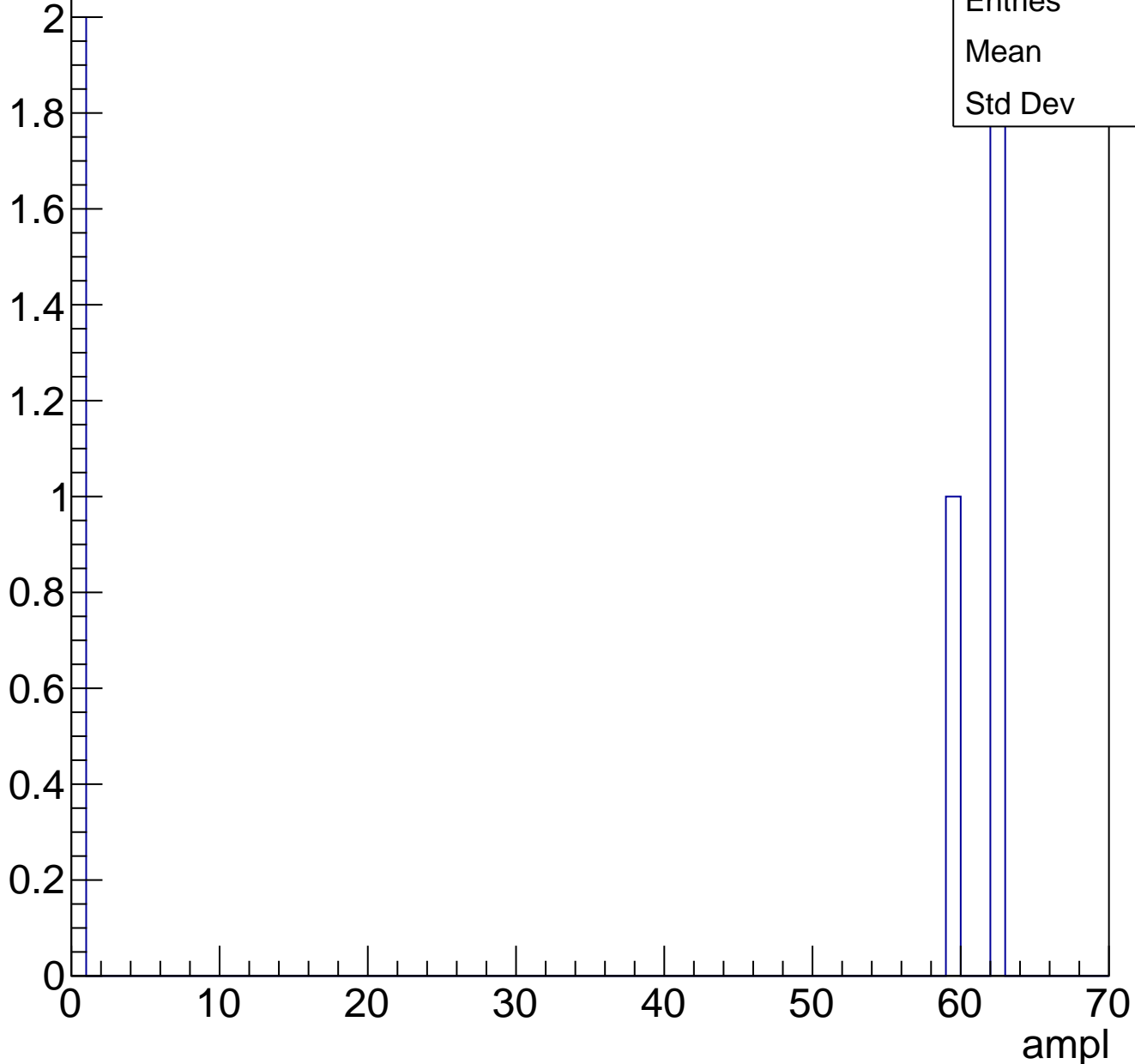
Entry



B1L103S, U14-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch122, adc0

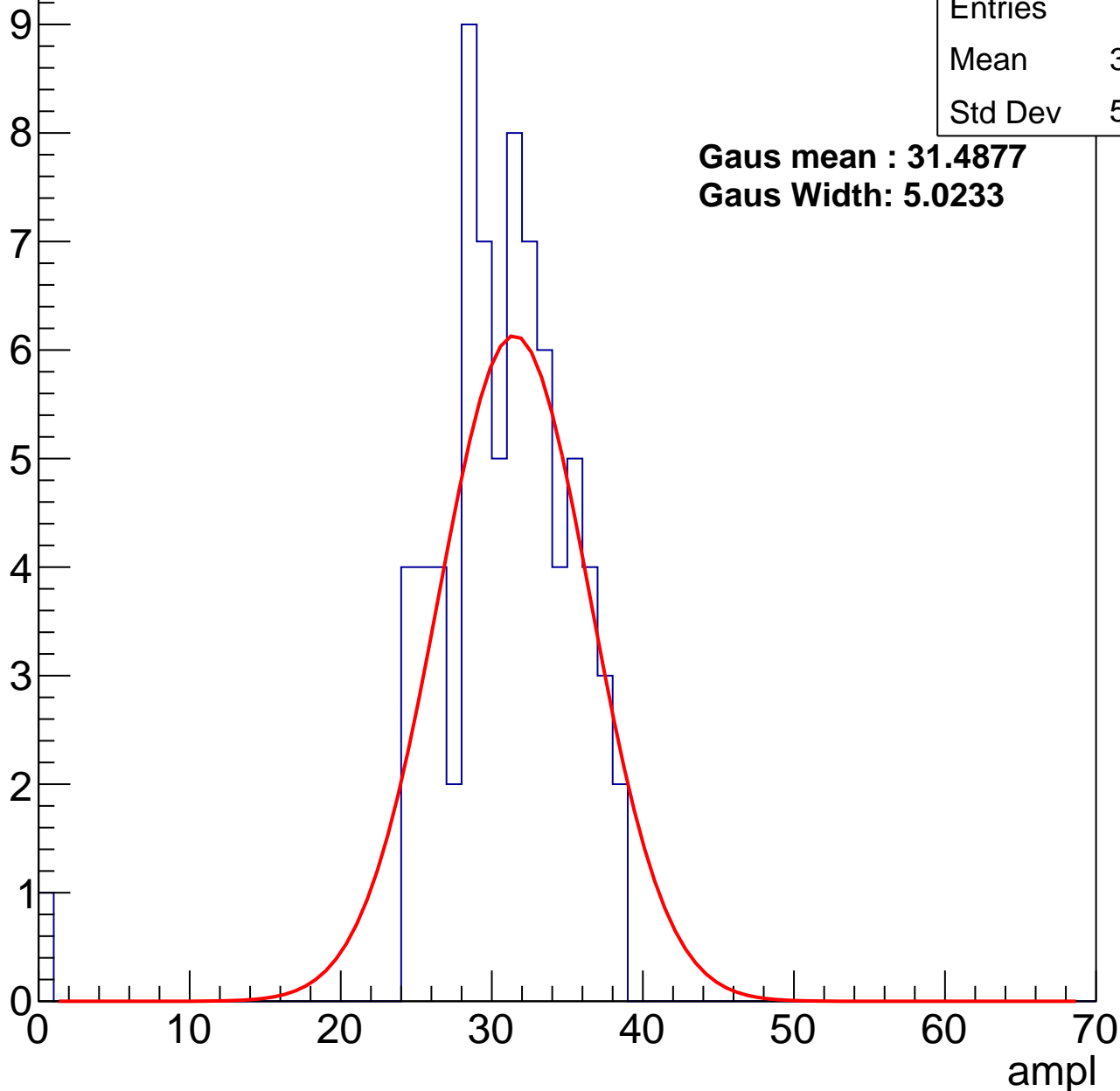
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	30.28
Std Dev	5.114

Gaus mean : 31.4877

Gaus Width: 5.0233



B1L103S, U14-ch122, adc1

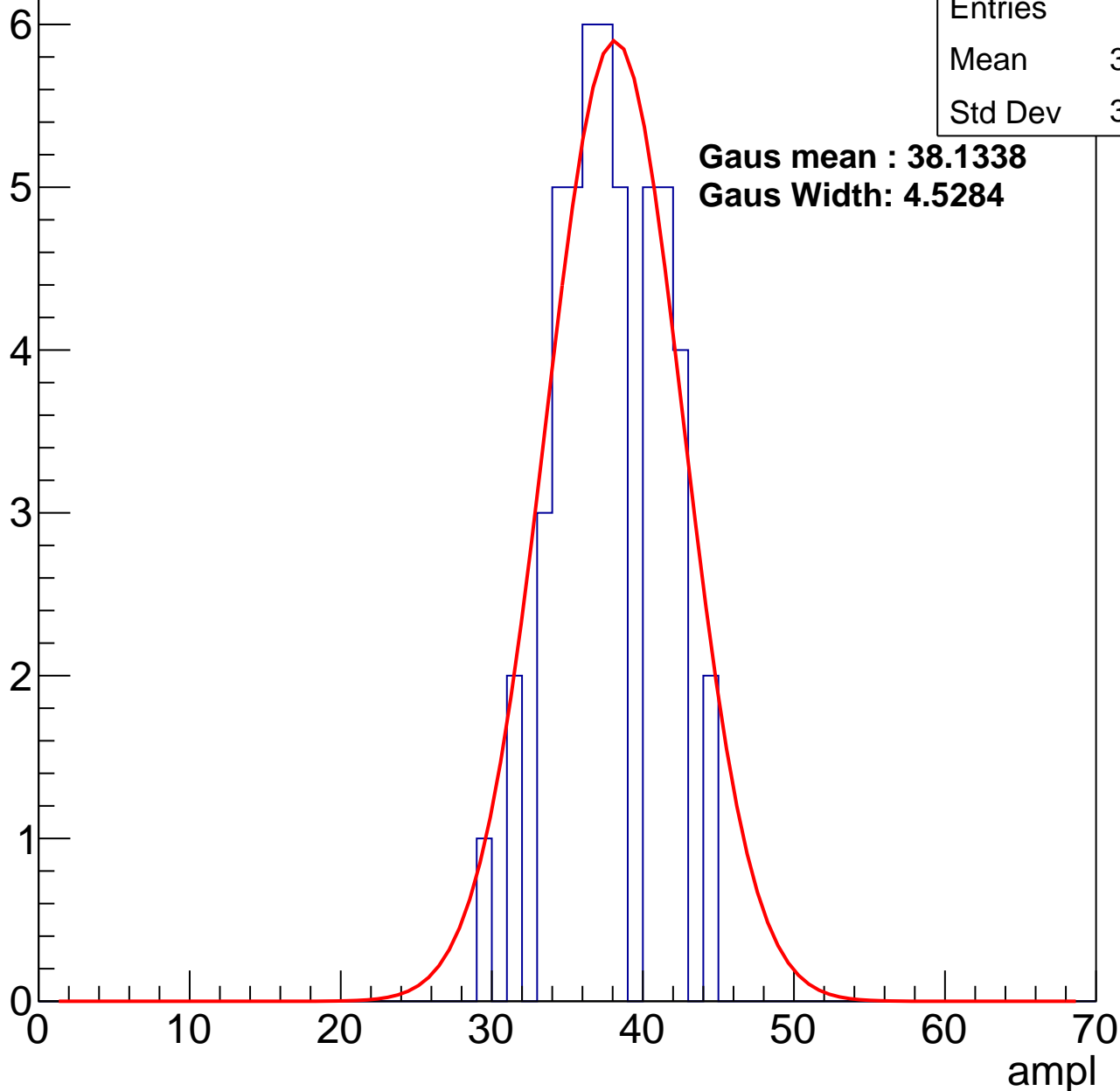
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	37.22
Std Dev	3.442

Gaus mean : 38.1338

Gaus Width: 4.5284



B1L103S, U14-ch122, adc2

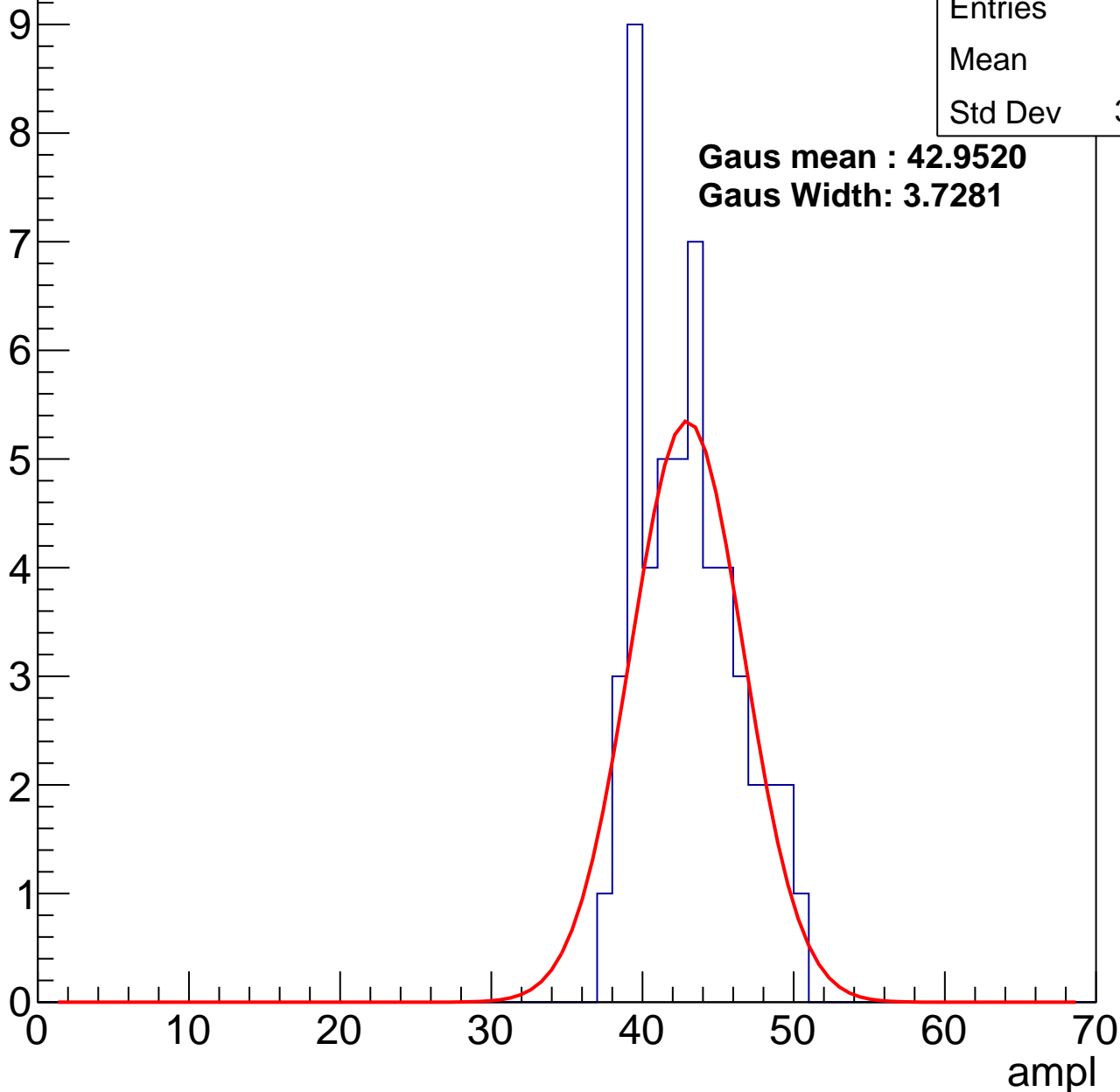
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	42.5
Std Dev	3.261

Gaus mean : 42.9520

Gaus Width: 3.7281

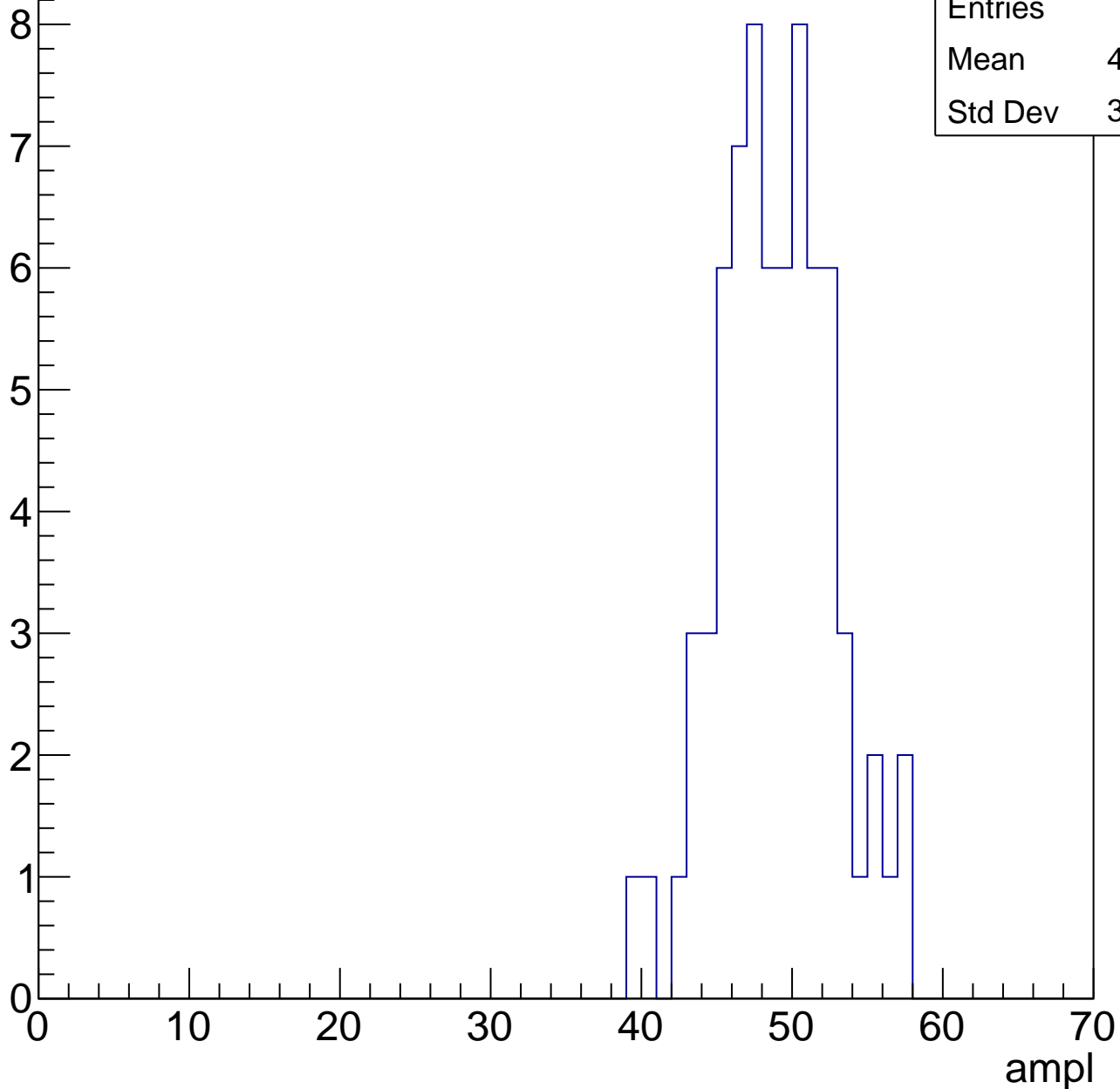


B1L103S, U14-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.49
Std Dev	3.752



B1L103S, U14-ch122, adc4

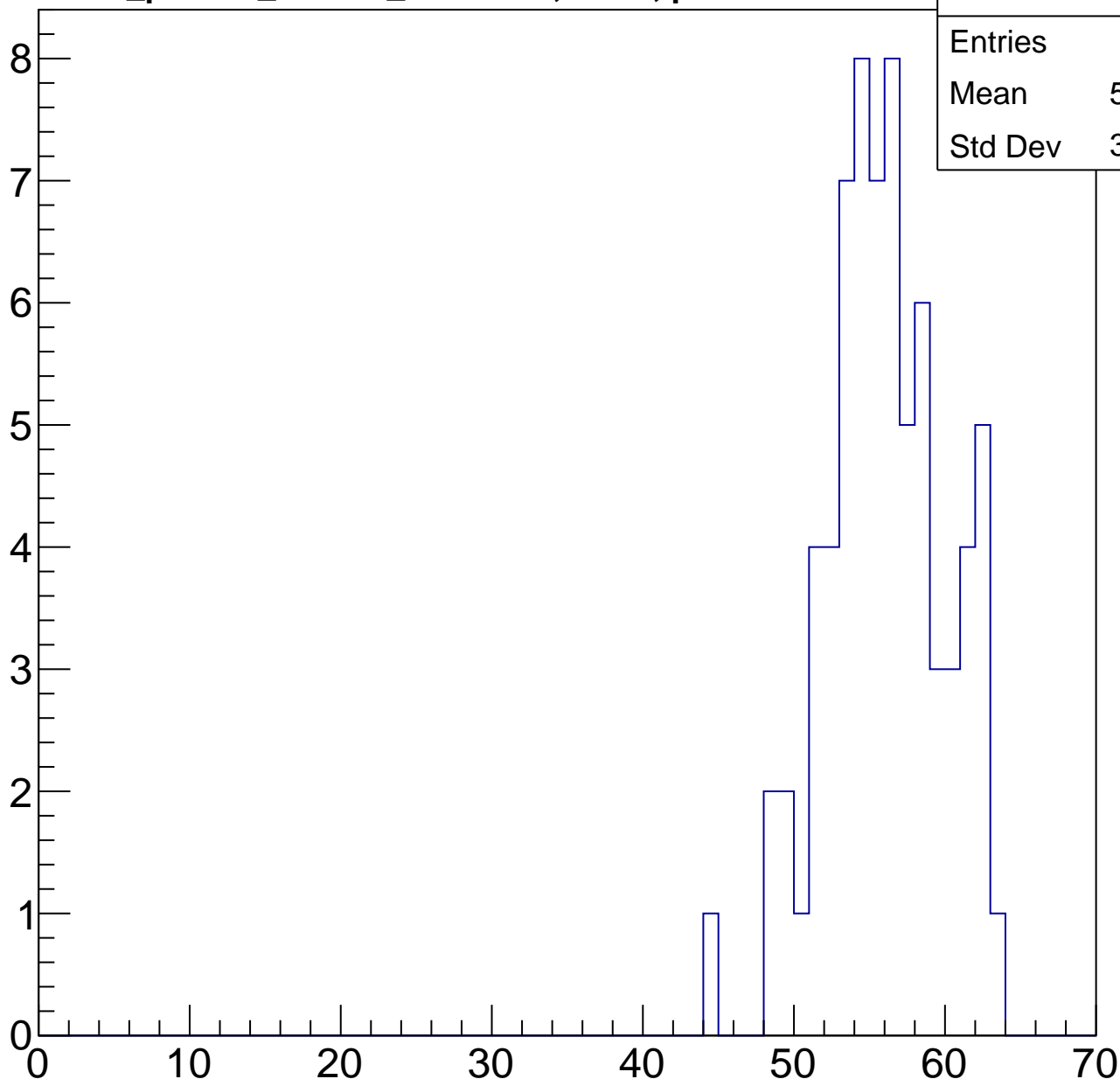
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	71
Mean	55.54
Std Dev	3.918

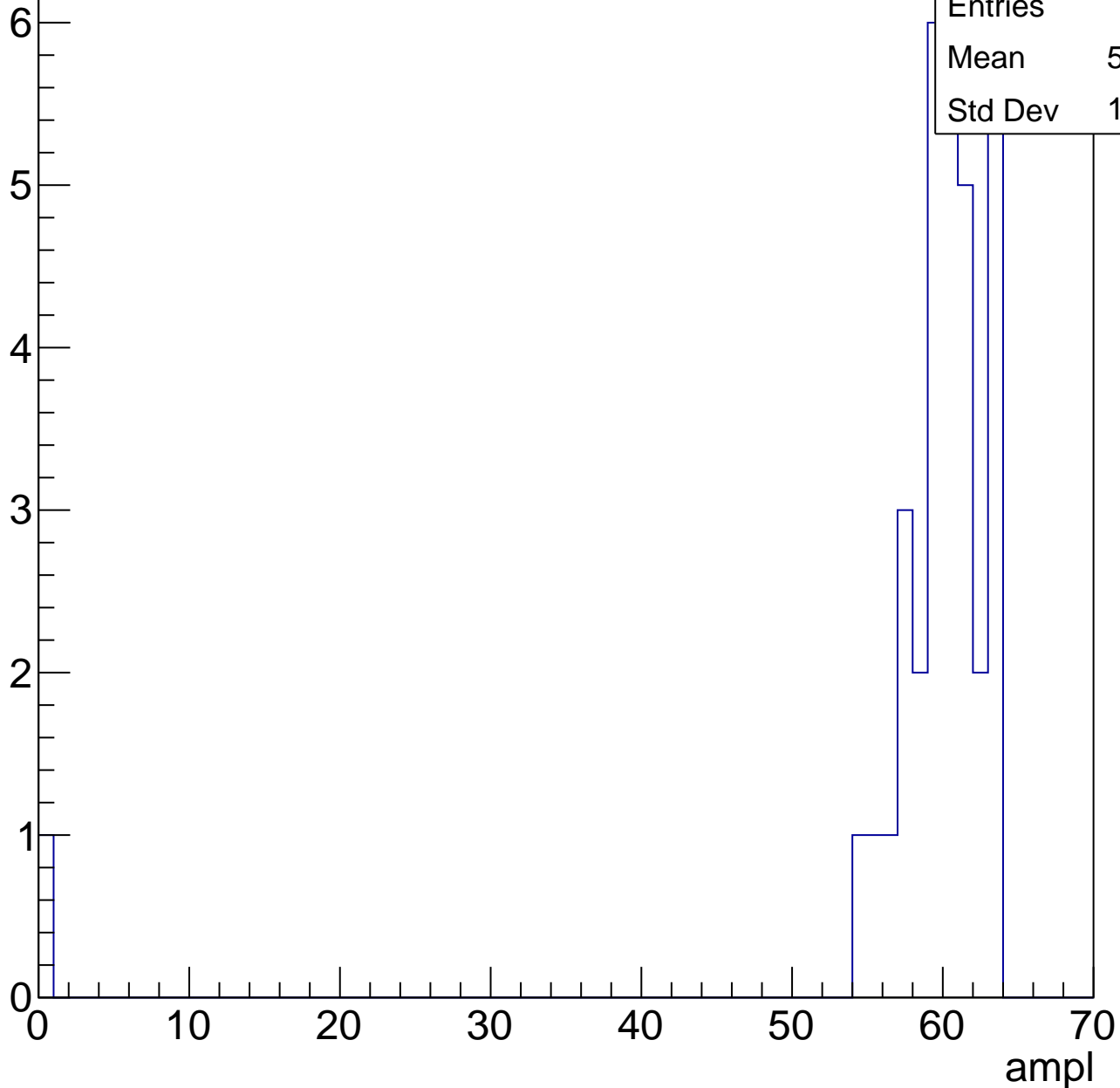
ampl



B1L103S, U14-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

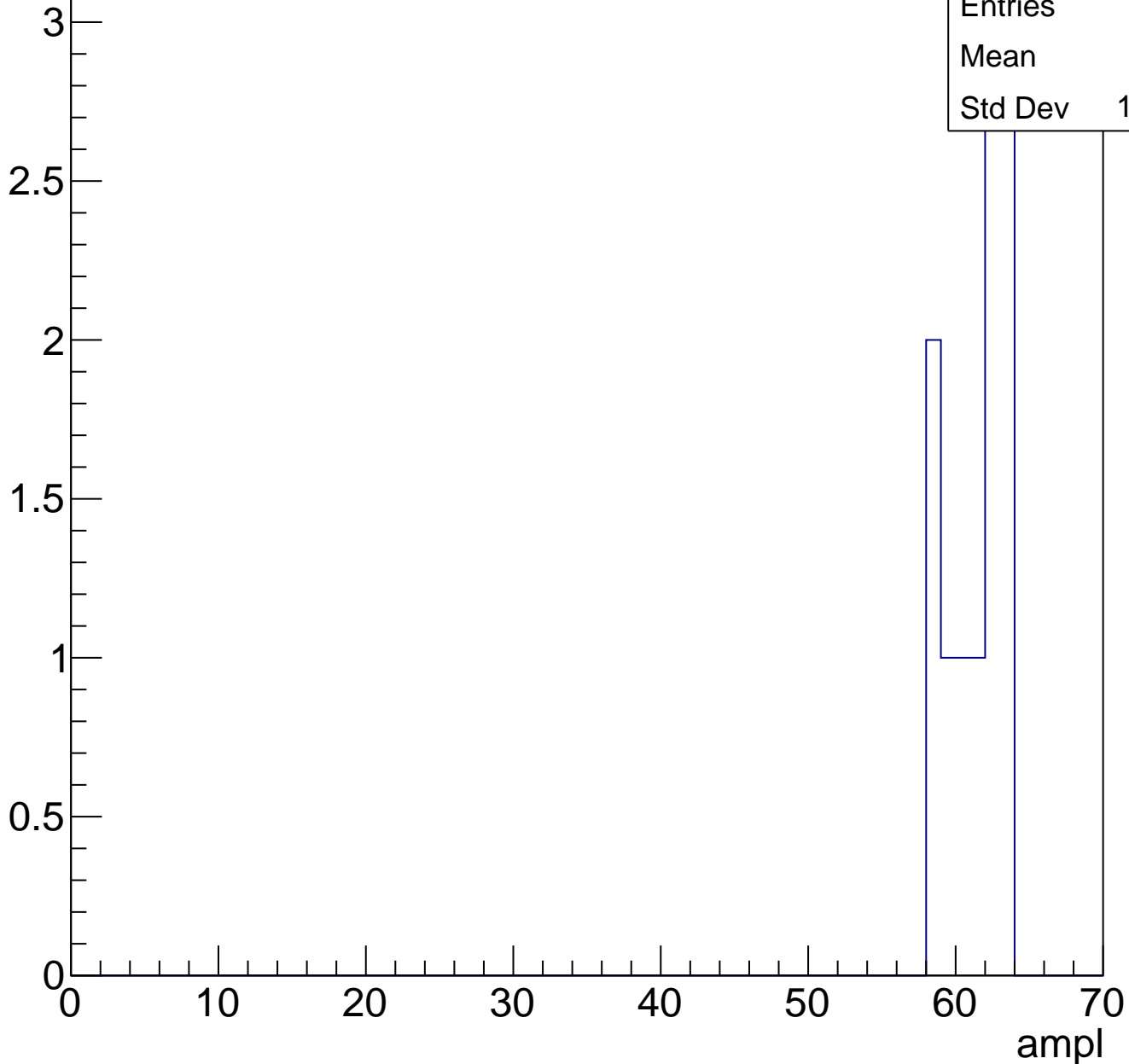
Entry



B1L103S, U14-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch123, adc0

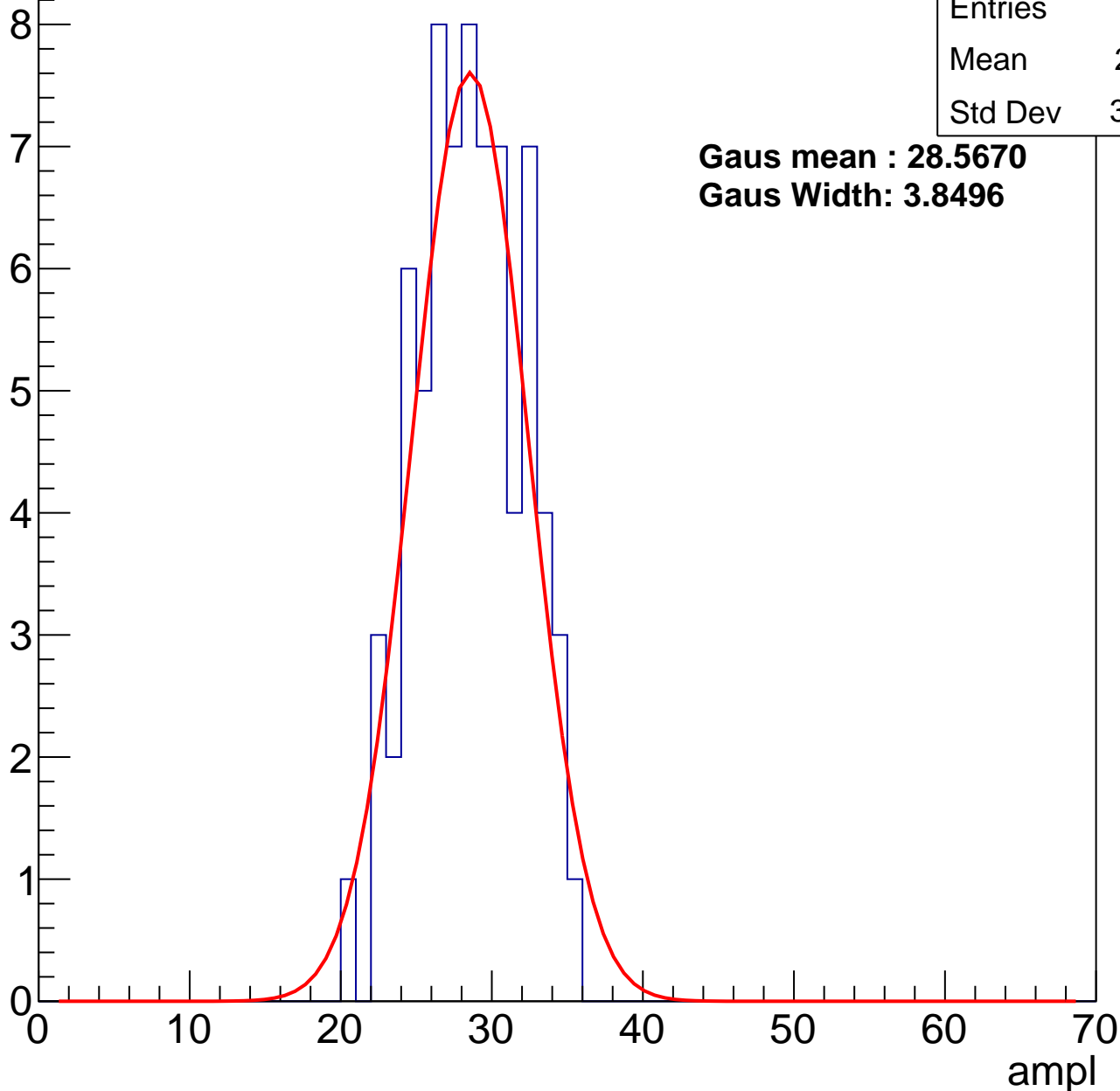
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28.11
Std Dev	3.394

Gaus mean : 28.5670

Gaus Width: 3.8496



B1L103S, U14-ch123, adc1

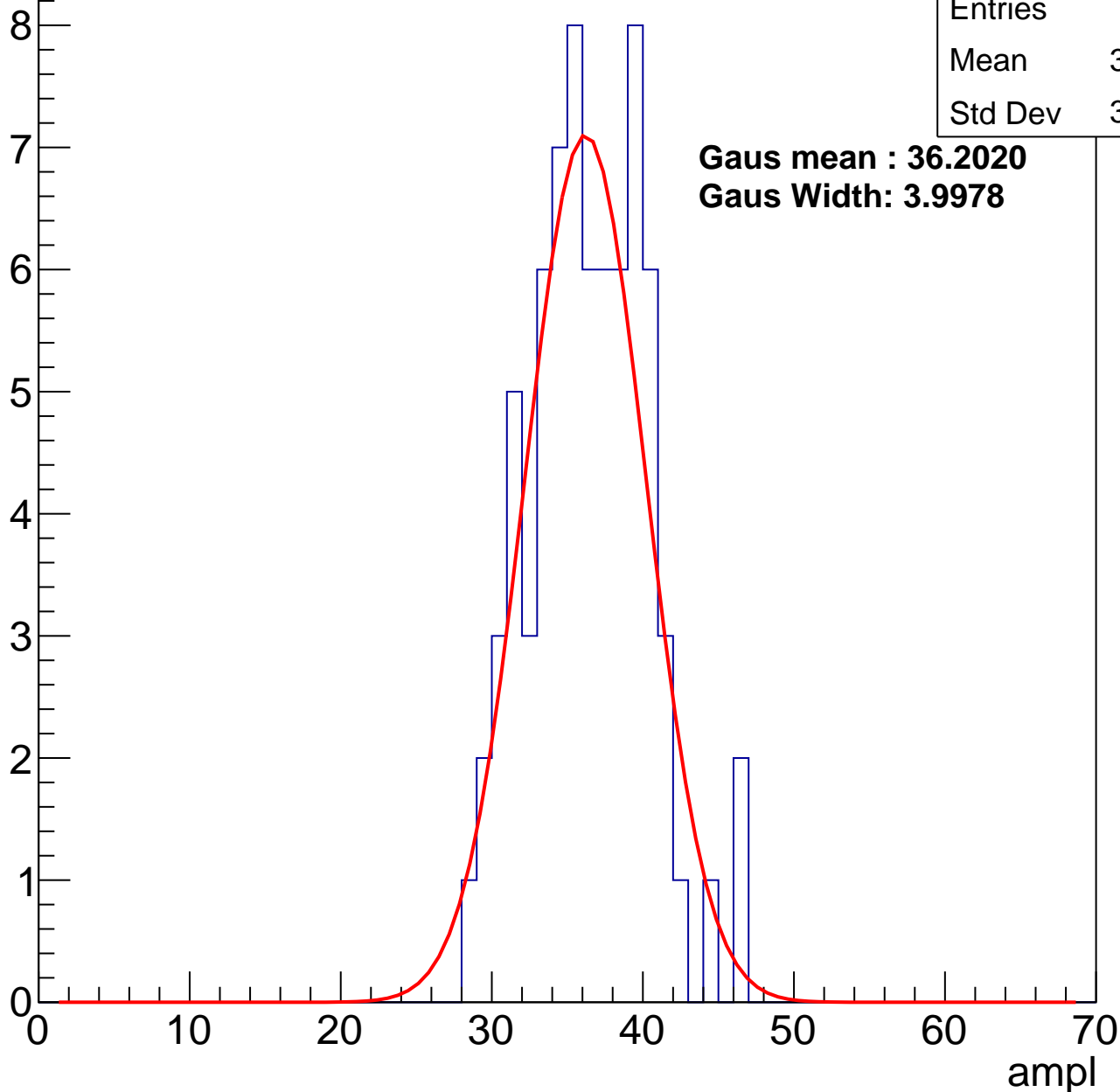
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	35.97
Std Dev	3.866

Gaus mean : 36.2020

Gaus Width: 3.9978



B1L103S, U14-ch123, adc2

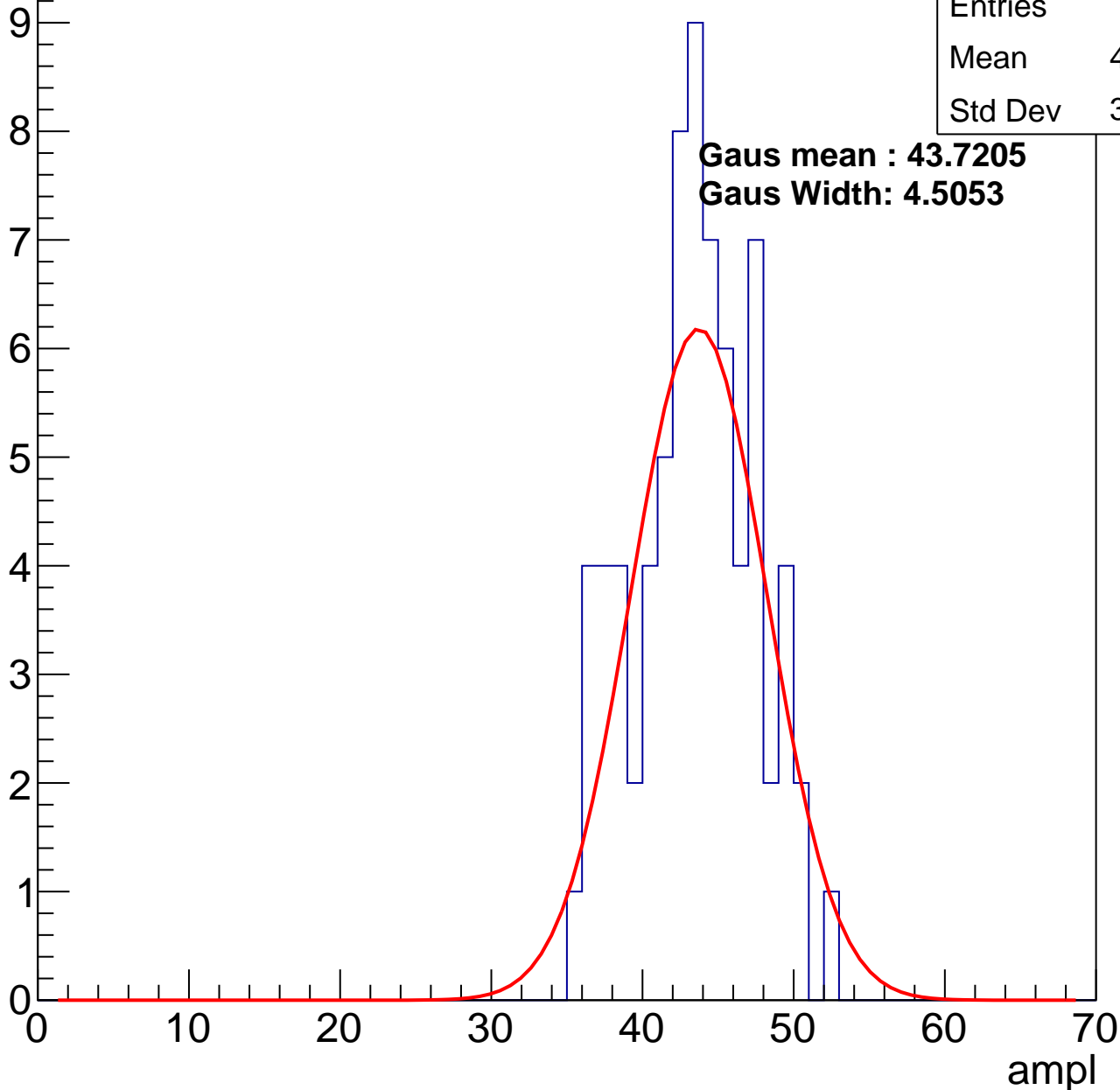
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	42.97
Std Dev	3.945

Gaus mean : 43.7205

Gaus Width: 4.5053

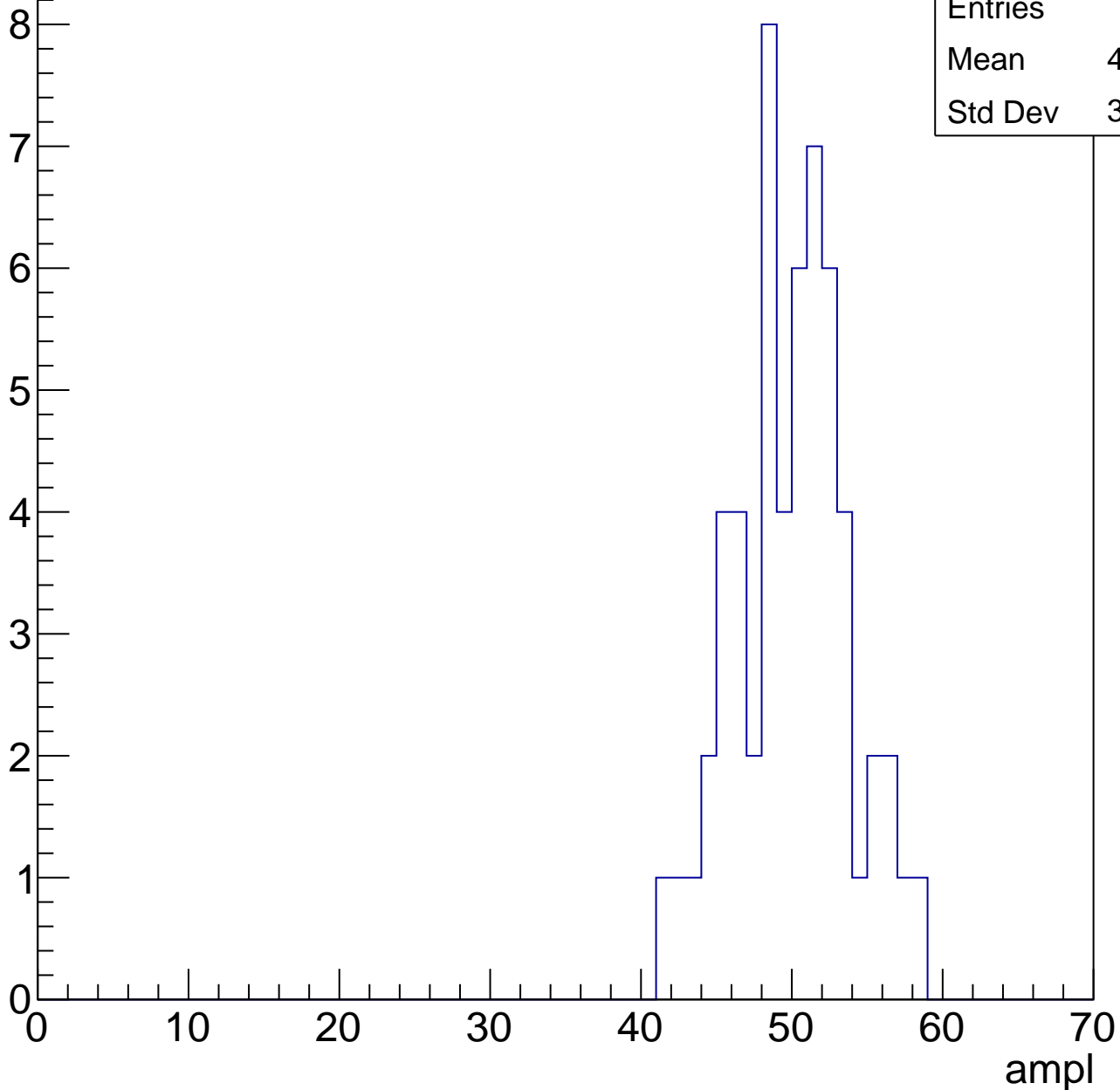


B1L103S, U14-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	49.54
Std Dev	3.723

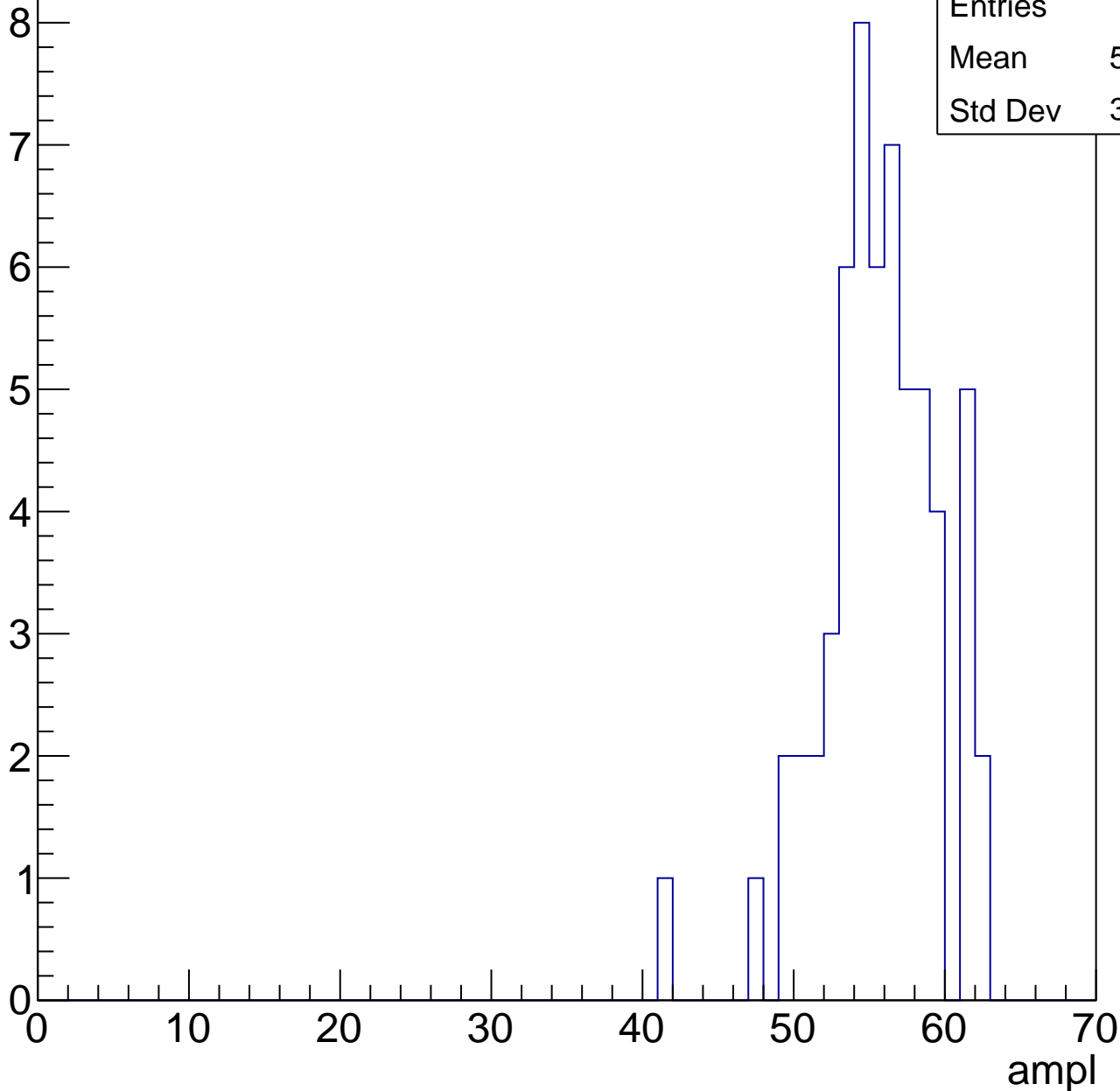


B1L103S, U14-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	55.19
Std Dev	3.873

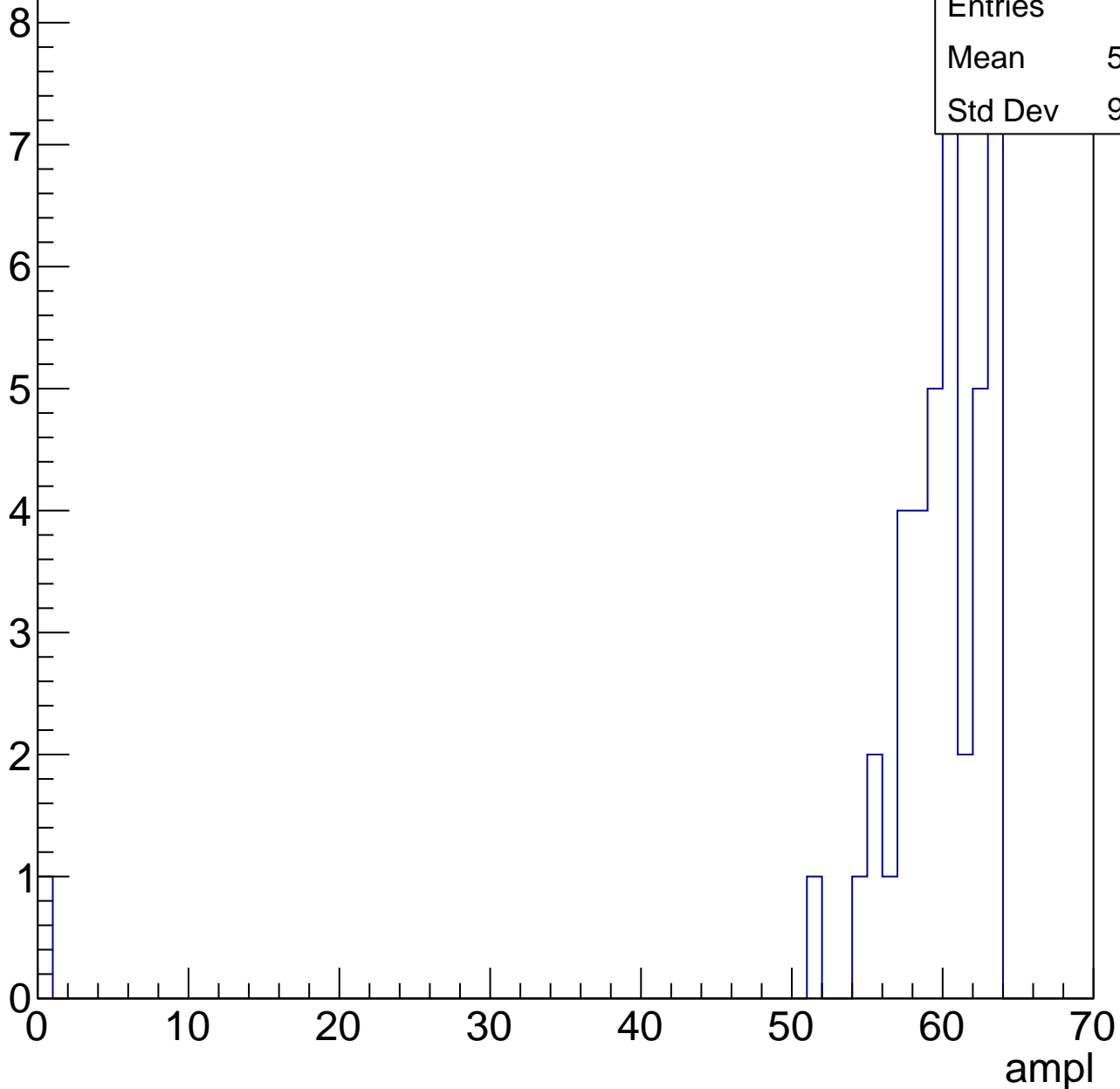


B1L103S, U14-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

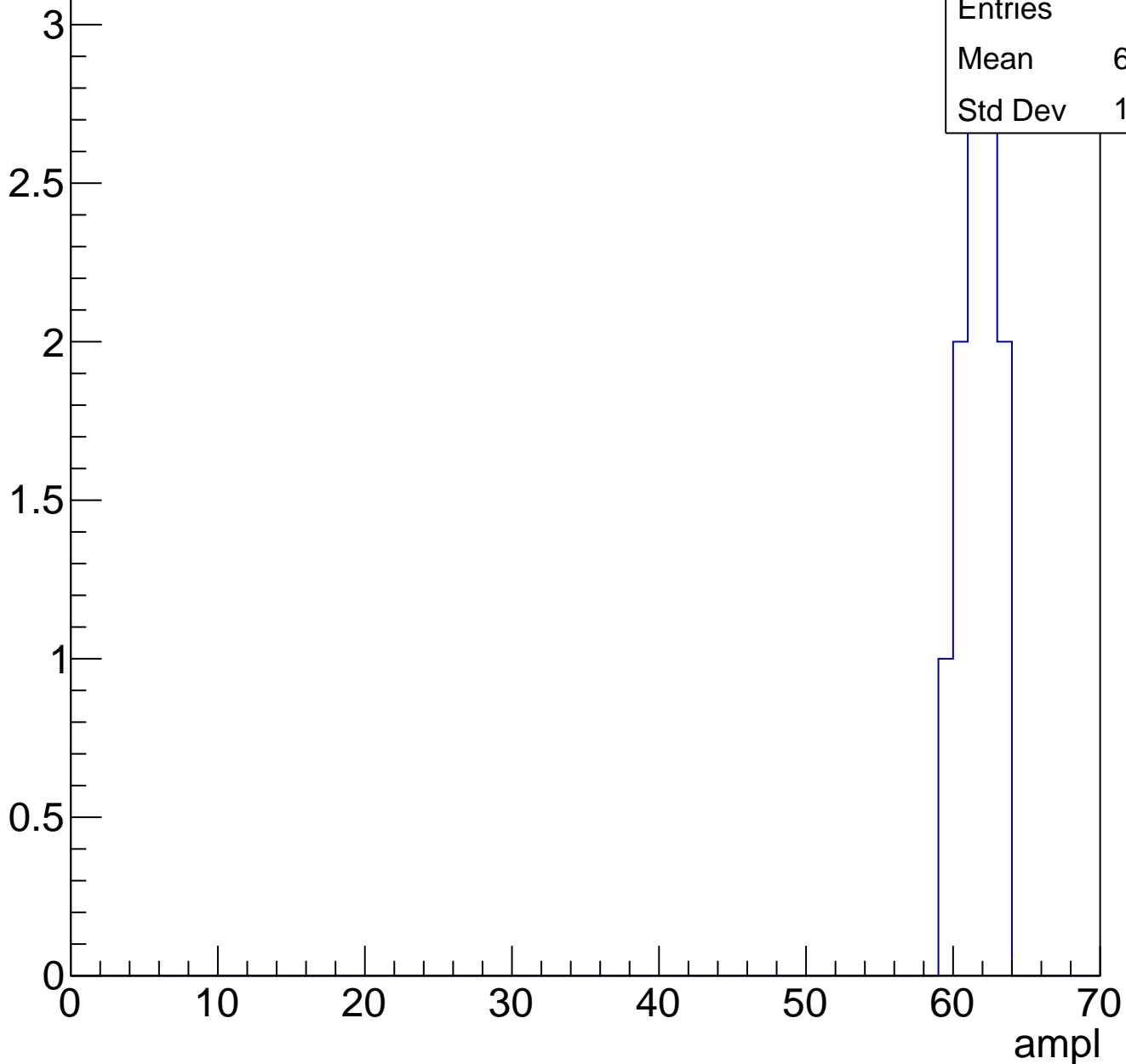
Entries	42
Mean	58.14
Std Dev	9.498



B1L103S, U14-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U14-ch124, adc0

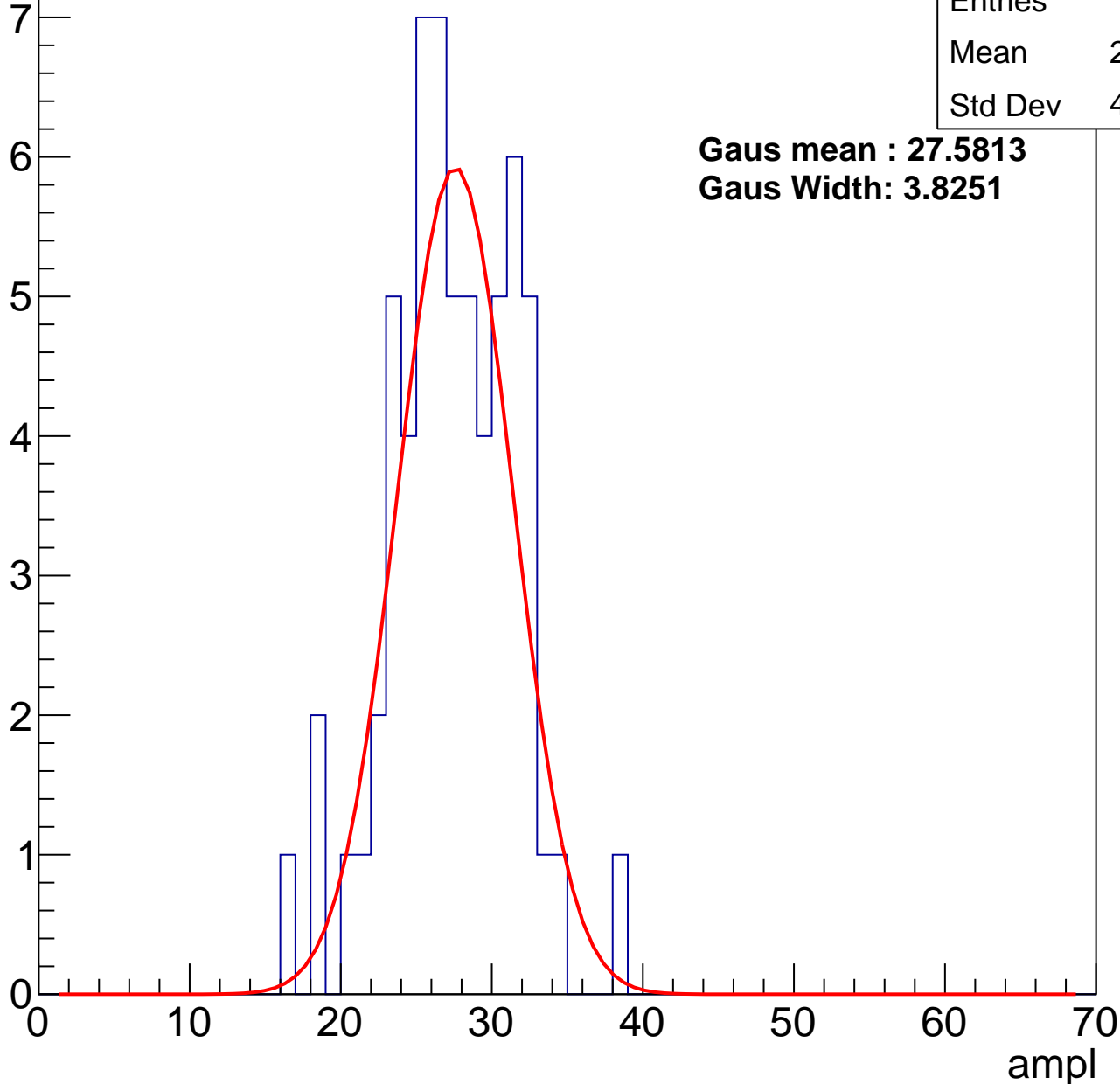
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	26.94
Std Dev	4.105

Gaus mean : 27.5813

Gaus Width: 3.8251



B1L103S, U14-ch124, adc1

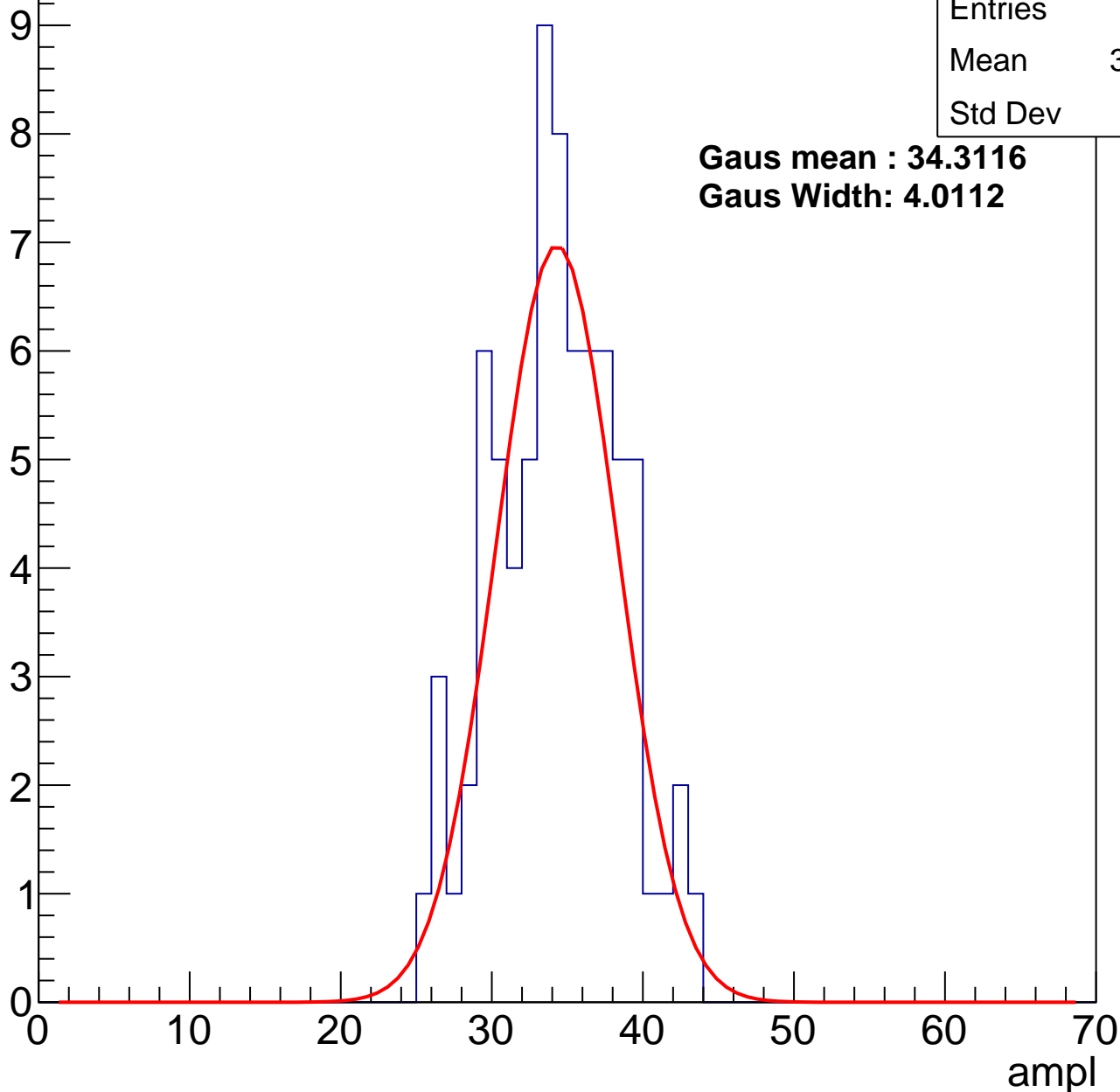
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	33.82
Std Dev	4.06

Gaus mean : 34.3116

Gaus Width: 4.0112



B1L103S, U14-ch124, adc2

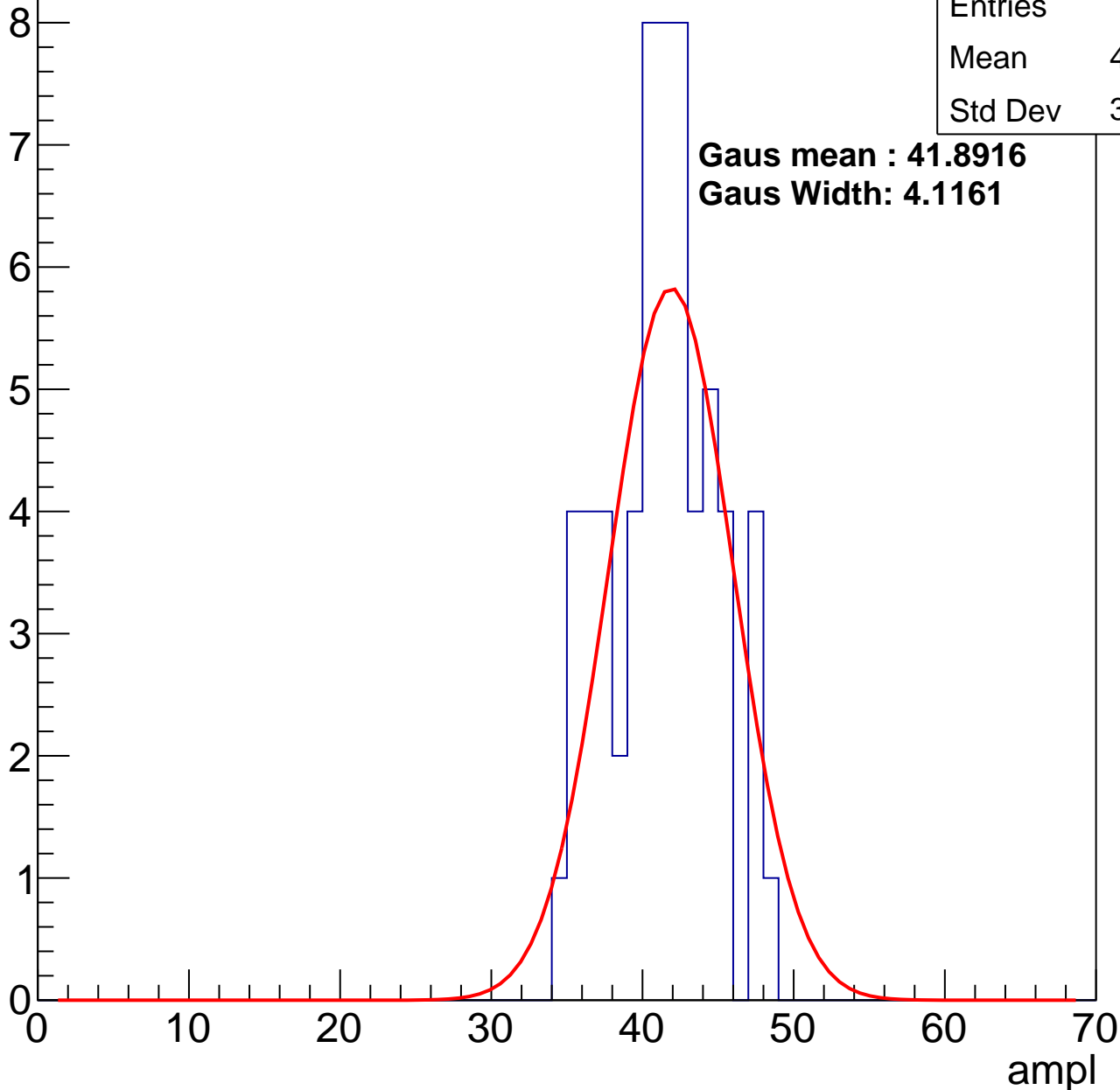
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	40.82
Std Dev	3.443

Gaus mean : 41.8916

Gaus Width: 4.1161

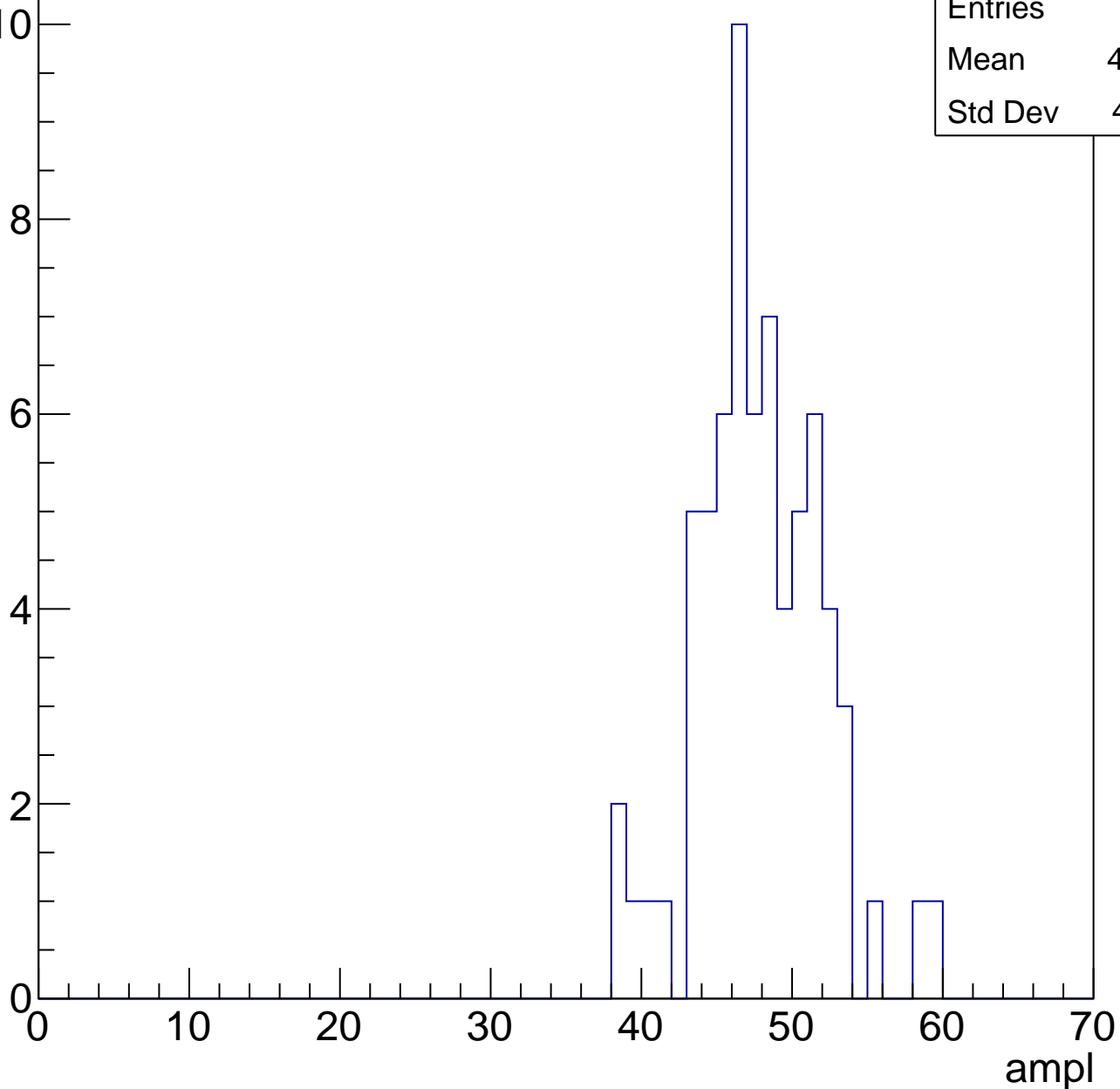


B1L103S, U14-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	47.39
Std Dev	4.101

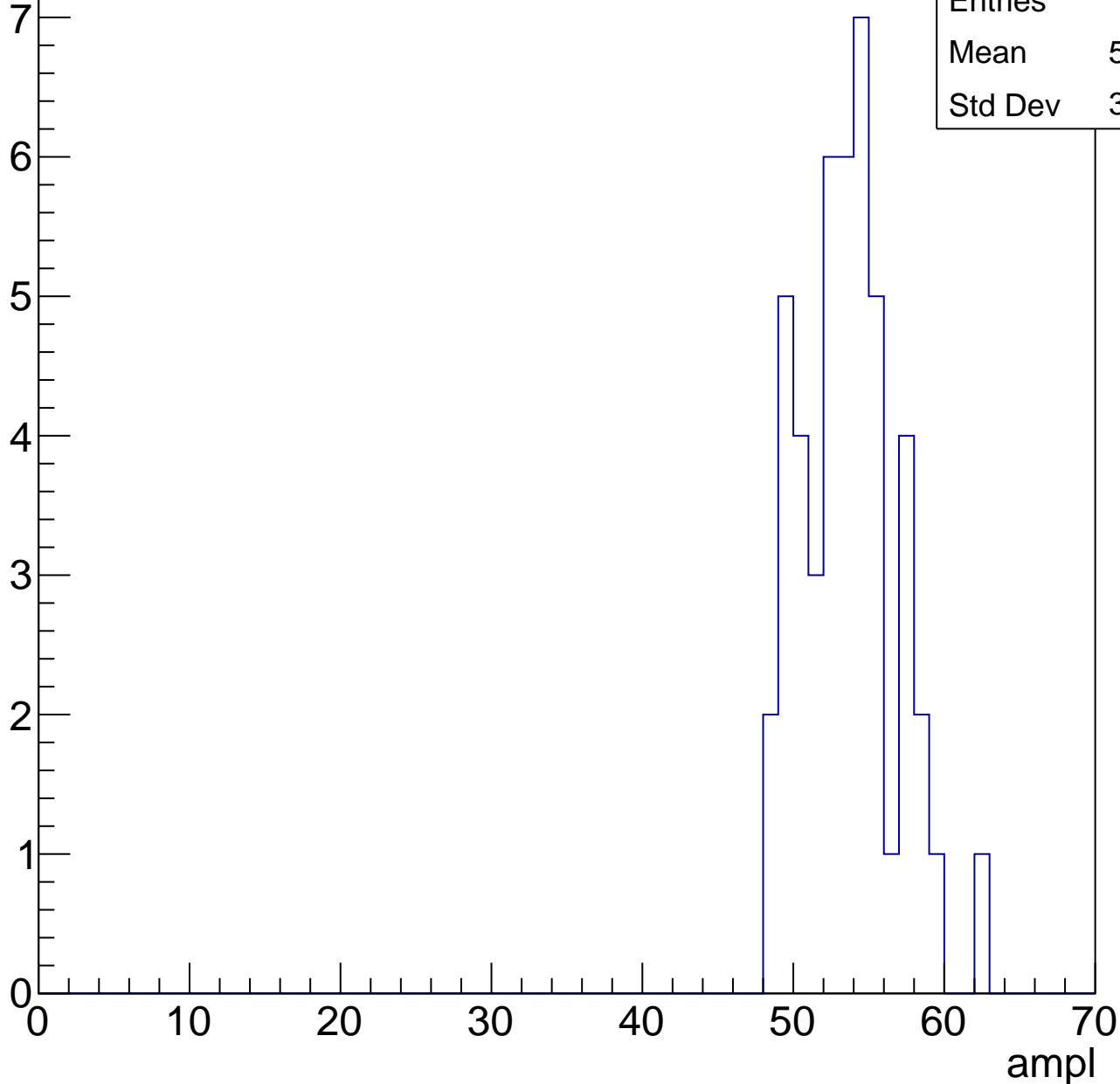


B1L103S, U14-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	53.15
Std Dev	3.094

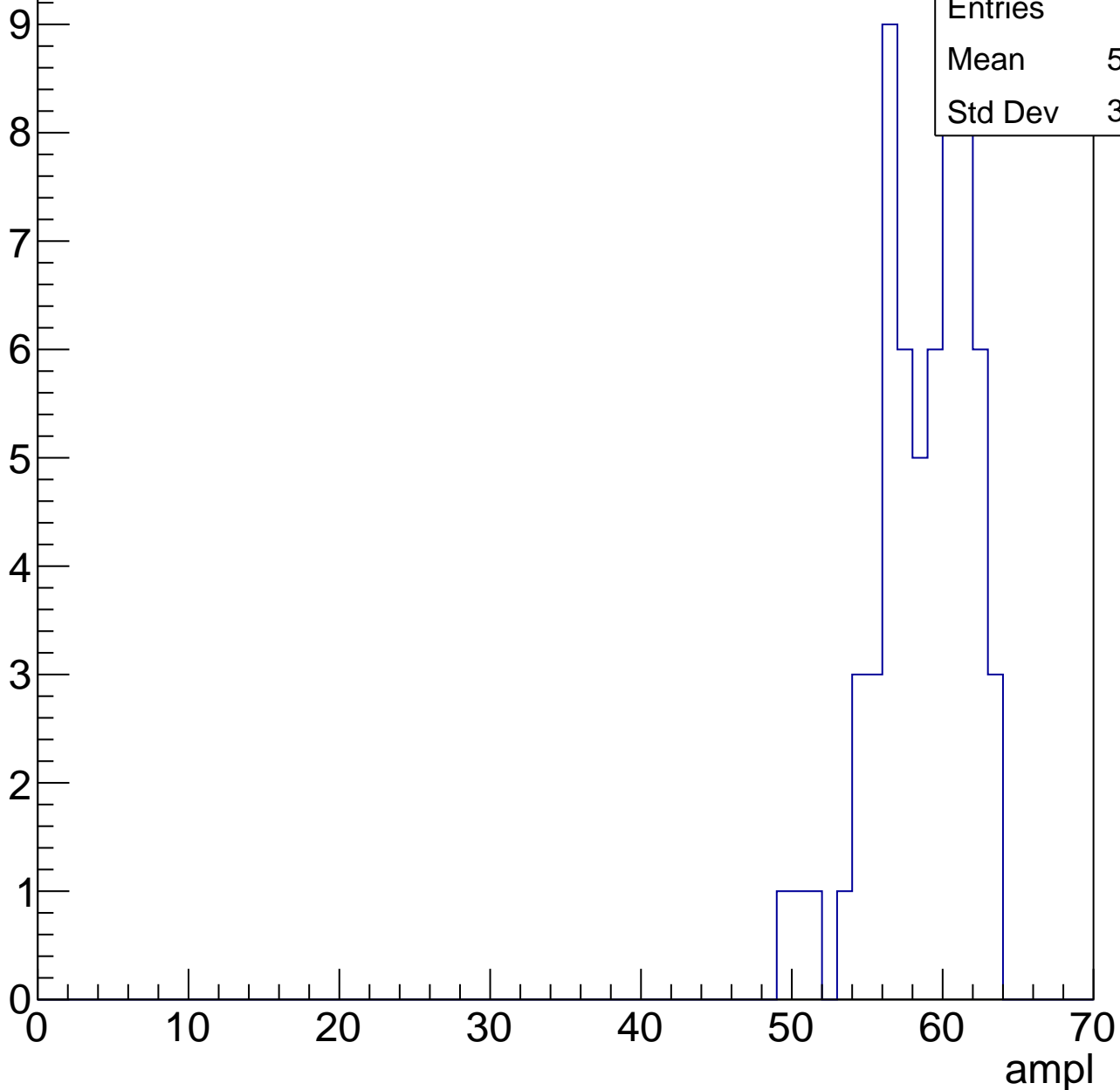


B1L103S, U14-ch124, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	58.25
Std Dev	3.152

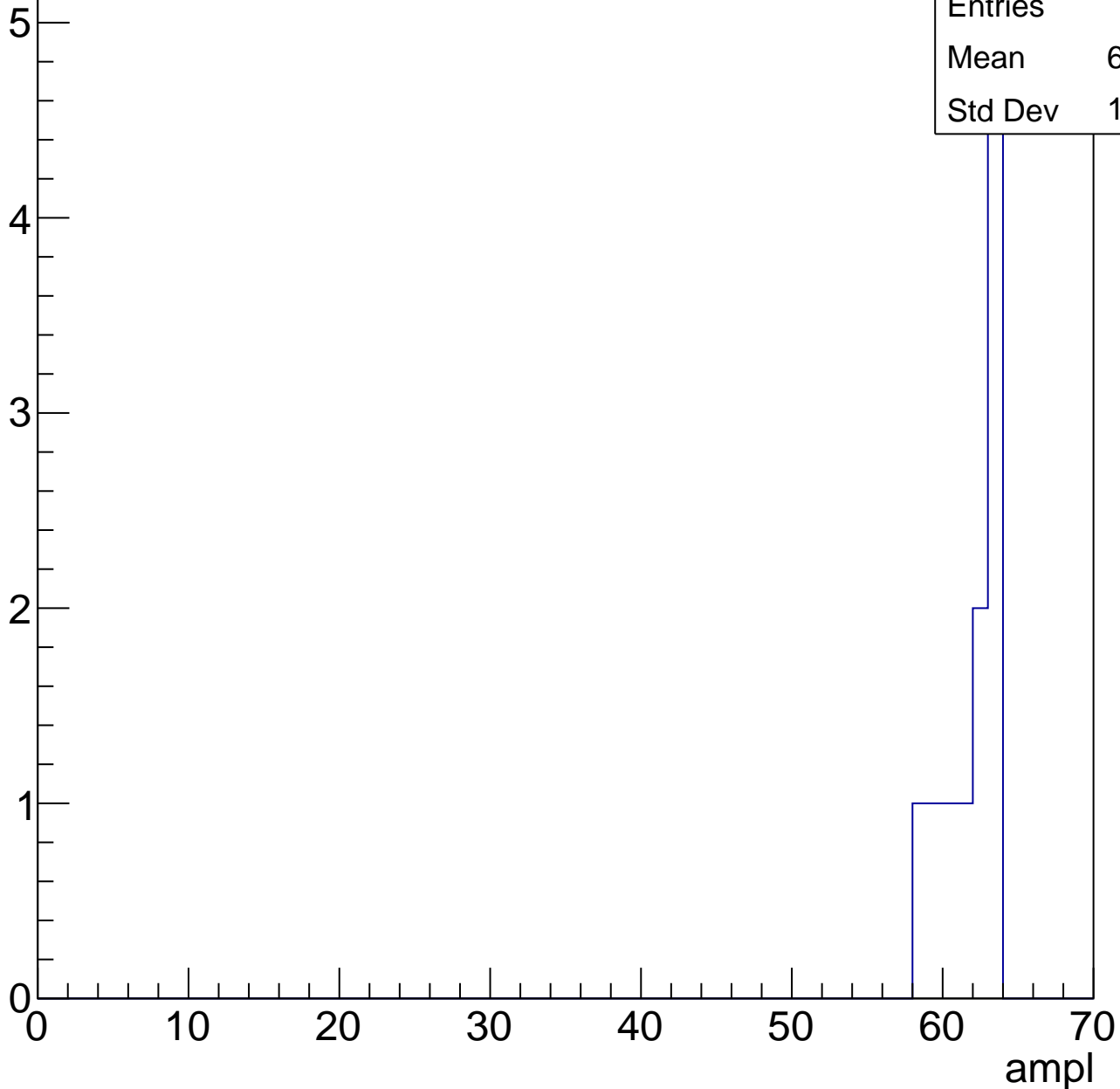


B1L103S, U14-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

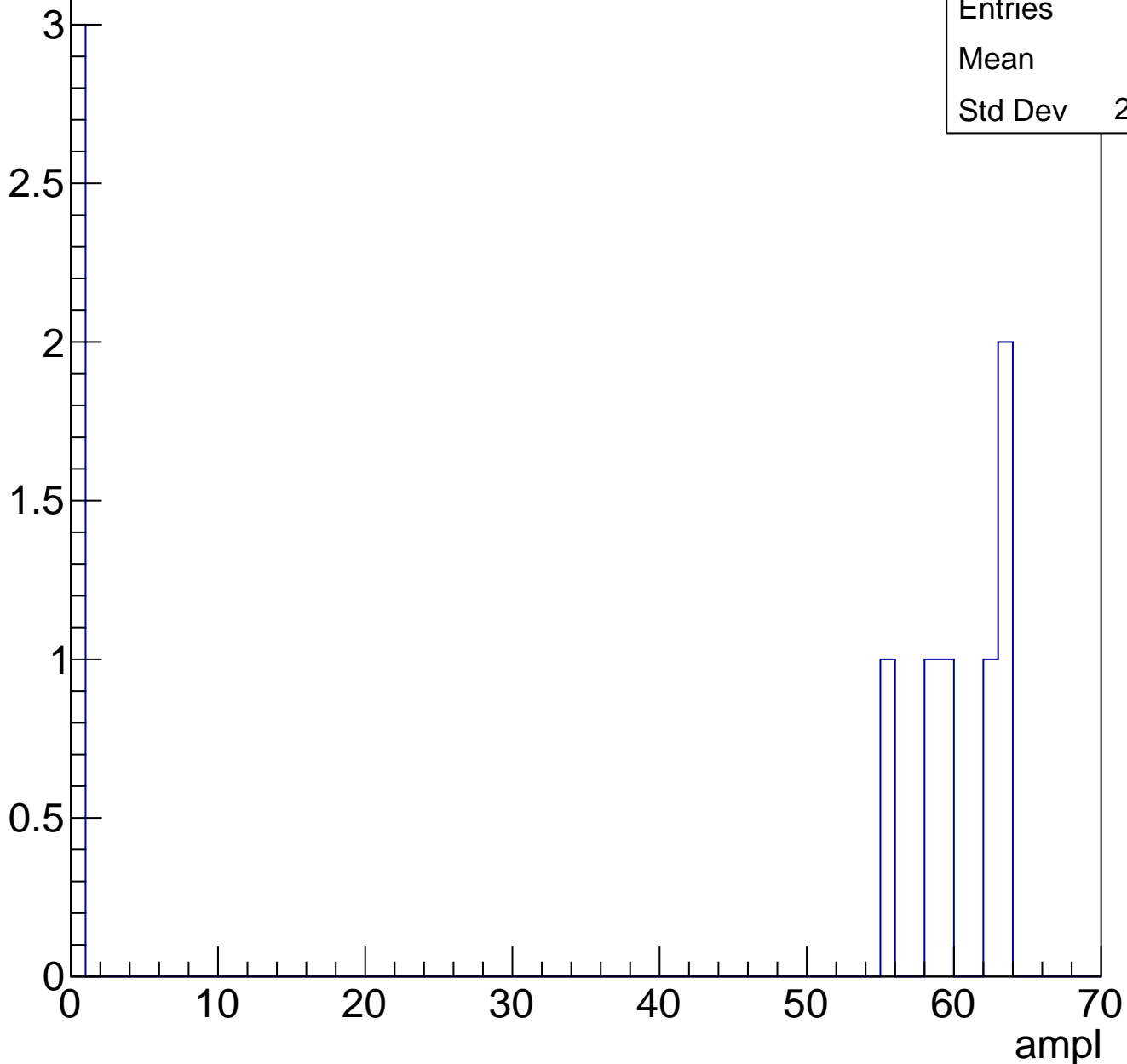
Entries	11
Mean	61.55
Std Dev	1.725



B1L103S, U14-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	9
Mean	40
Std Dev	28.39

B1L103S, U14-ch125, adc0

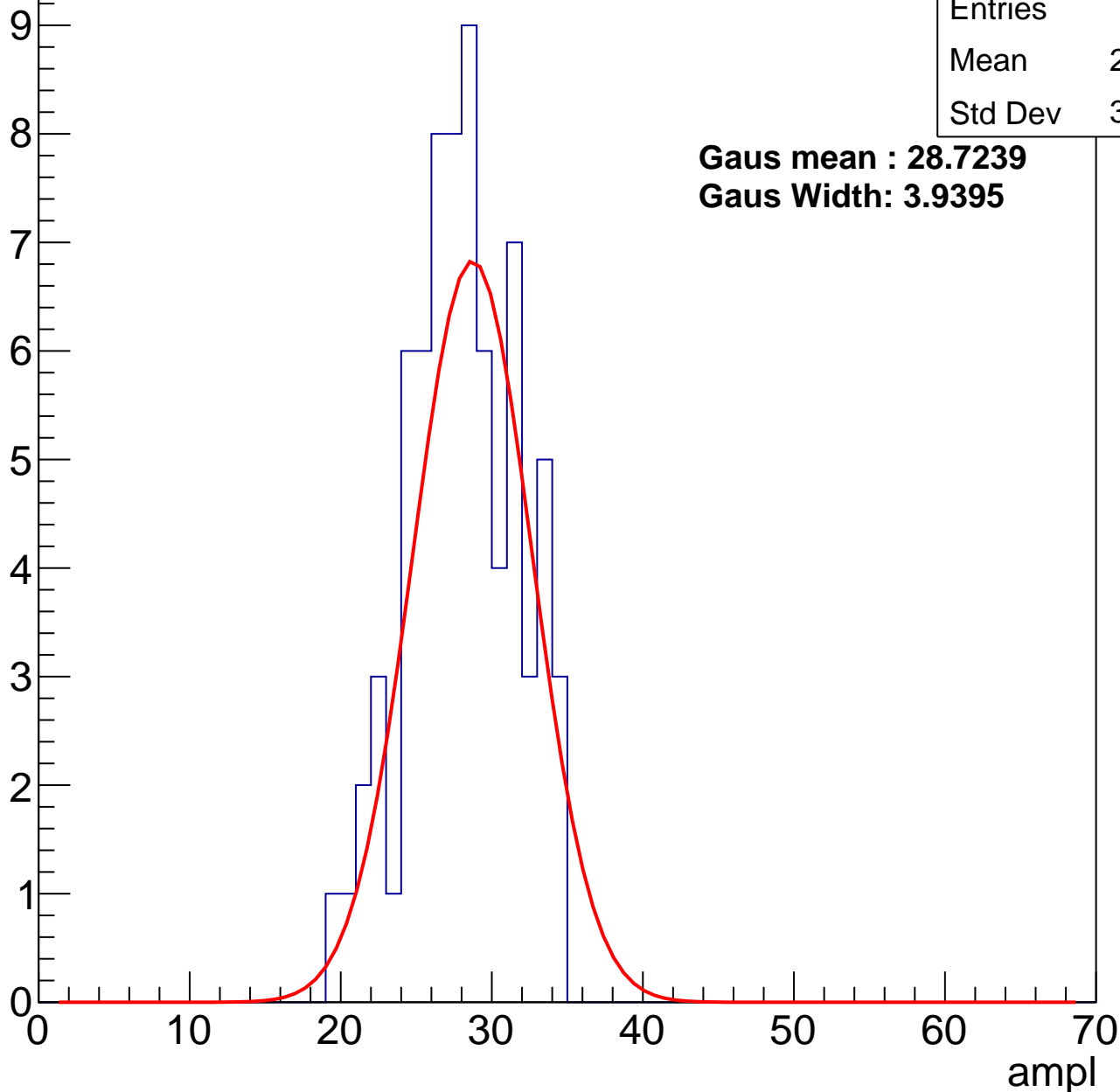
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	27.59
Std Dev	3.549

Gaus mean : 28.7239

Gaus Width: 3.9395



B1L103S, U14-ch125, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	34.59
Std Dev	4.24

Gaus mean : 35.0404

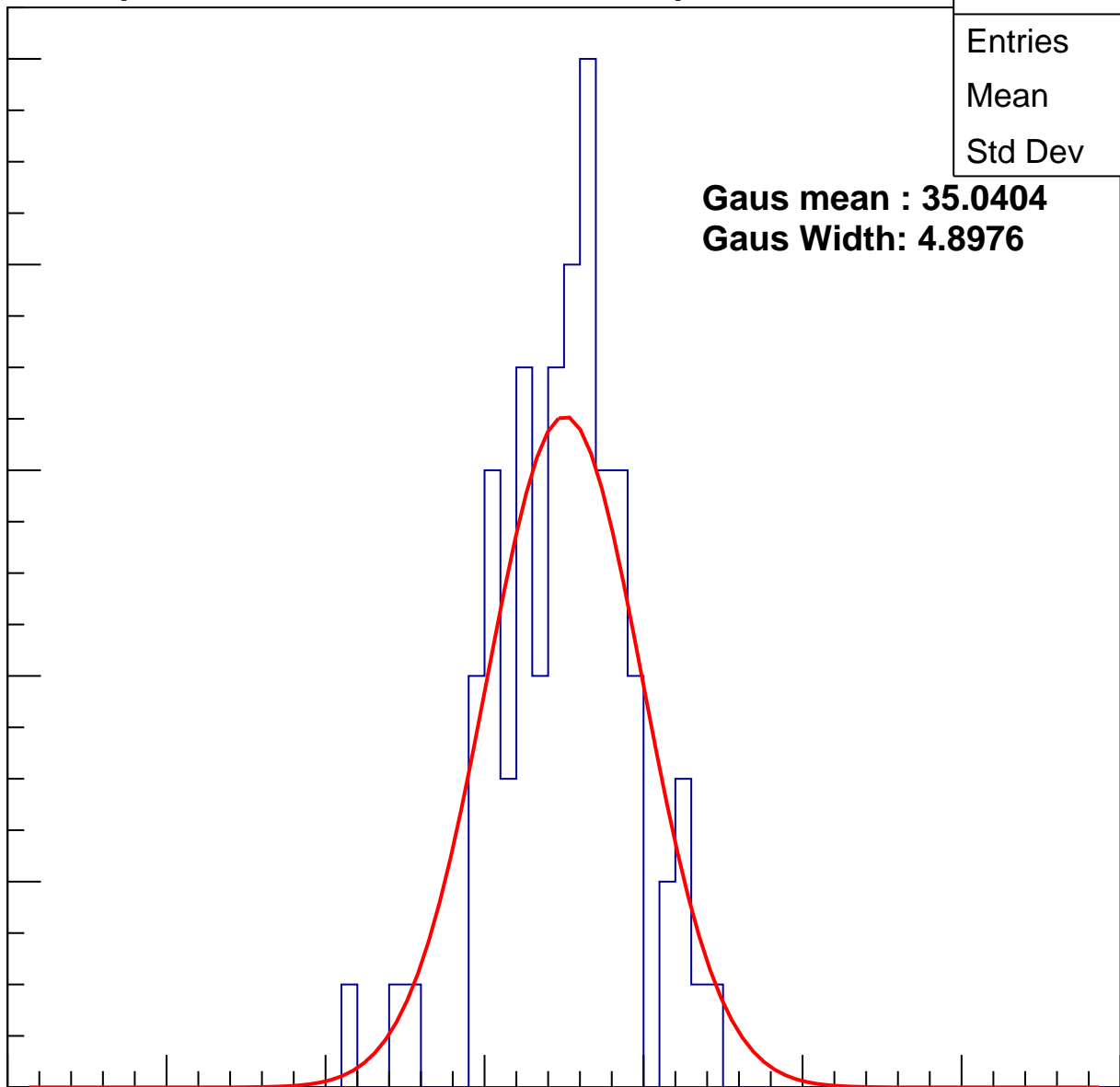
Gaus Width: 4.8976

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U14-ch125, adc2

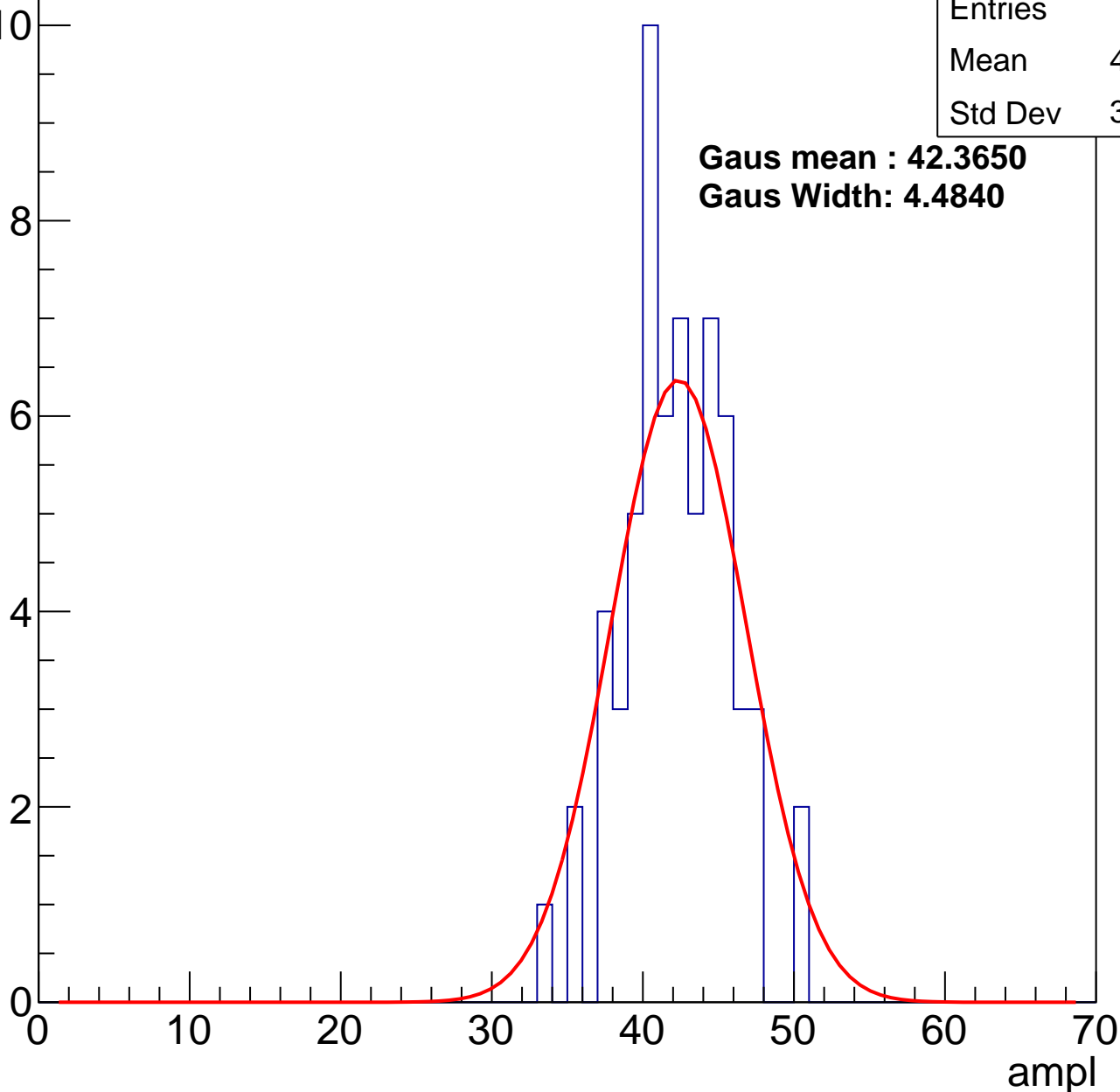
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	41.75
Std Dev	3.428

Gaus mean : 42.3650

Gaus Width: 4.4840

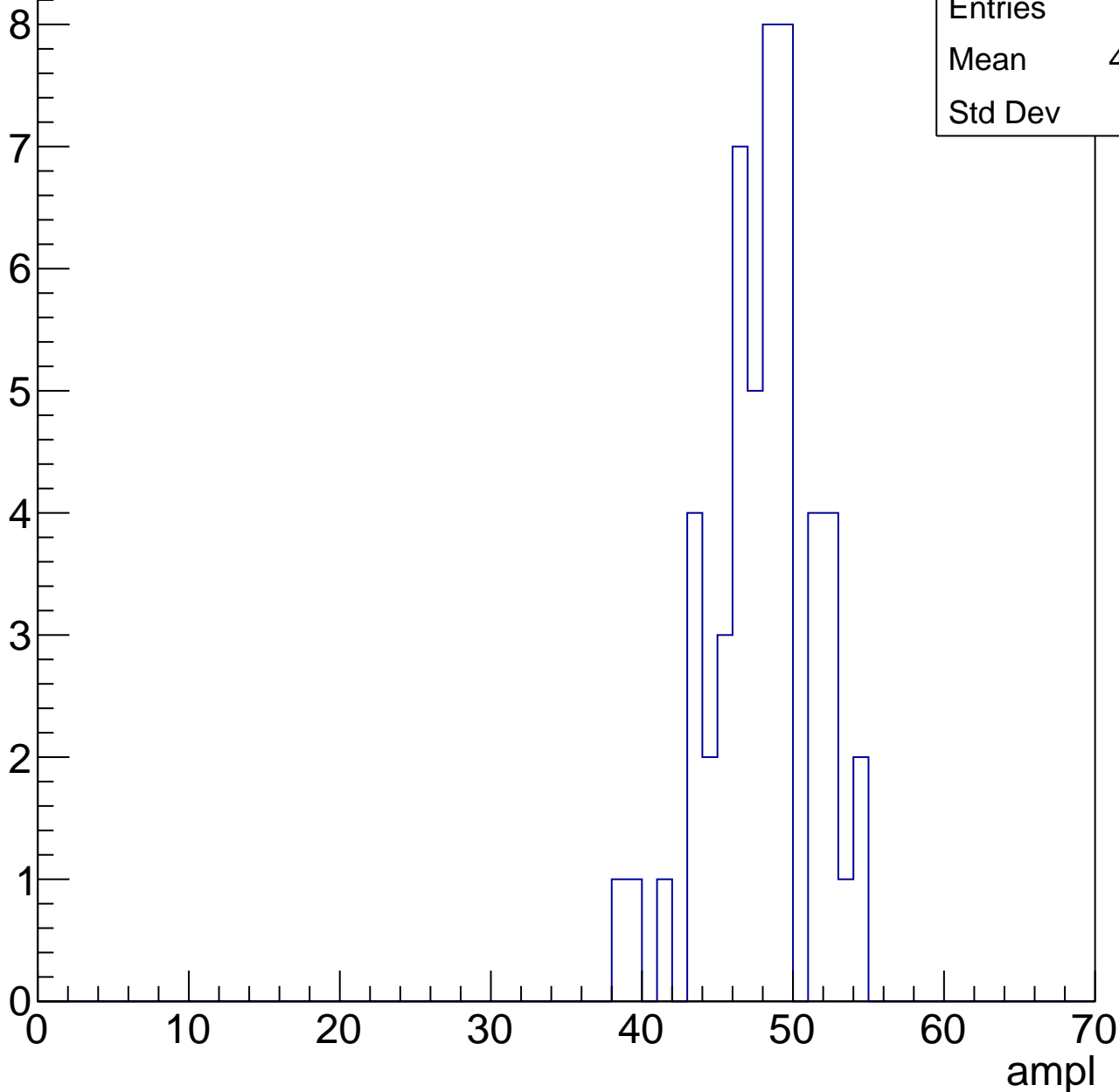


B1L103S, U14-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	47.43
Std Dev	3.46

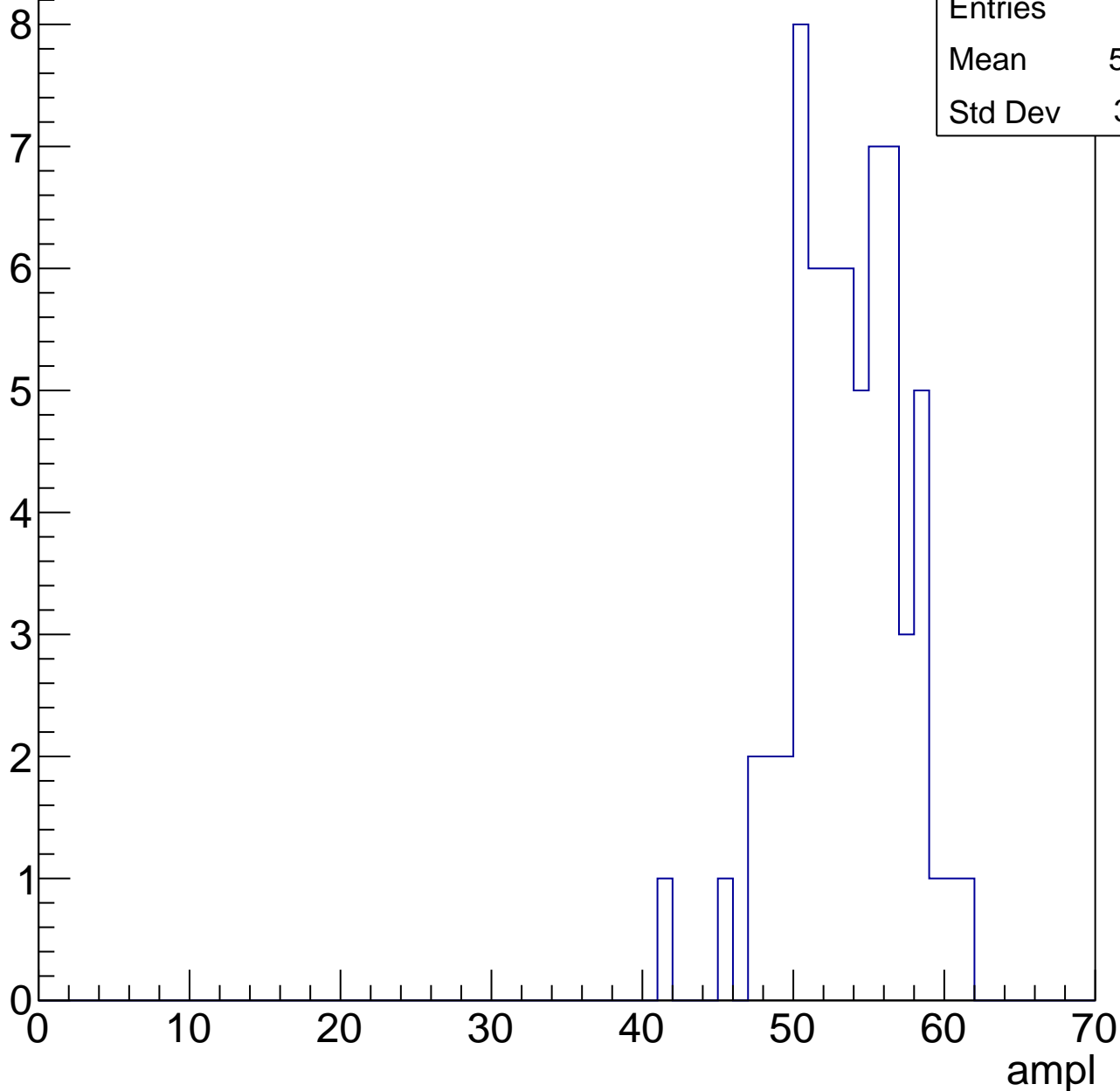


B1L103S, U14-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	53.09
Std Dev	3.711

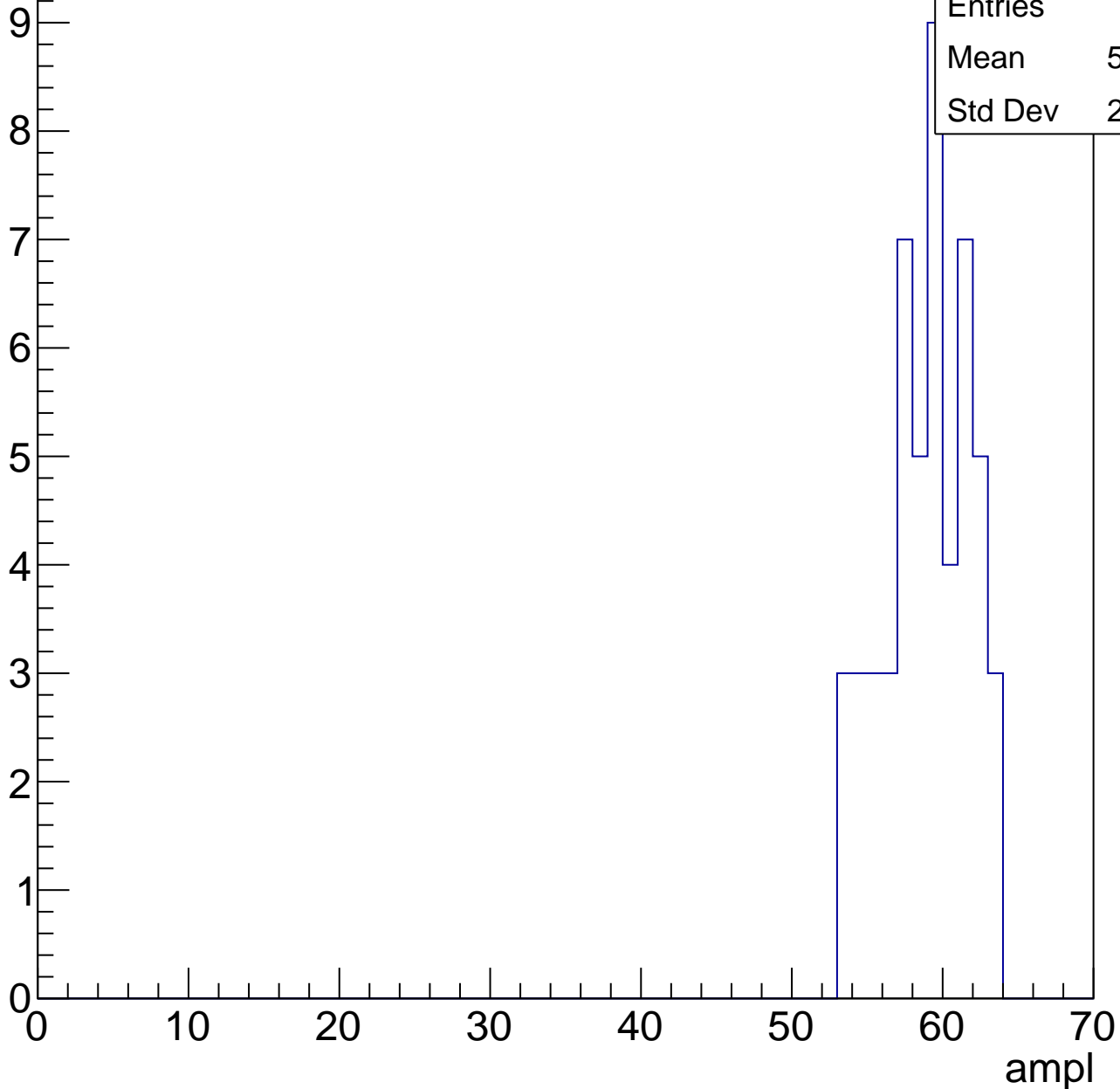


B1L103S, U14-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	58.46
Std Dev	2.777

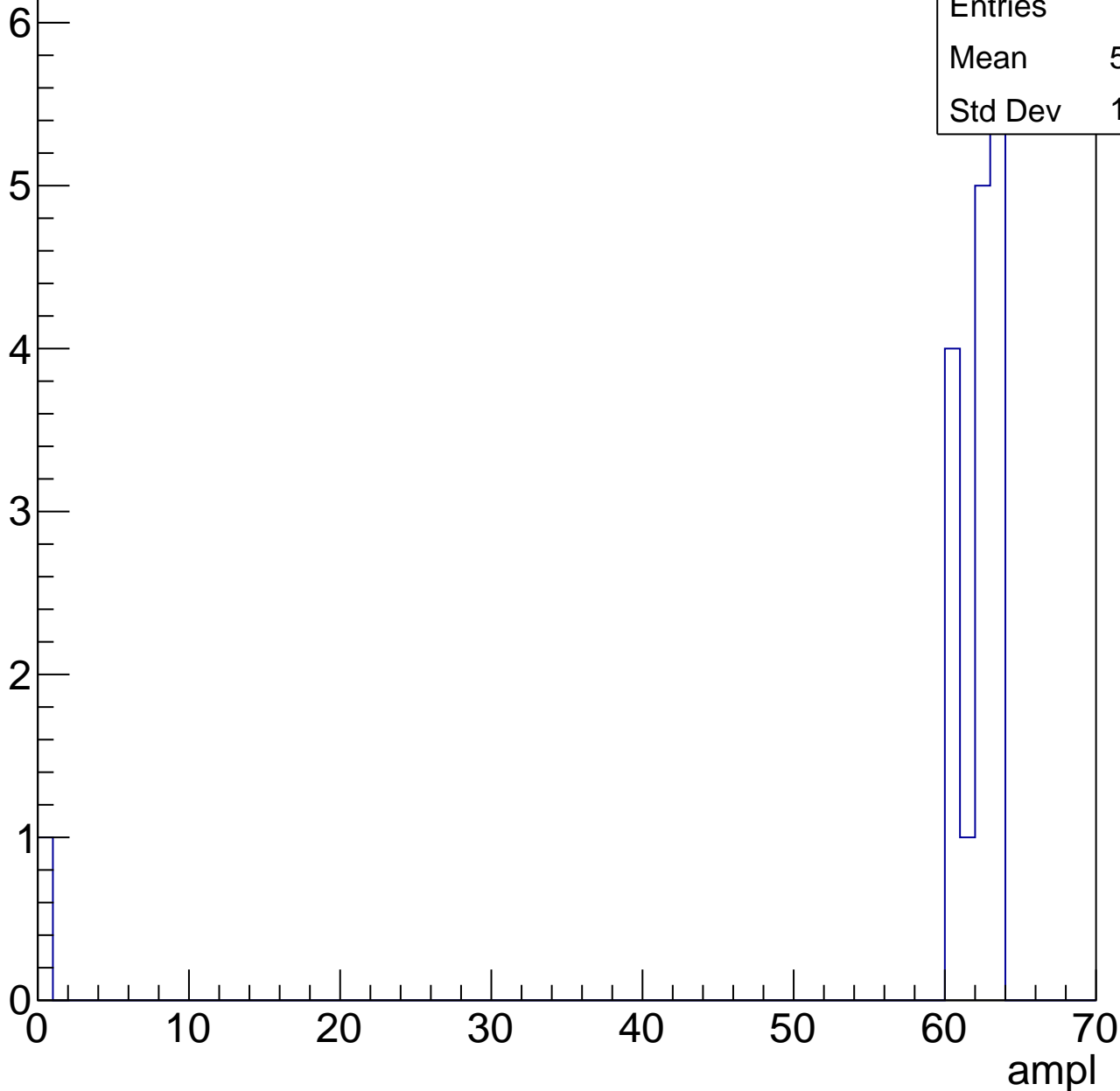


B1L103S, U14-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

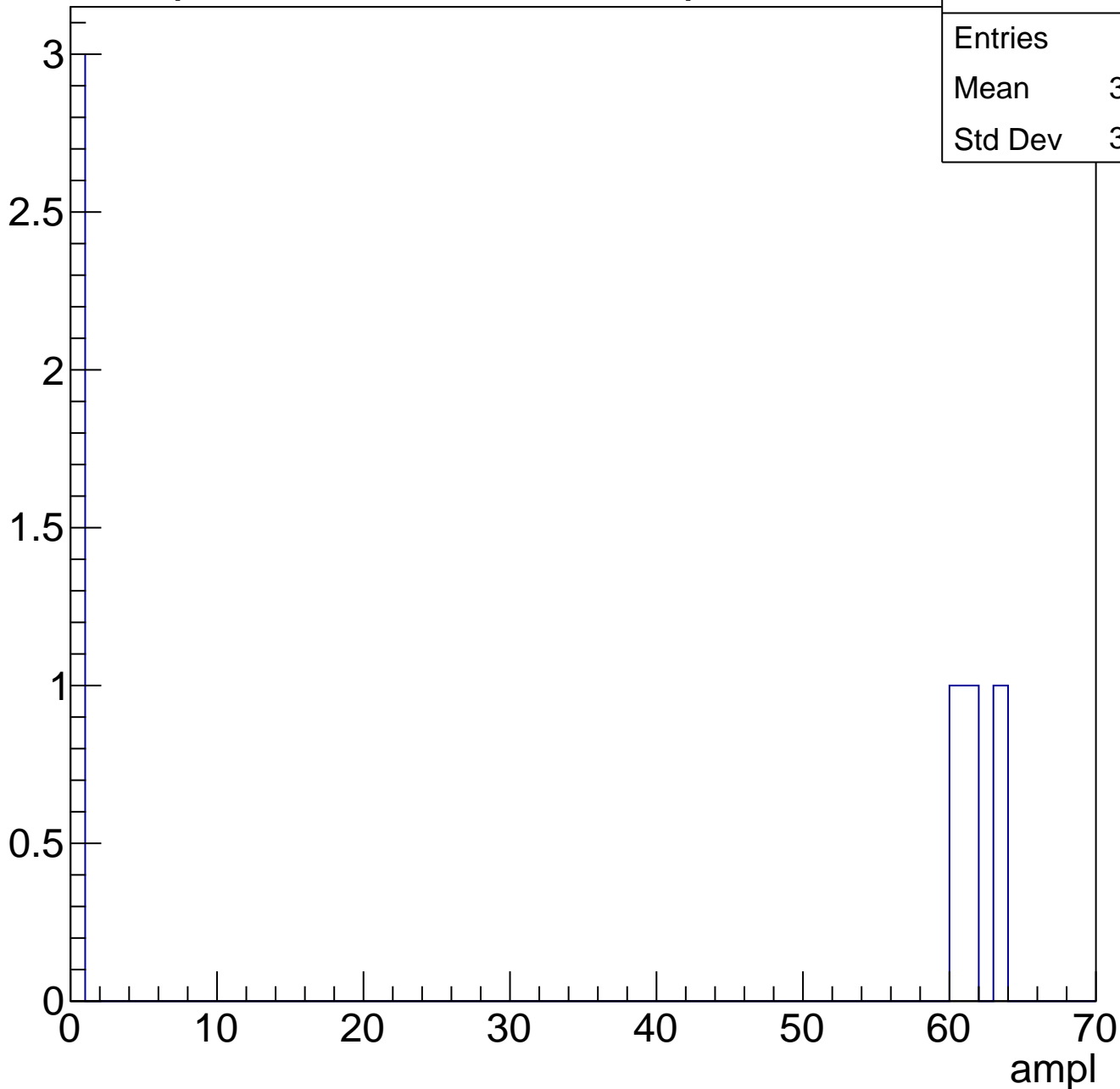
Entries	17
Mean	58.18
Std Dev	14.59



B1L103S, U14-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch126, adc0

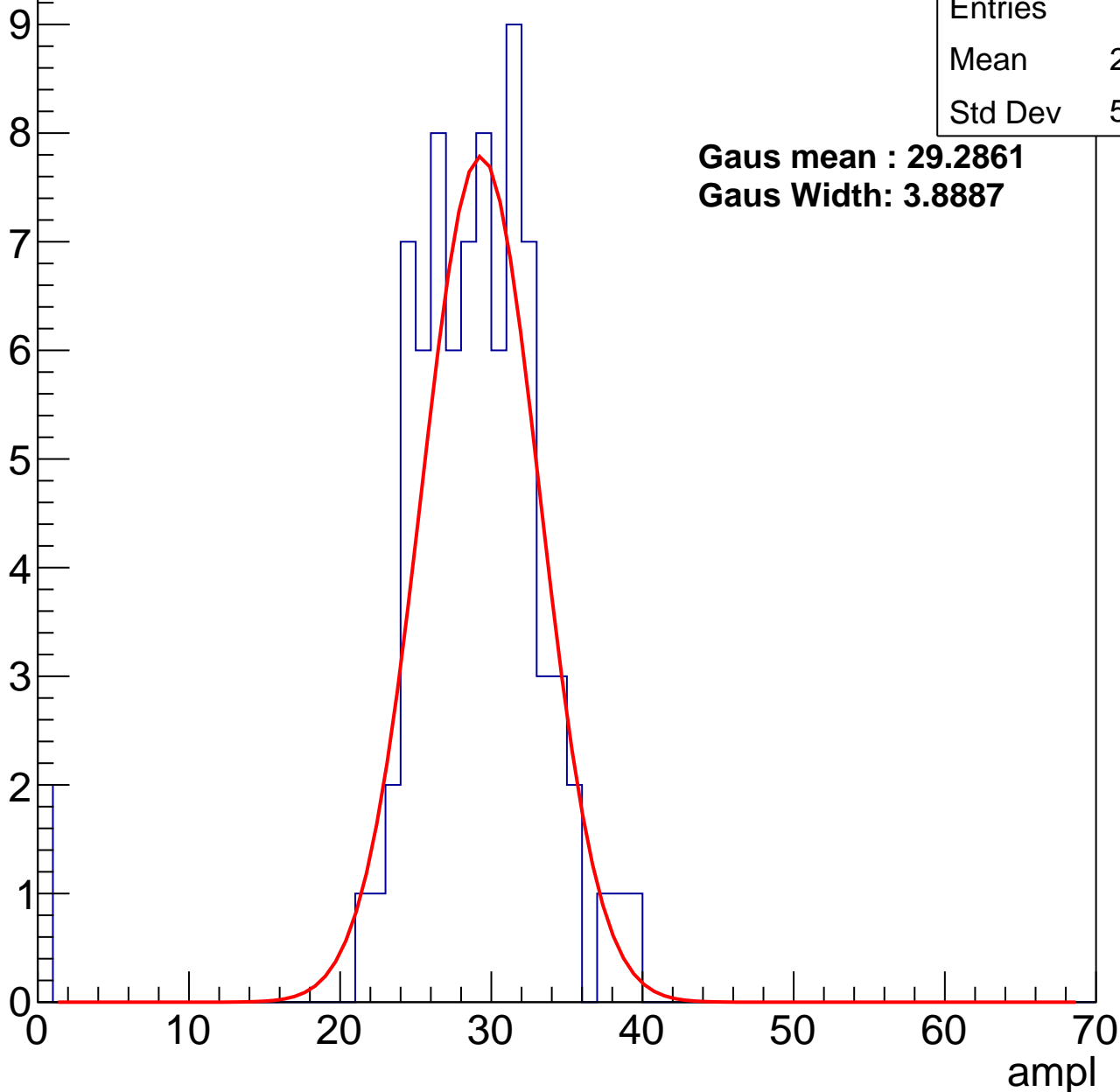
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	28.06
Std Dev	5.789

Gaus mean : 29.2861

Gaus Width: 3.8887



B1L103S, U14-ch126, adc1

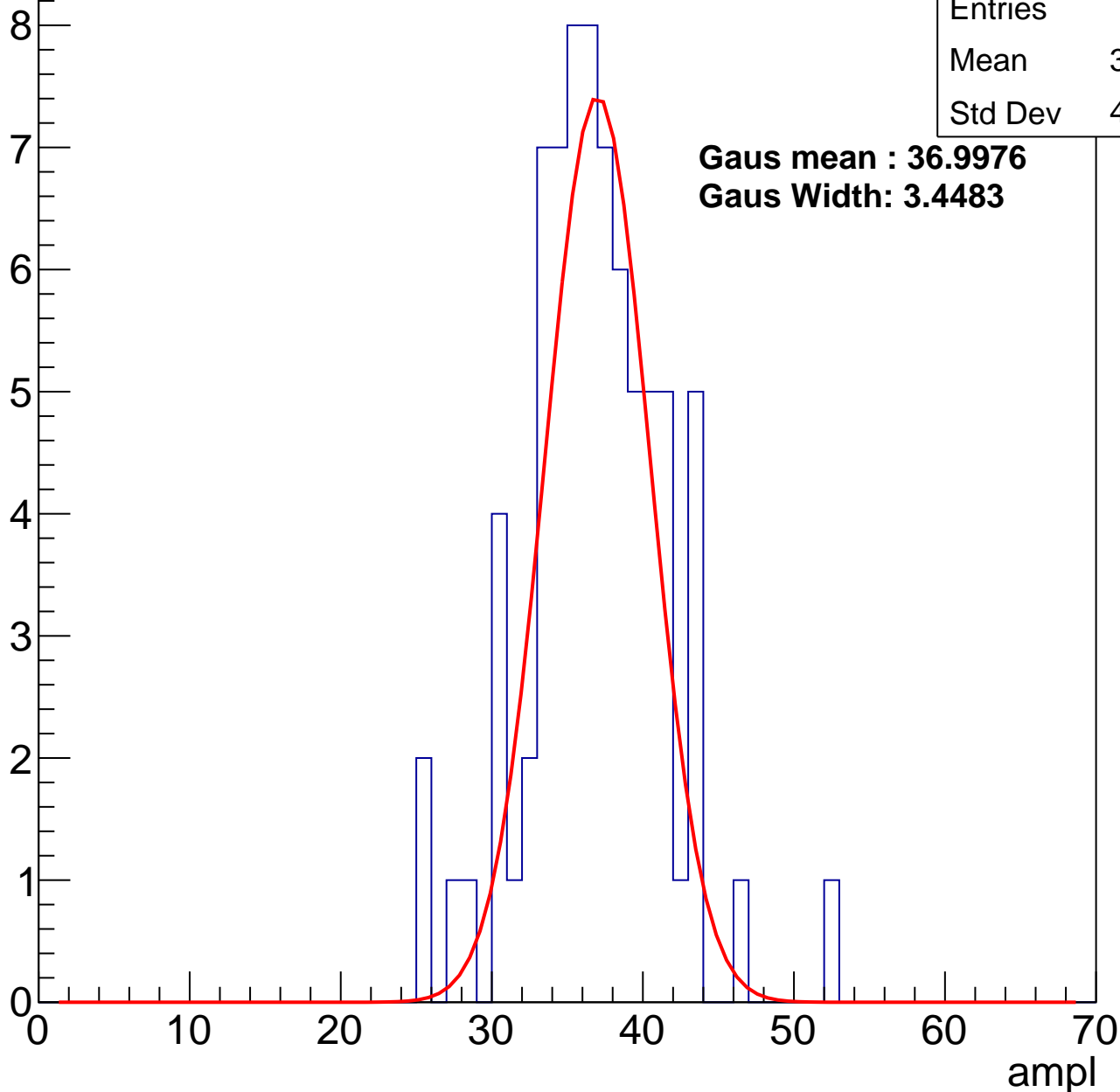
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.35
Std Dev	4.552

Gaus mean : 36.9976

Gaus Width: 3.4483



B1L103S, U14-ch126, adc2

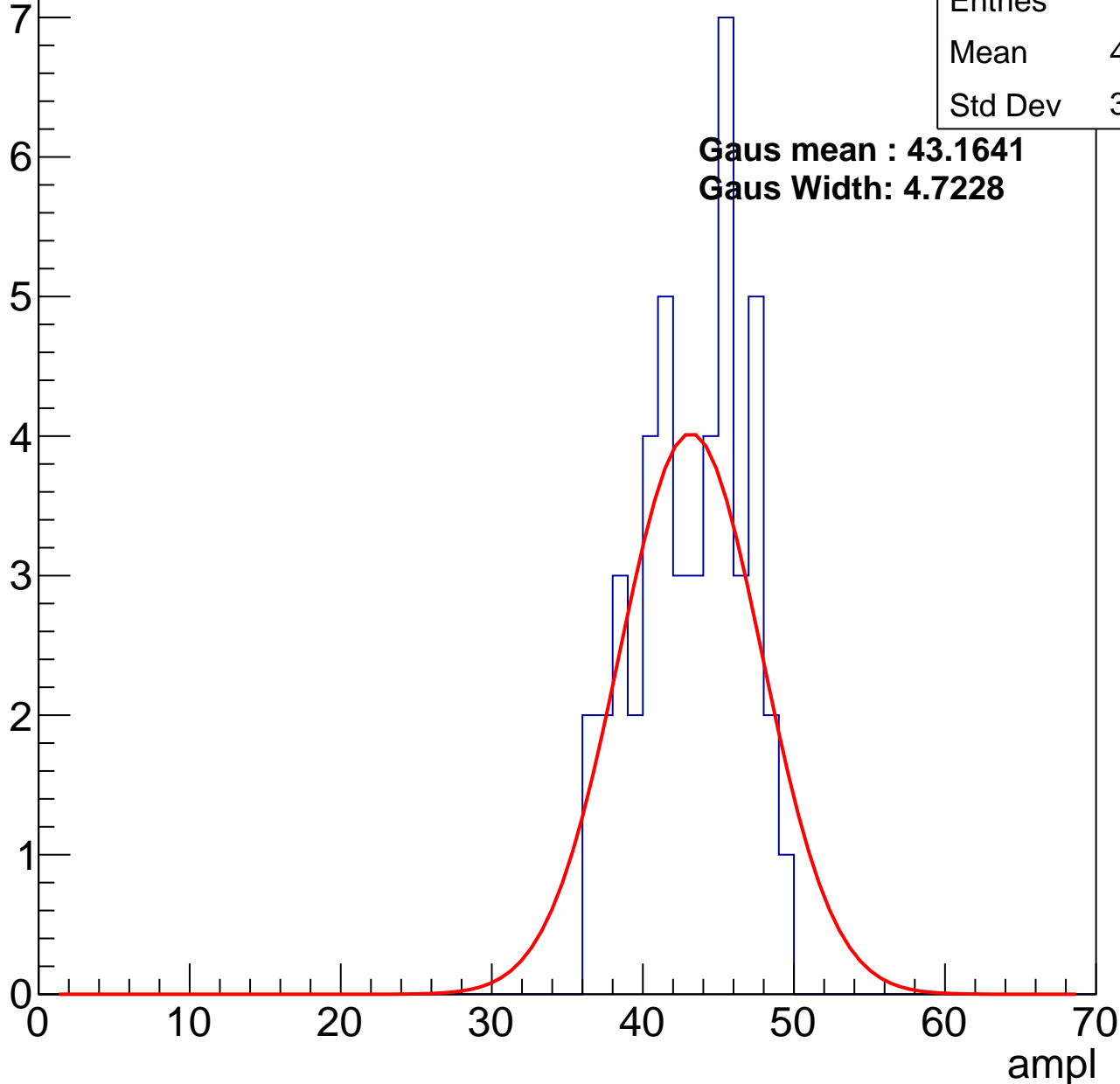
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	42.76
Std Dev	3.484

Gaus mean : 43.1641

Gaus Width: 4.7228

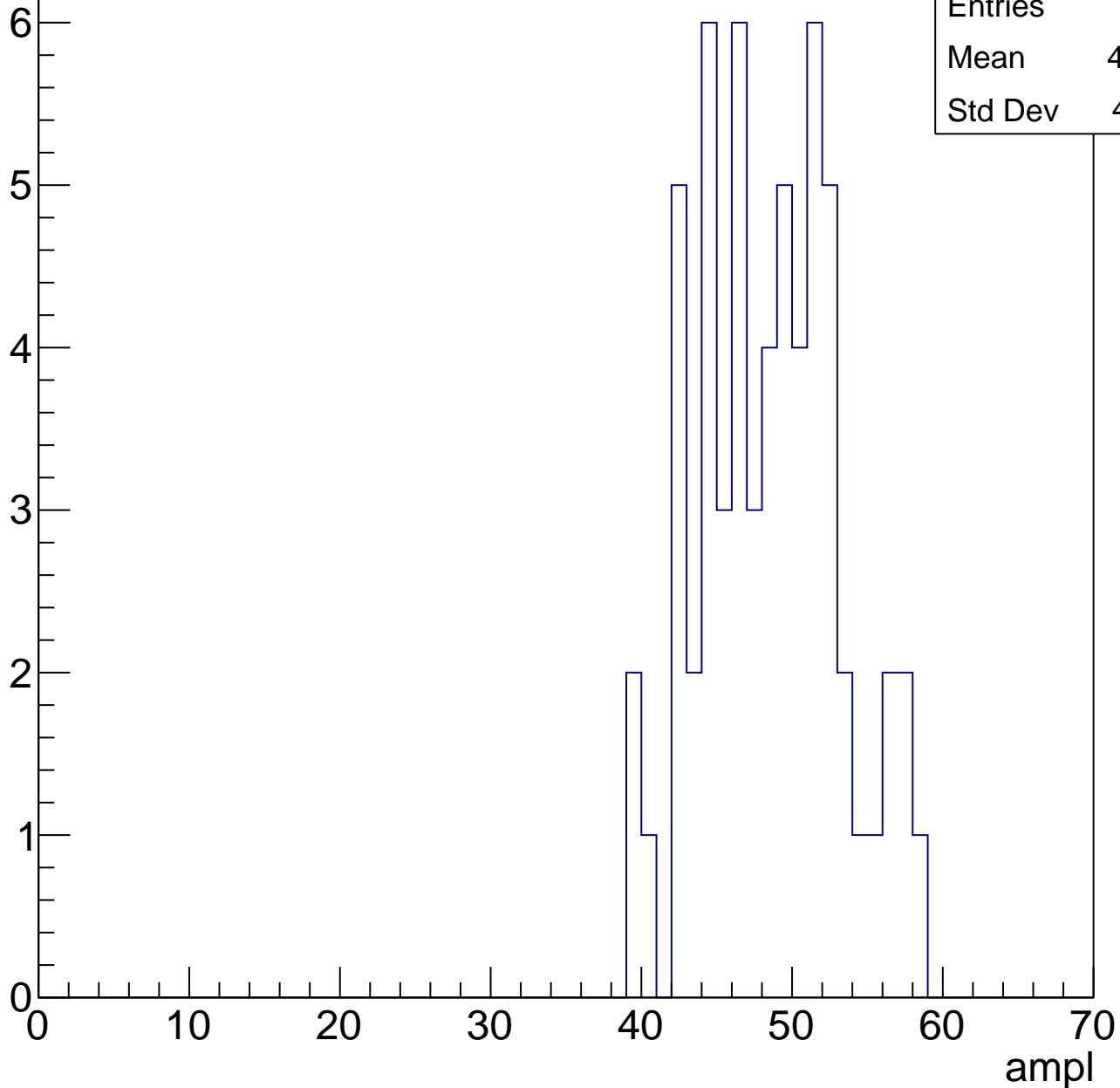


B1L103S, U14-ch126, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	48.07
Std Dev	4.591

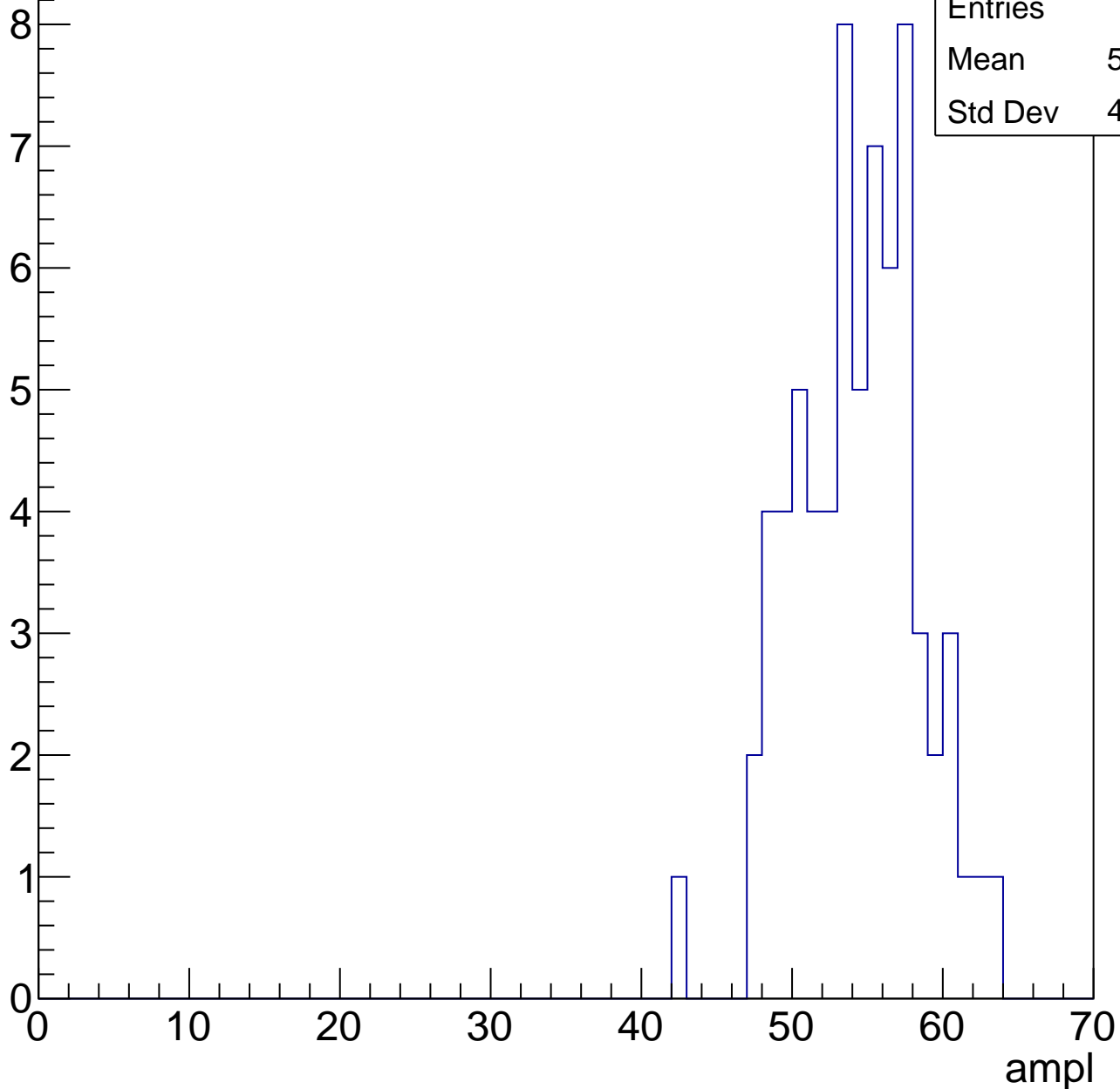


B1L103S, U14-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	53.84
Std Dev	4.042

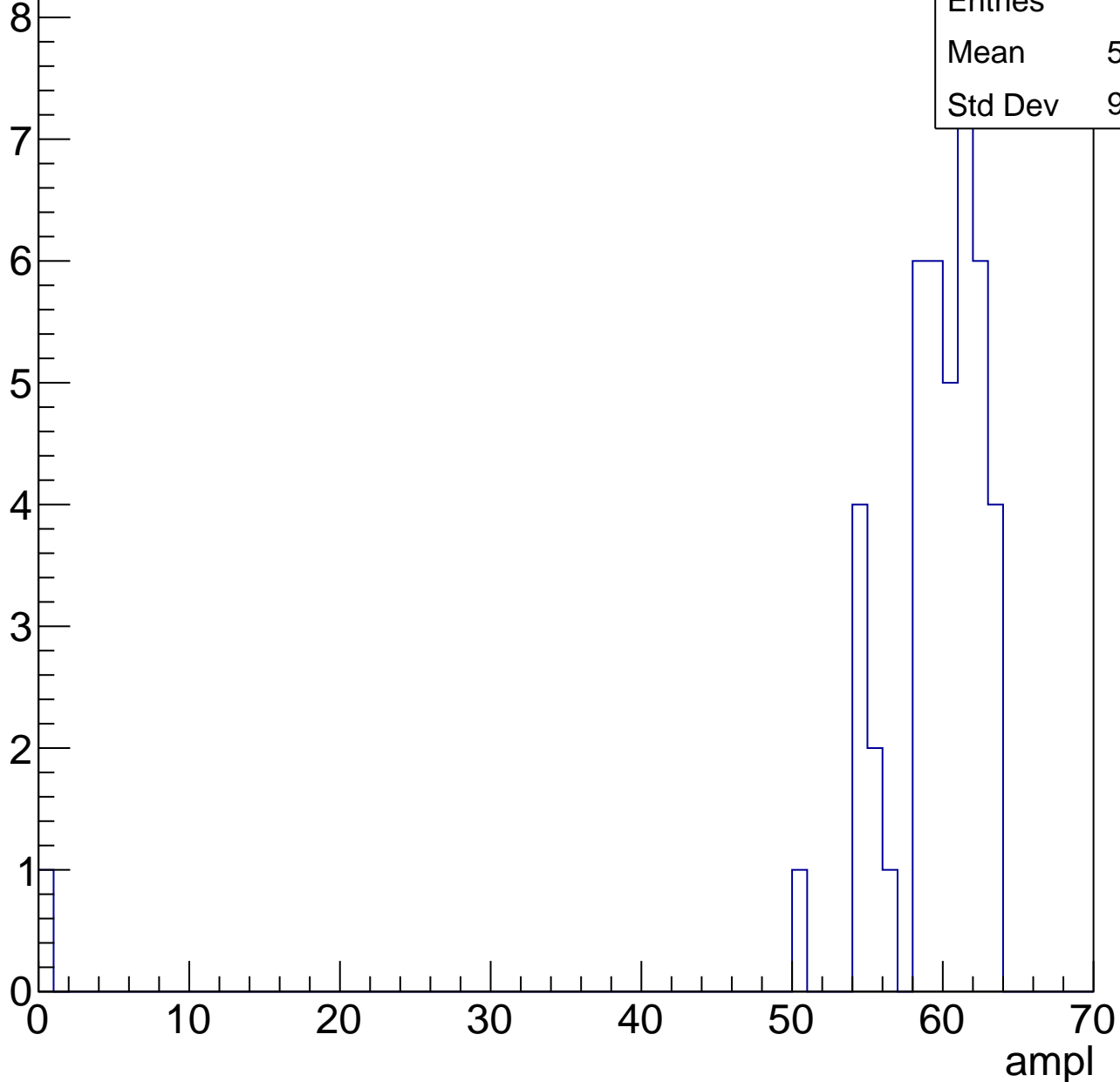


B1L103S, U14-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

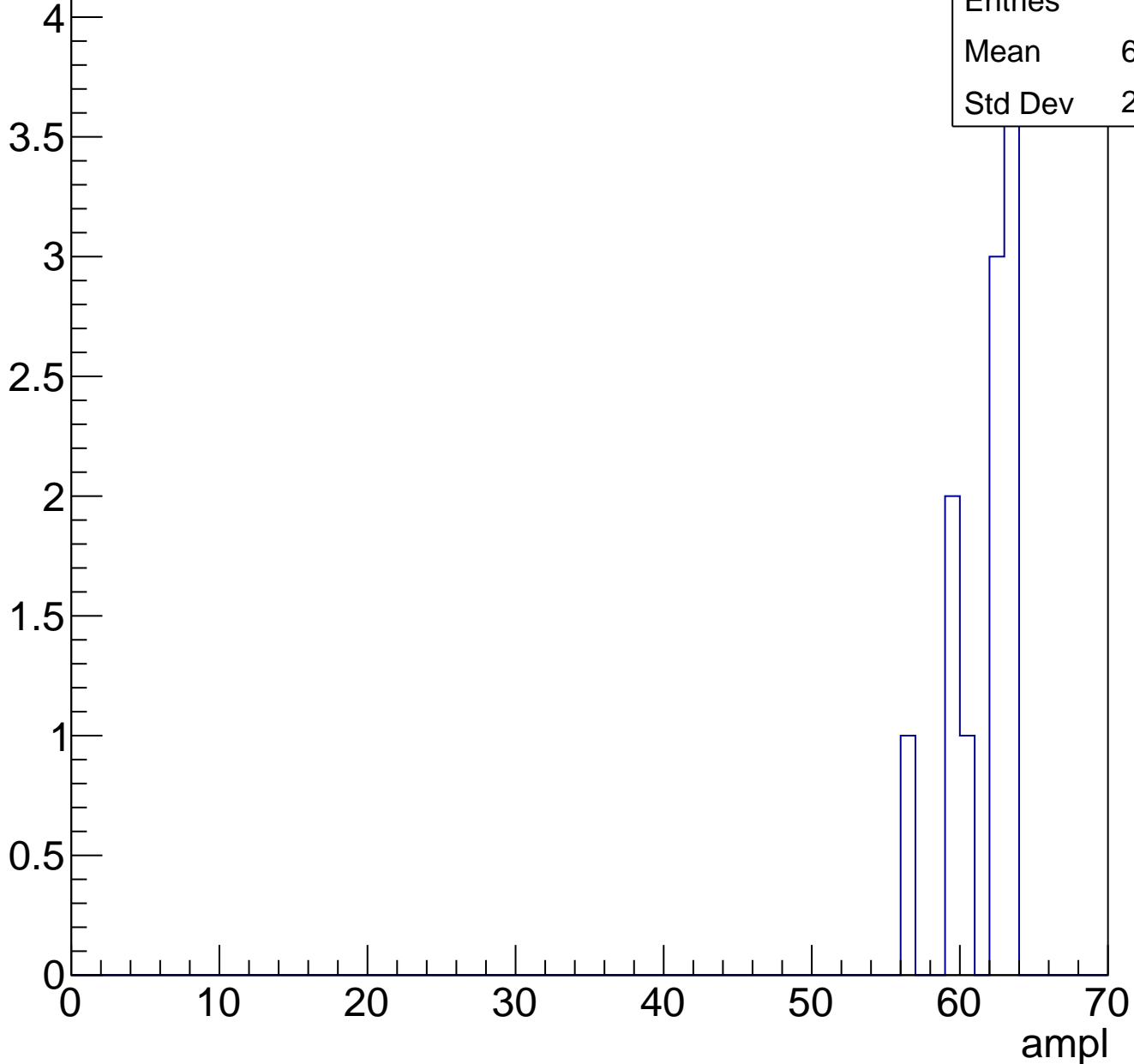
Entries	44
Mean	57.86
Std Dev	9.302



B1L103S, U14-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

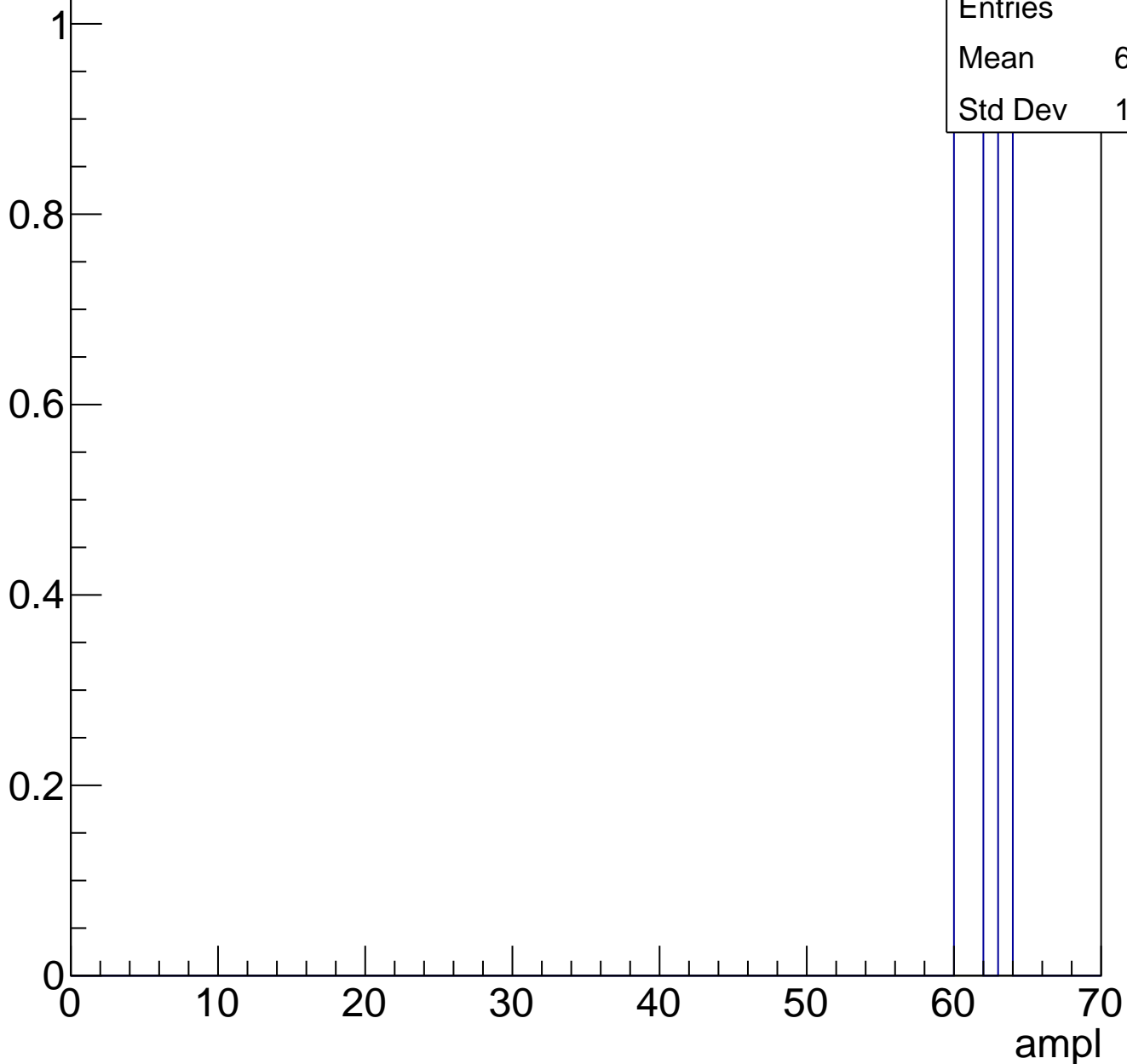
Entry



B1L103S, U14-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch127, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	29.95
Std Dev	5.165

Gaus mean : 30.7661

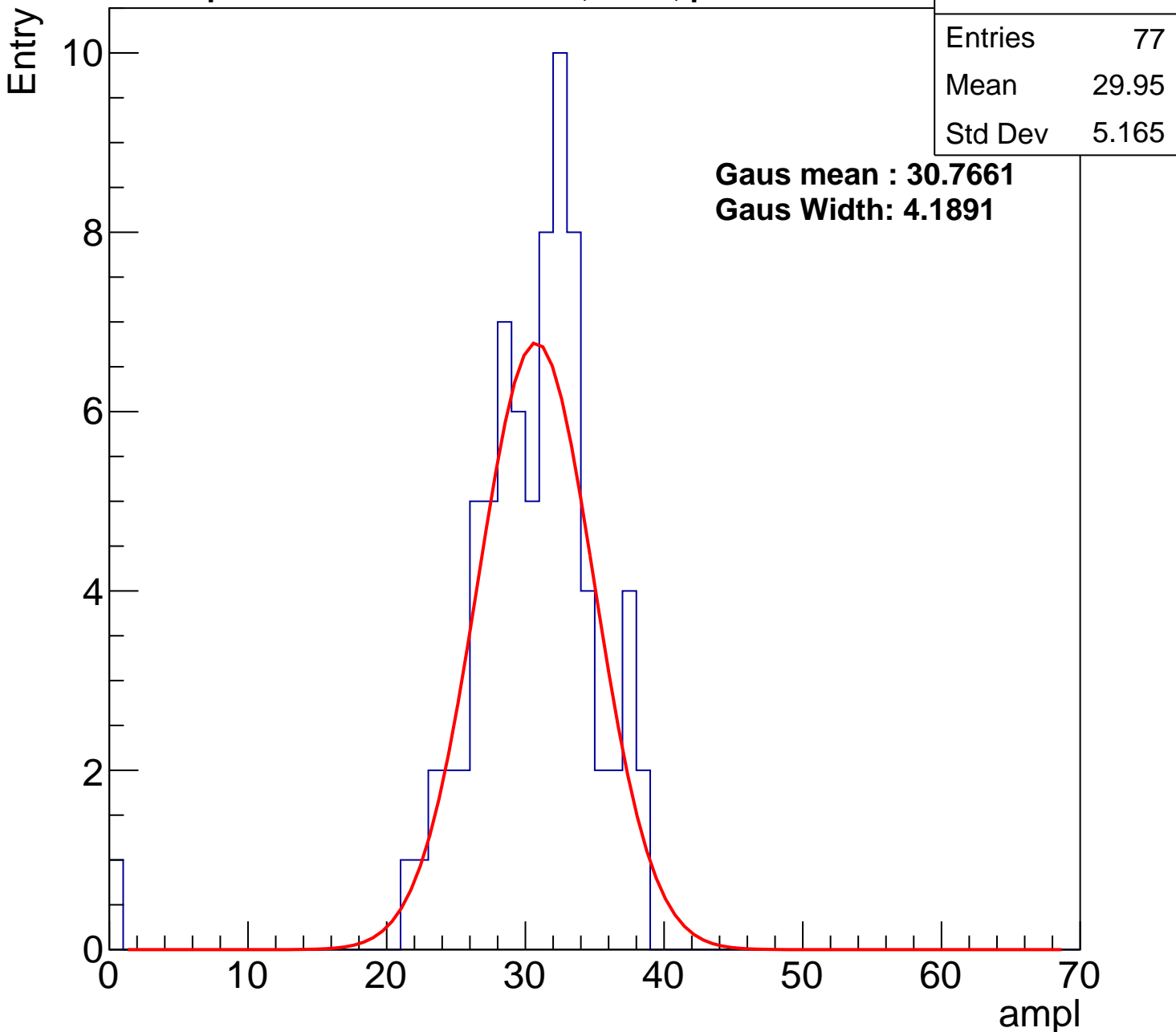
Gaus Width: 4.1891

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U14-ch127, adc1

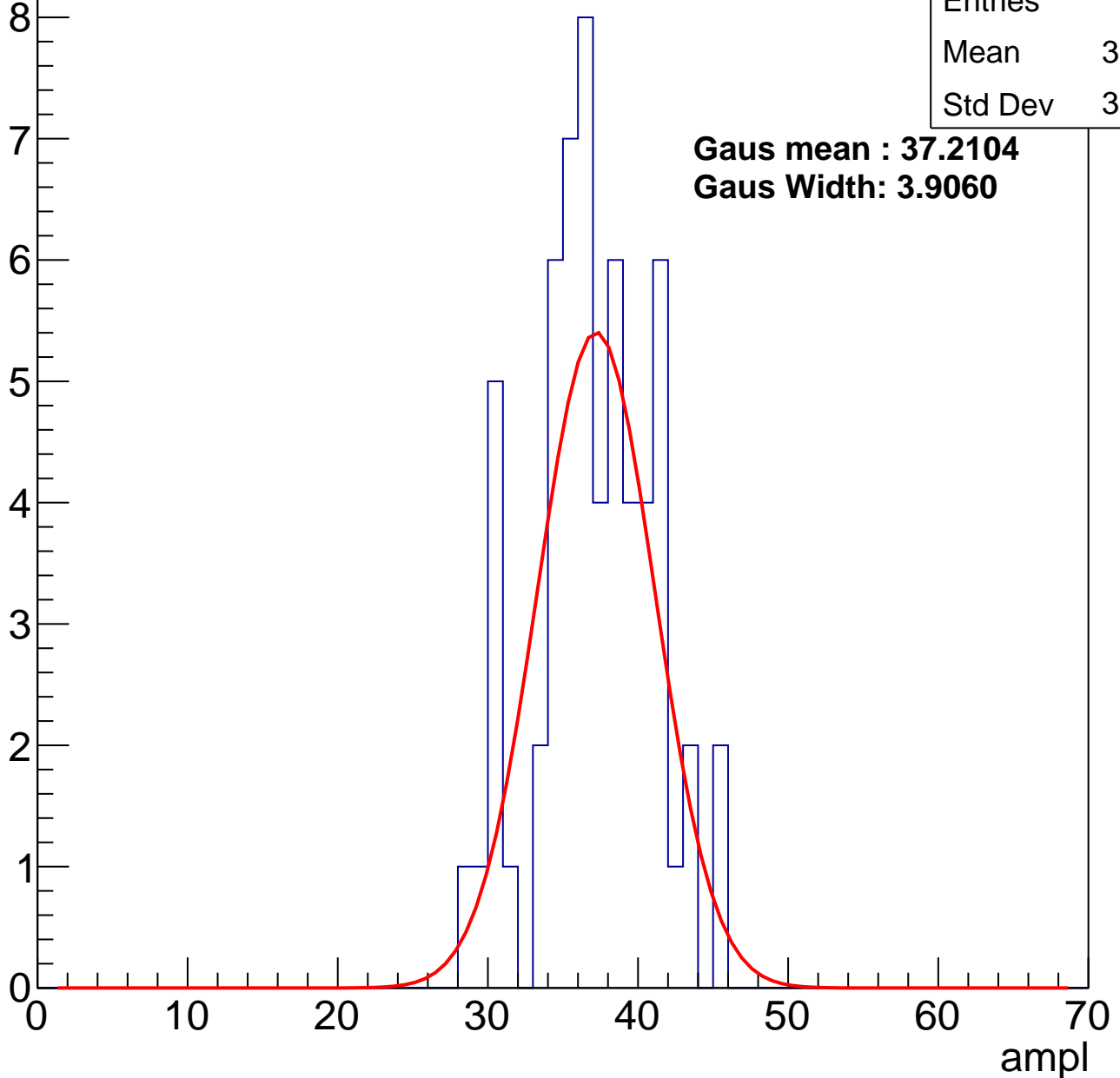
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.62
Std Dev	3.912

Gaus mean : 37.2104

Gaus Width: 3.9060



B1L103S, U14-ch127, adc2

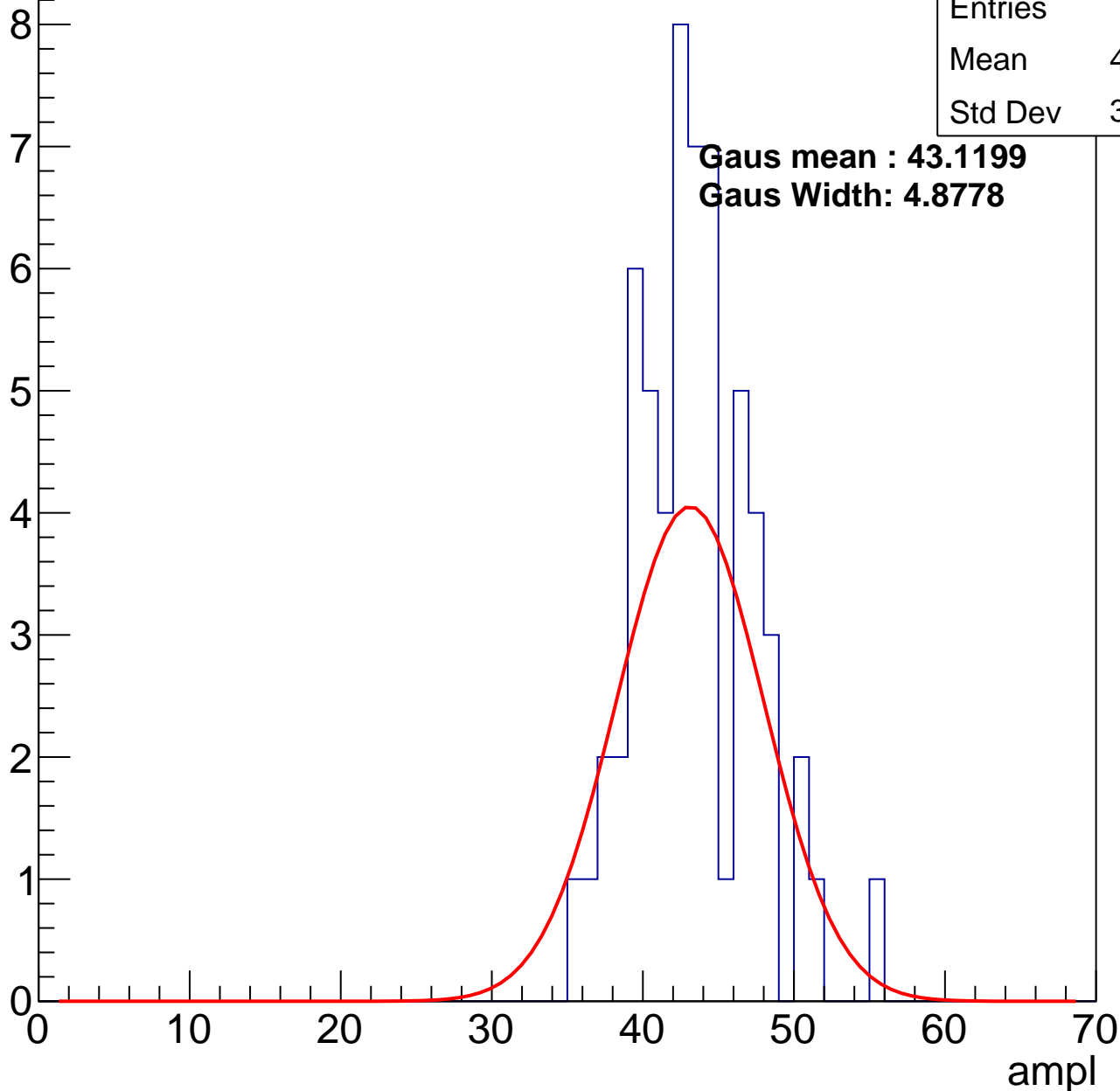
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.95
Std Dev	3.875

Gaus mean : 43.1199

Gaus Width: 4.8778

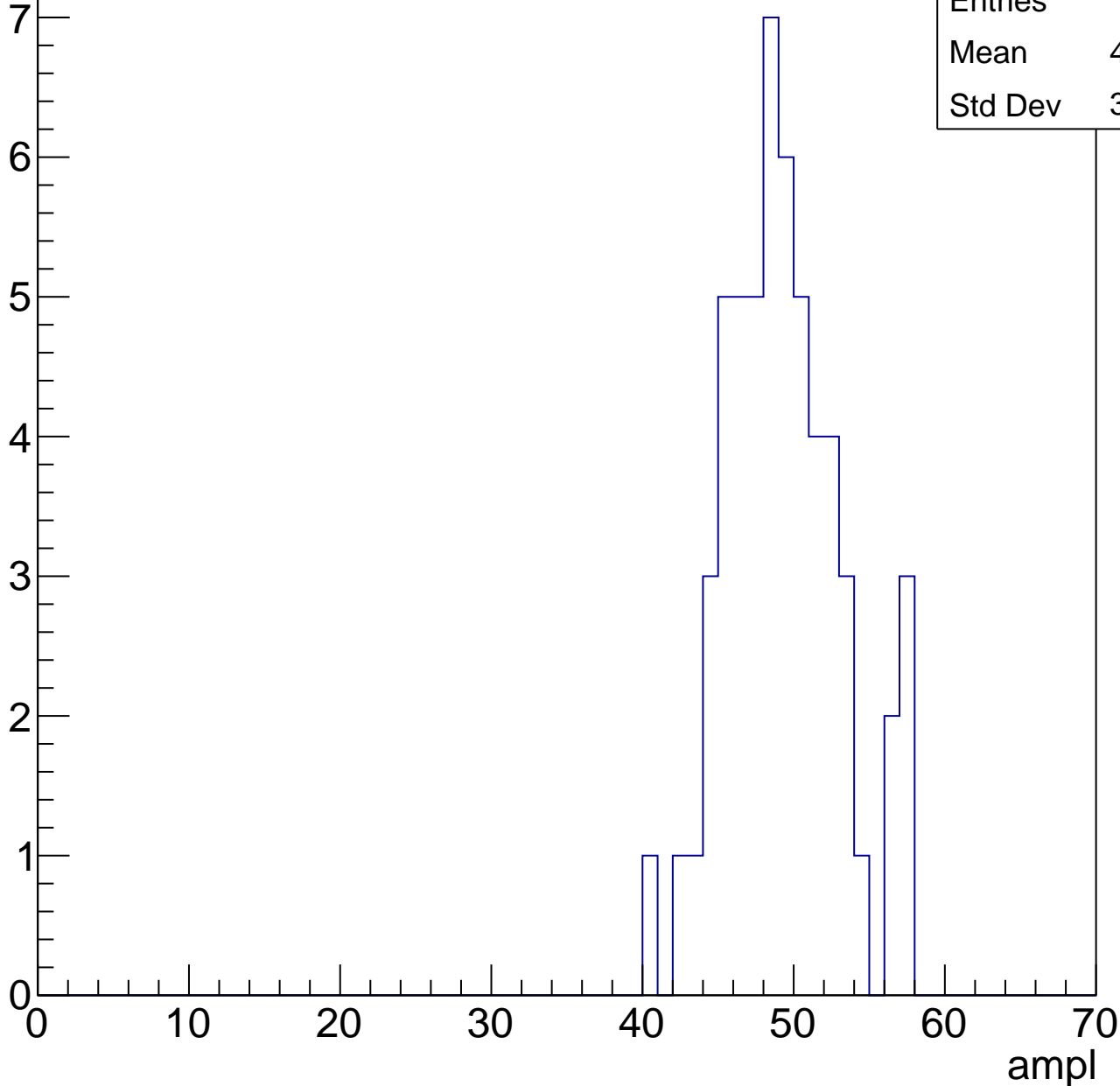


B1L103S, U14-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	48.84
Std Dev	3.807

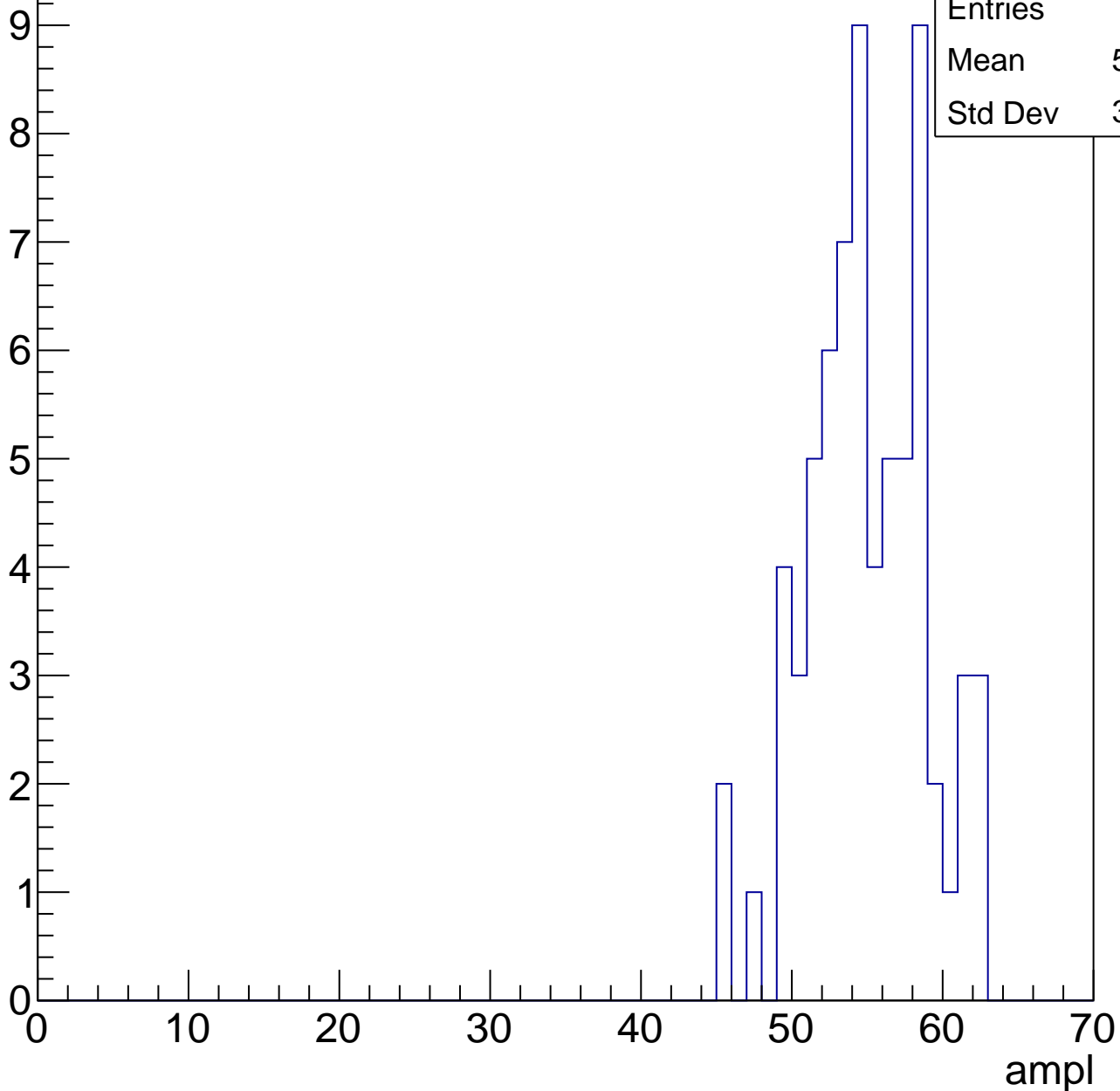


B1L103S, U14-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

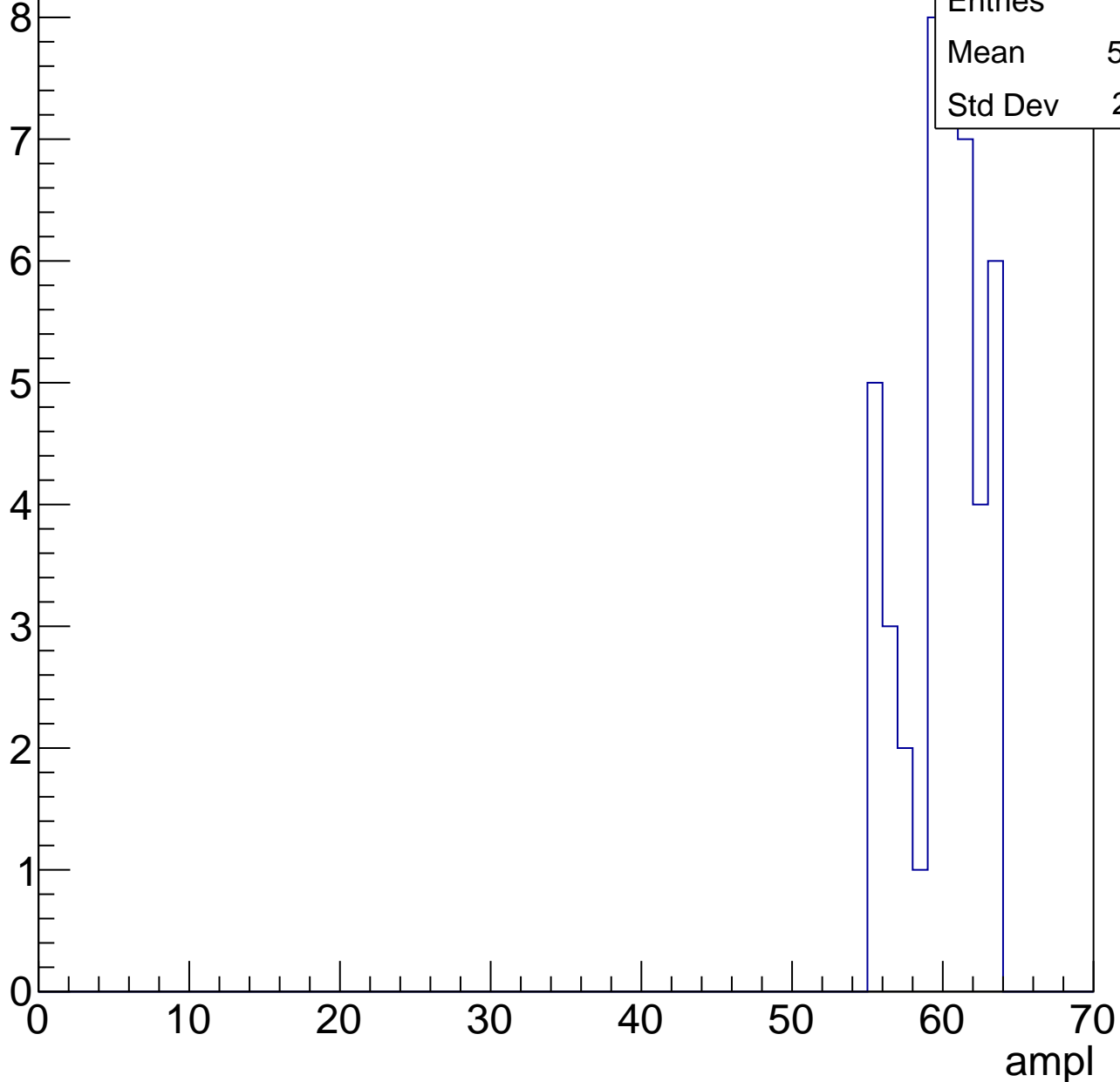
Entries	69
Mean	54.51
Std Dev	3.911



B1L103S, U14-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

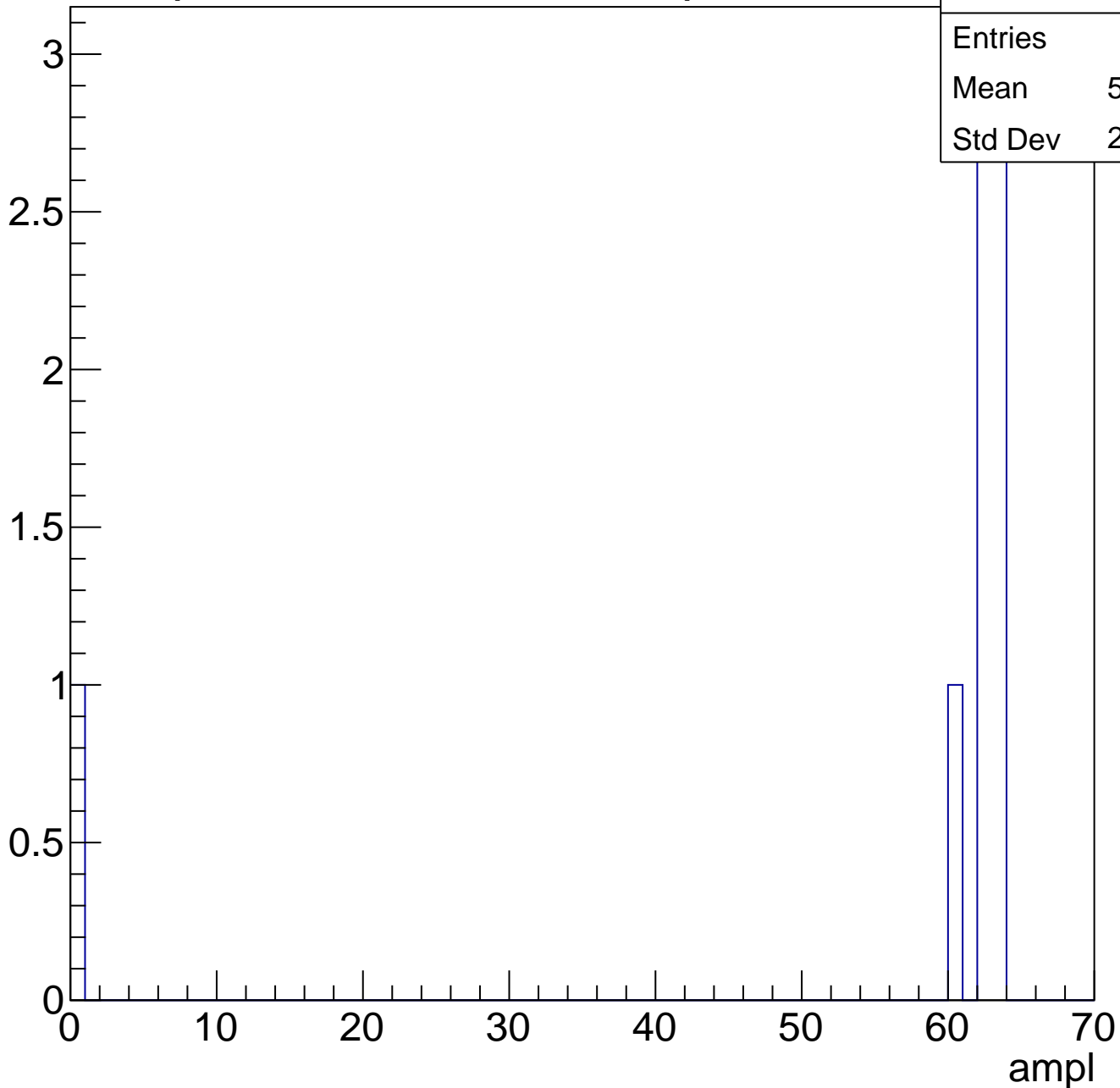
Entry



B1L103S, U14-ch127, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

