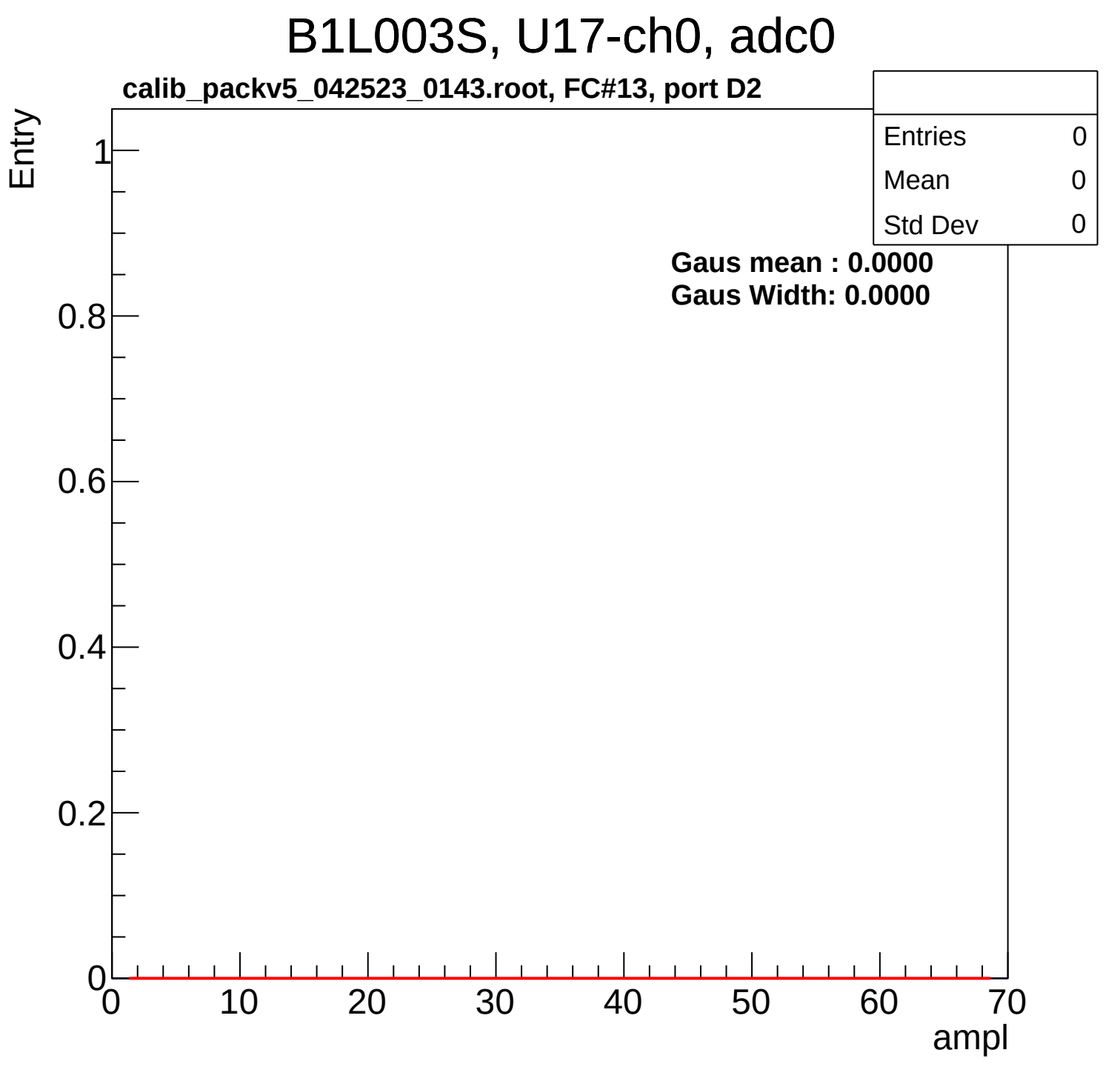
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Std Dev 0
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Gaus Width: 0.0000
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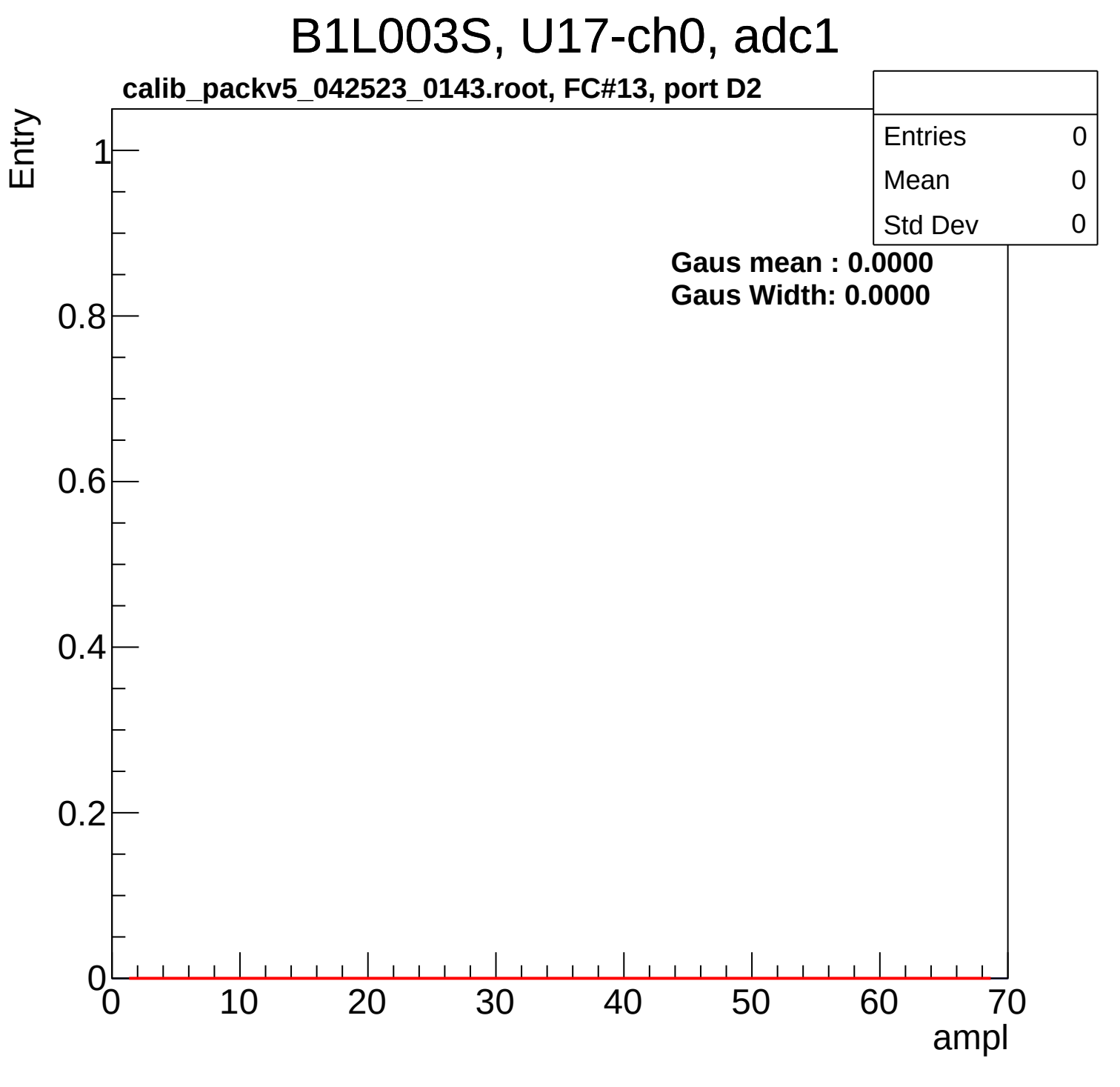
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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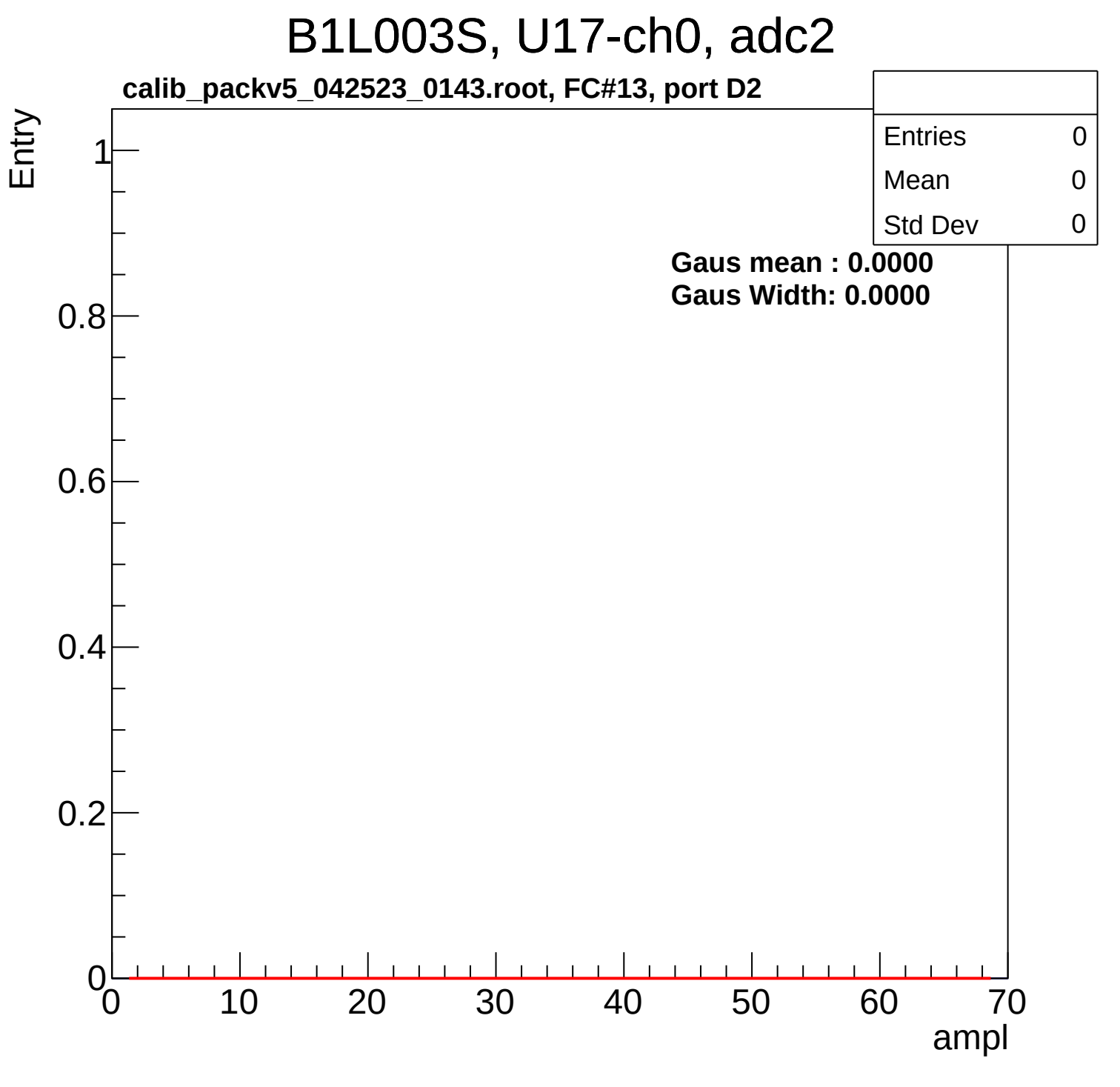
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Std Dev 0
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Gaus Width: 0.0000
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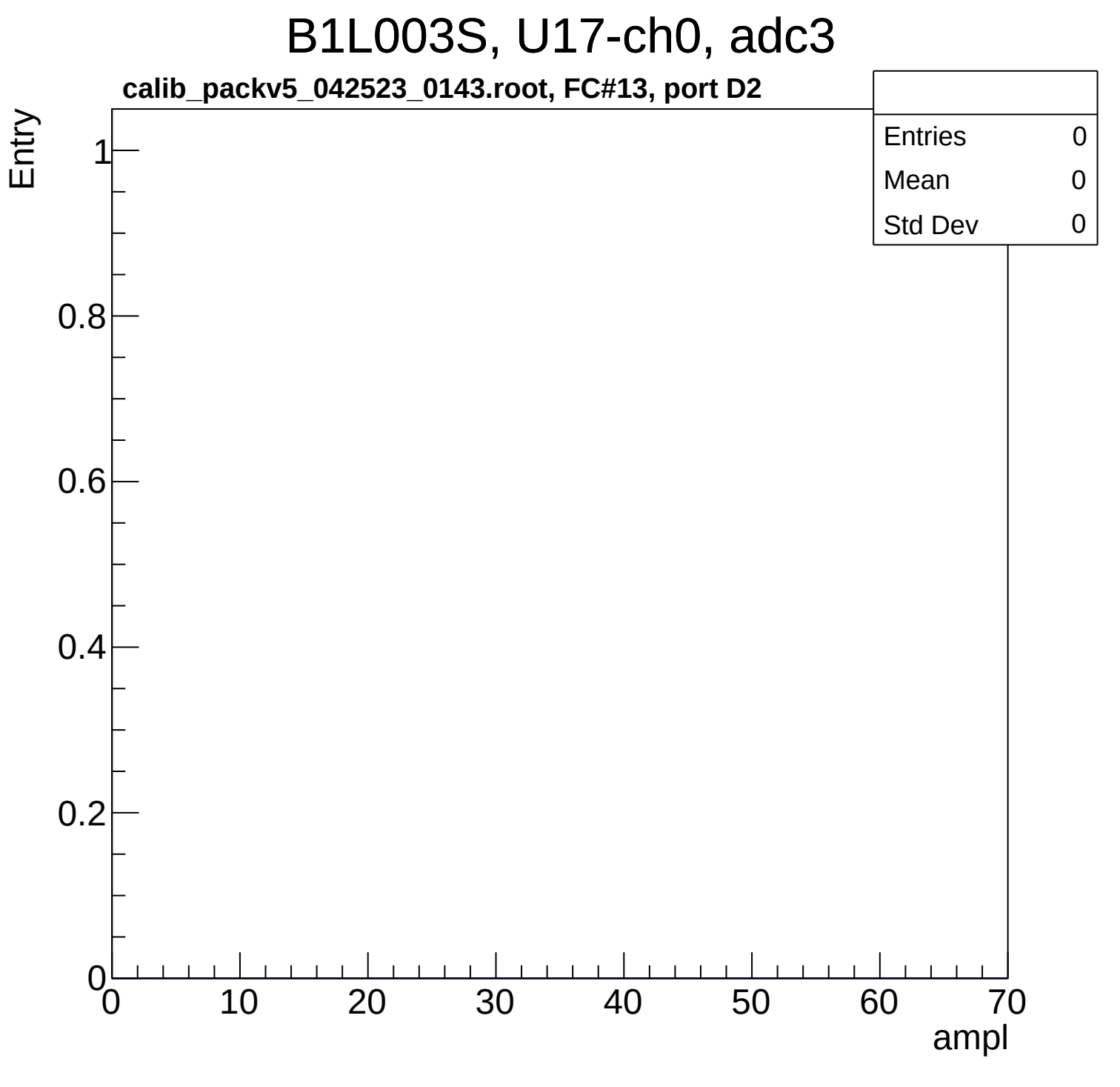
ampl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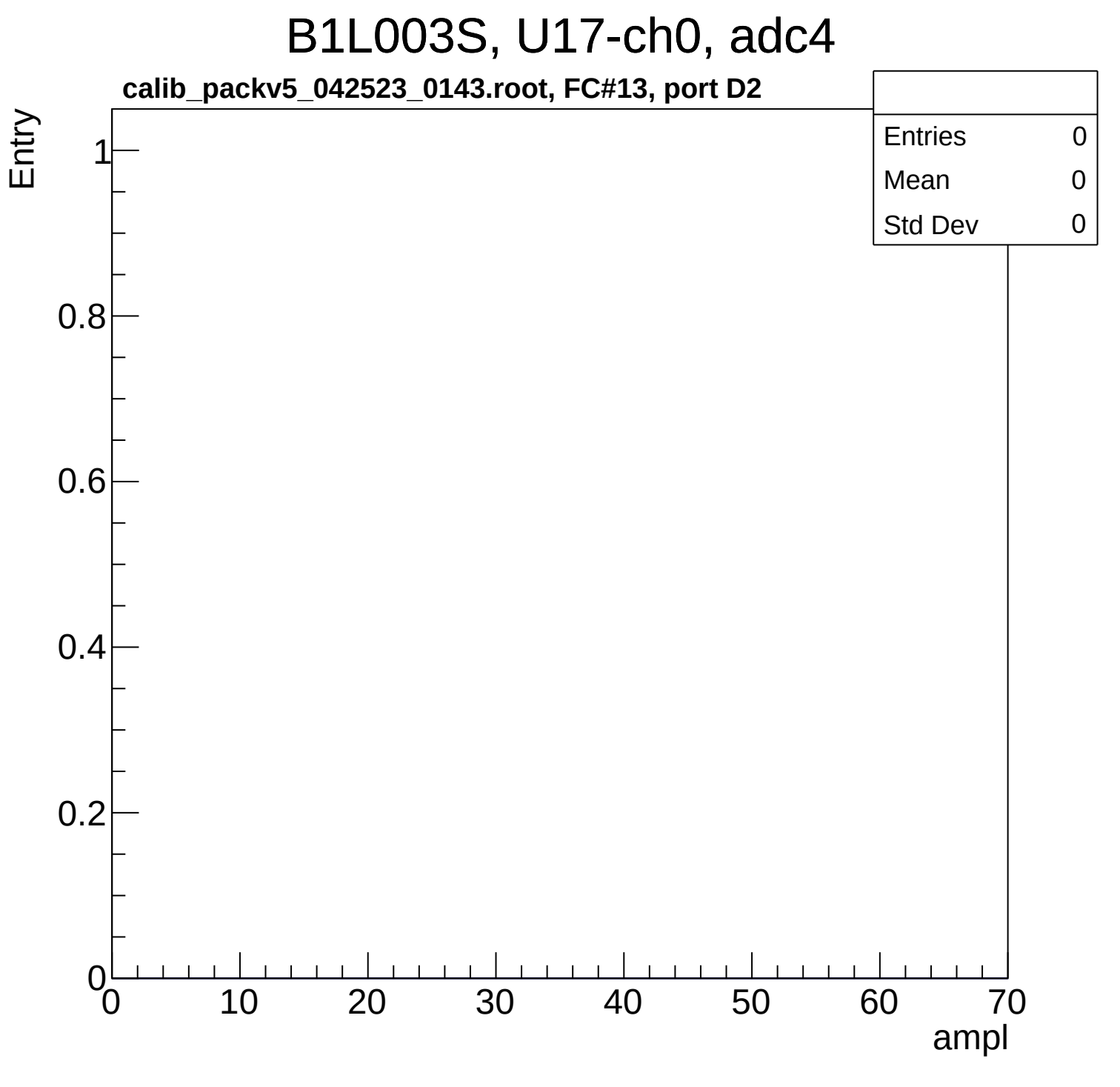
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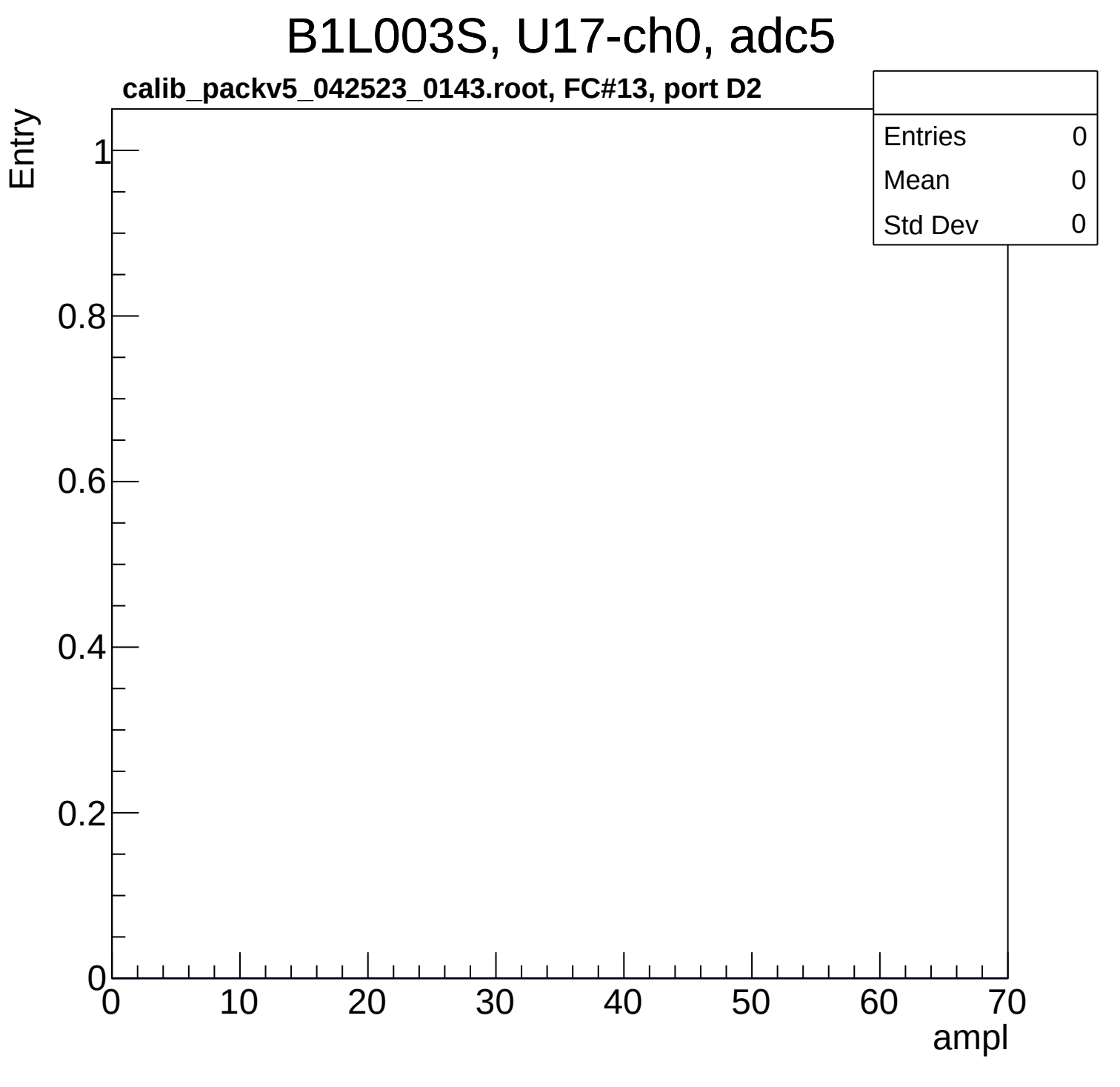
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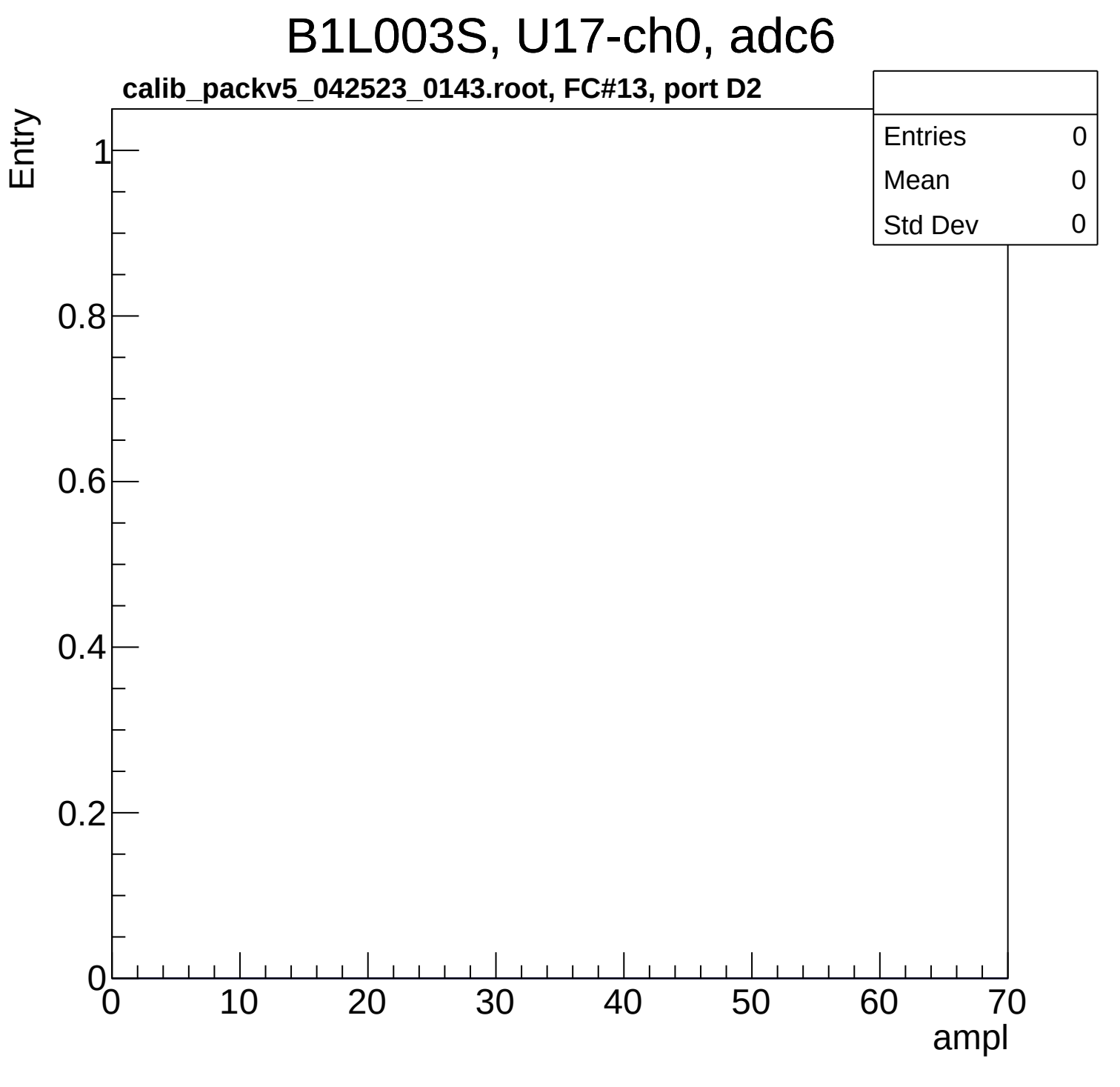
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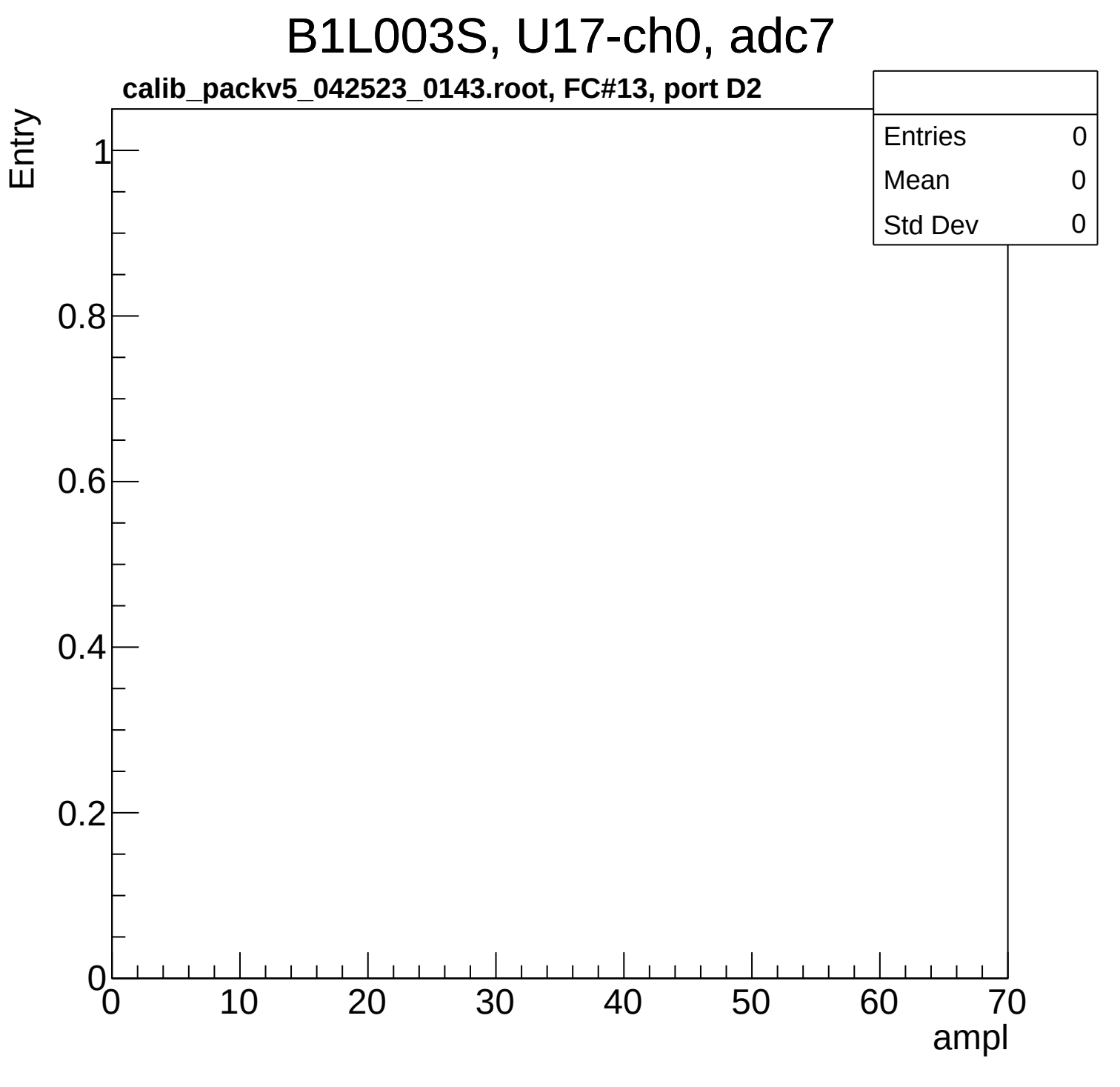
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Std Dev 0
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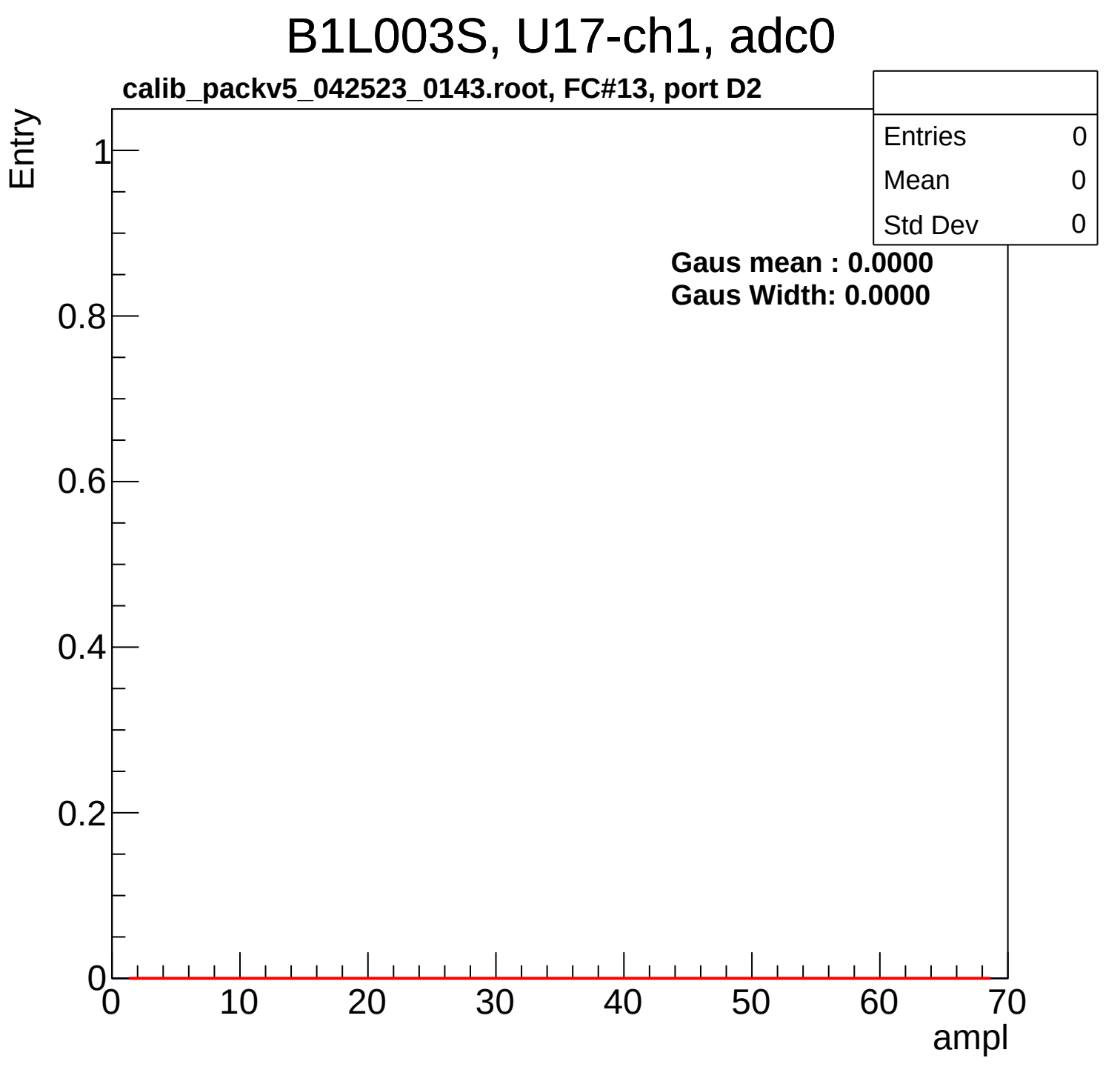
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0.4
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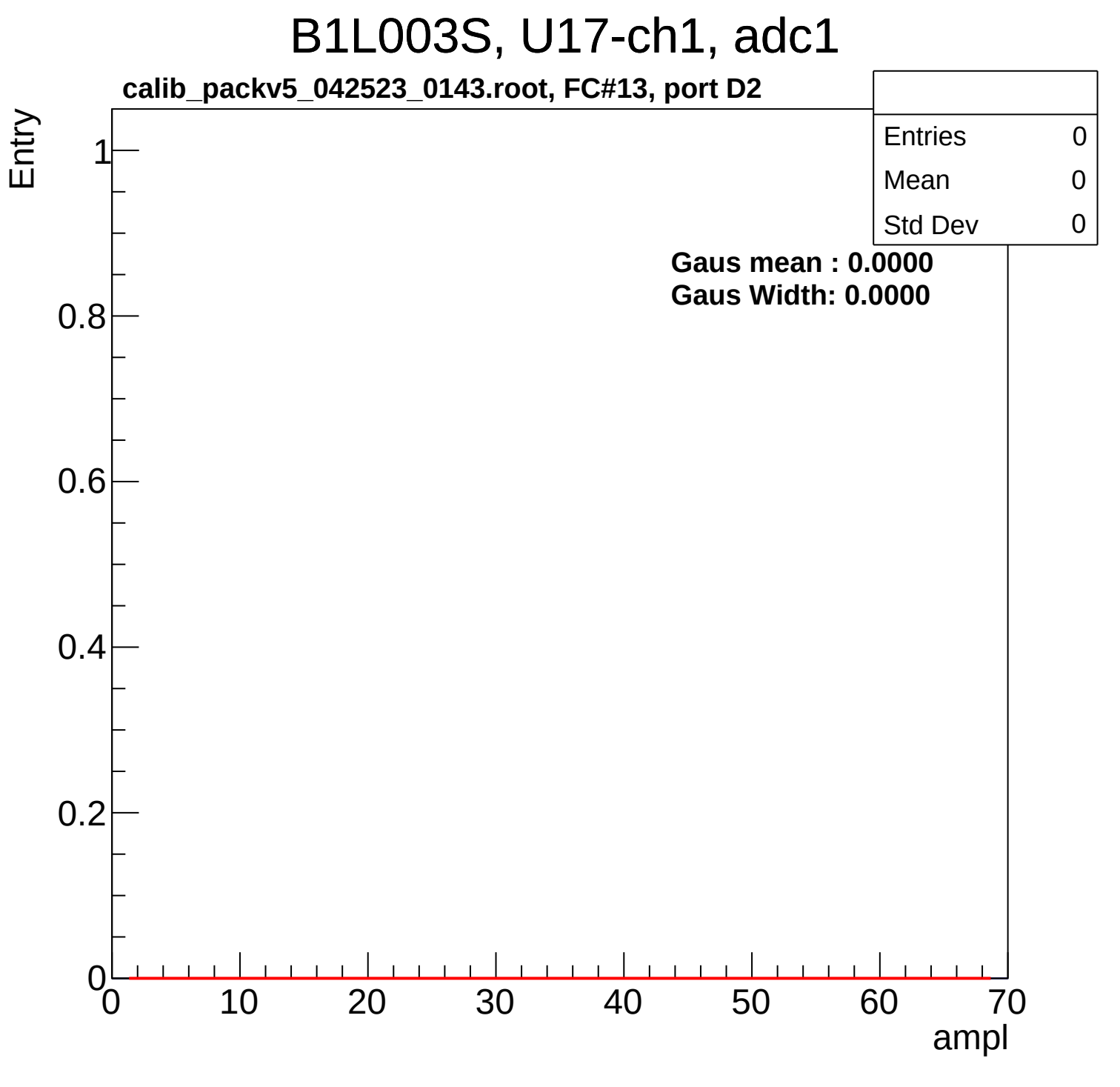
ampl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Std Dev 0
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Gaus Width: 0.0000
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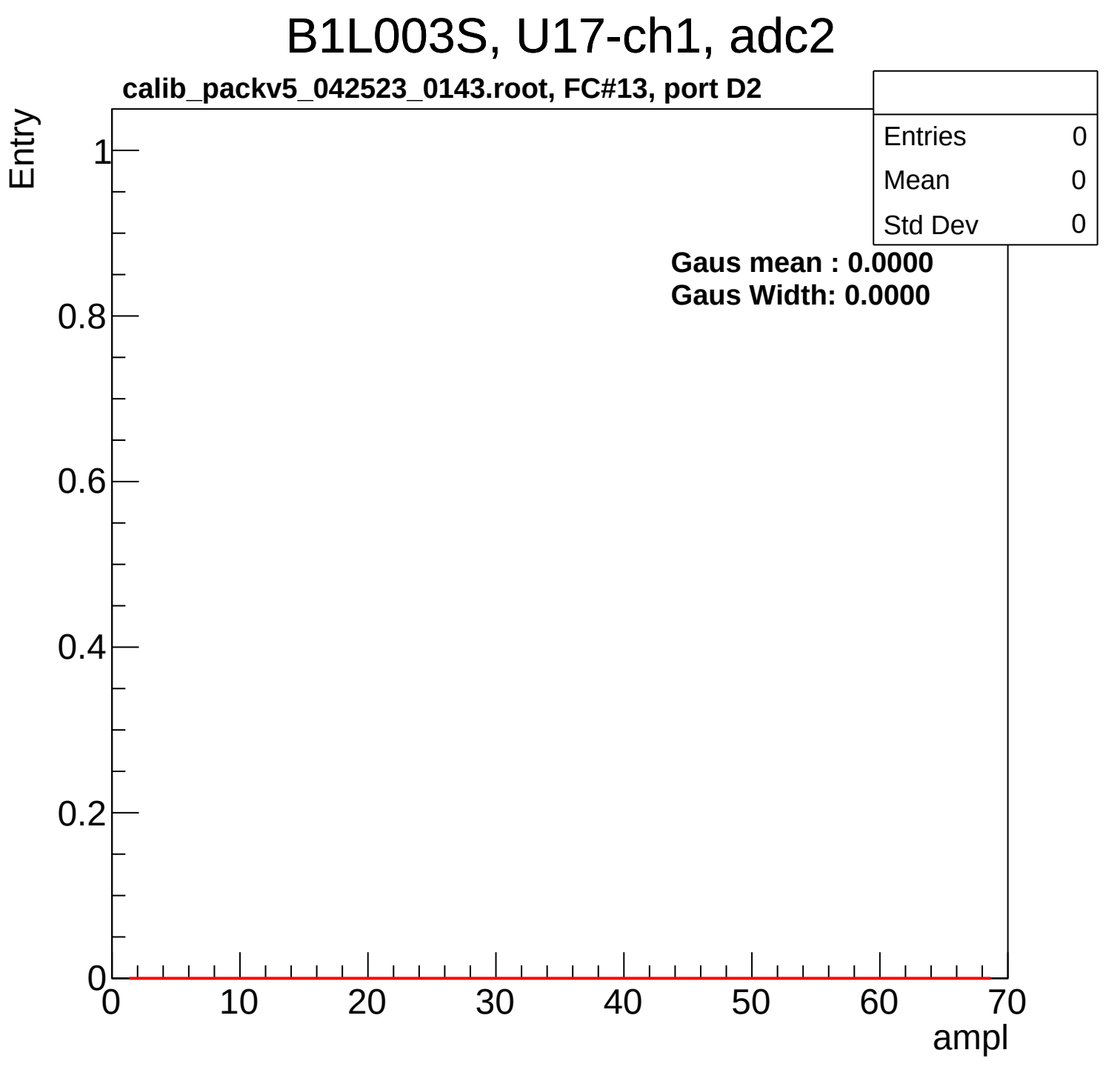
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Std Dev 0
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Gaus Width: 0.0000
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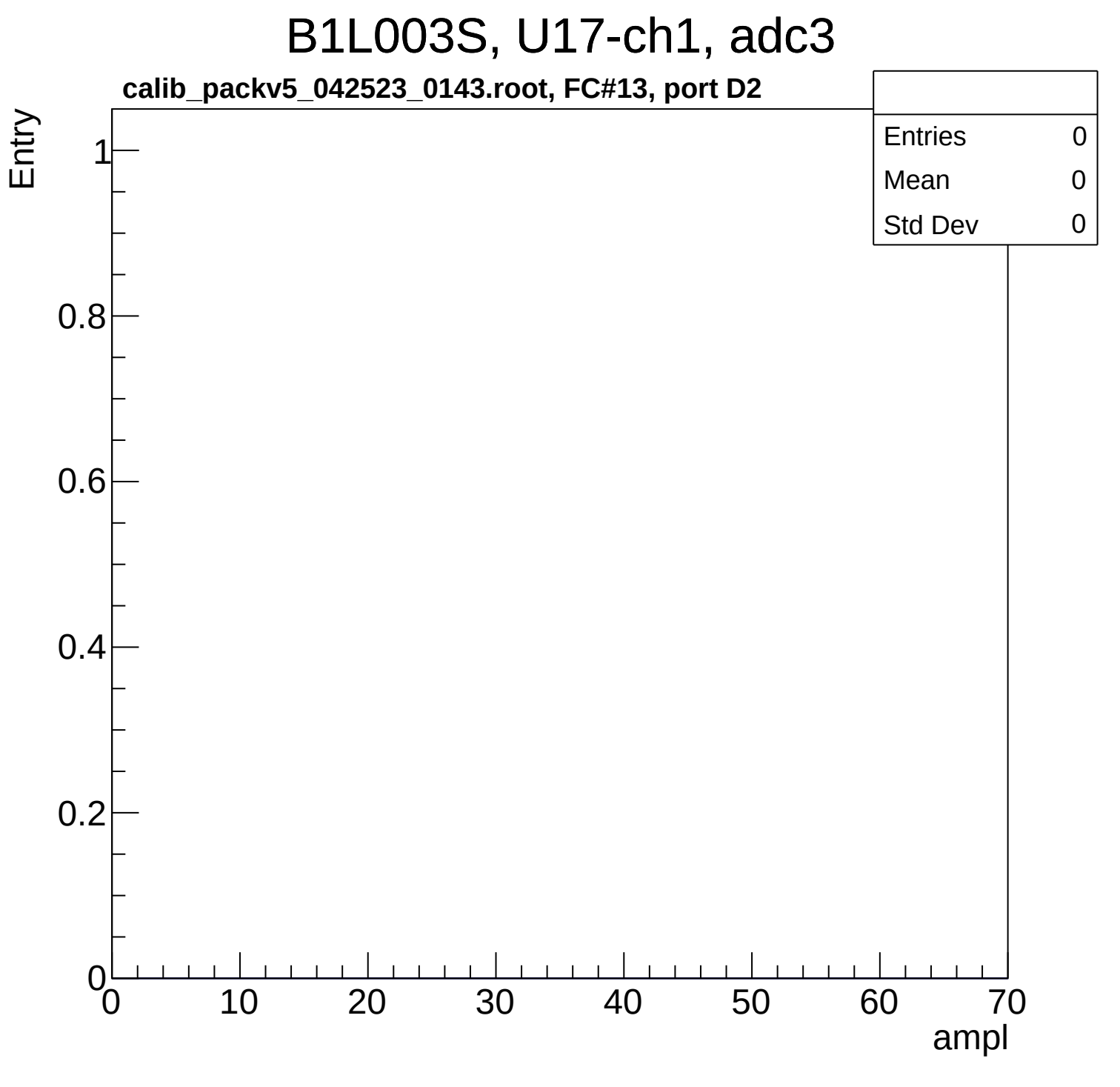
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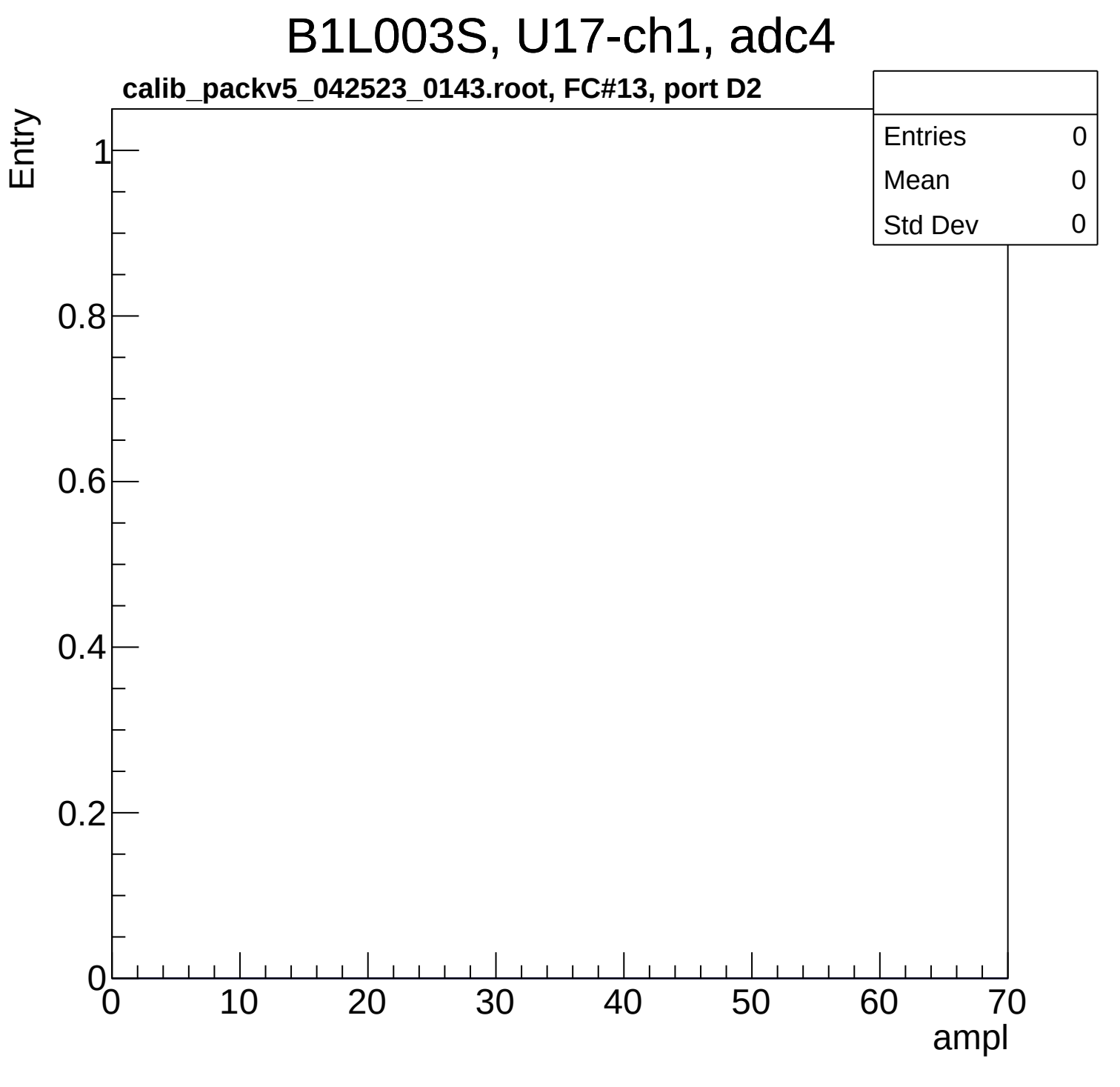
ampl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Std Dev 0
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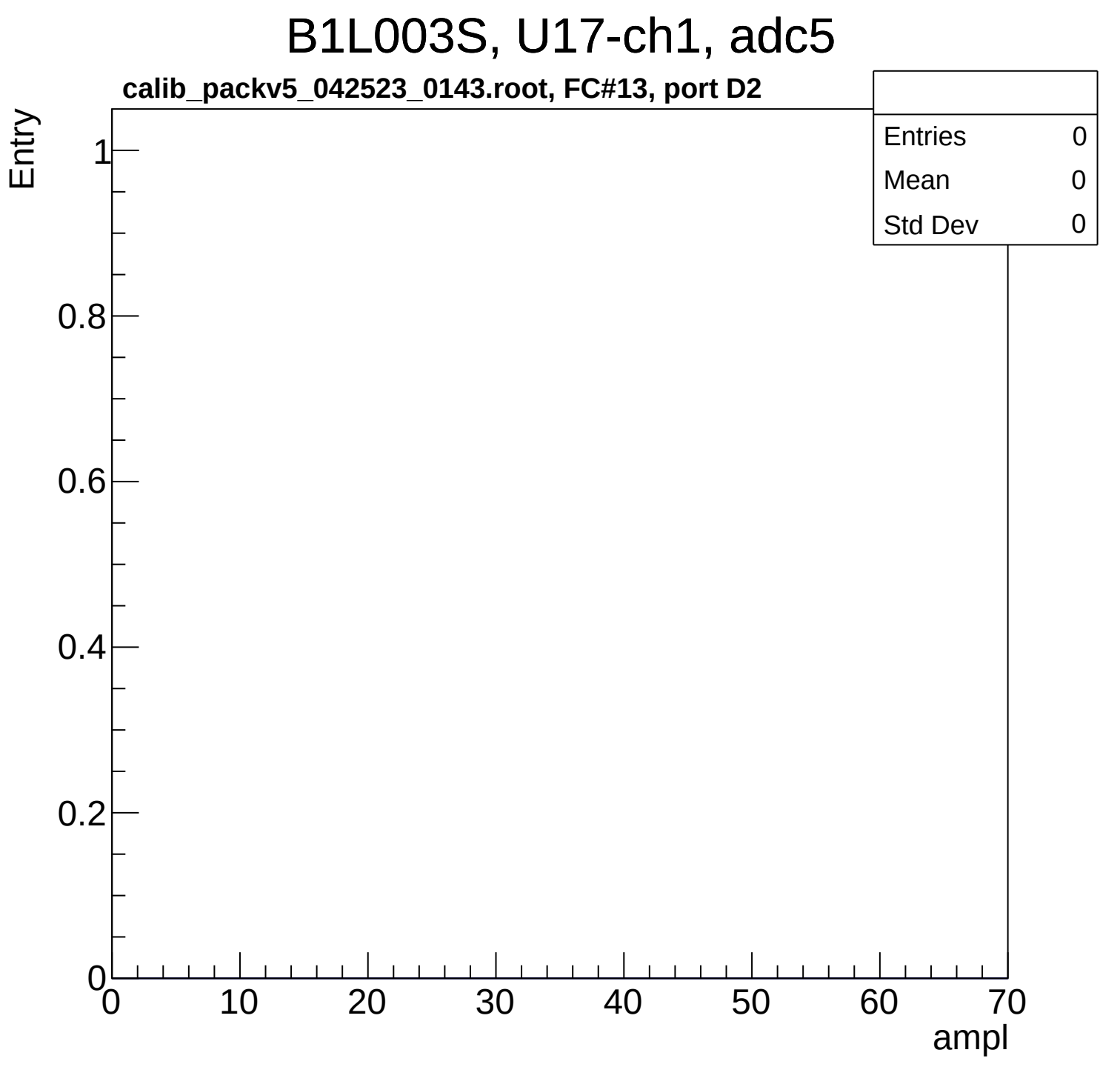
ampl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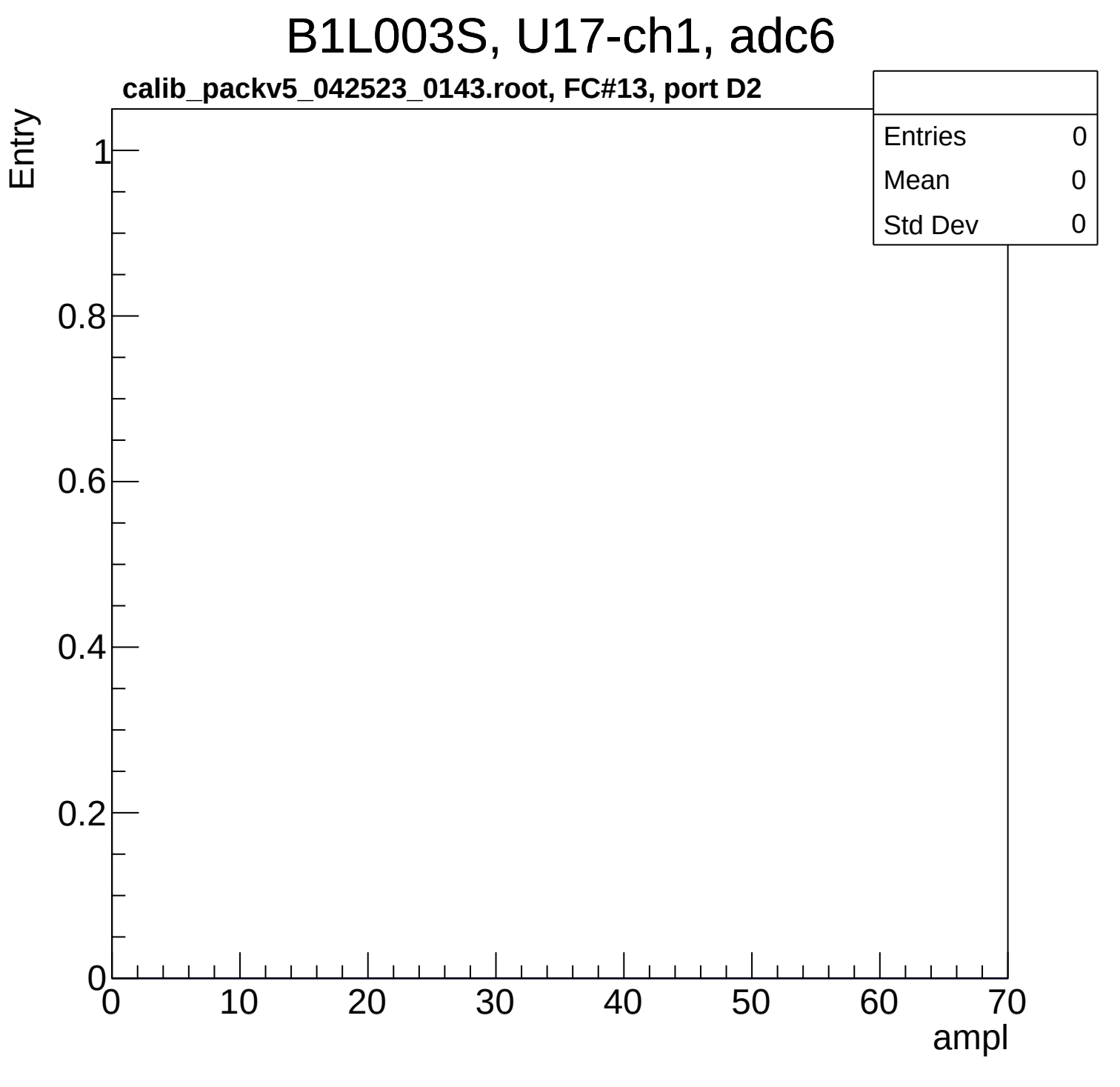
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Std Dev 0
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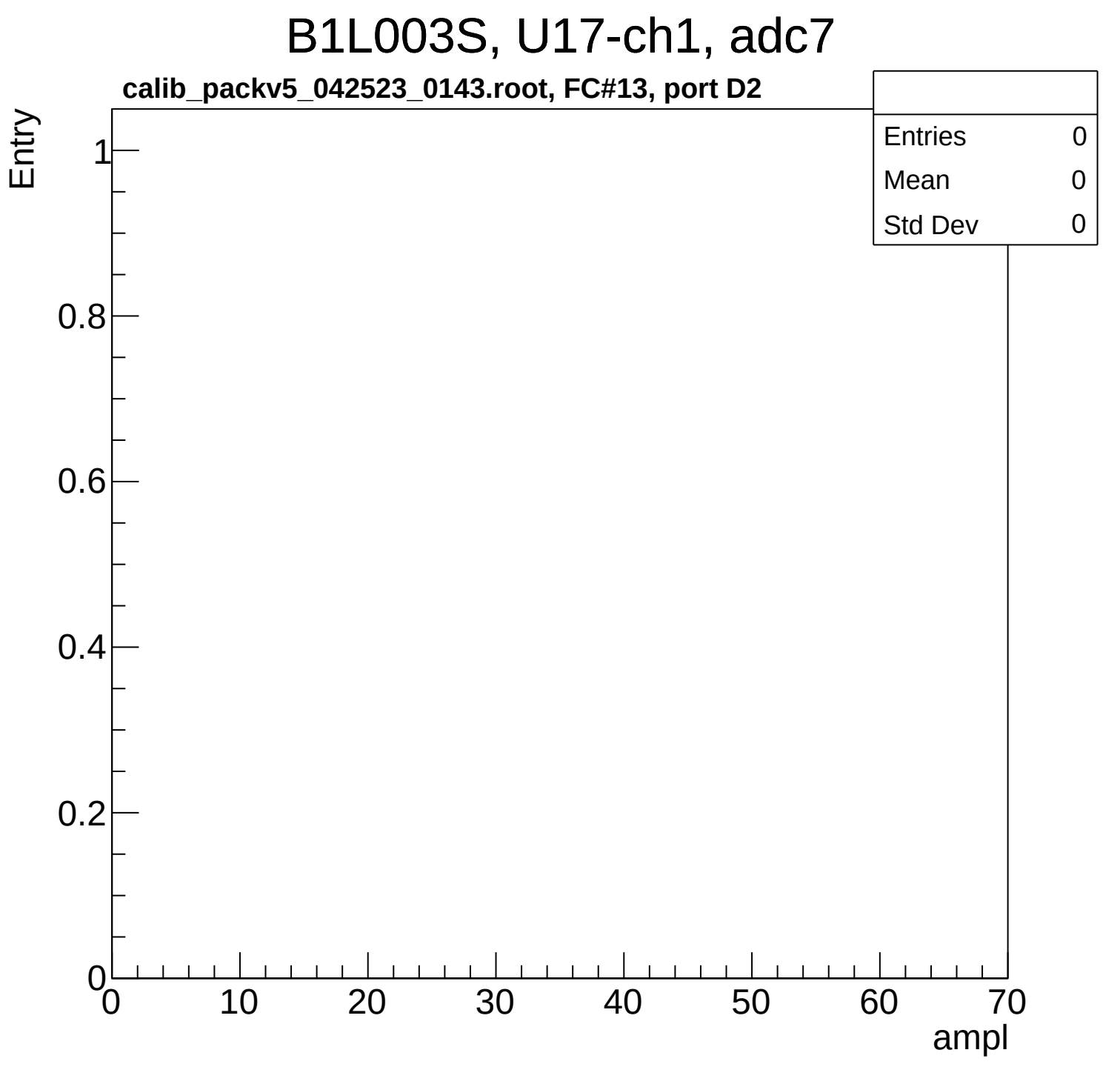
ampl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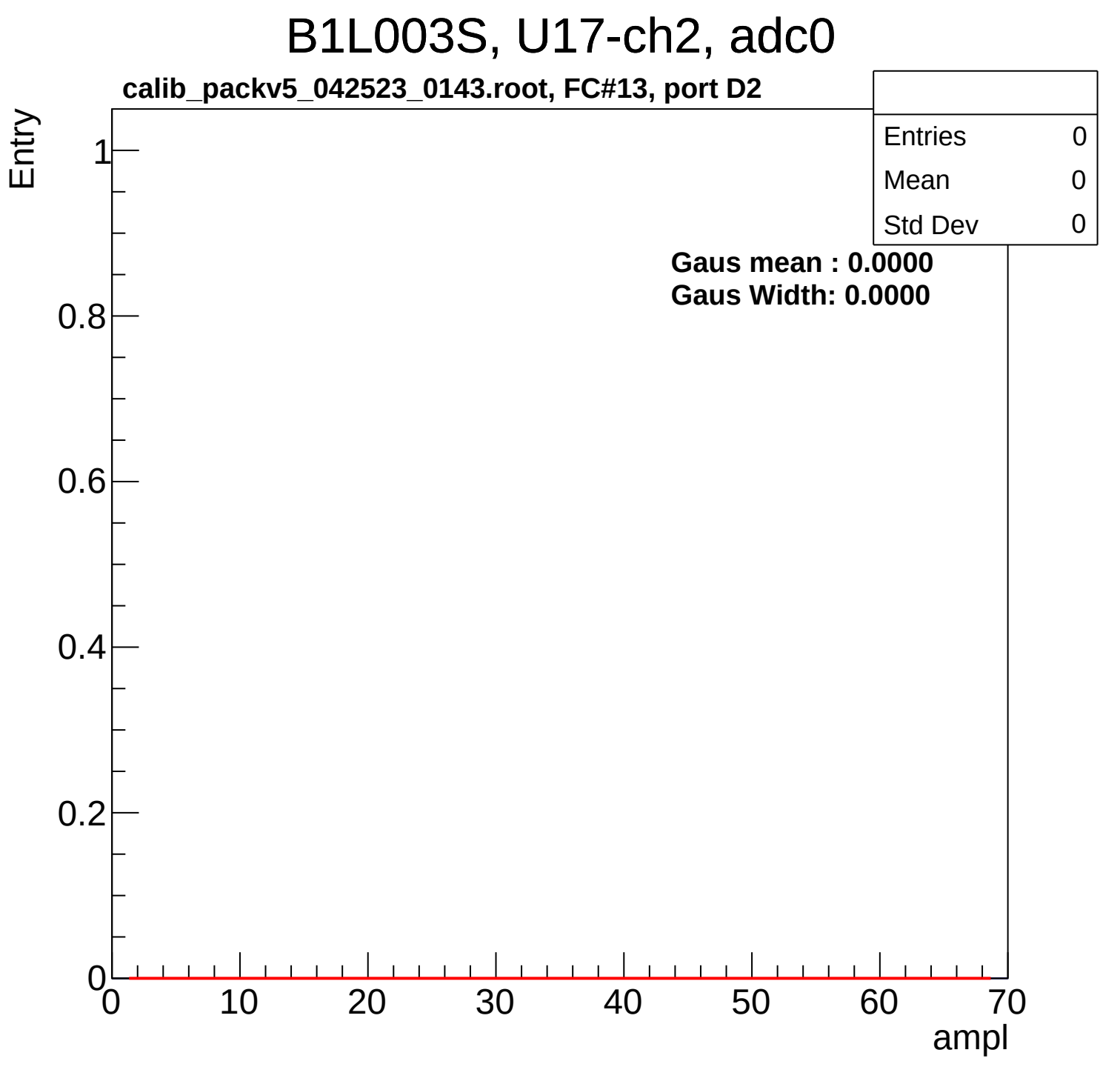
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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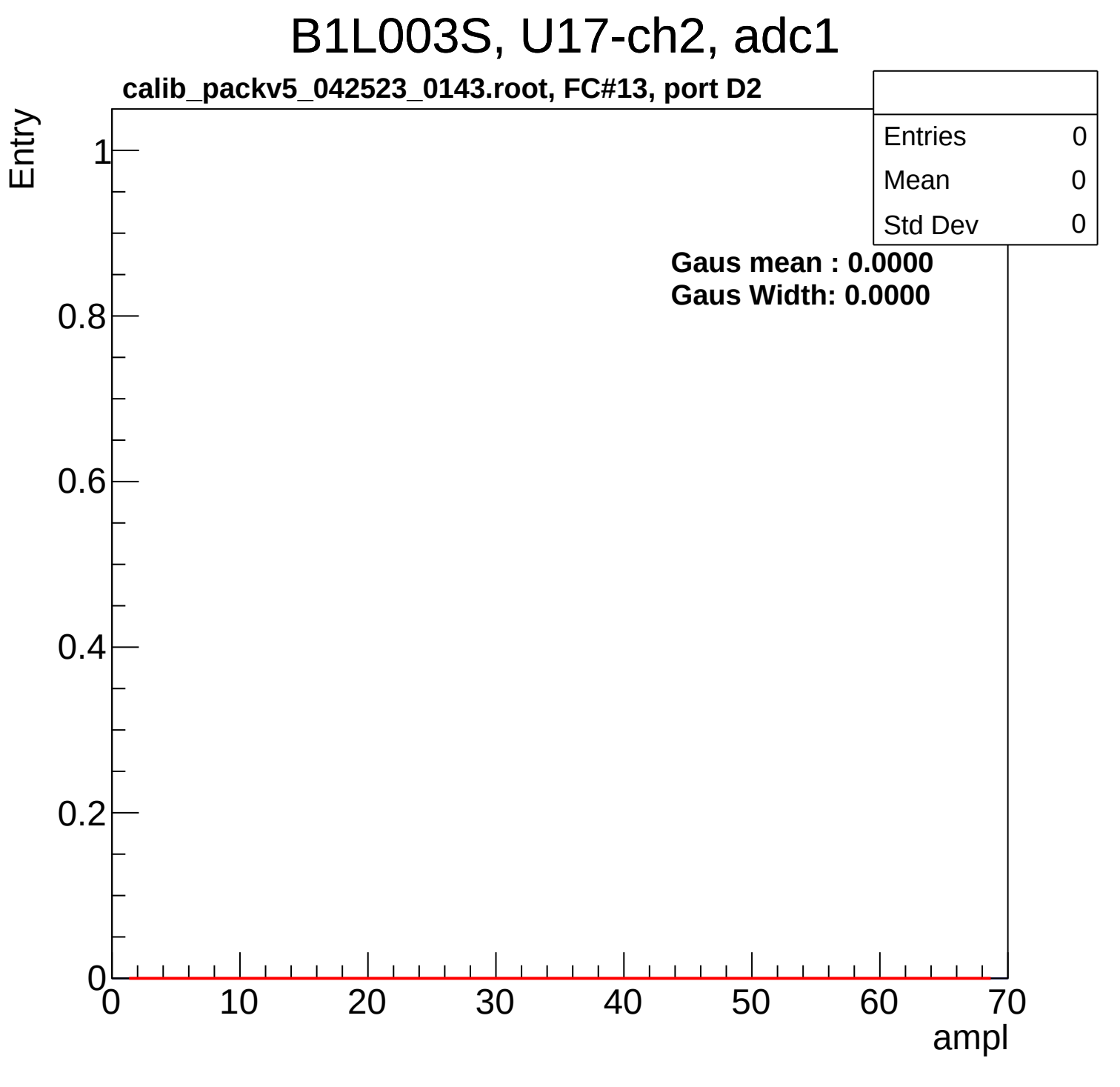
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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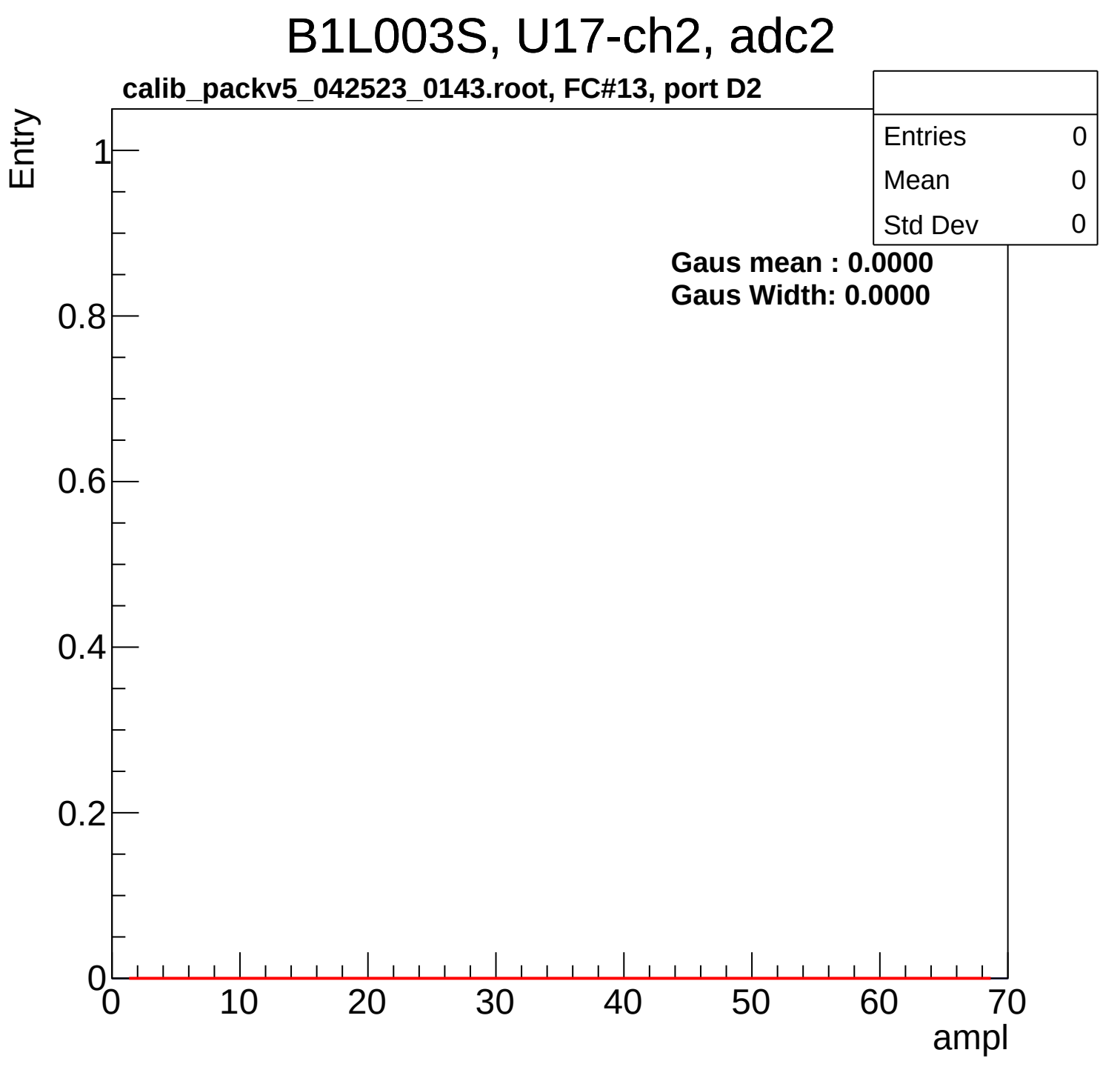
ampl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Gaus Width: 0.0000
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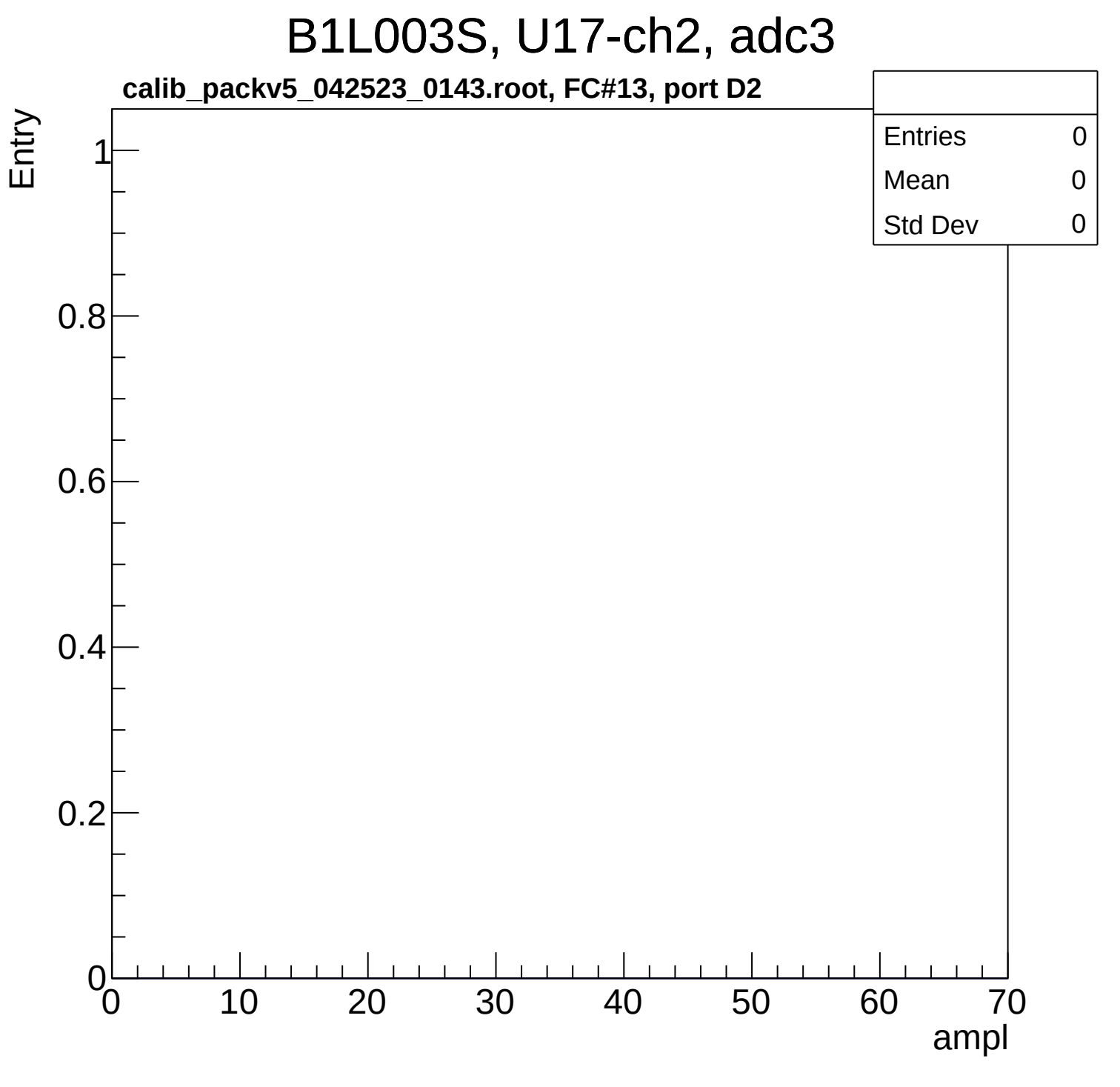
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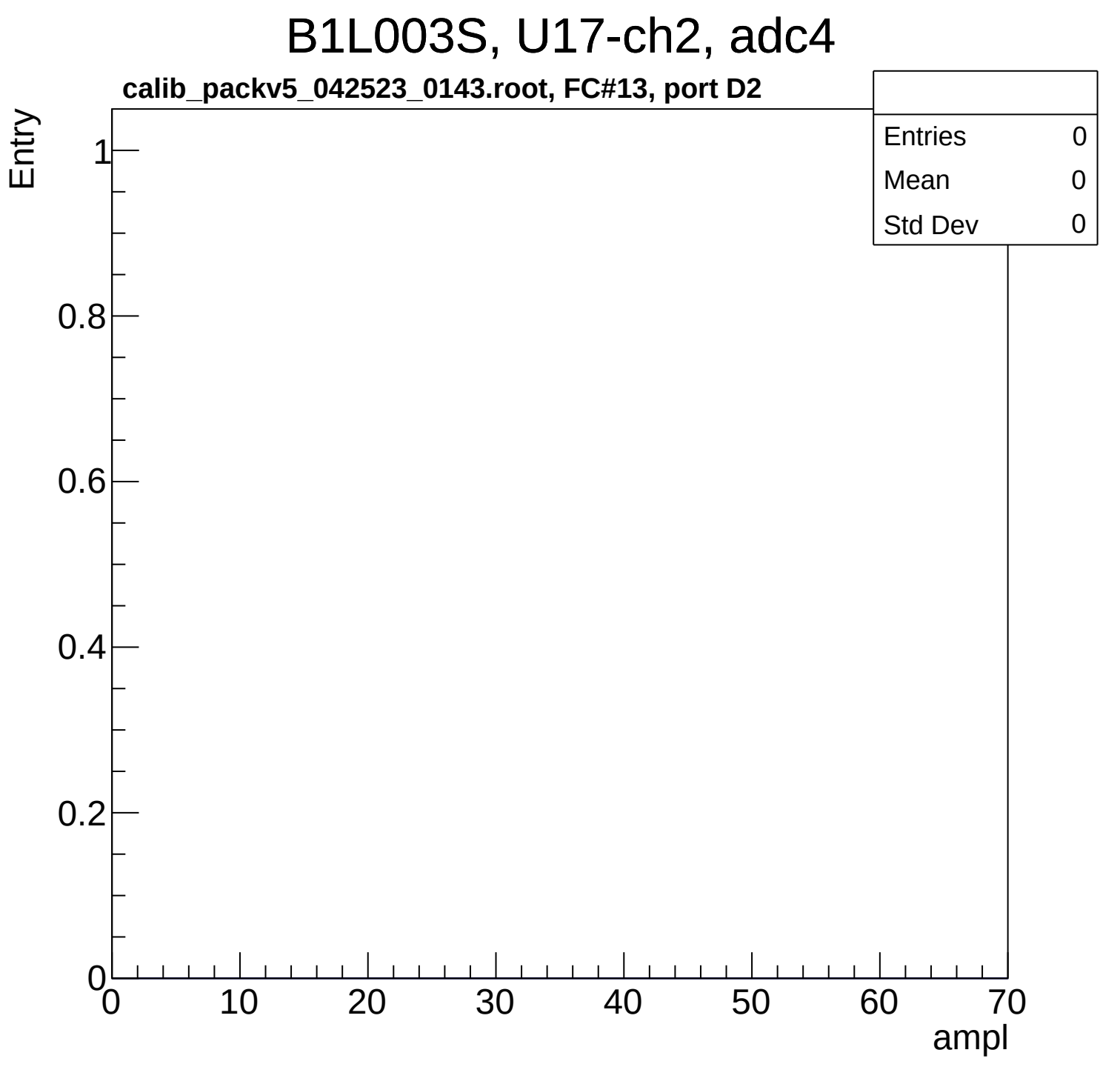
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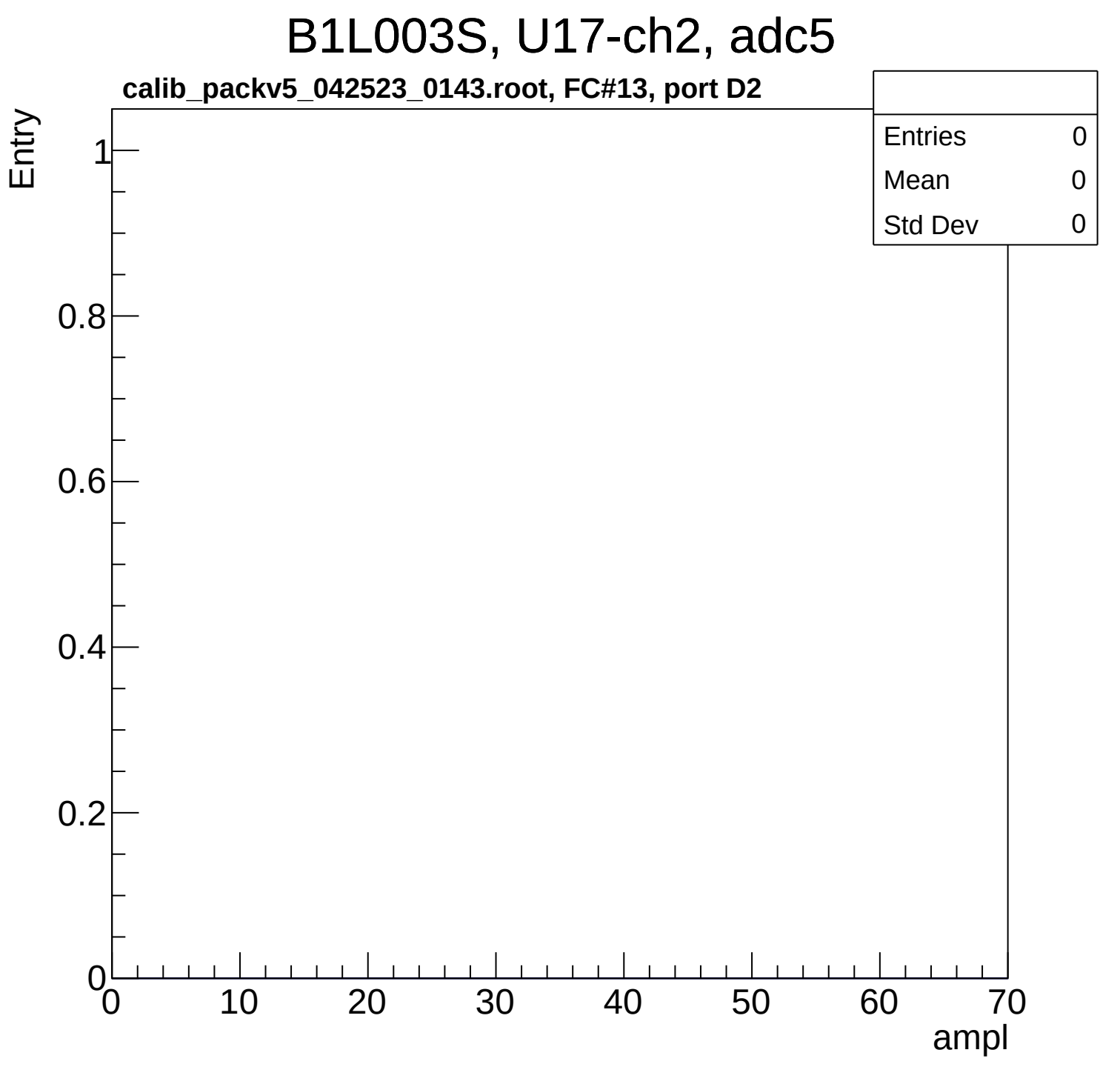
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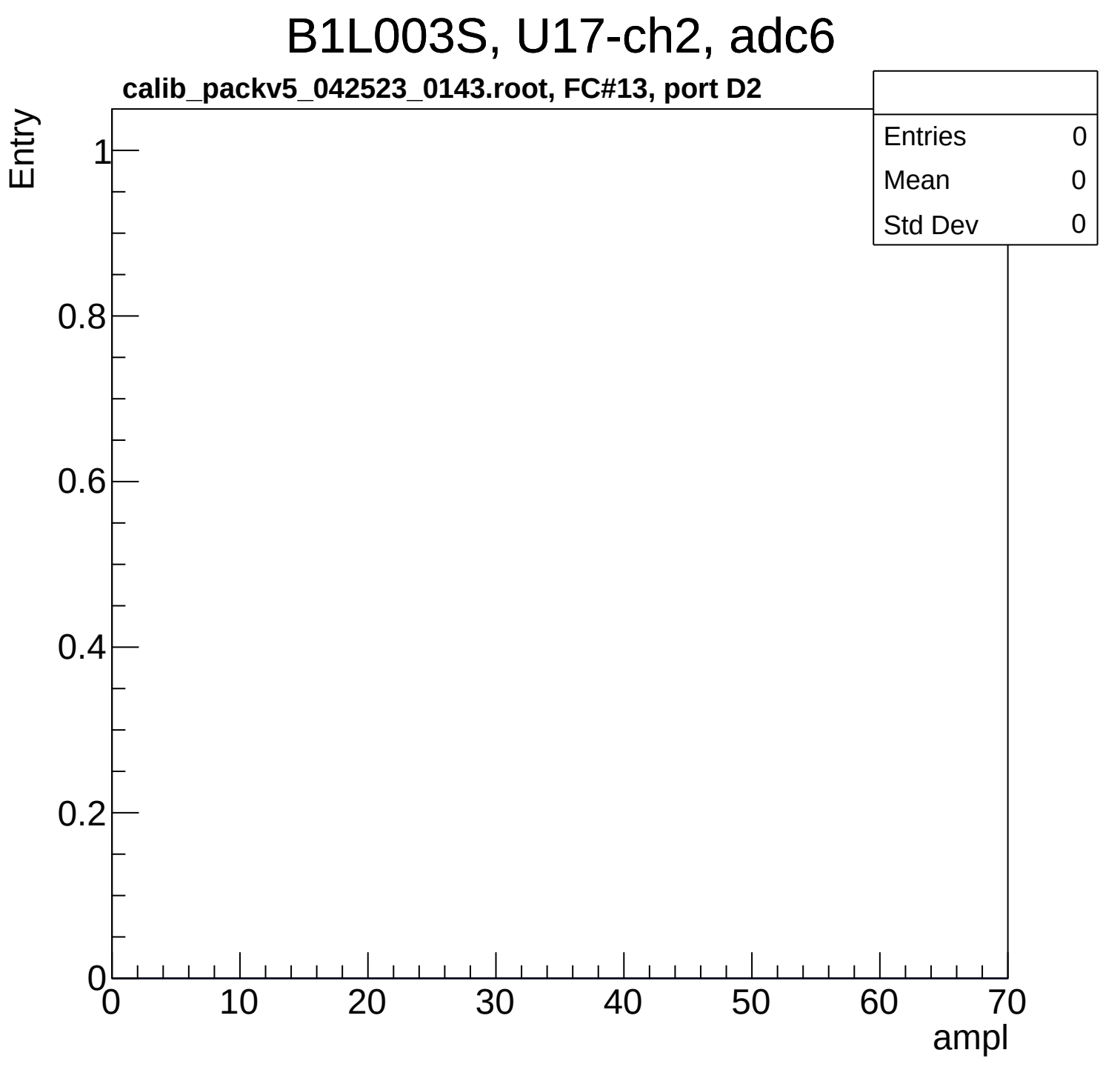
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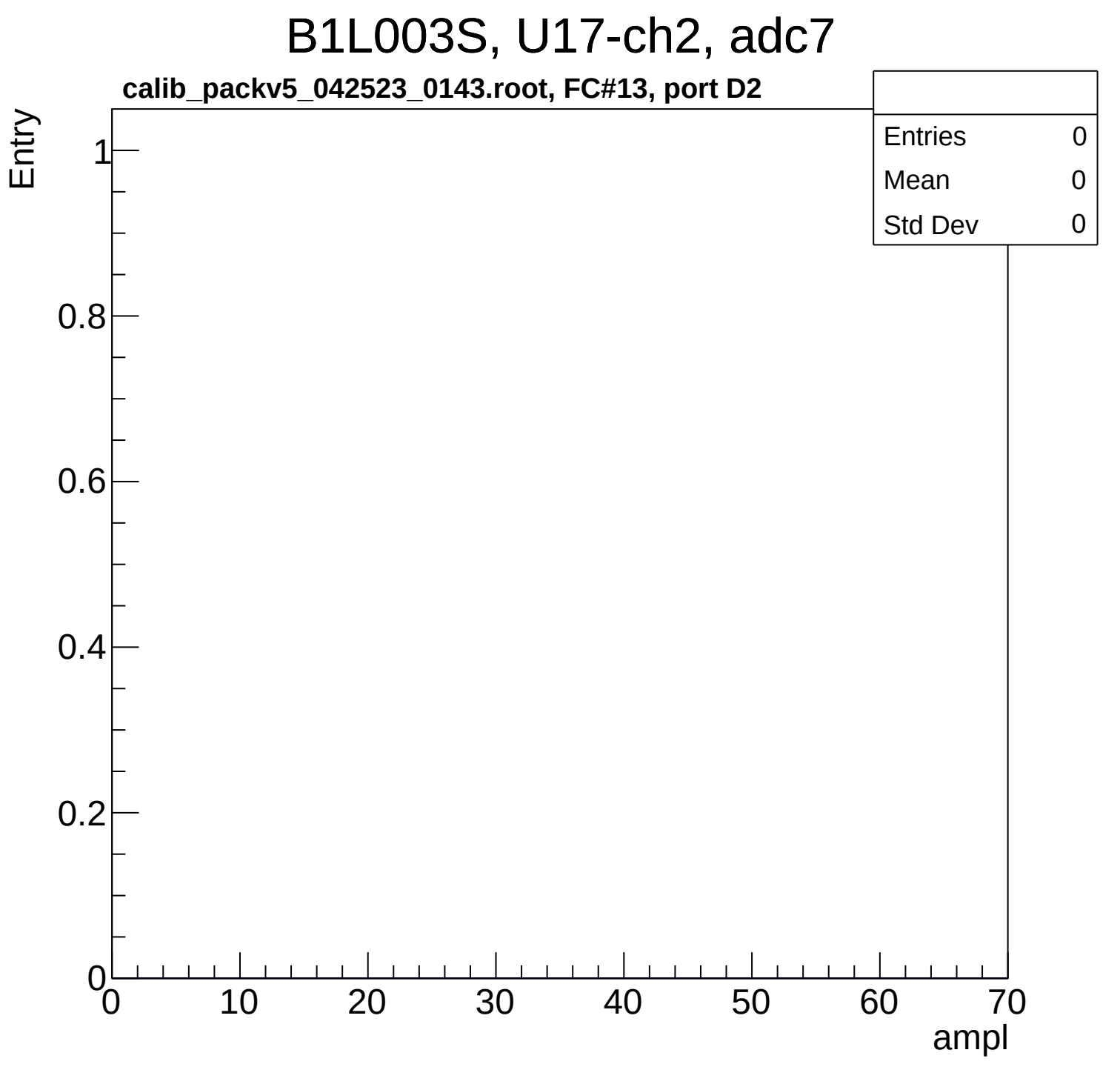
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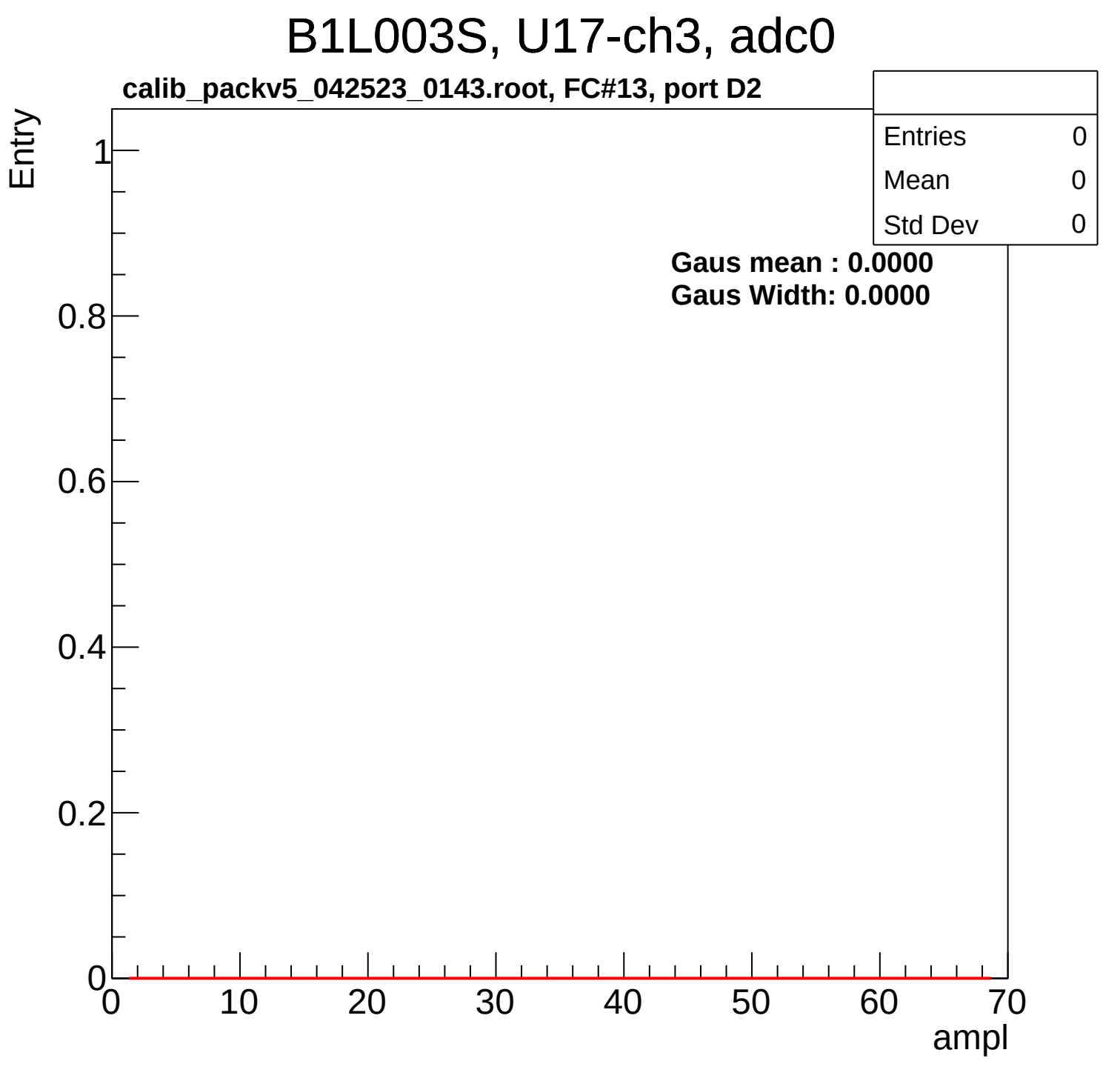
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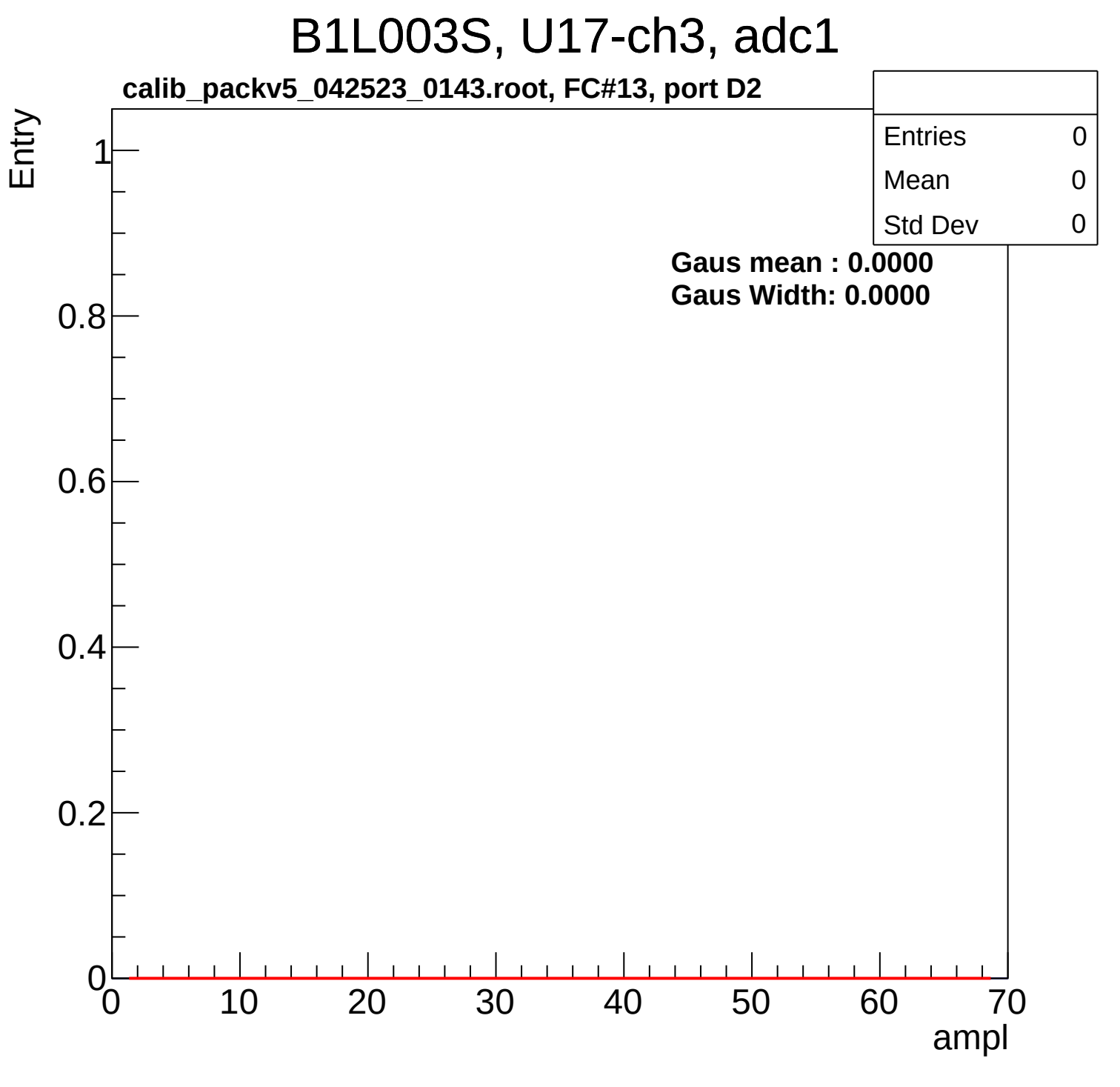
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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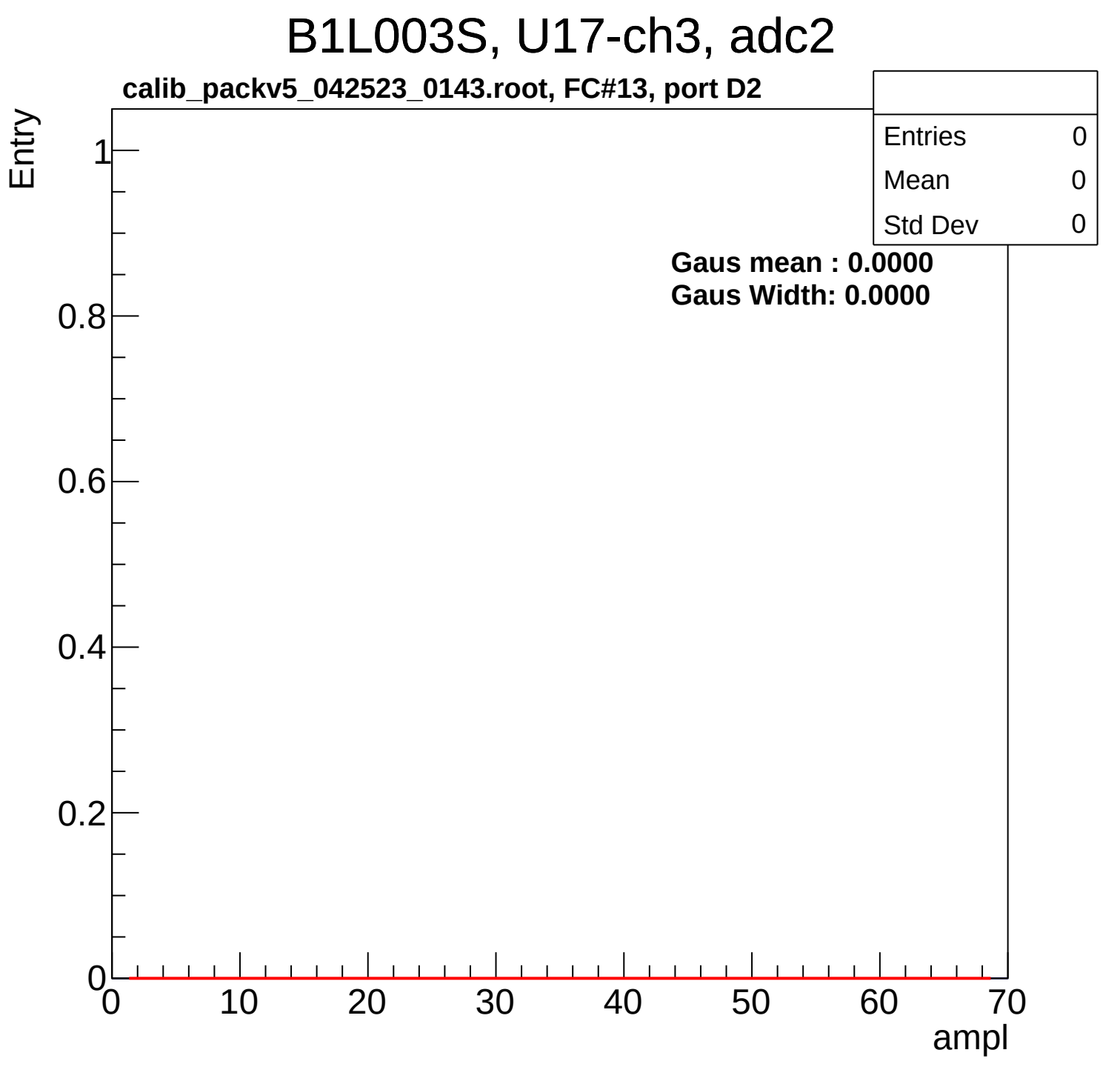
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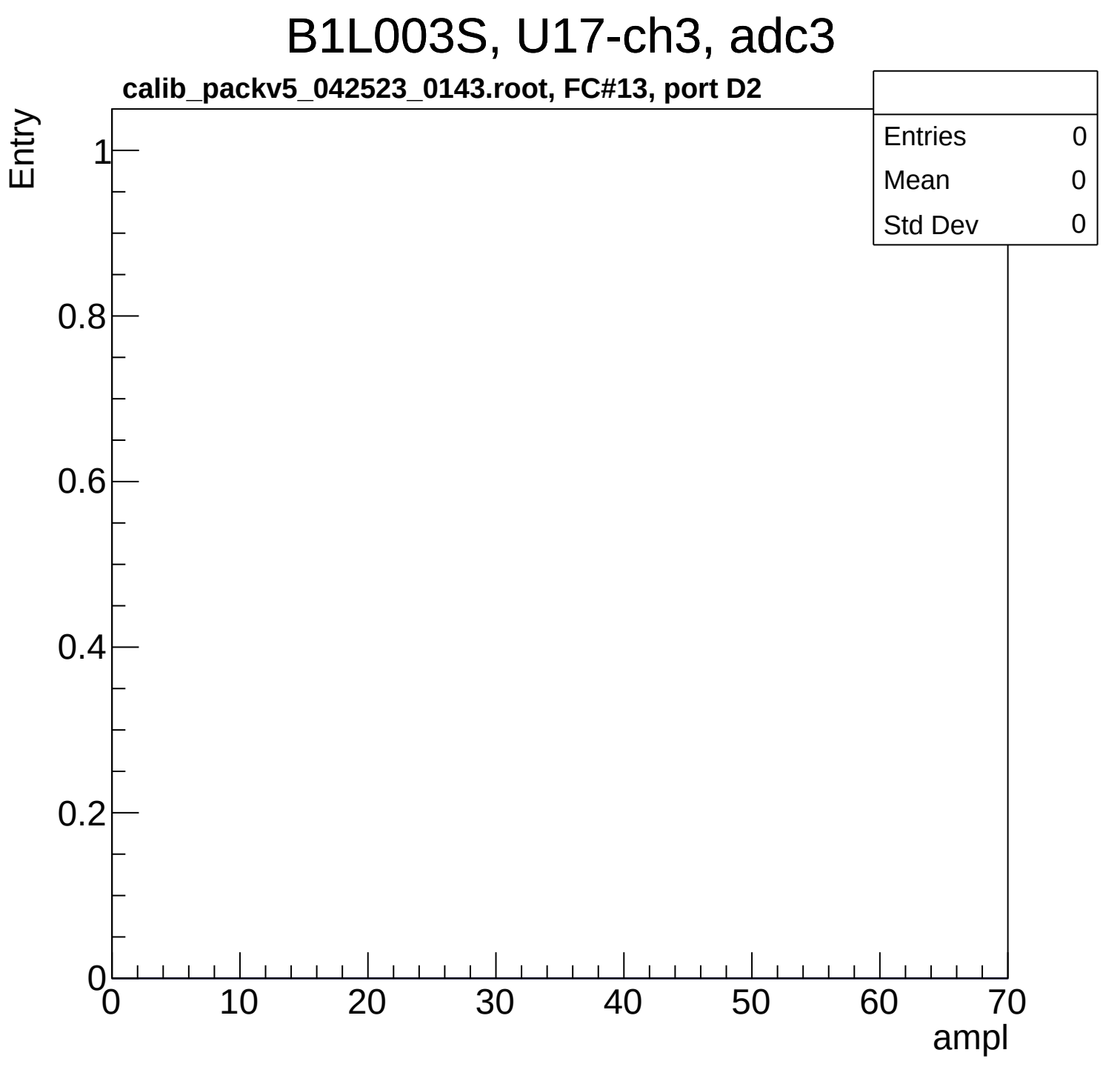
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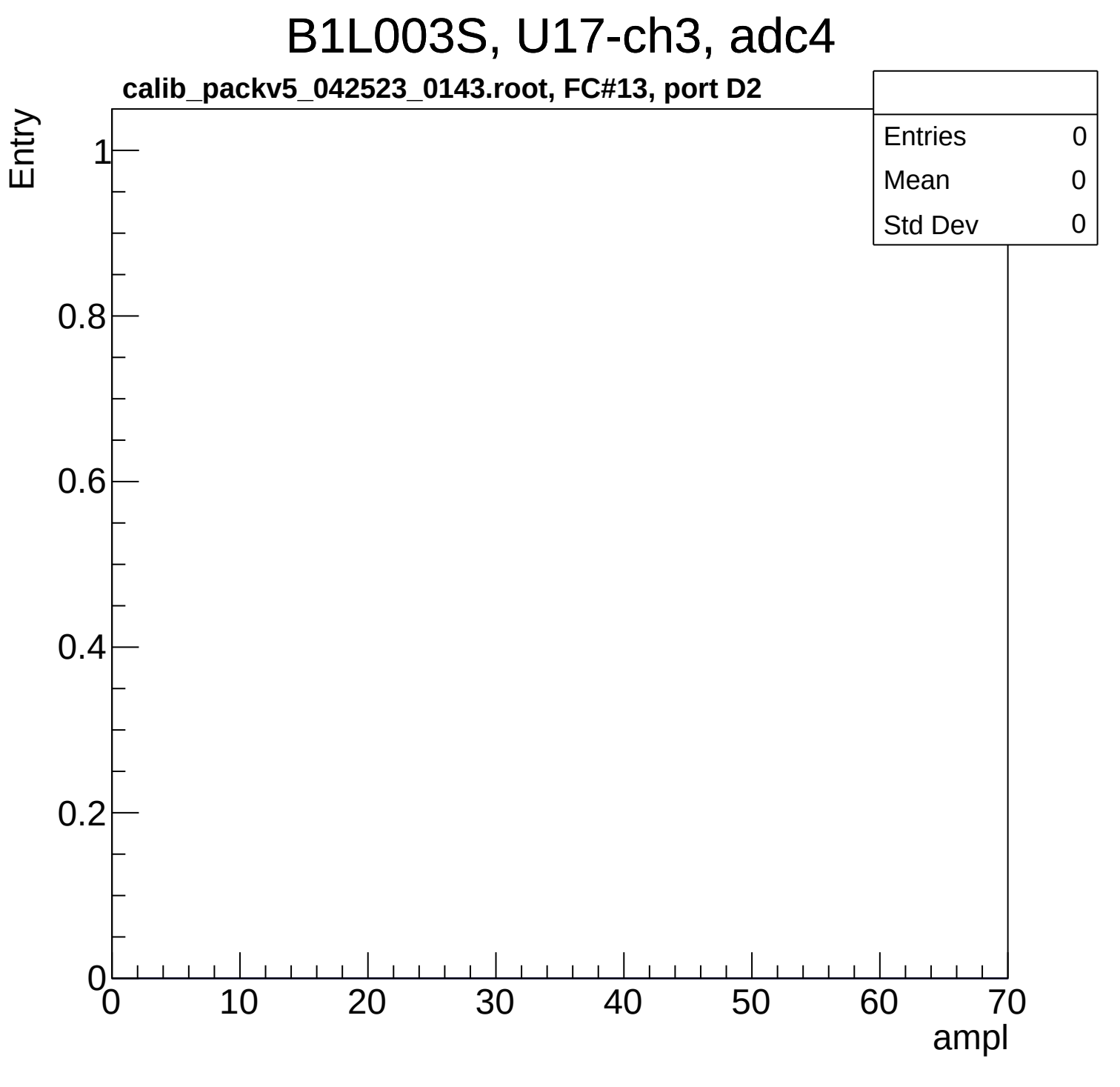
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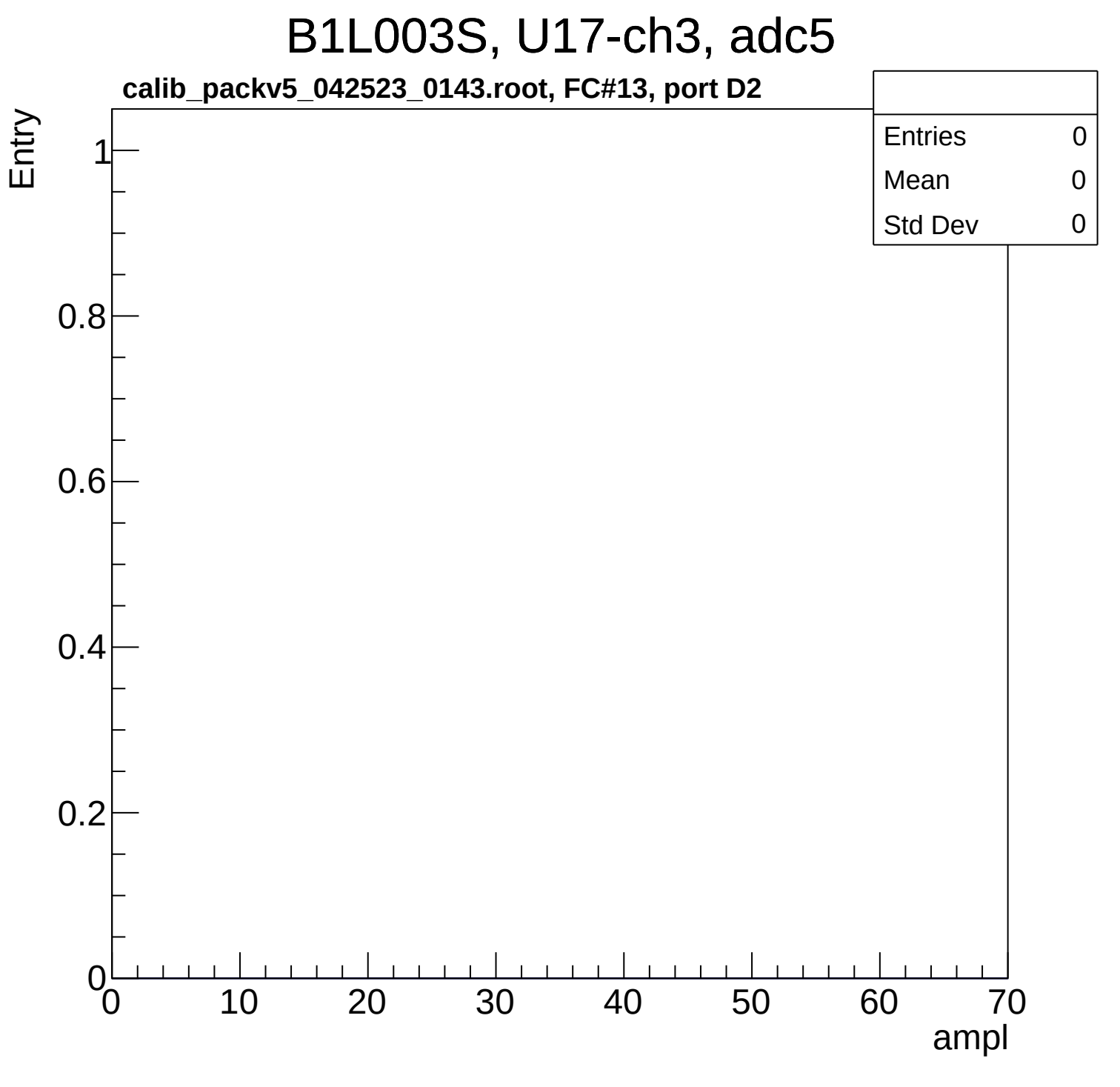
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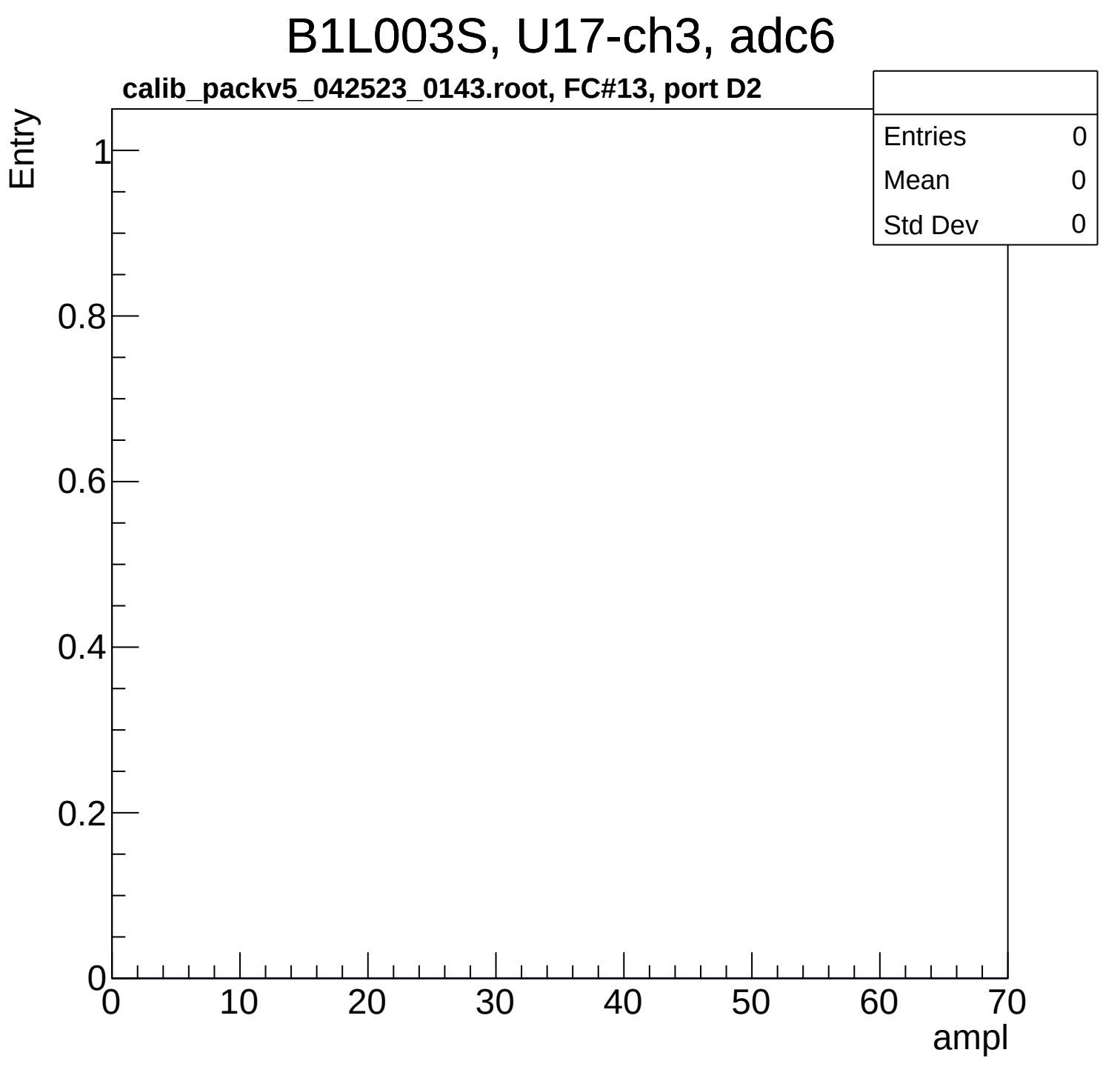
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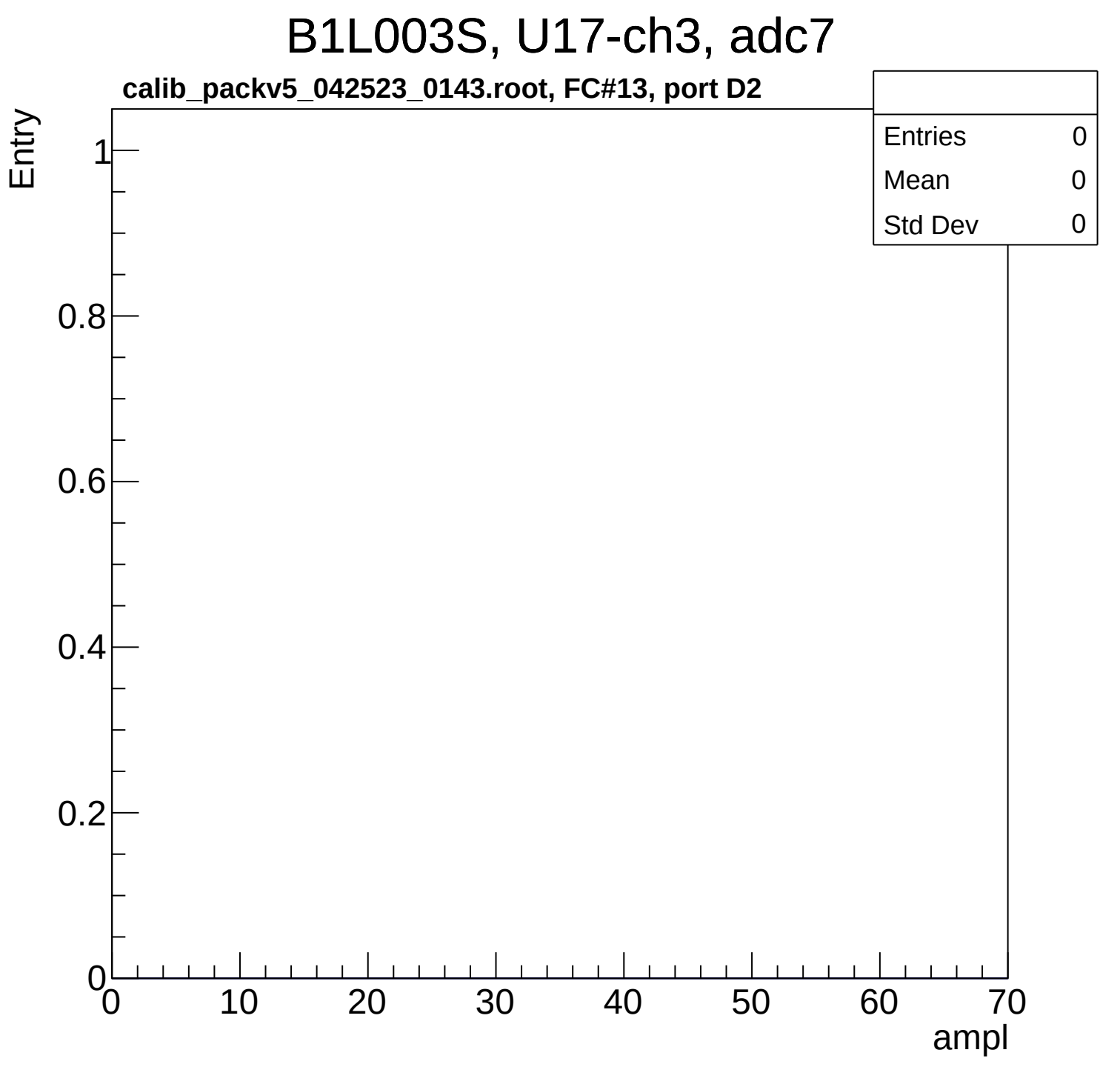
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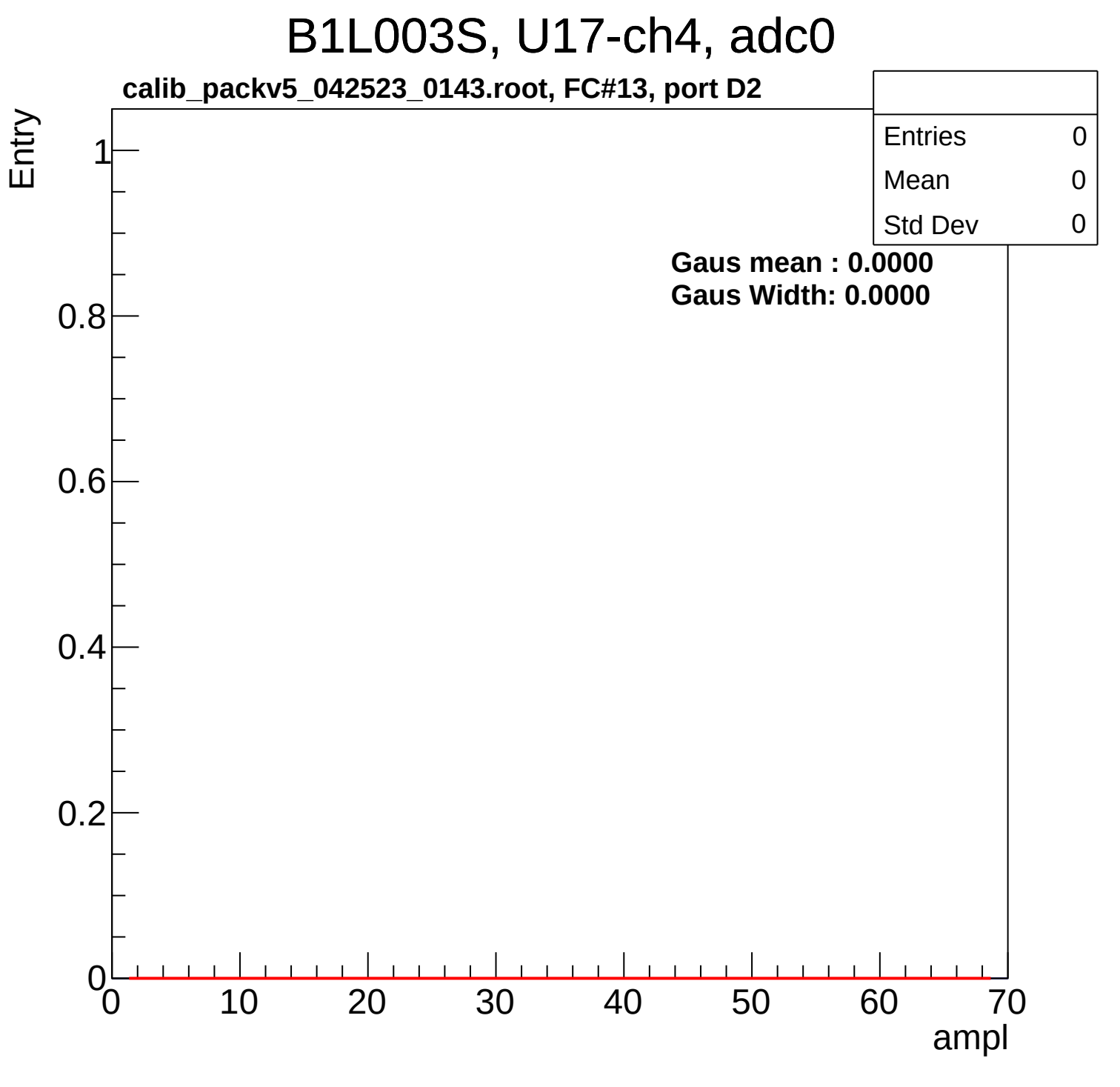
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Std Dev 0
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Gaus Width: 0.0000
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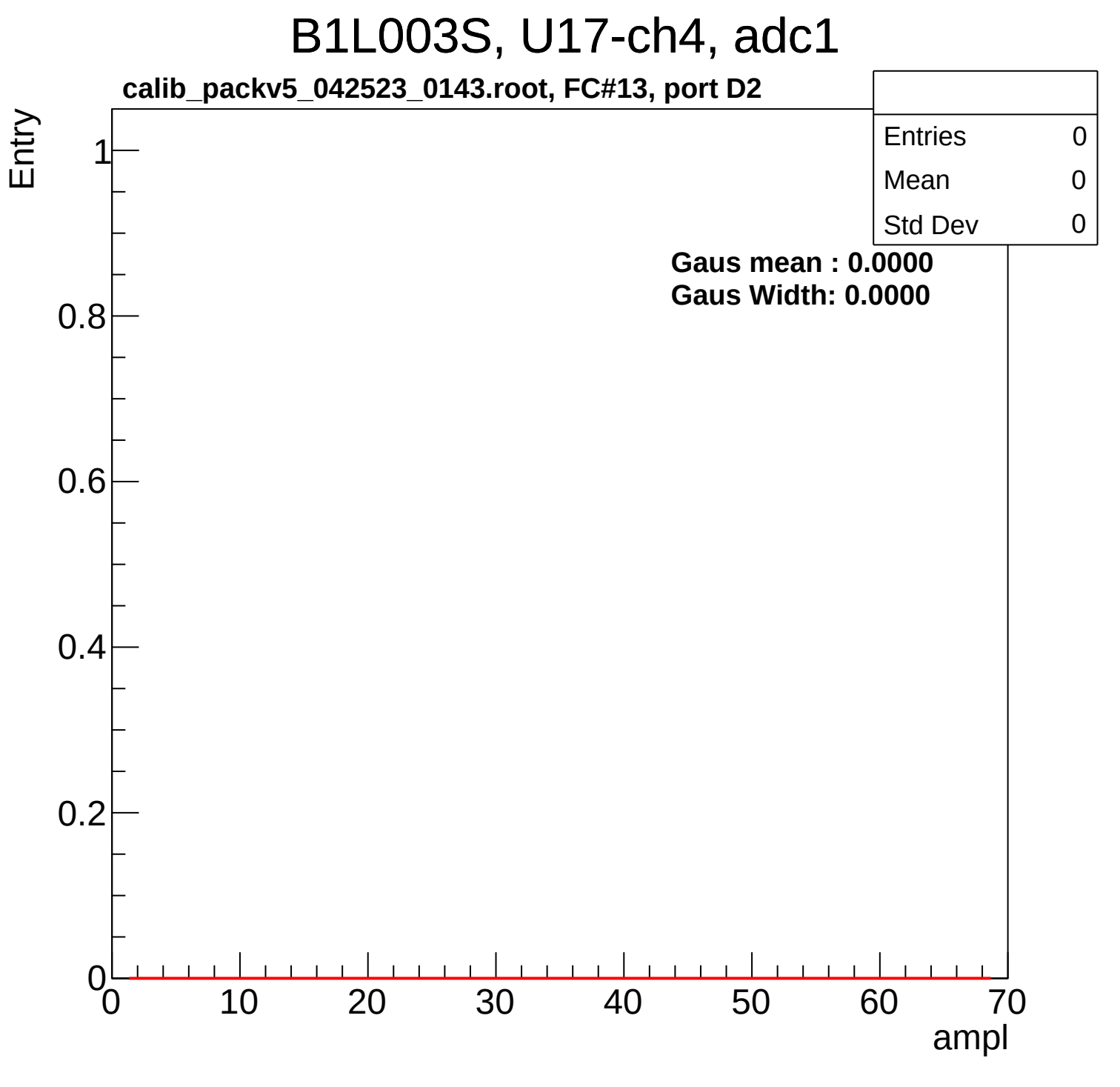
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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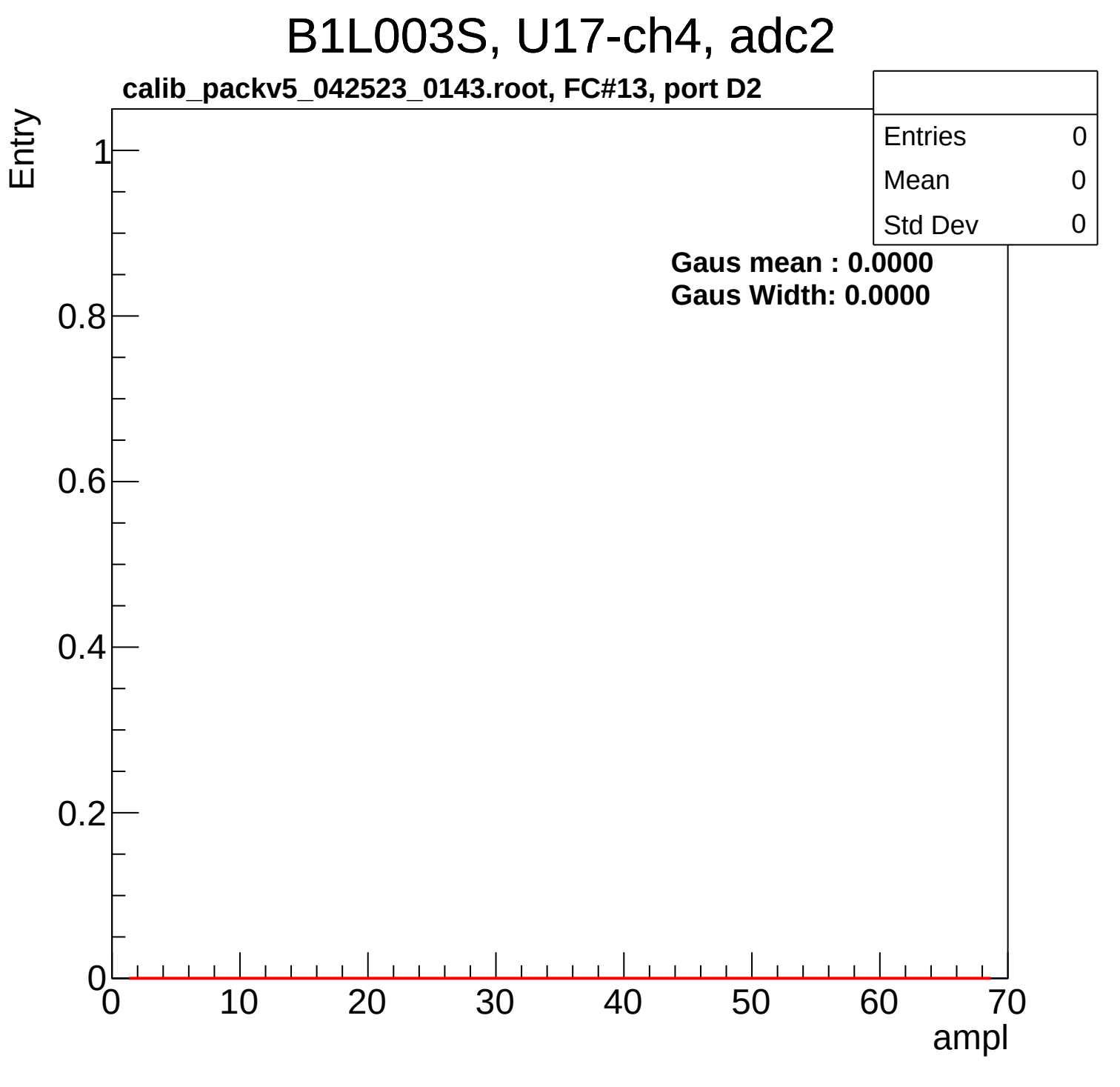
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Gaus Width: 0.0000
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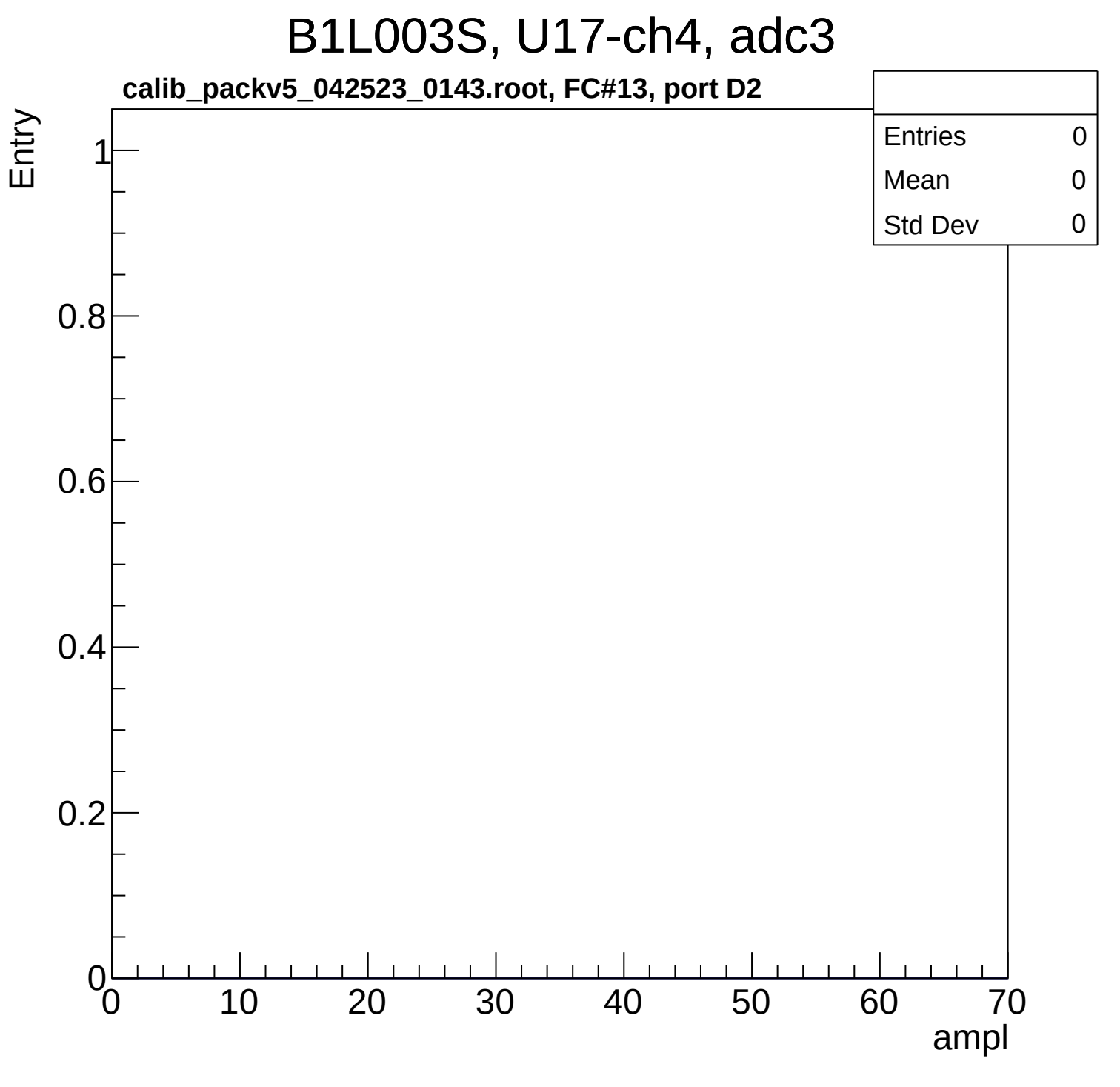
ampl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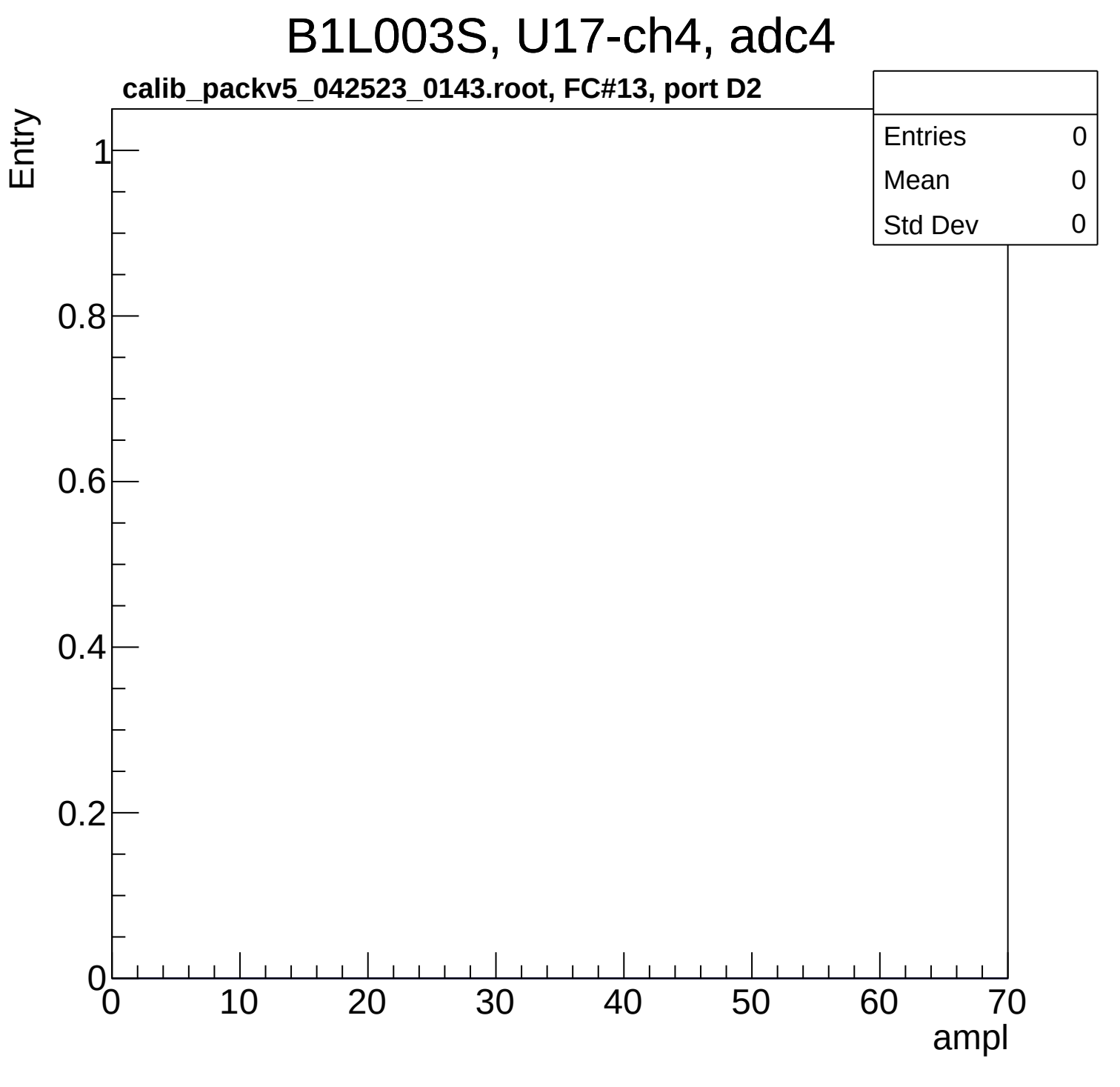
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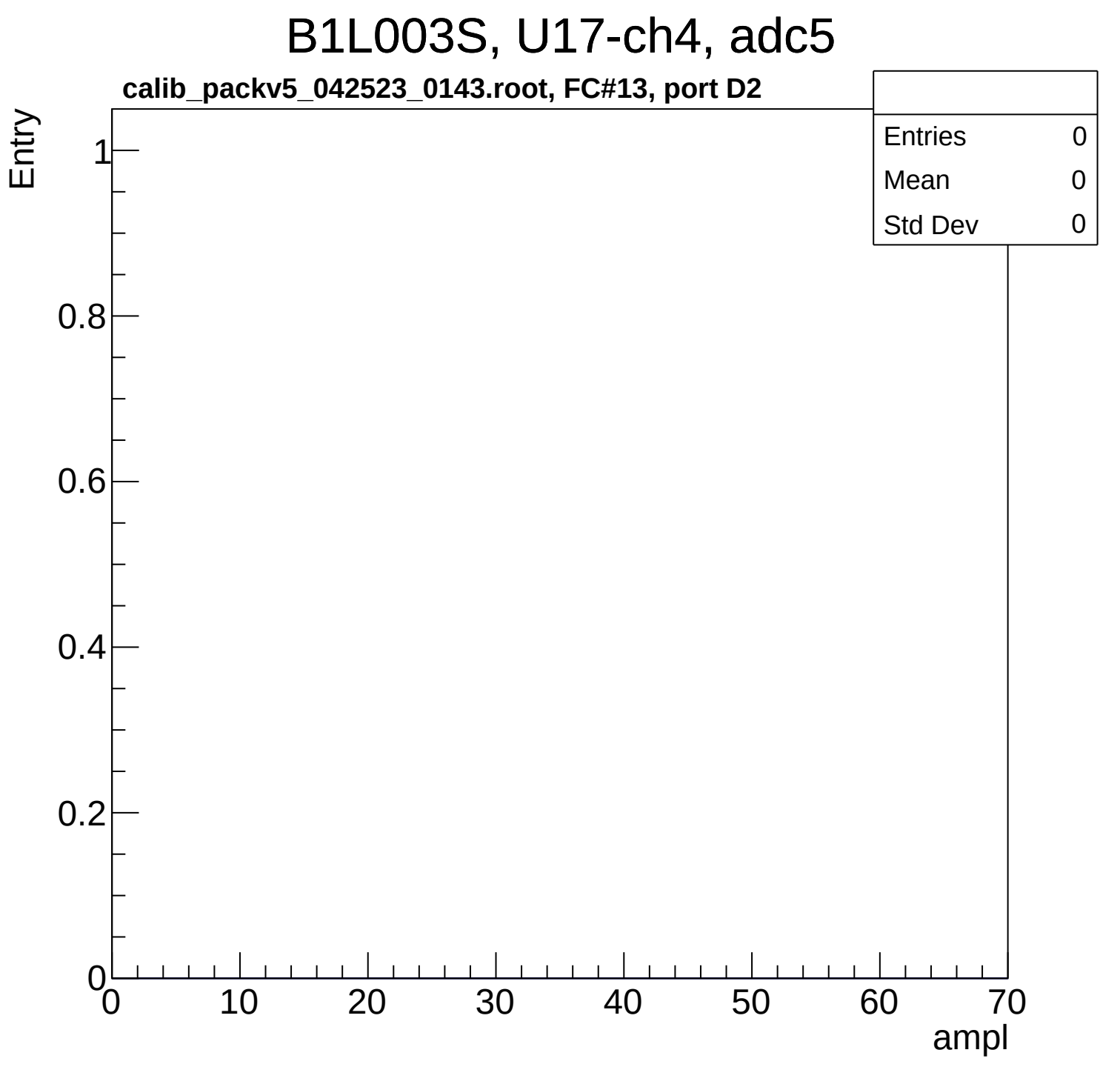
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calib_packv5_042523_0143.root, FC#13, port D2
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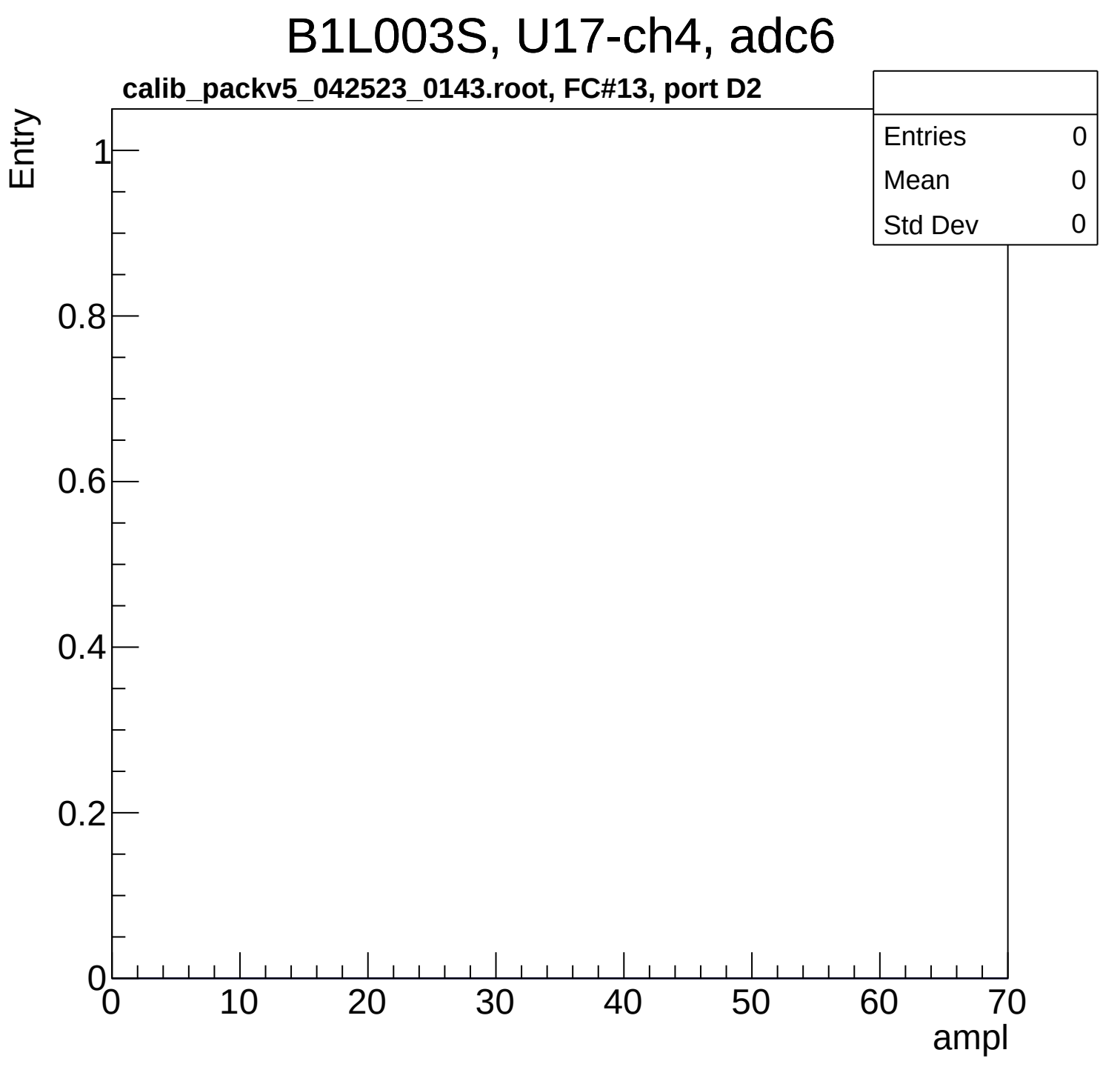
B1L003S, U17-ch4, adc6
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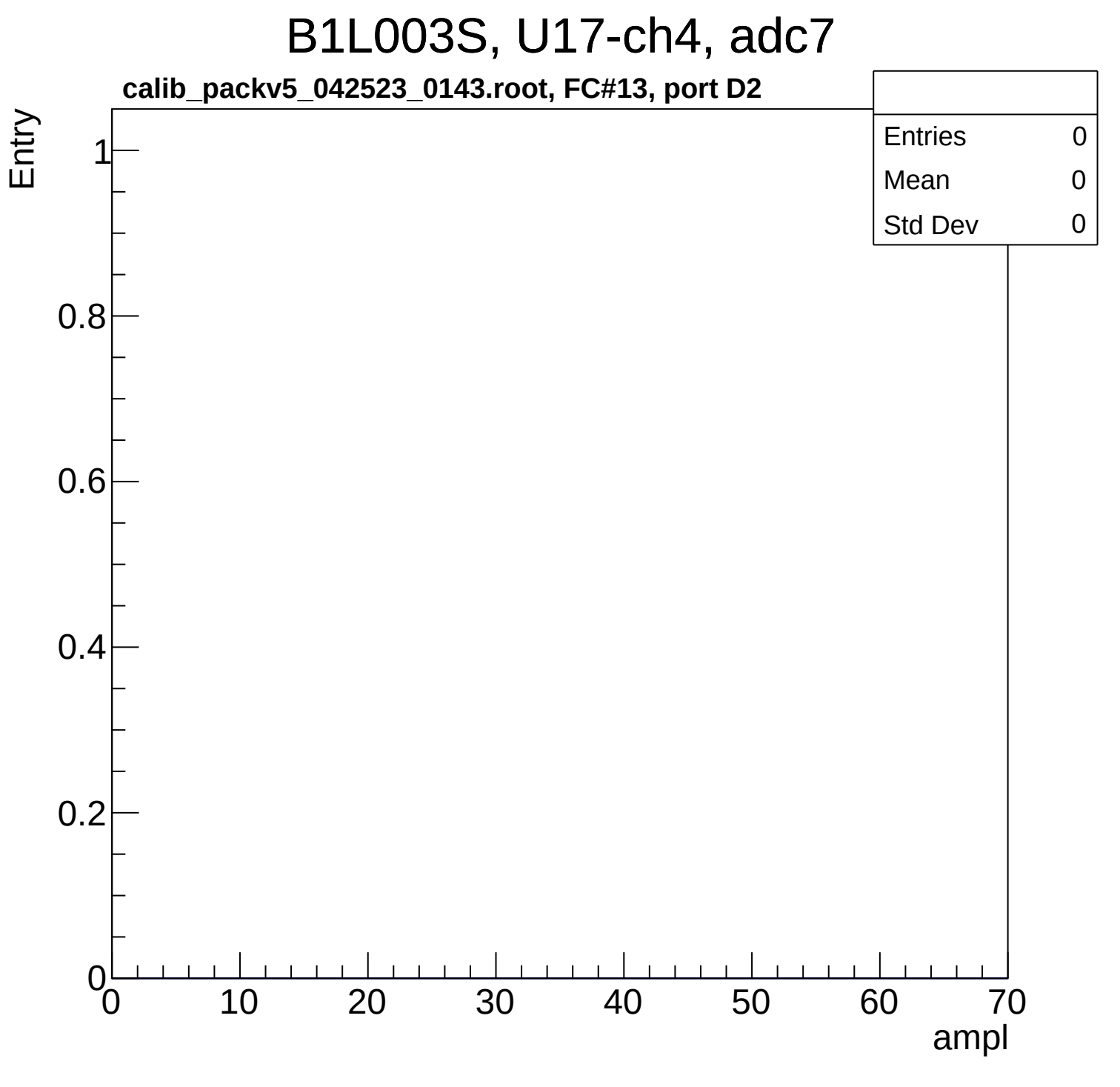
ampl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calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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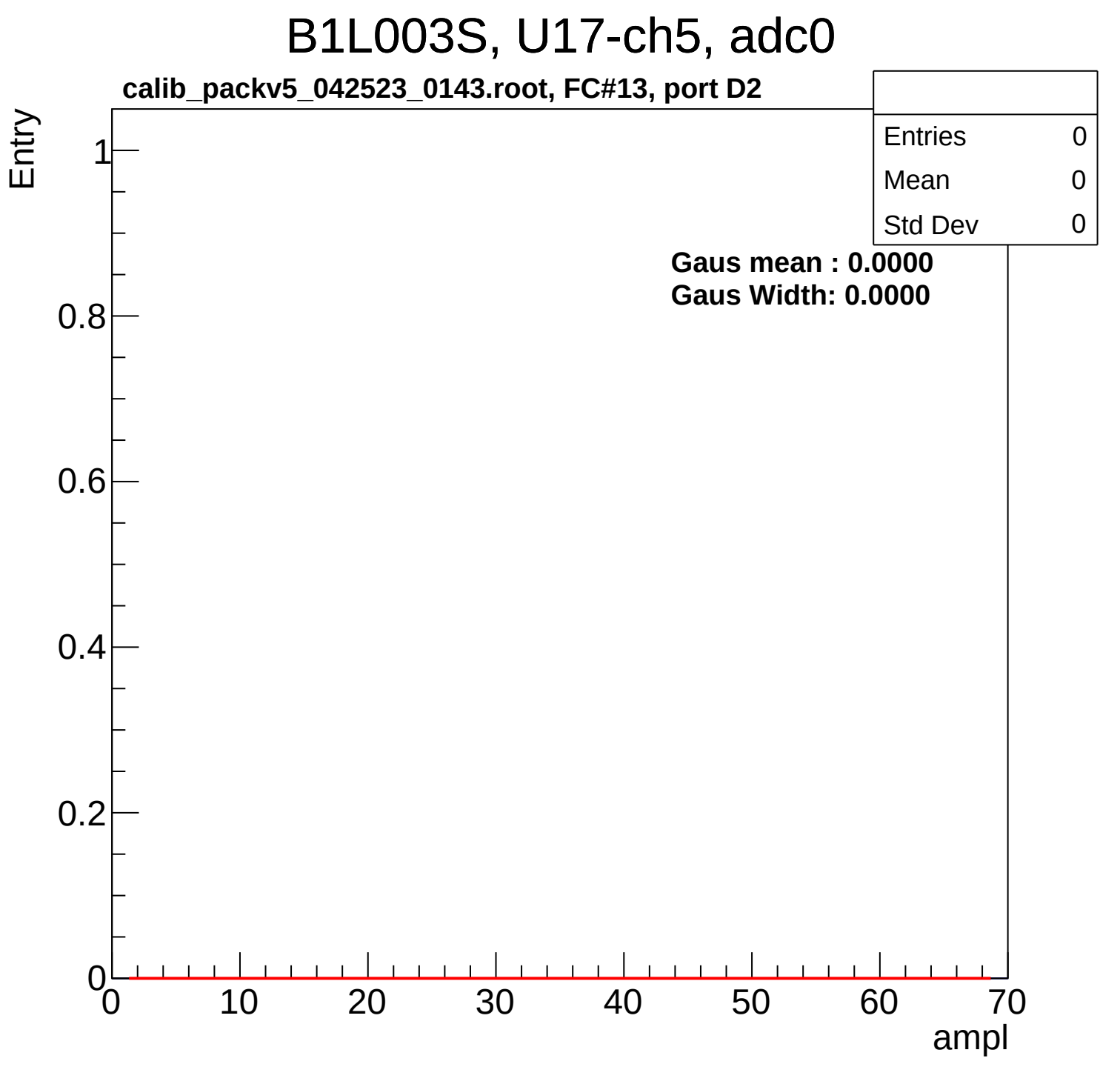
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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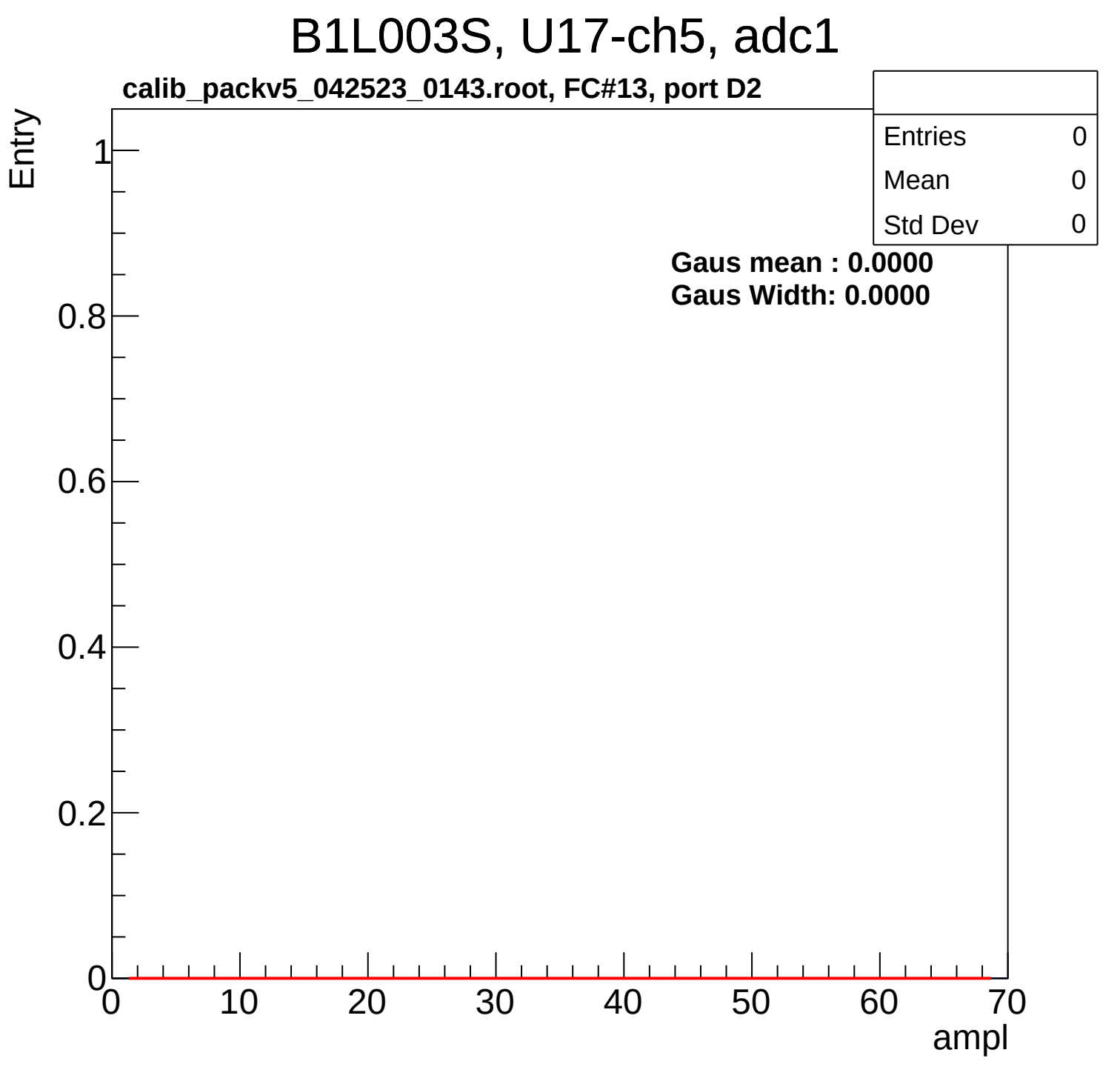
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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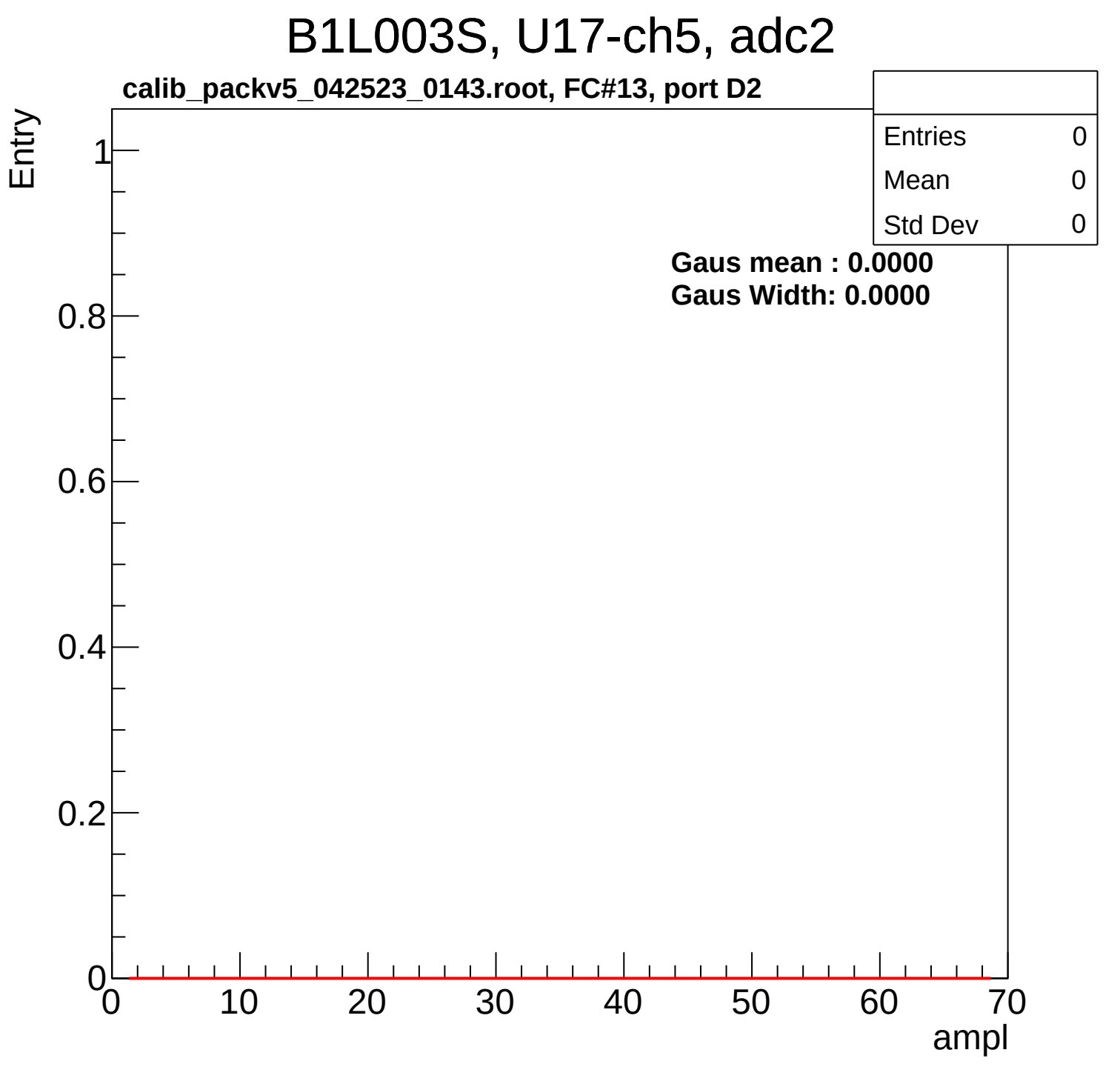
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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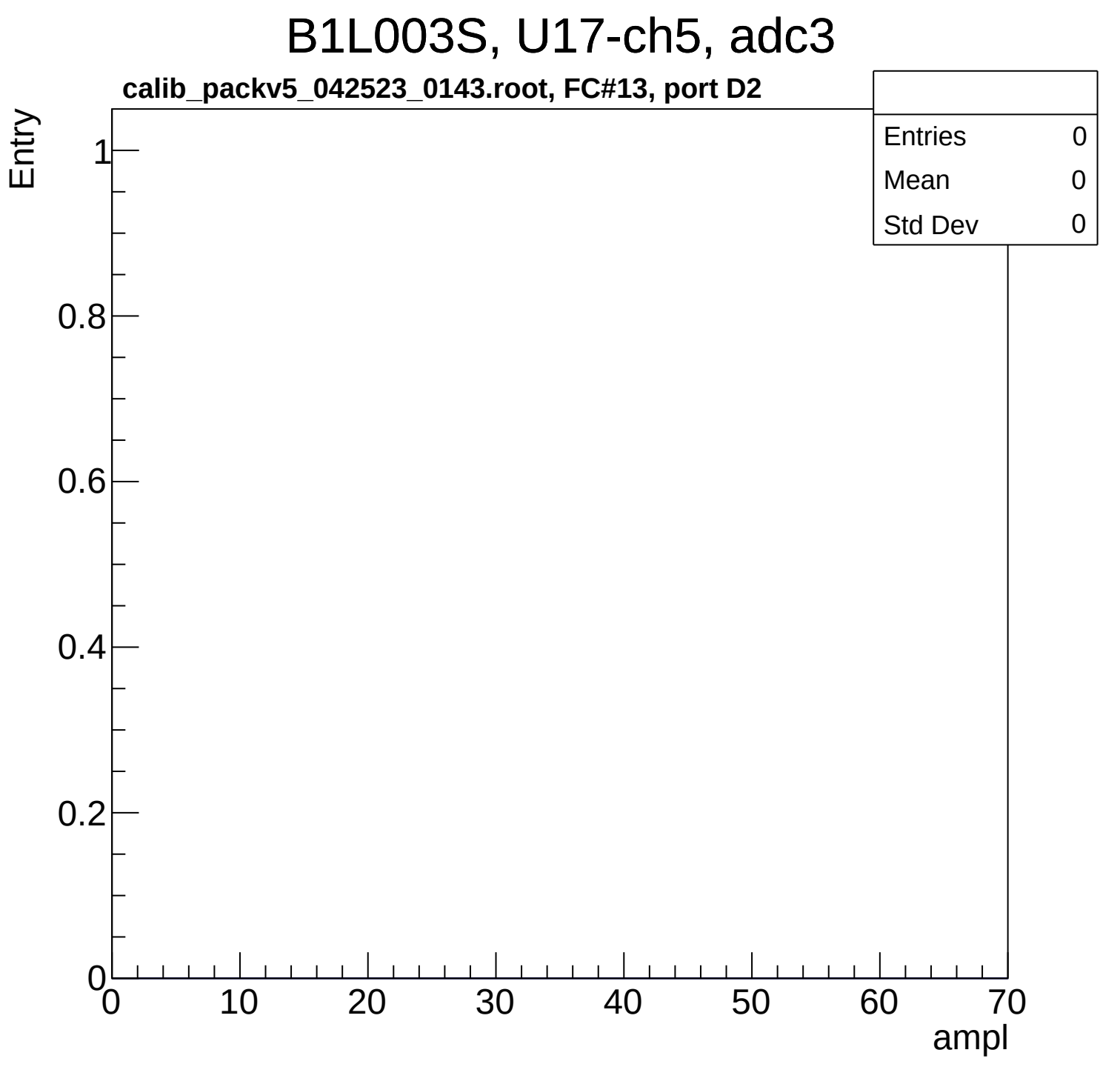
ampl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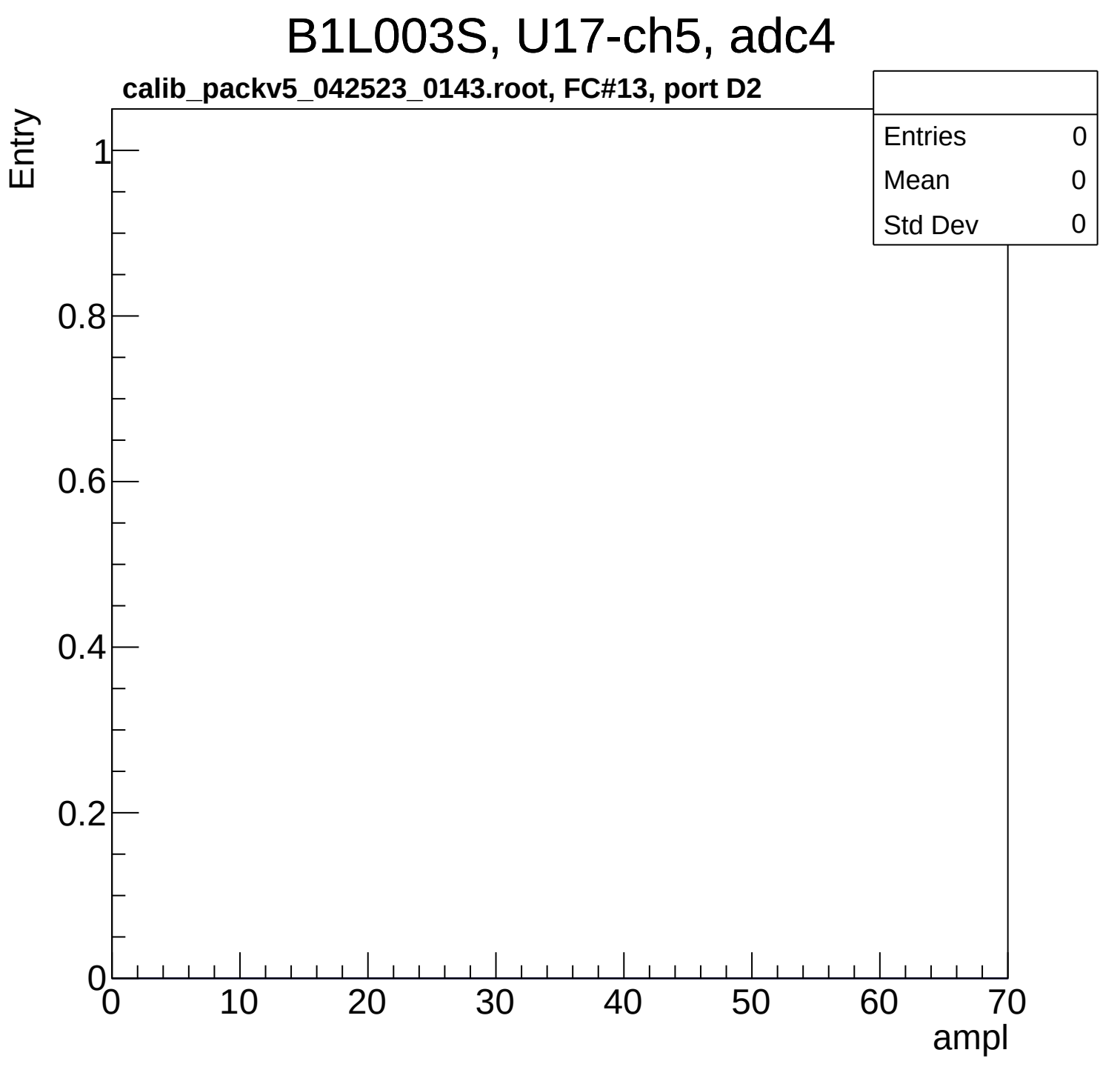
ampl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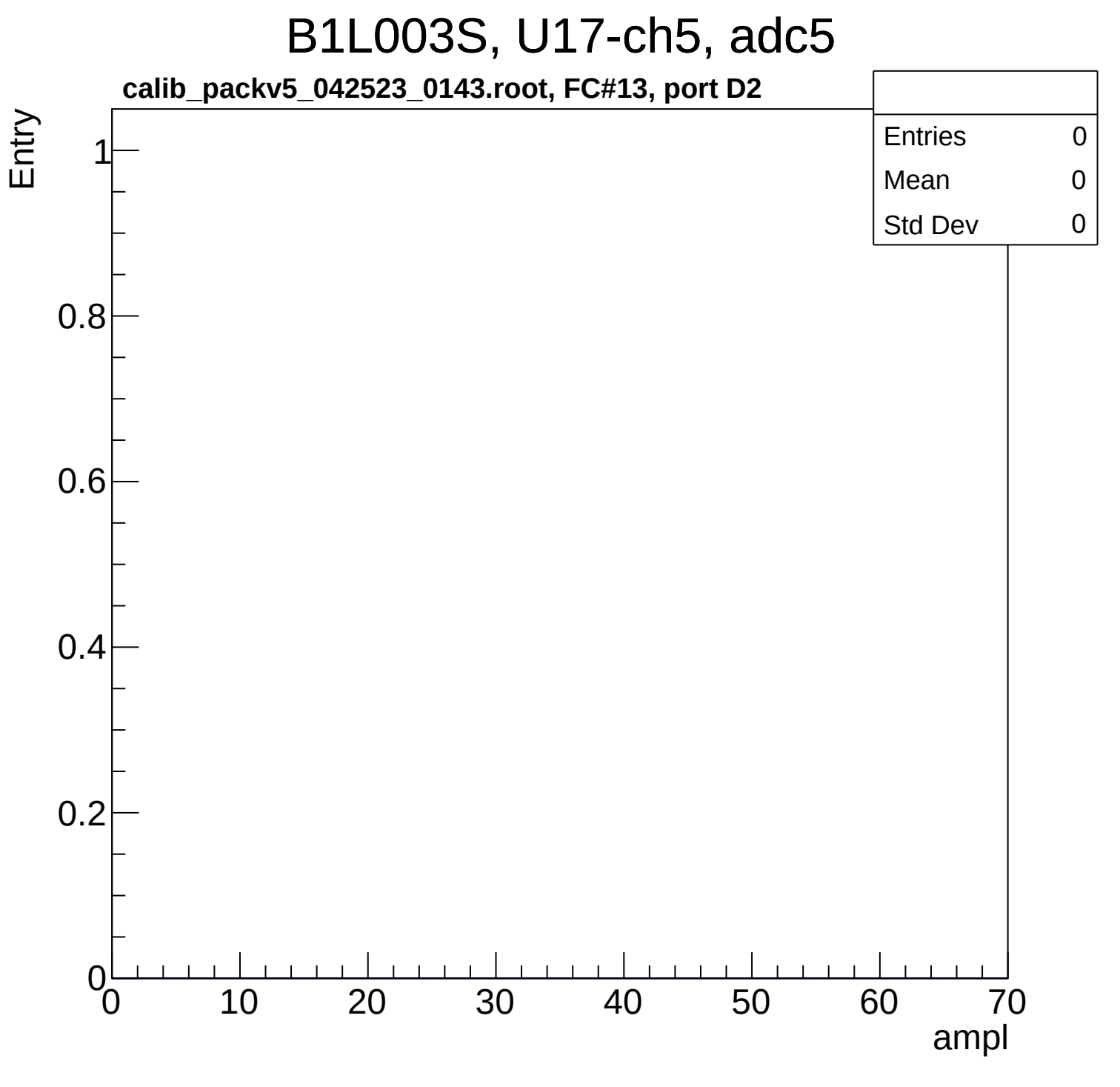
ampl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Std Dev 0
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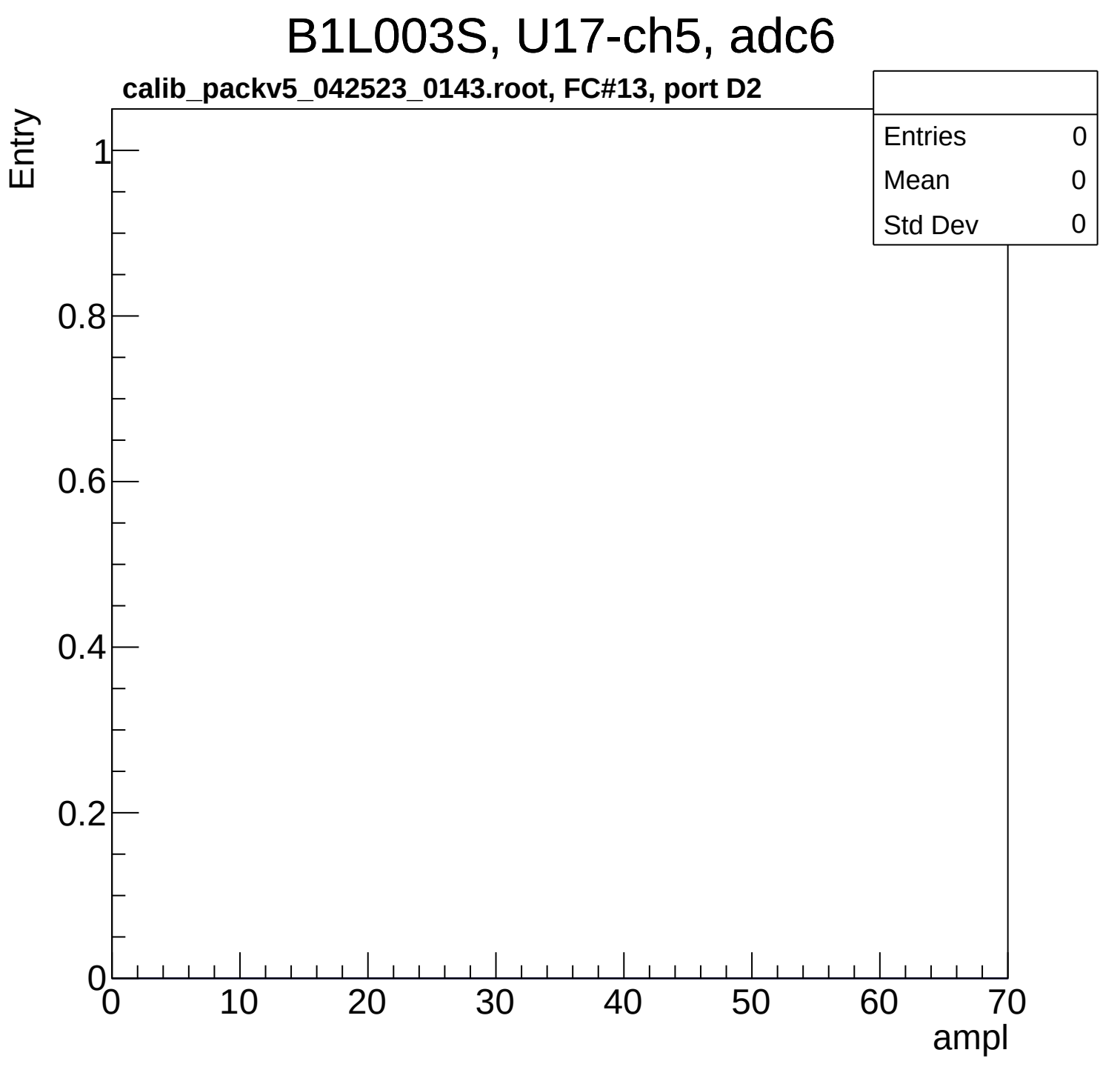
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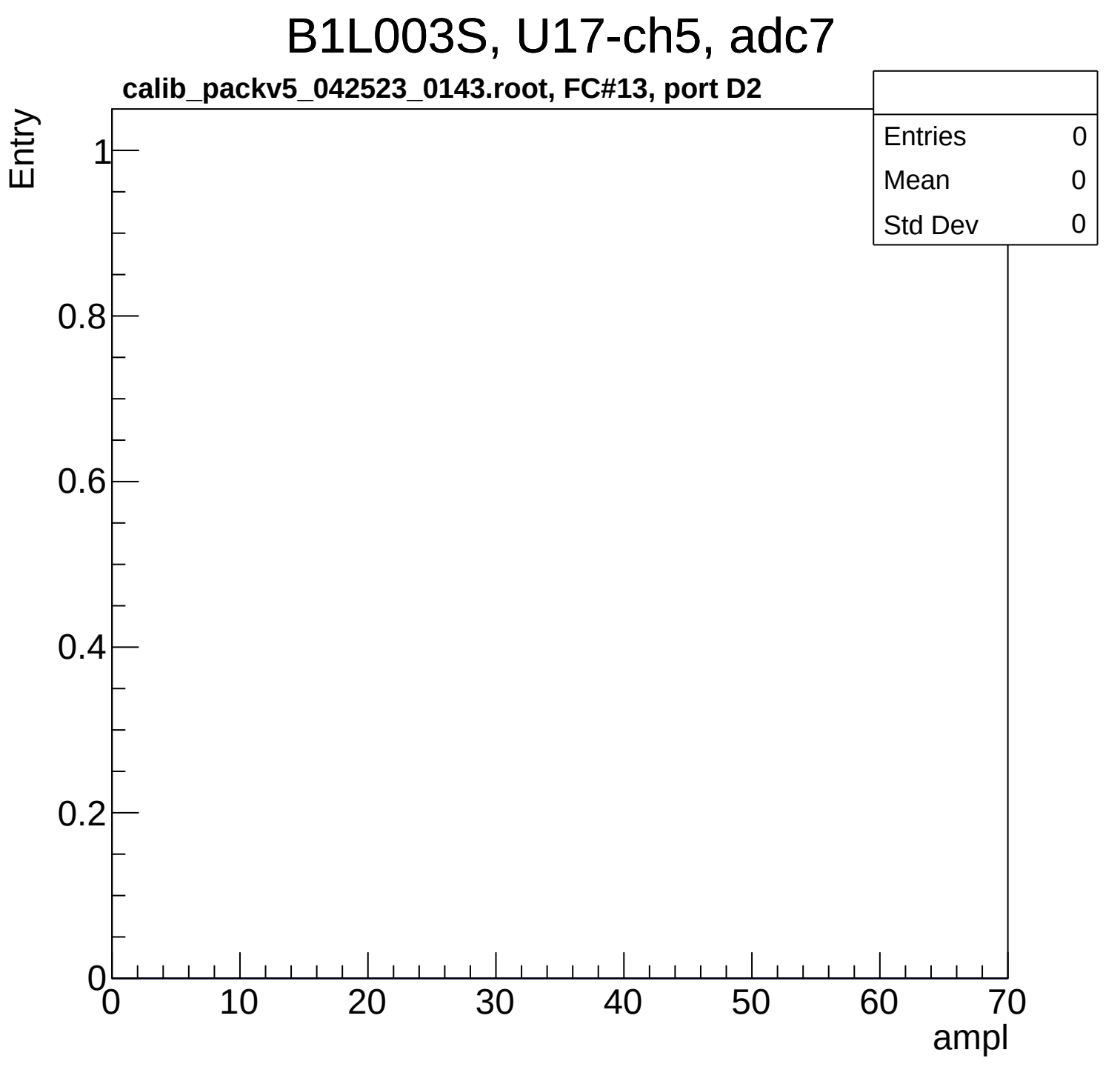
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calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
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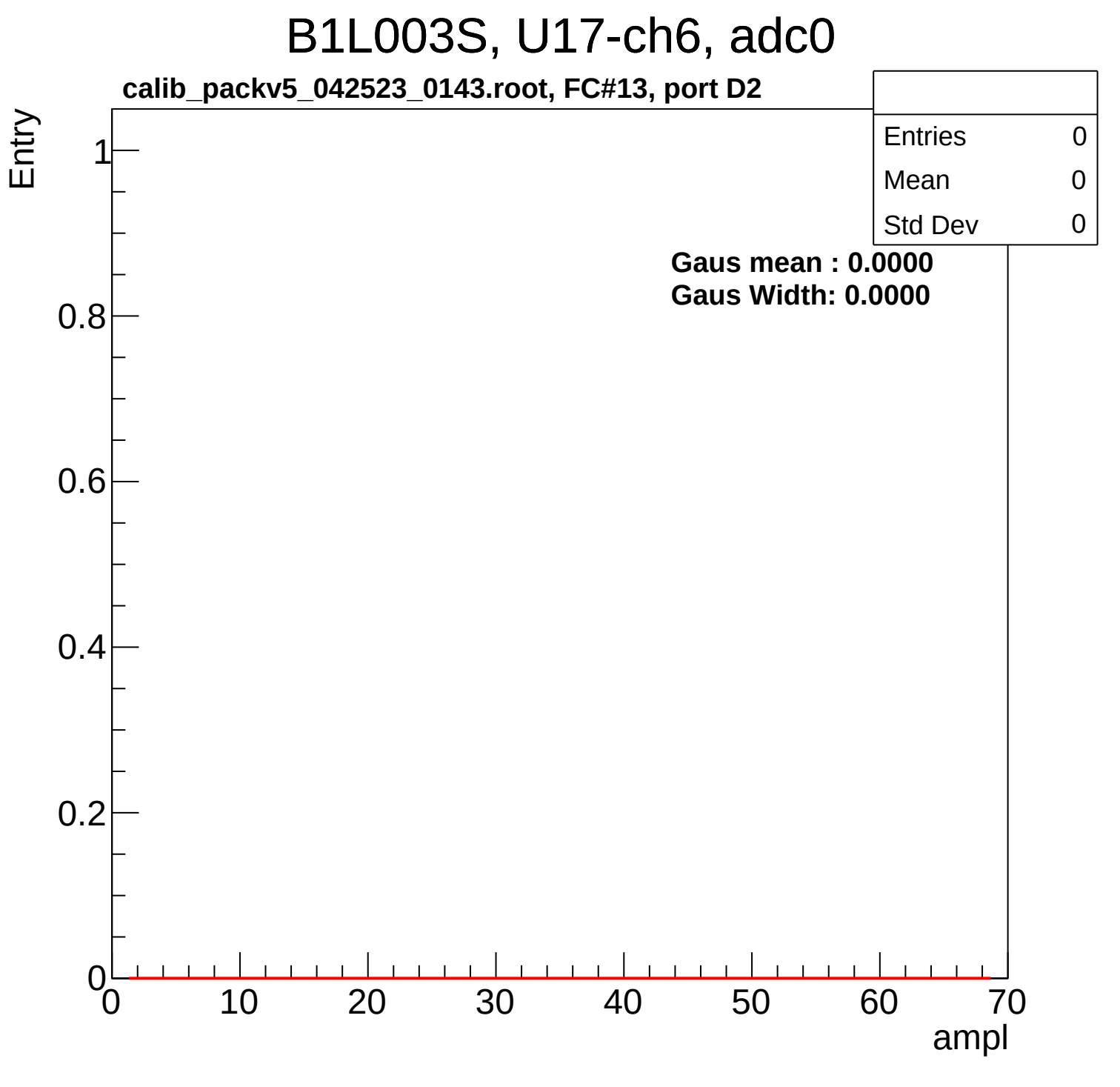
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calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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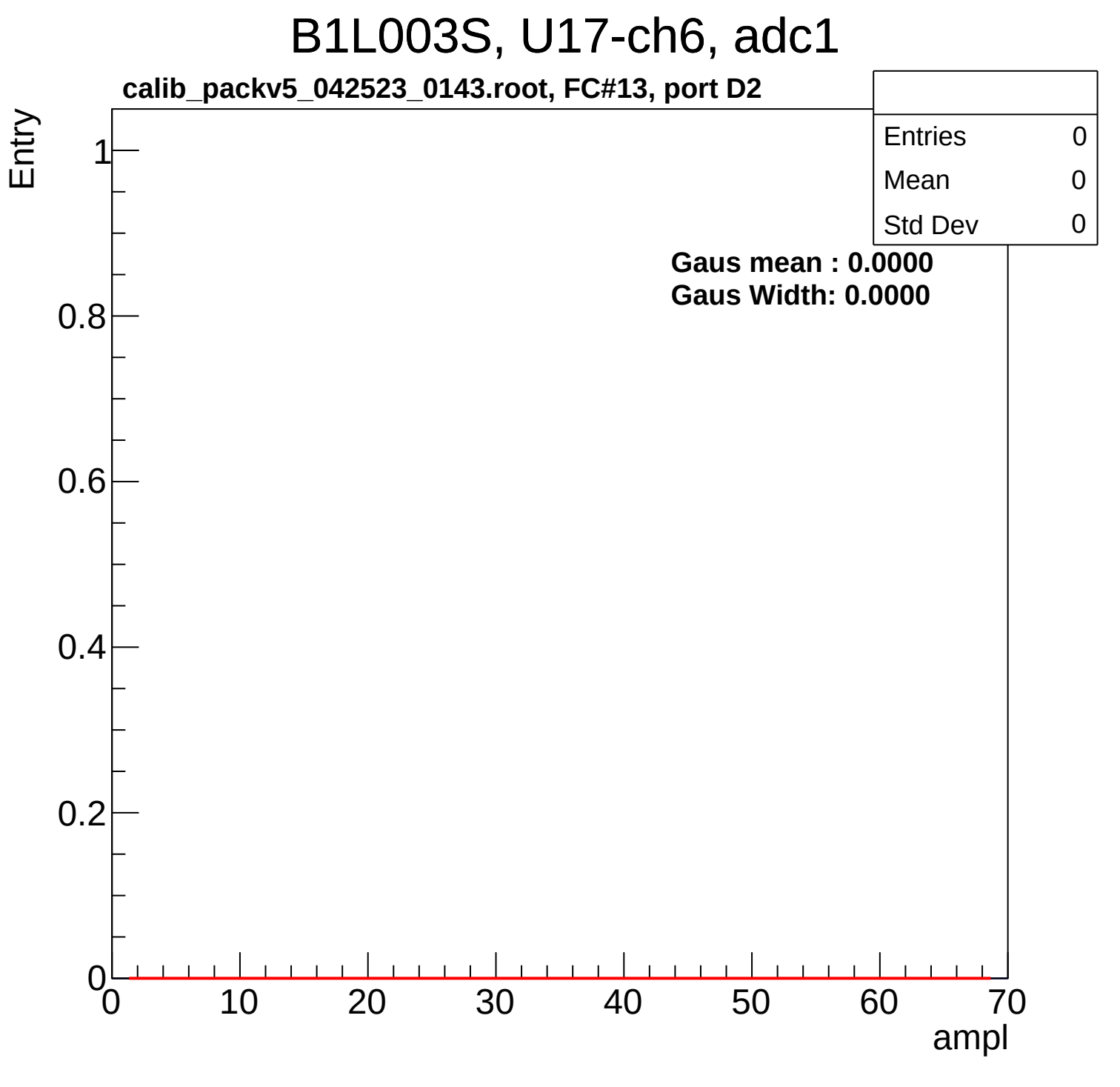
ampl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calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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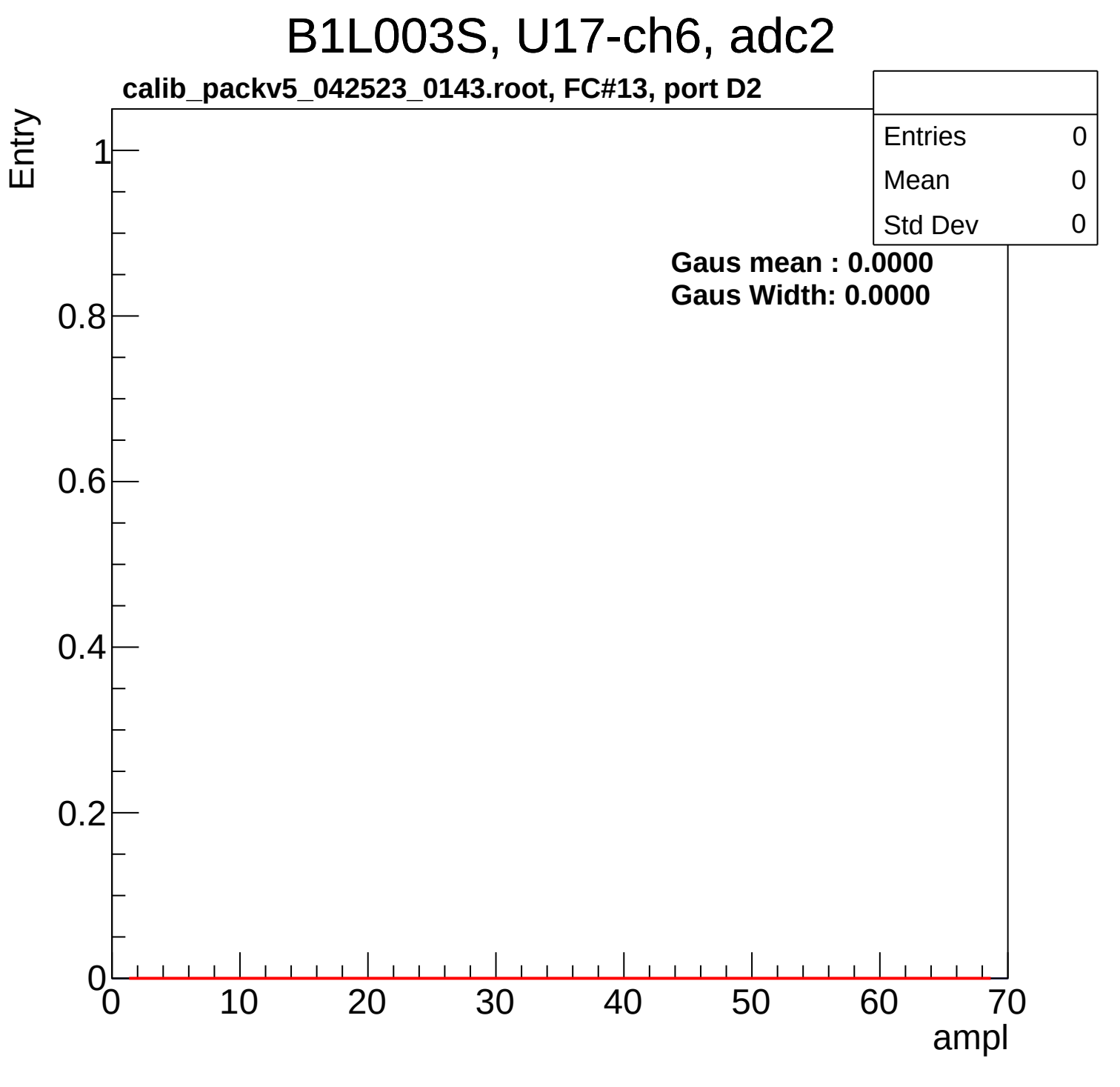
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Std Dev 0
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Gaus Width: 0.0000
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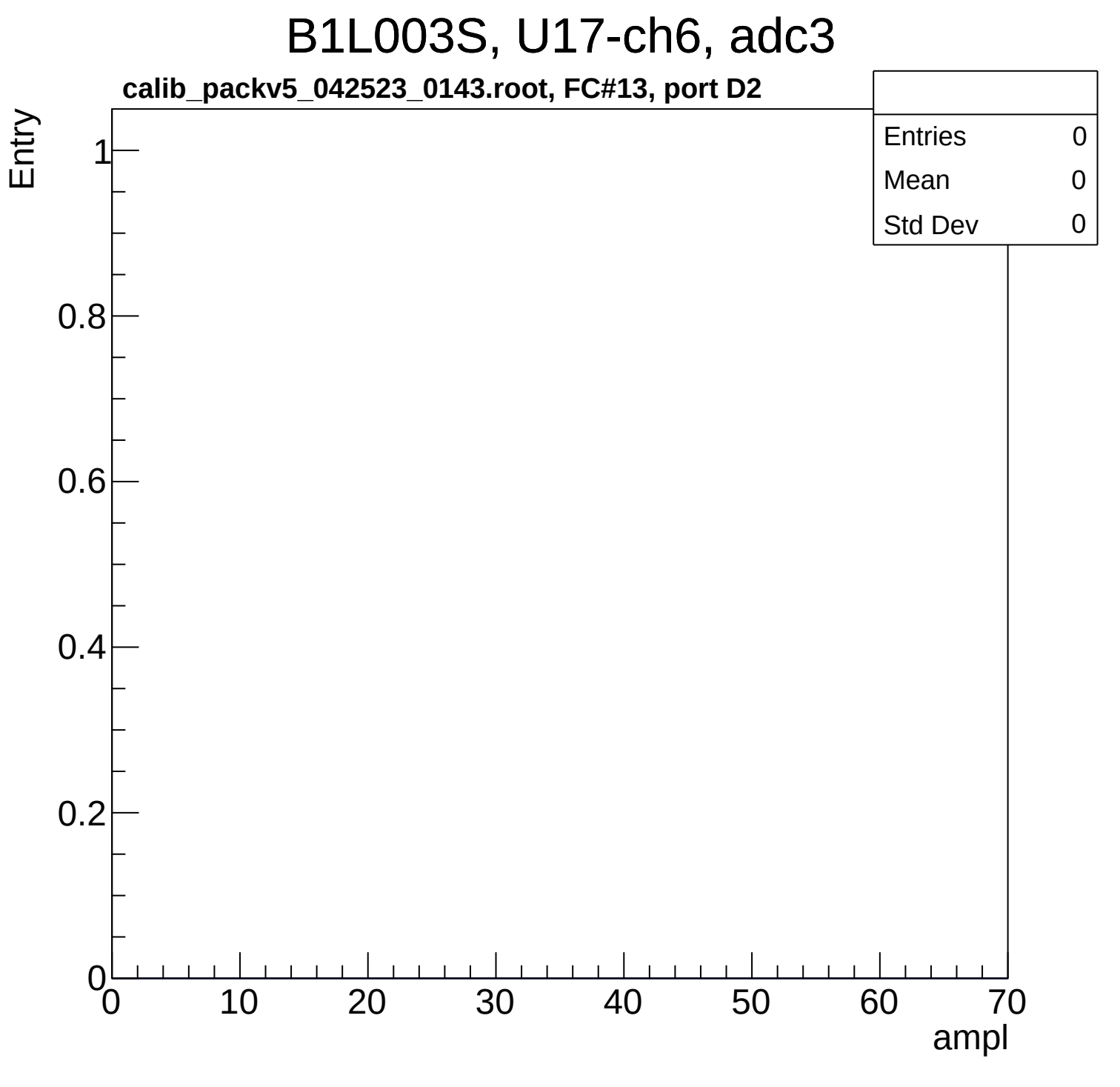
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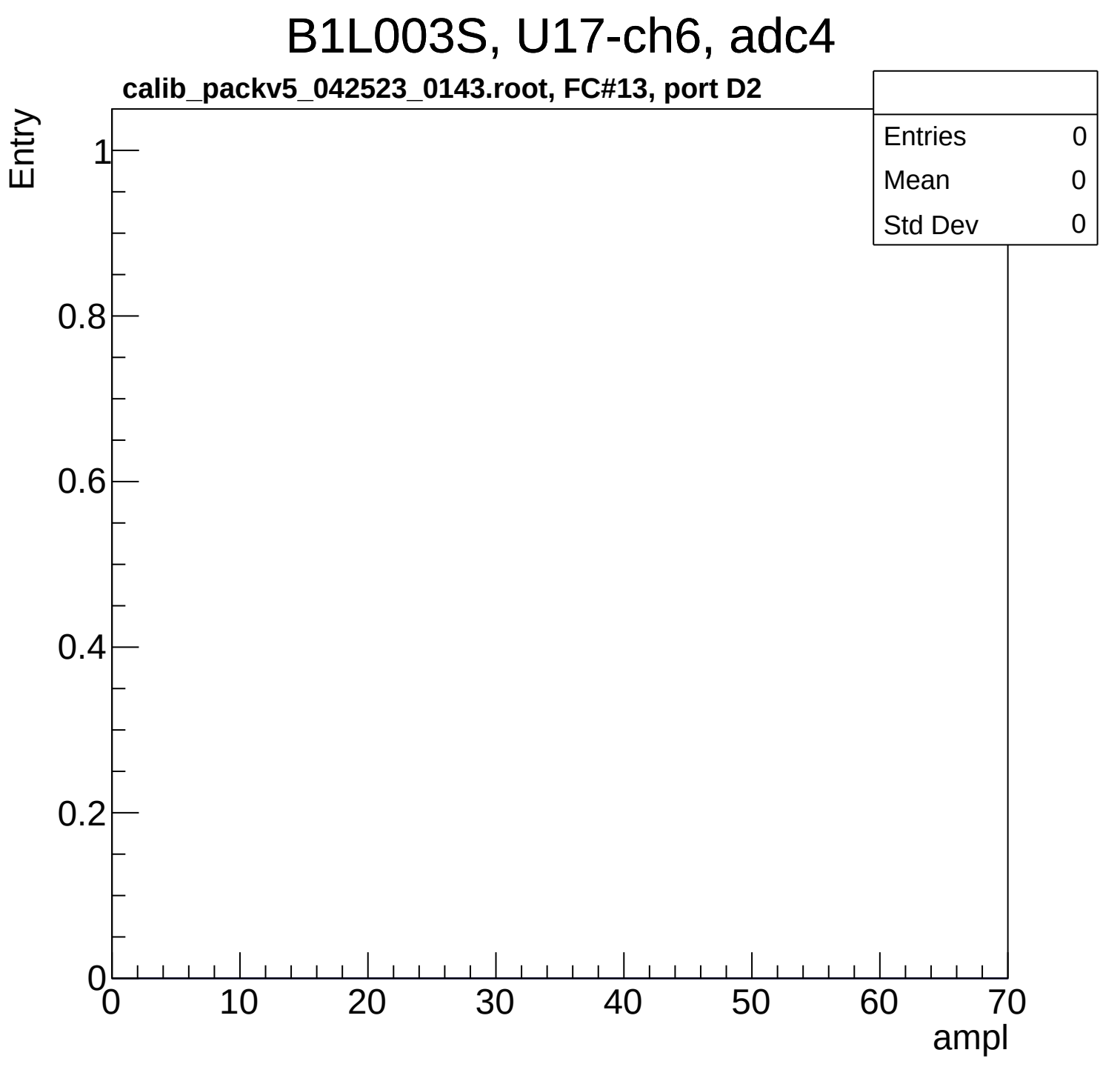
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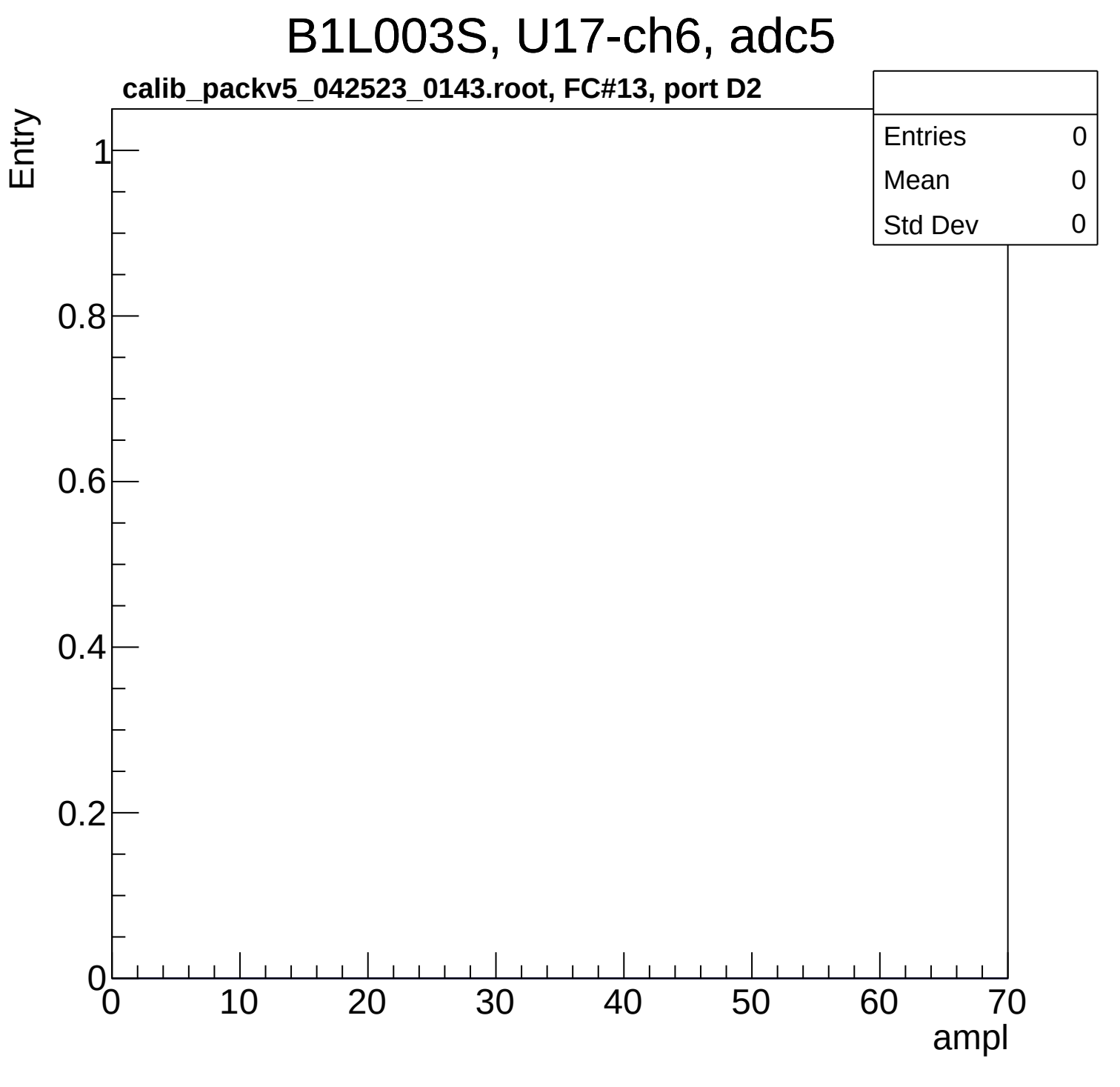
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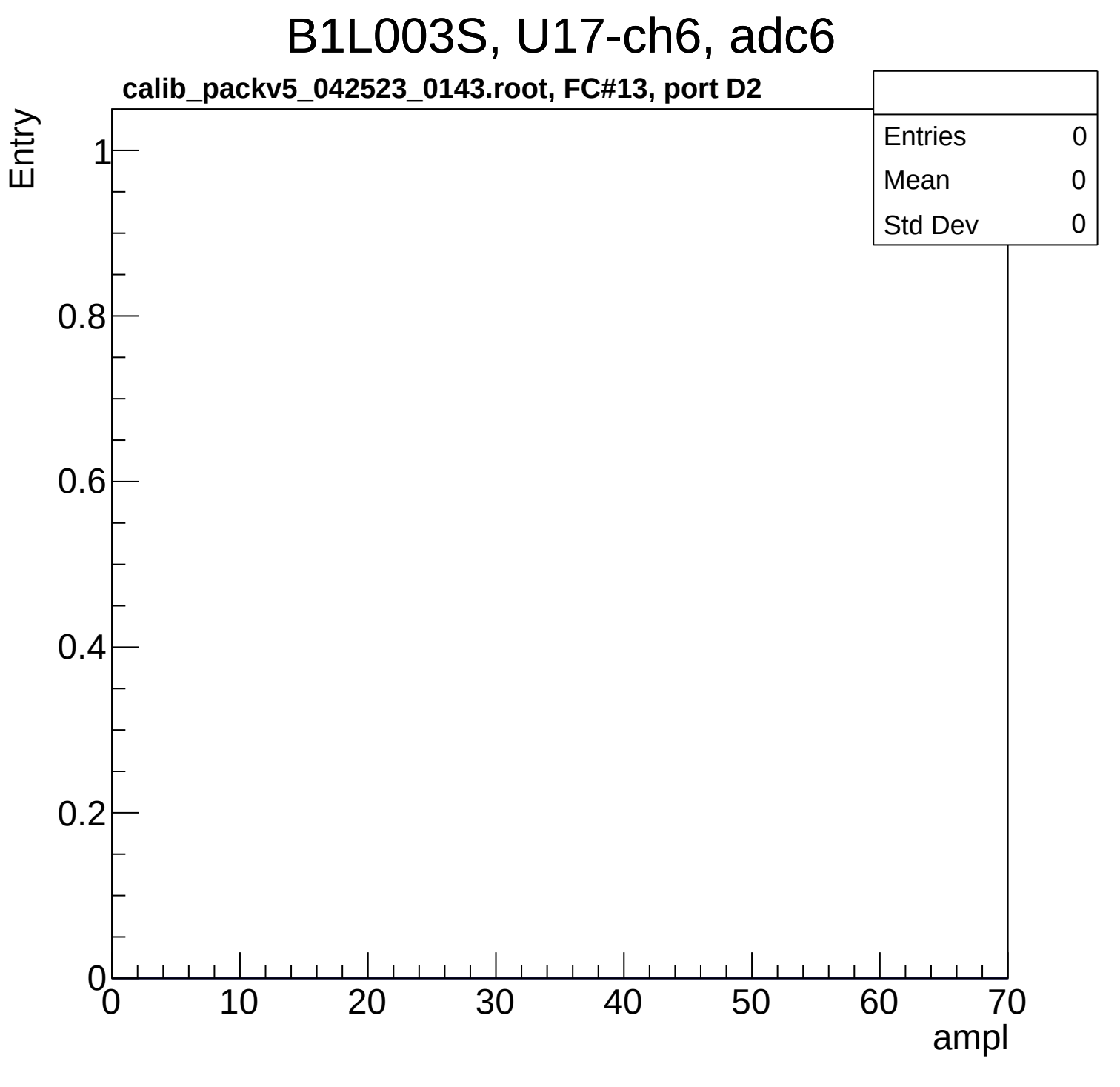
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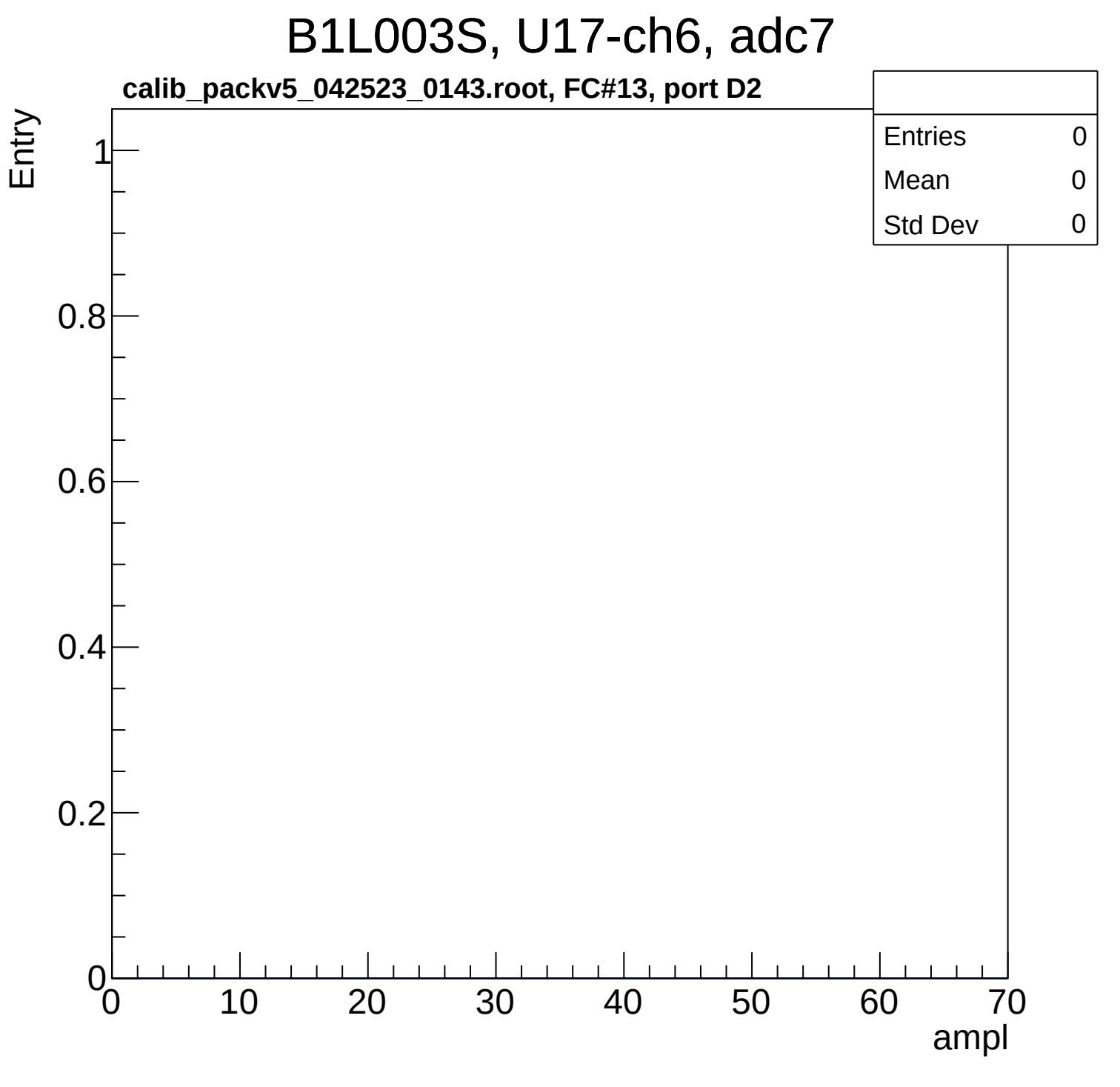
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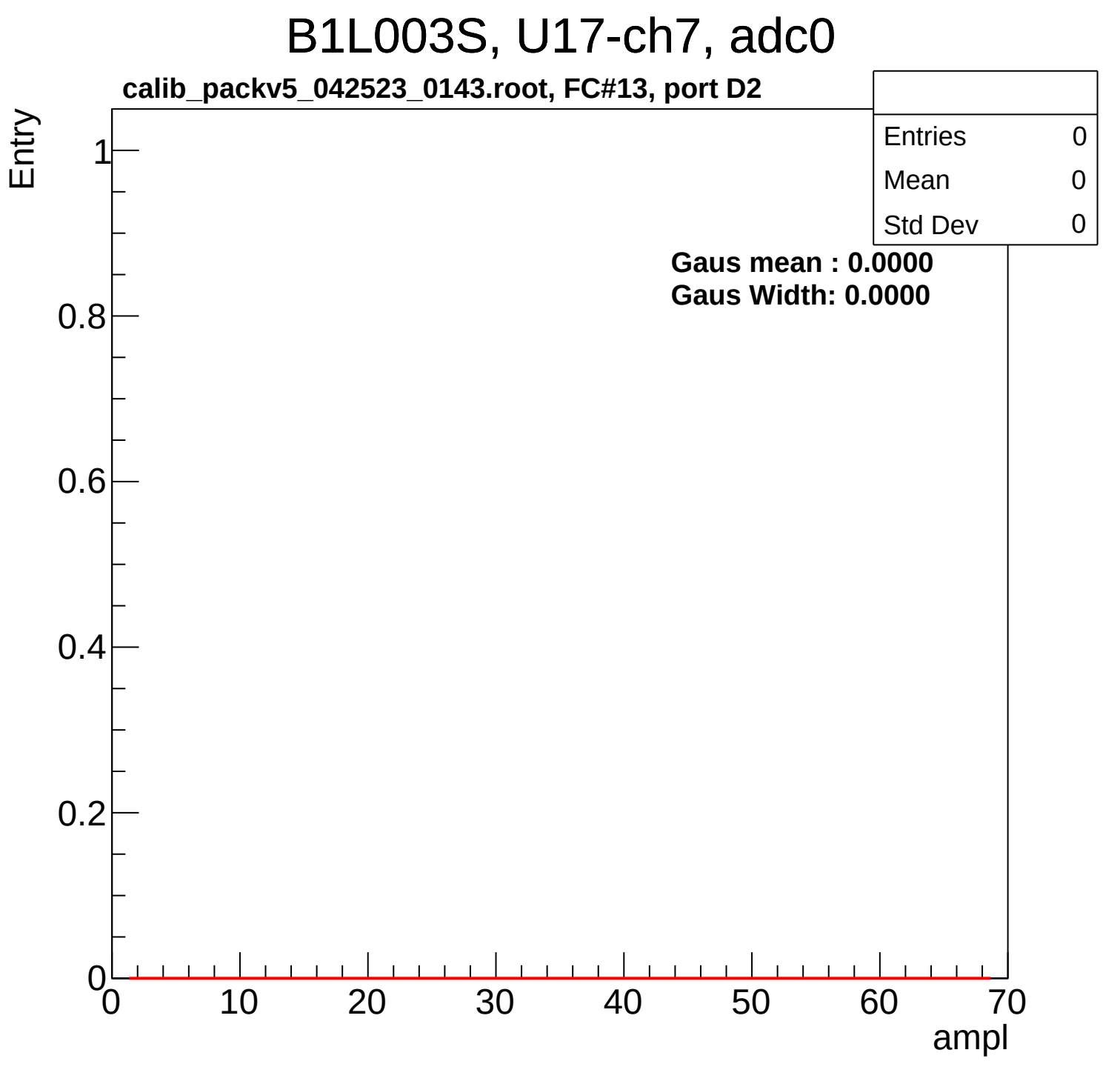
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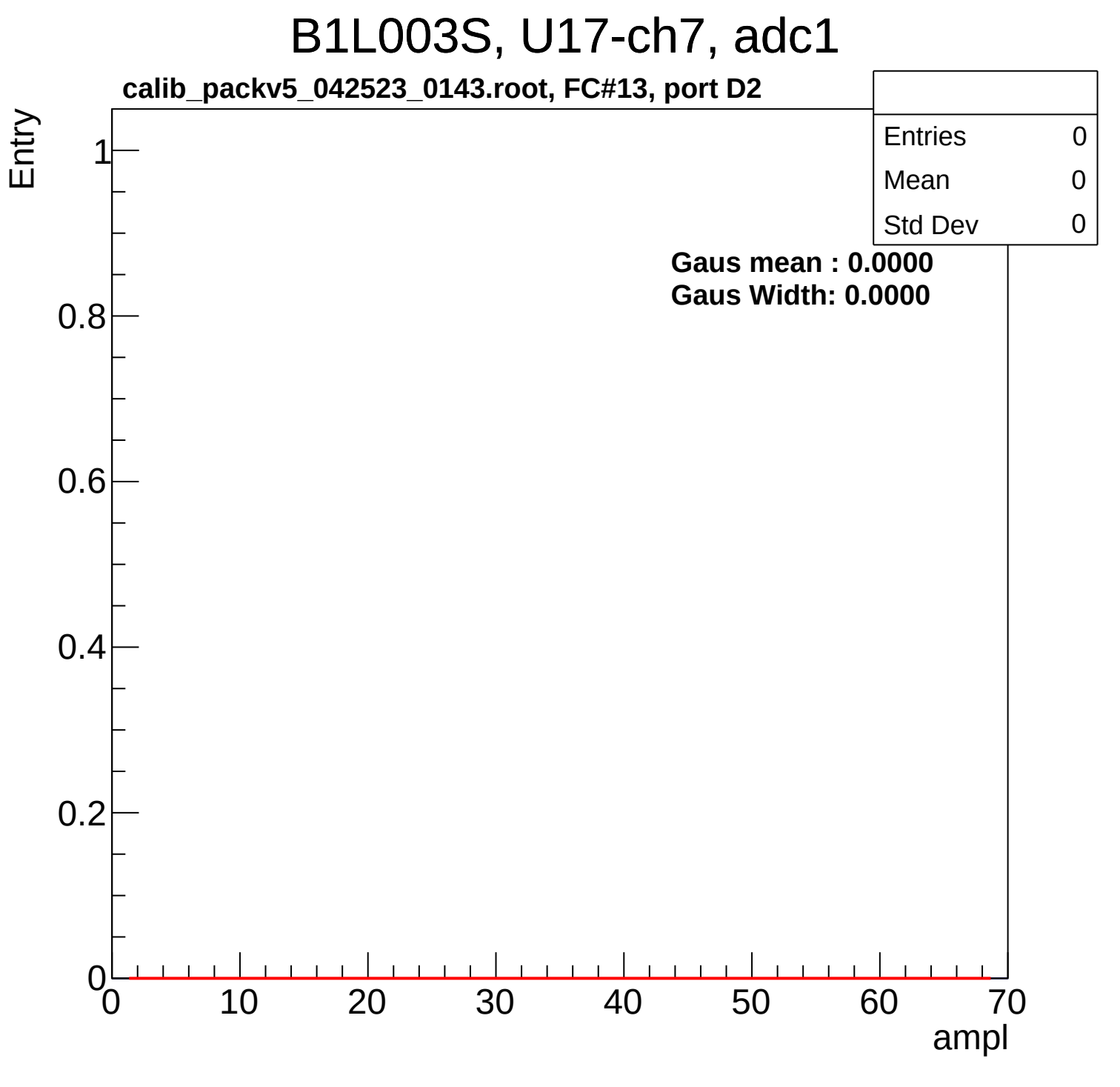
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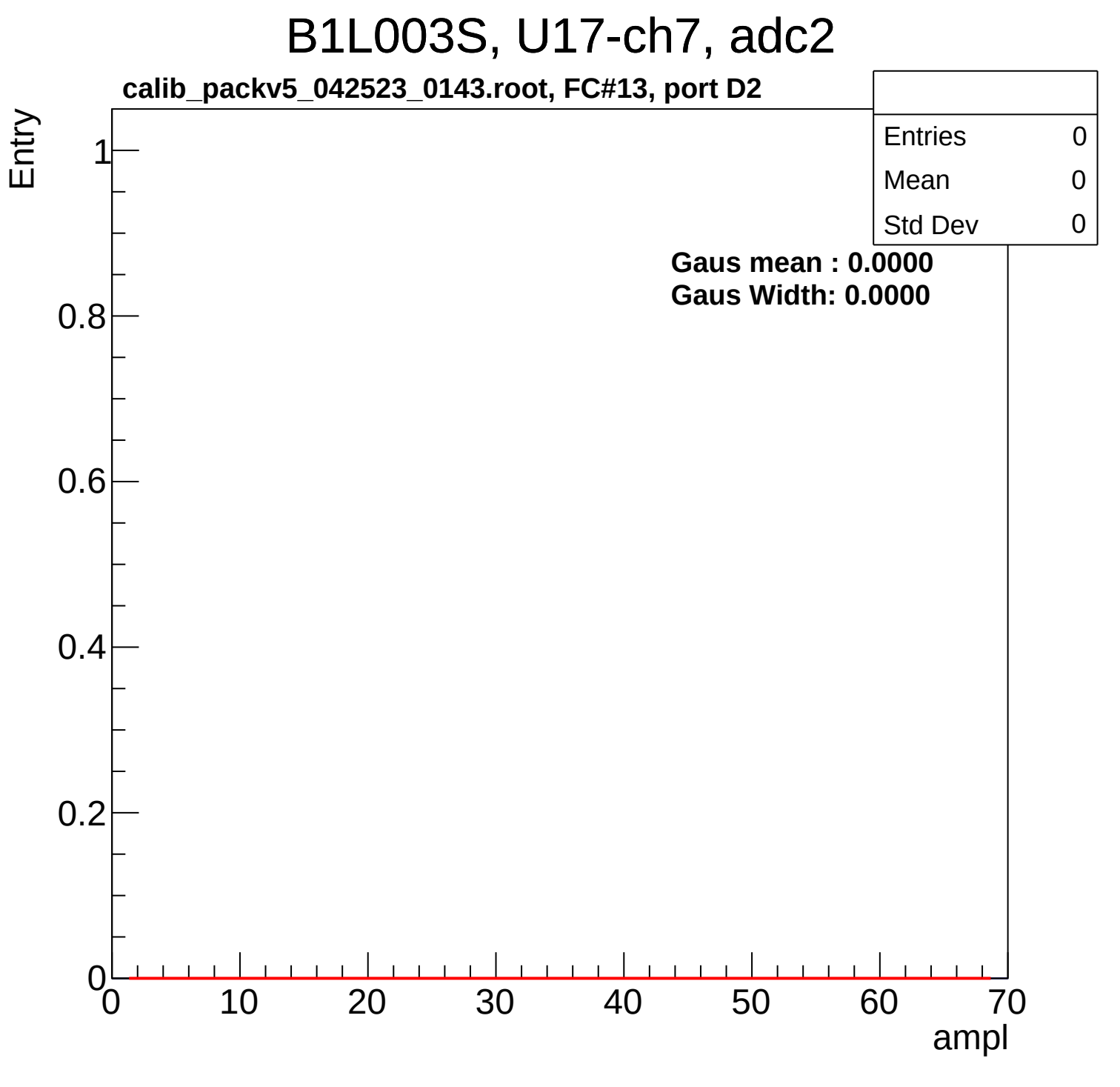
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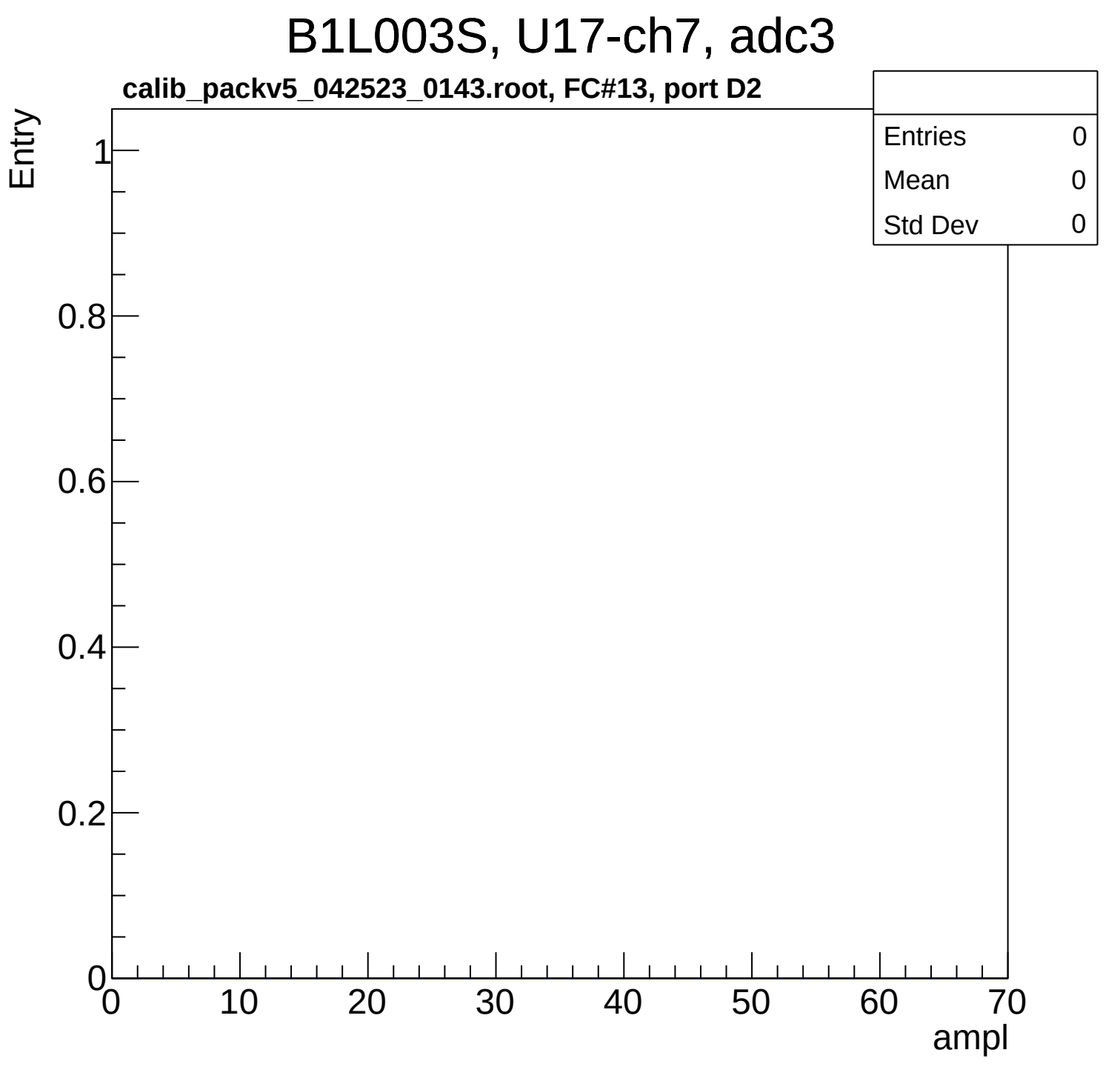
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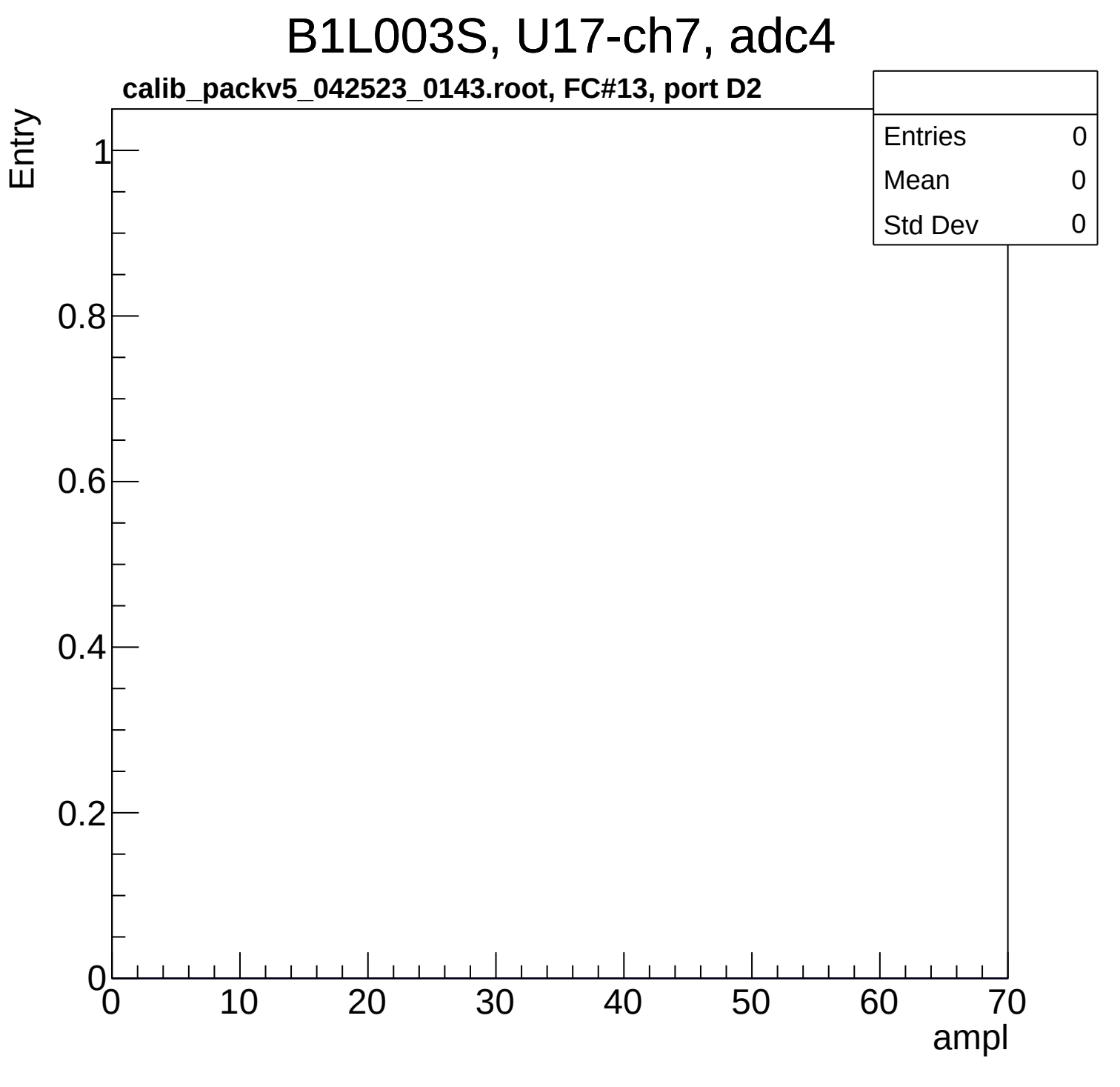
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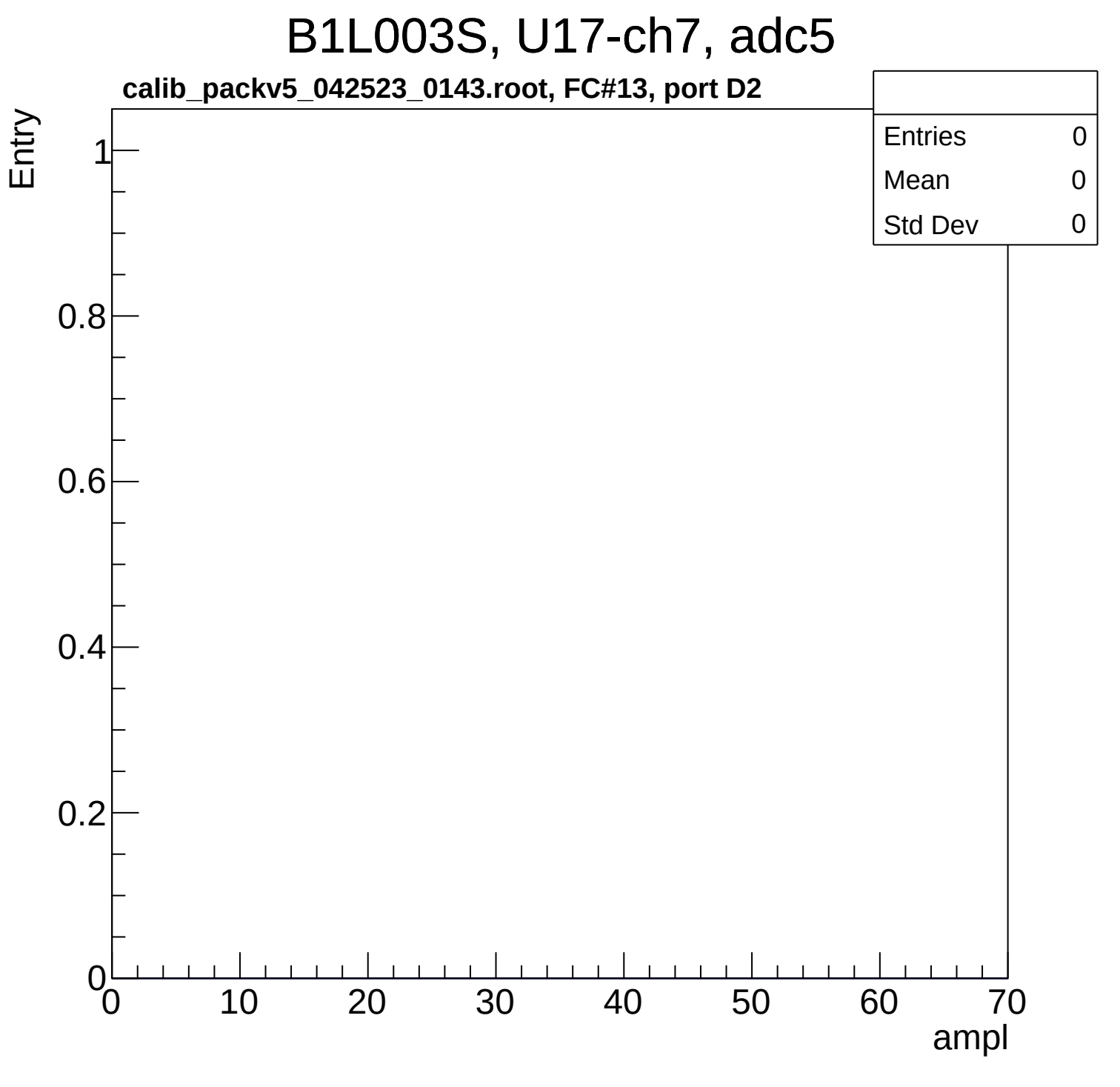
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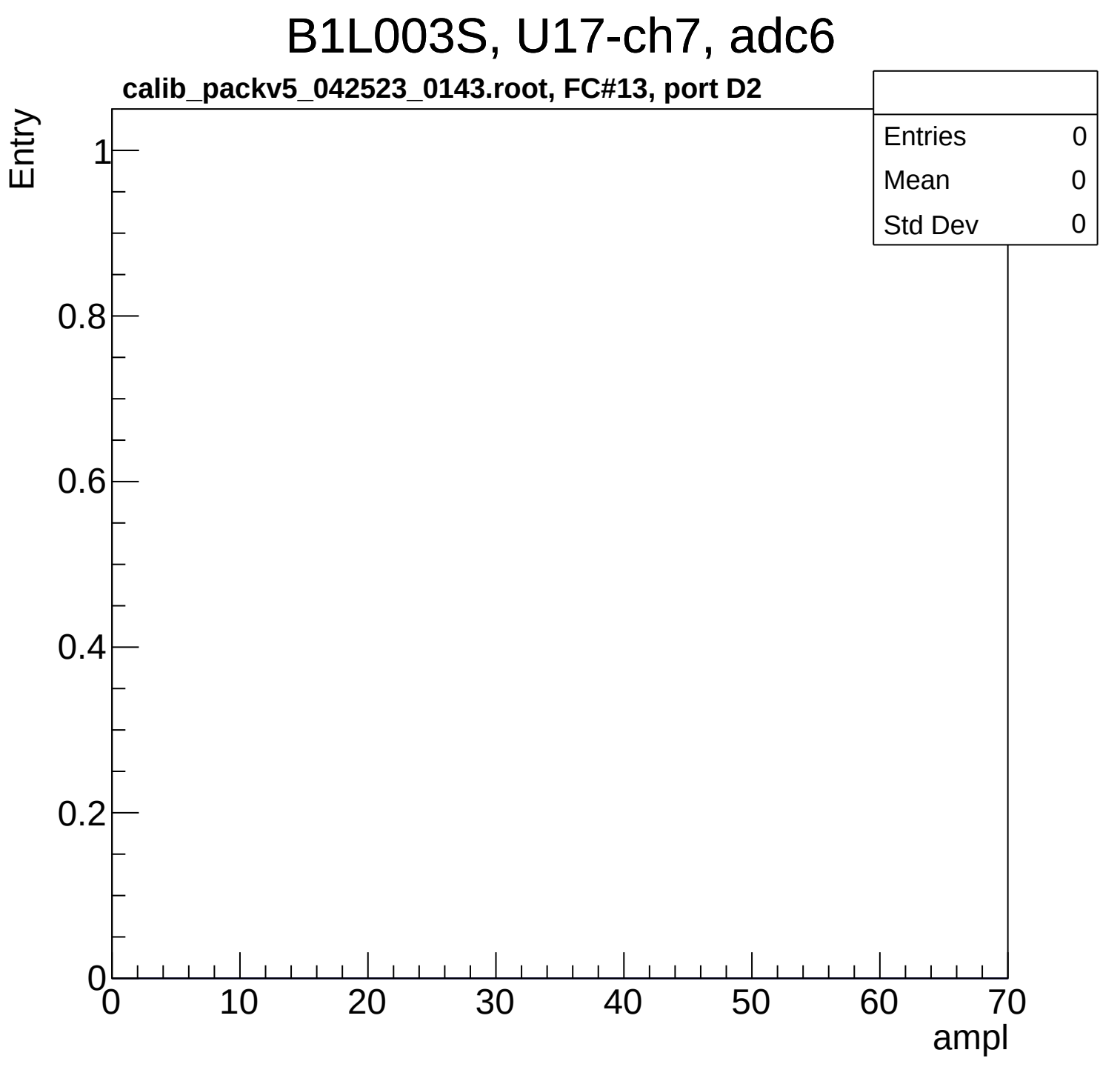
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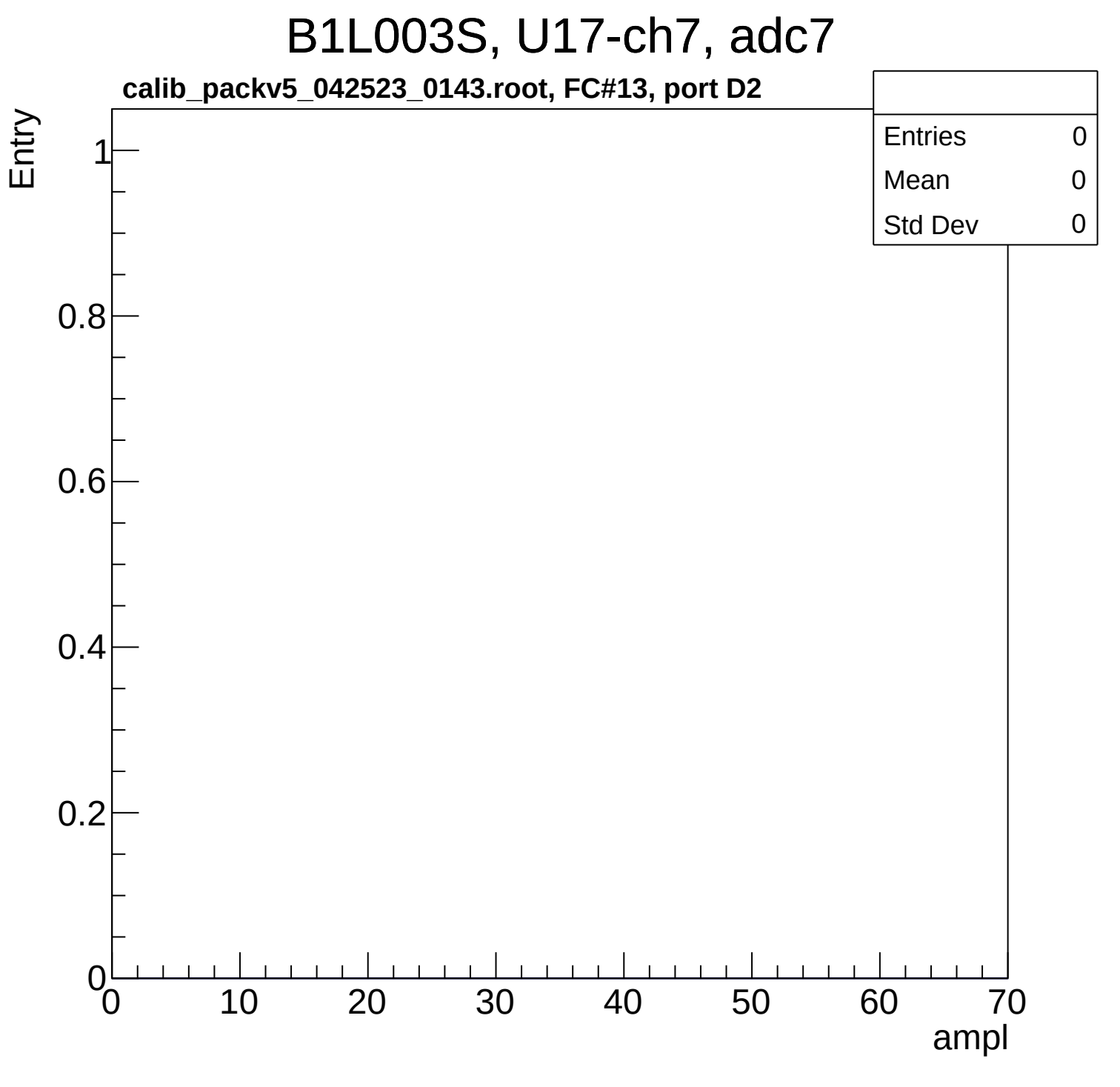
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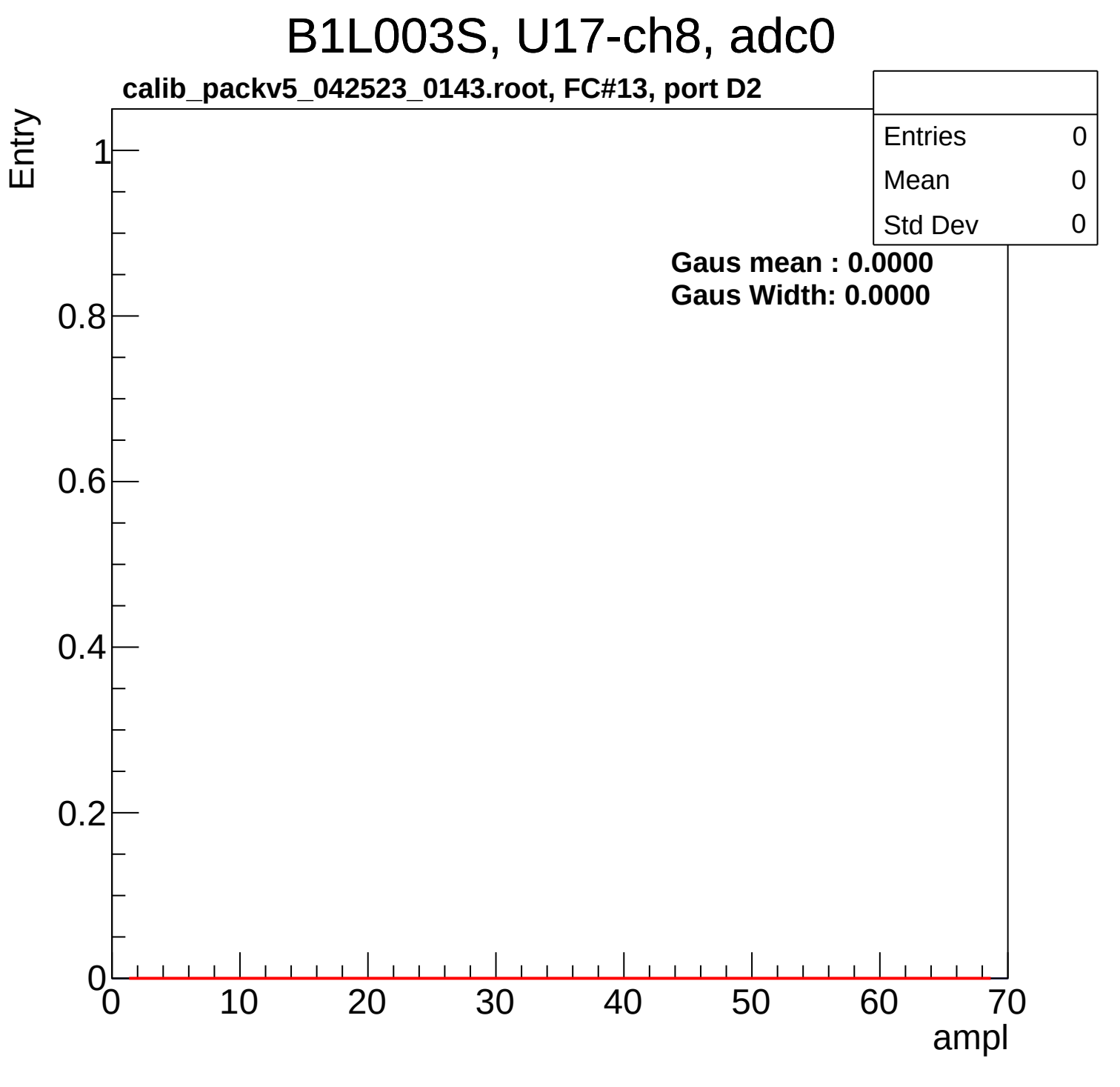
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Std Dev 0
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Gaus Width: 0.0000
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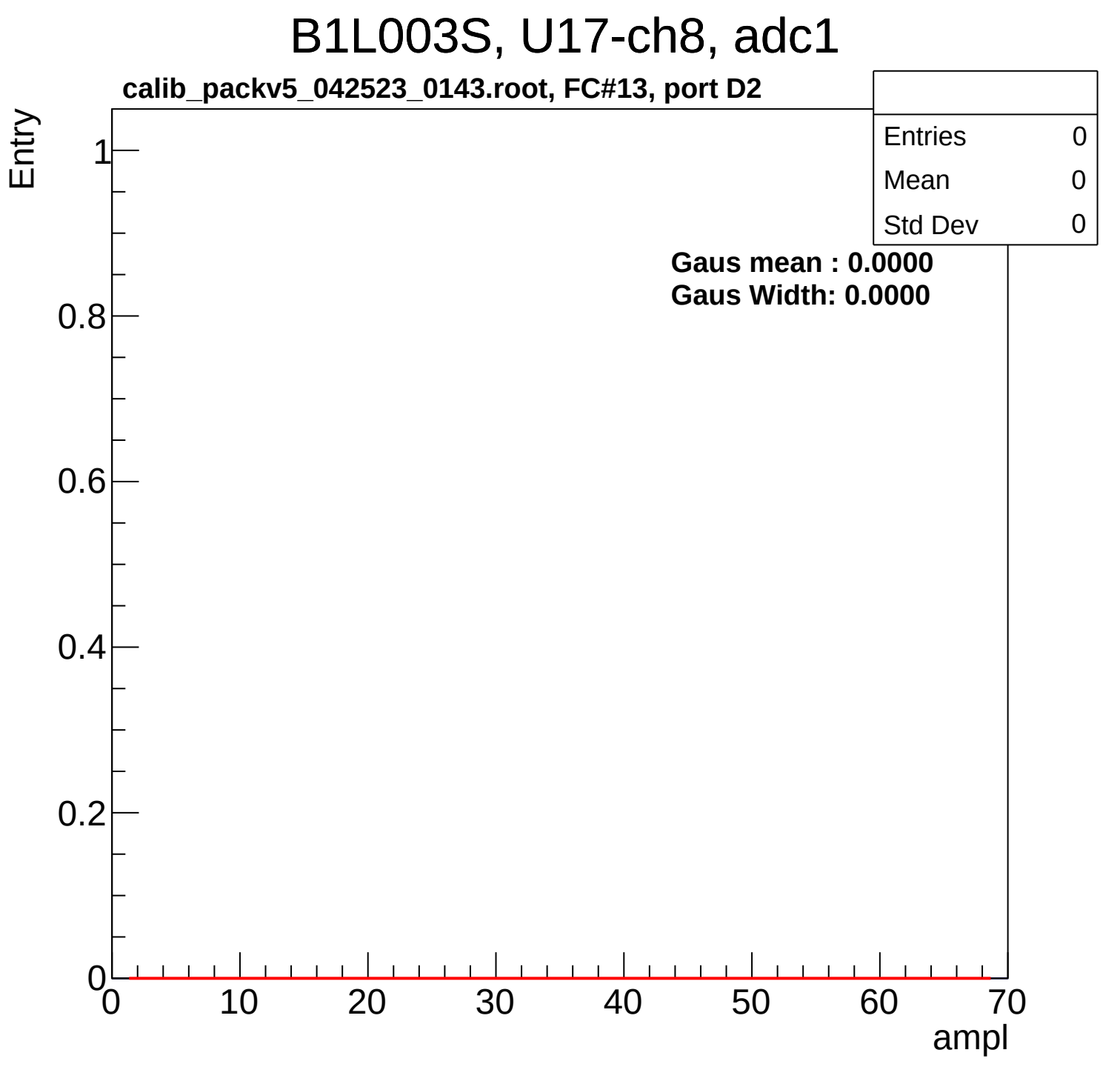
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Gaus mean : 0.0000
Gaus Width: 0.0000
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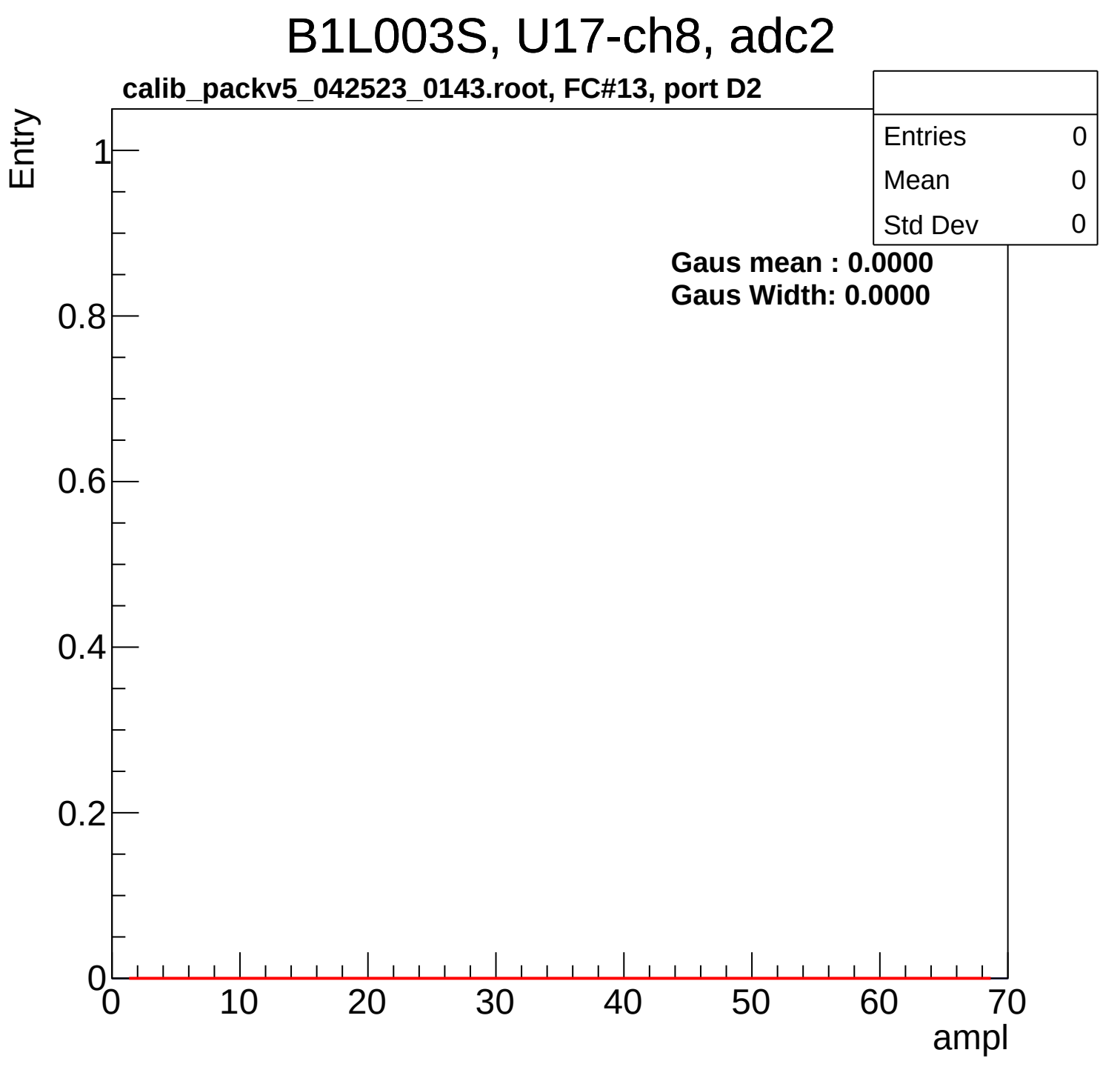
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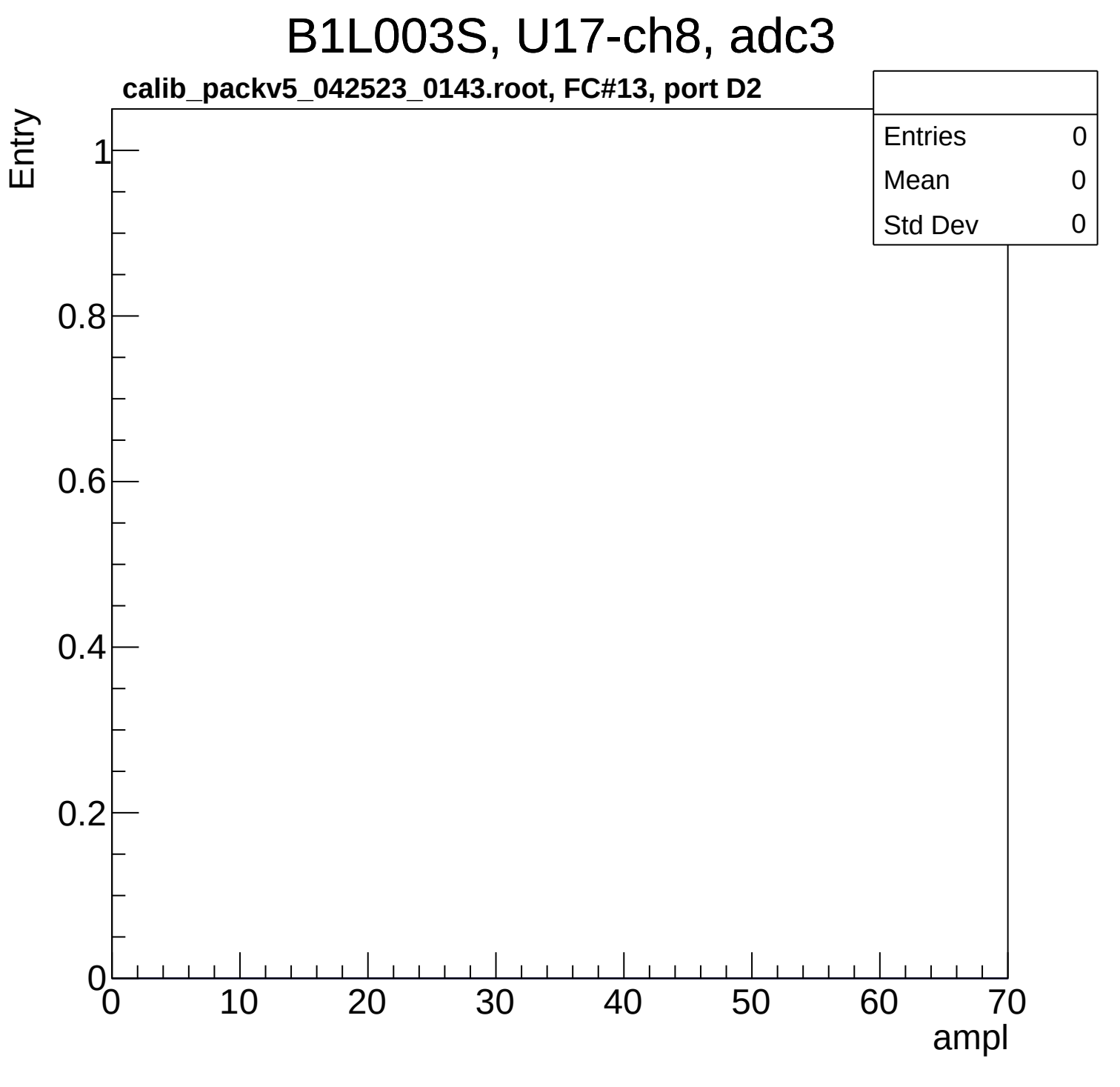
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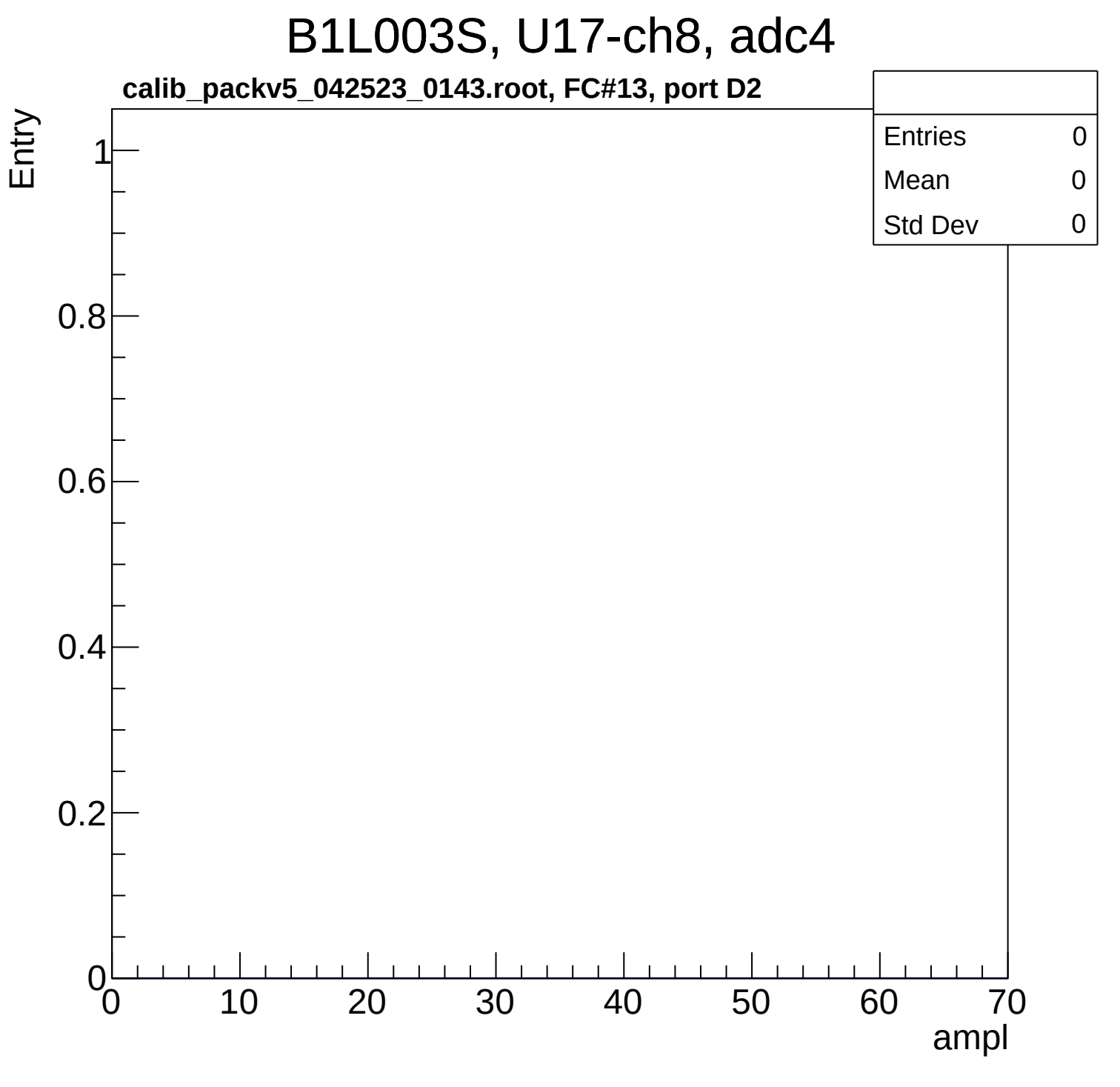
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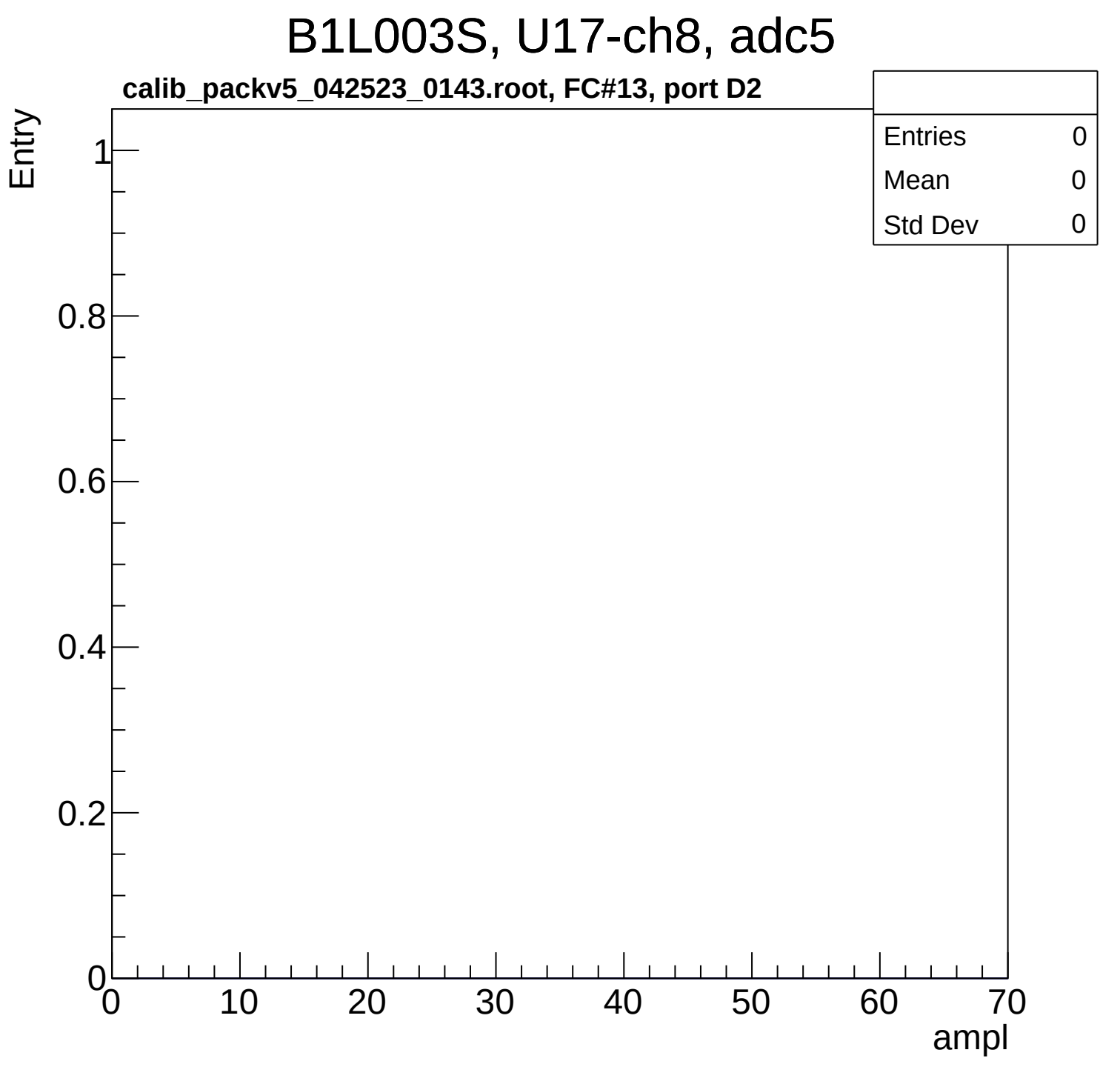
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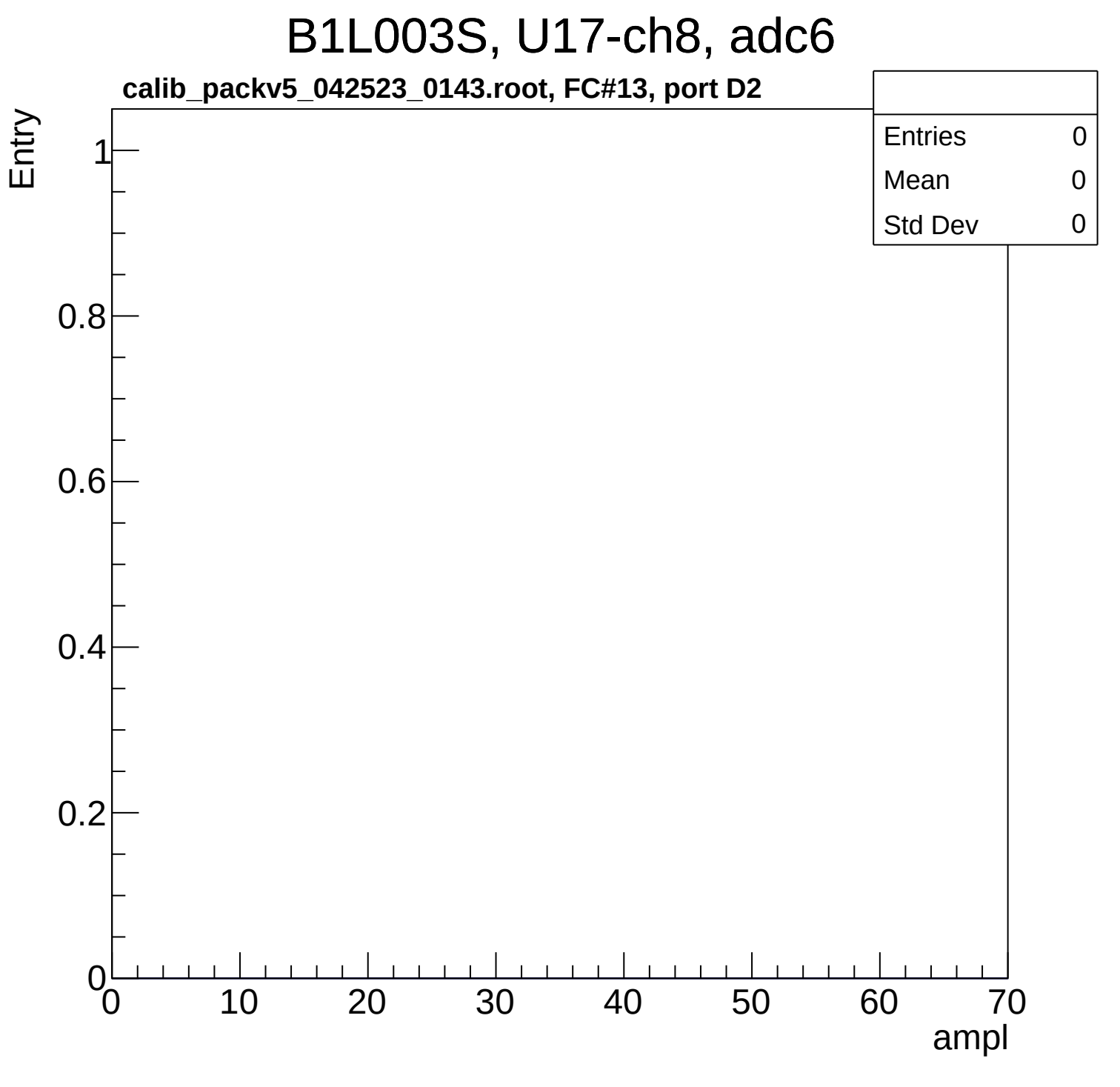
B1L003S, U17-ch8, adc6
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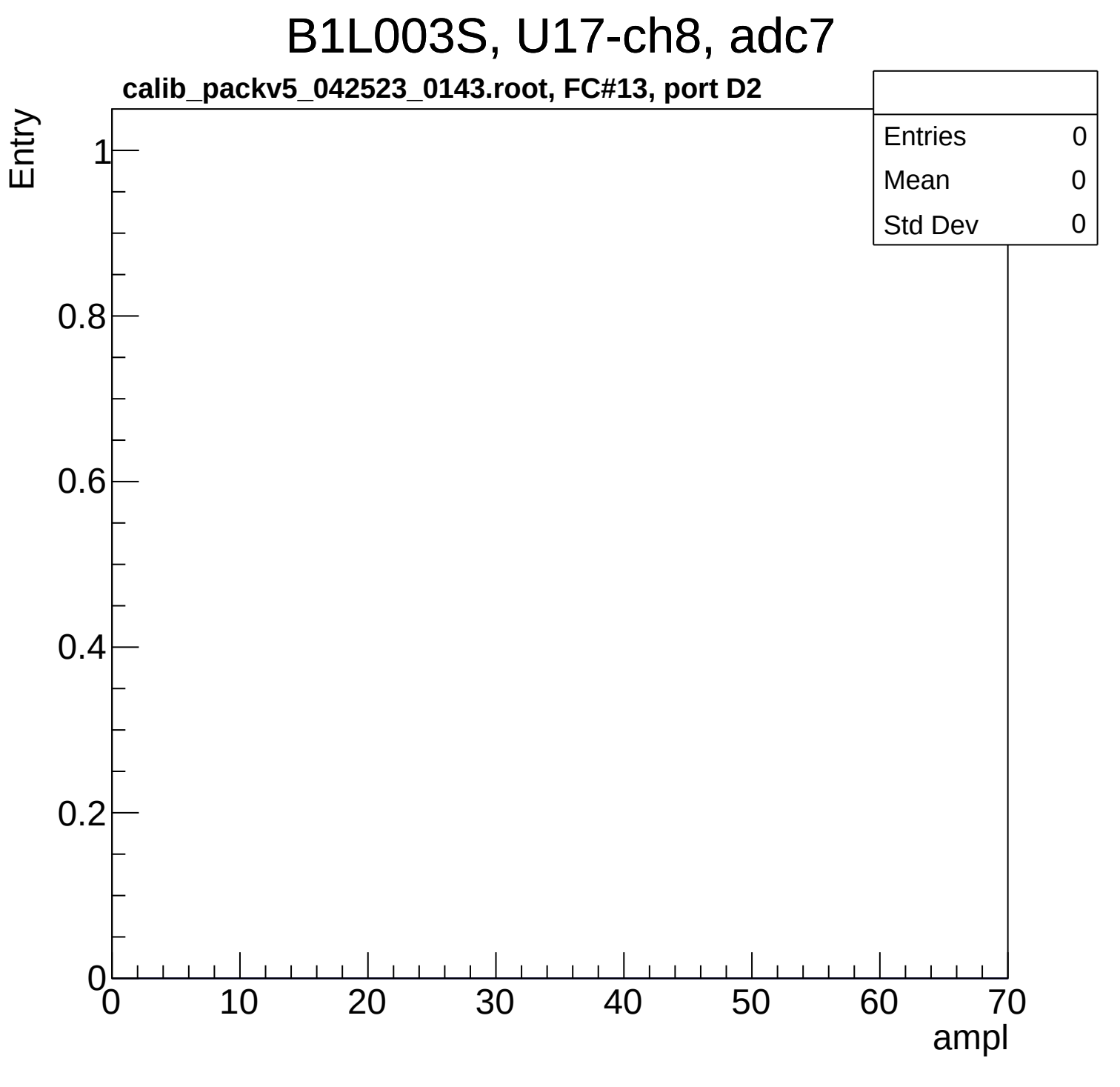
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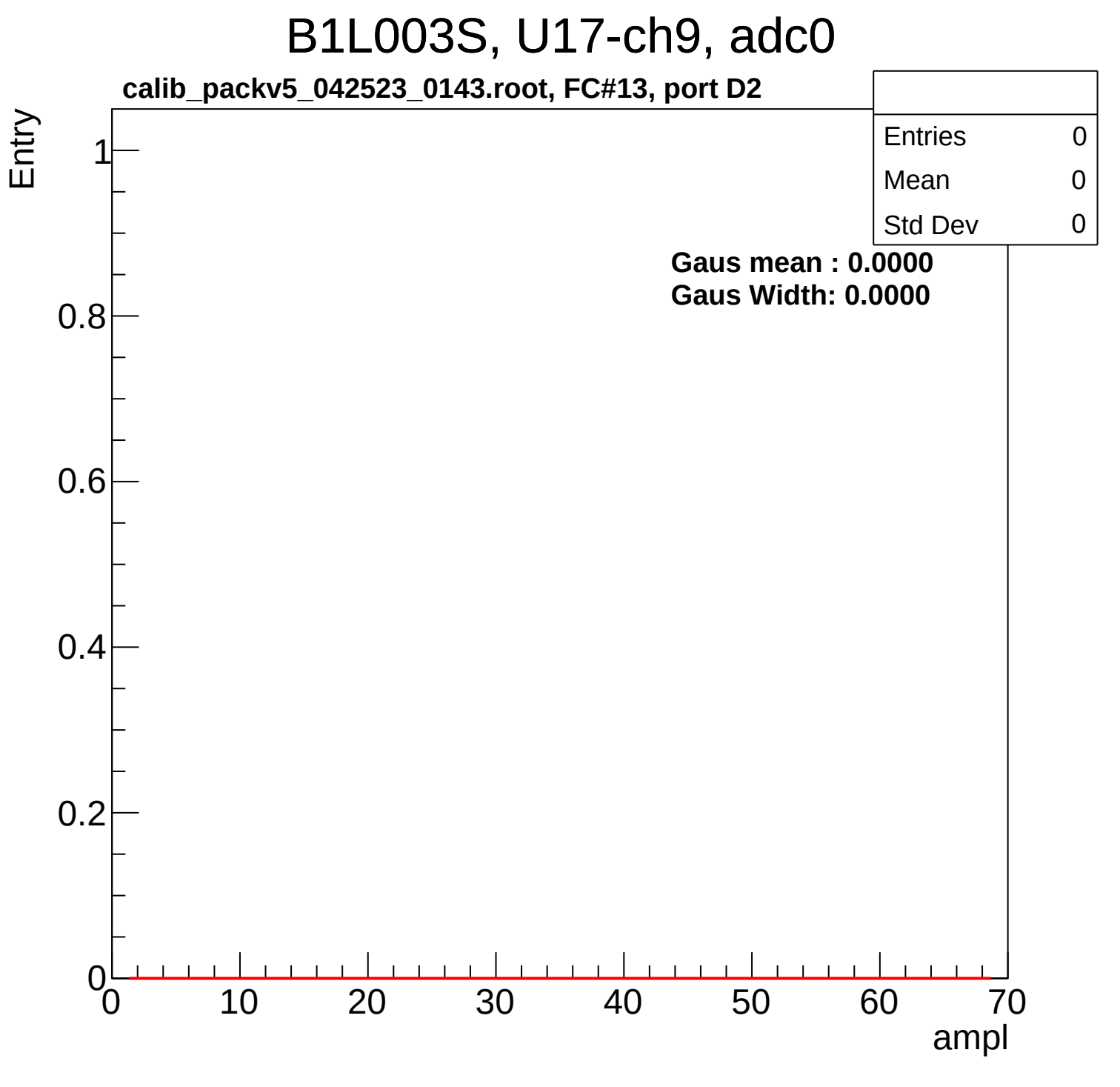
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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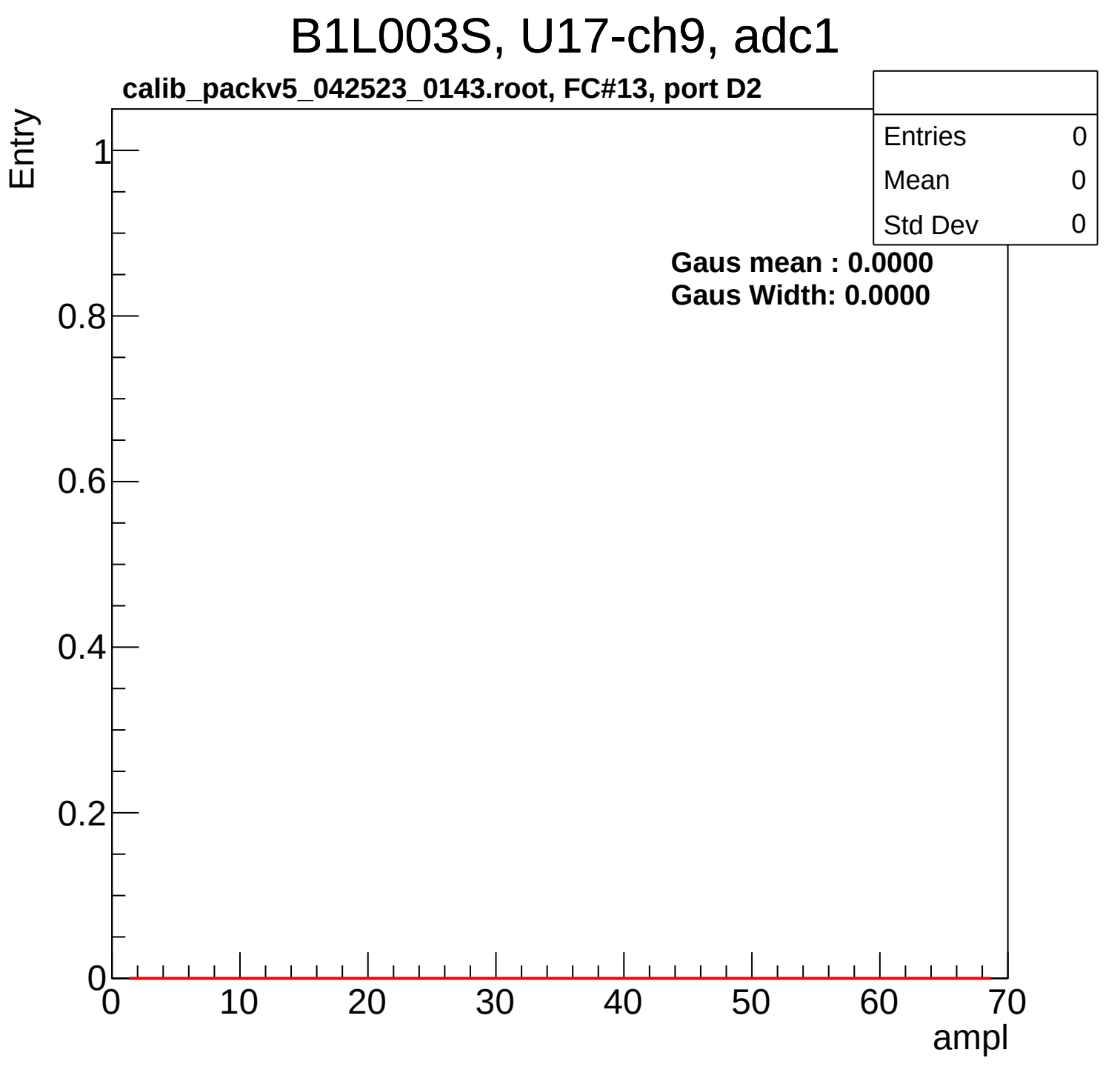
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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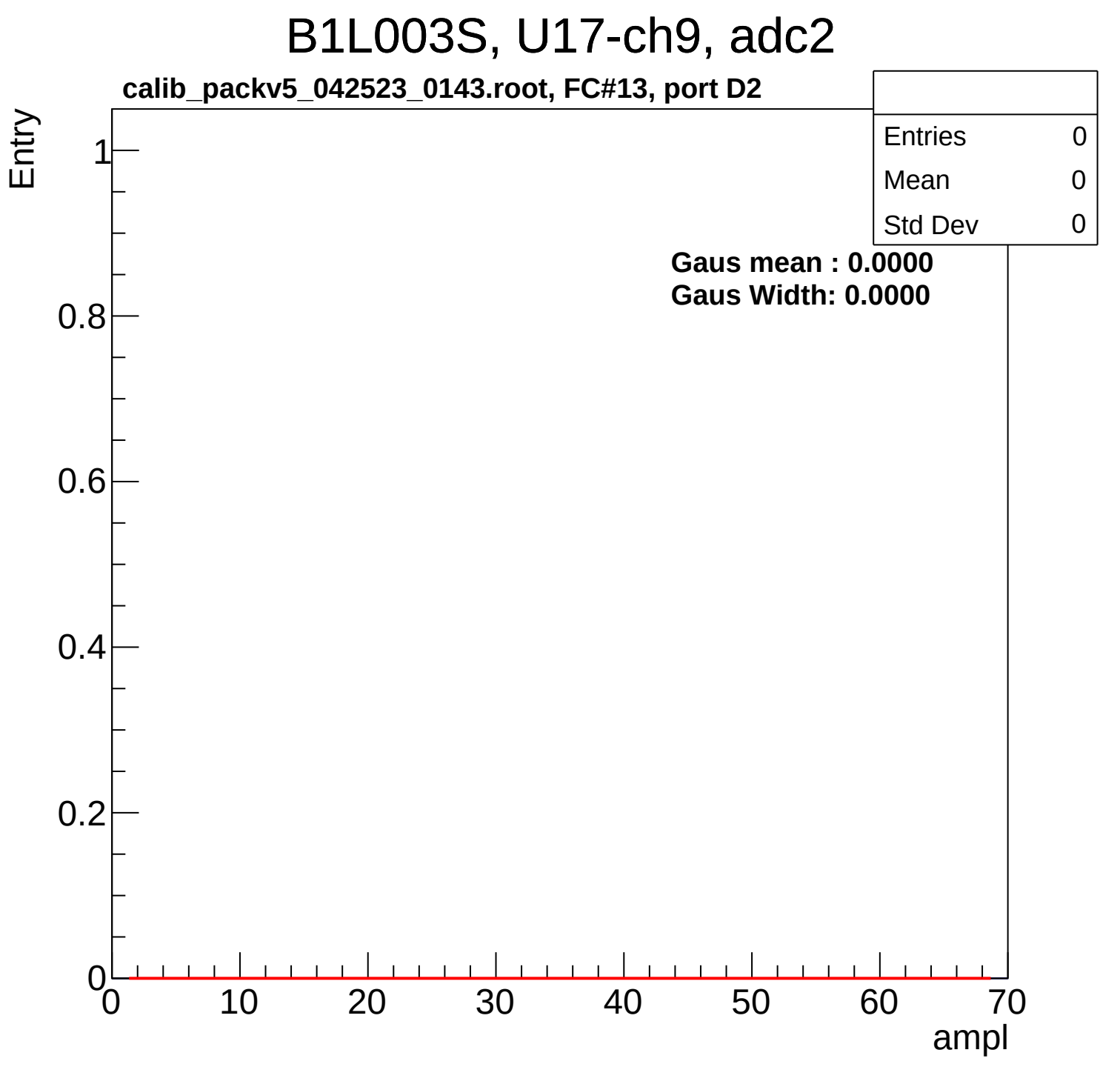
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Gaus Width: 0.0000
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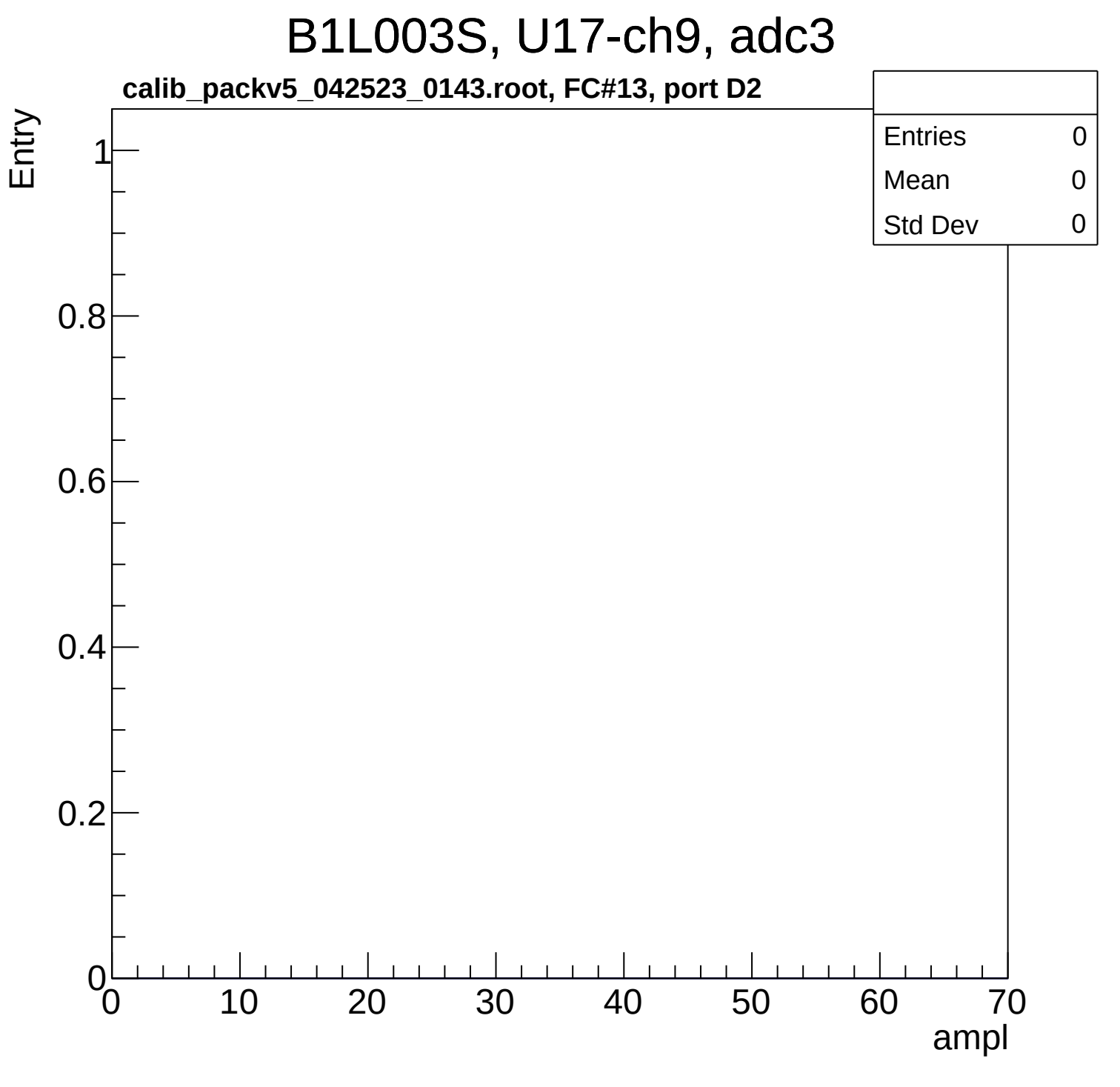
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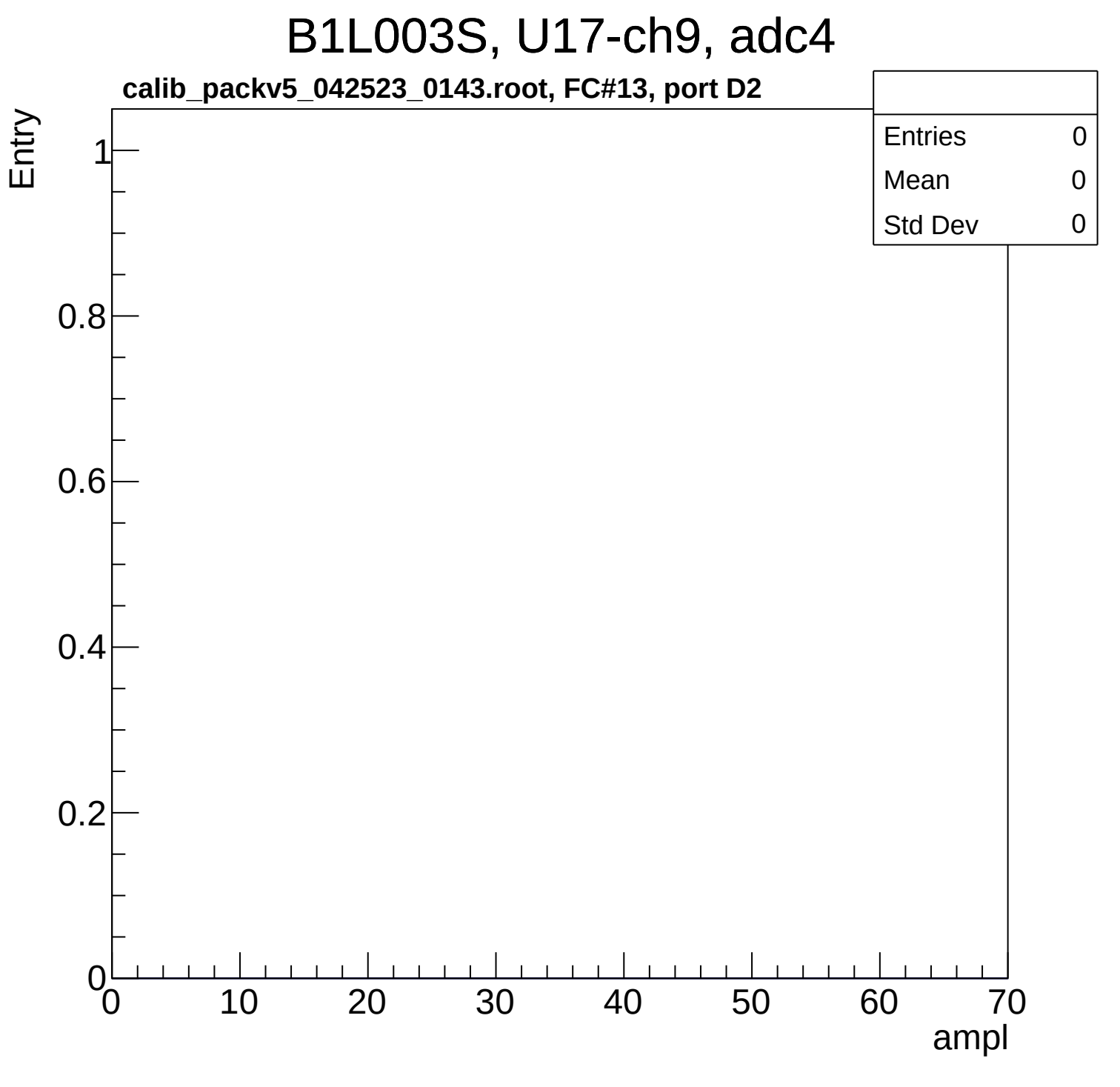
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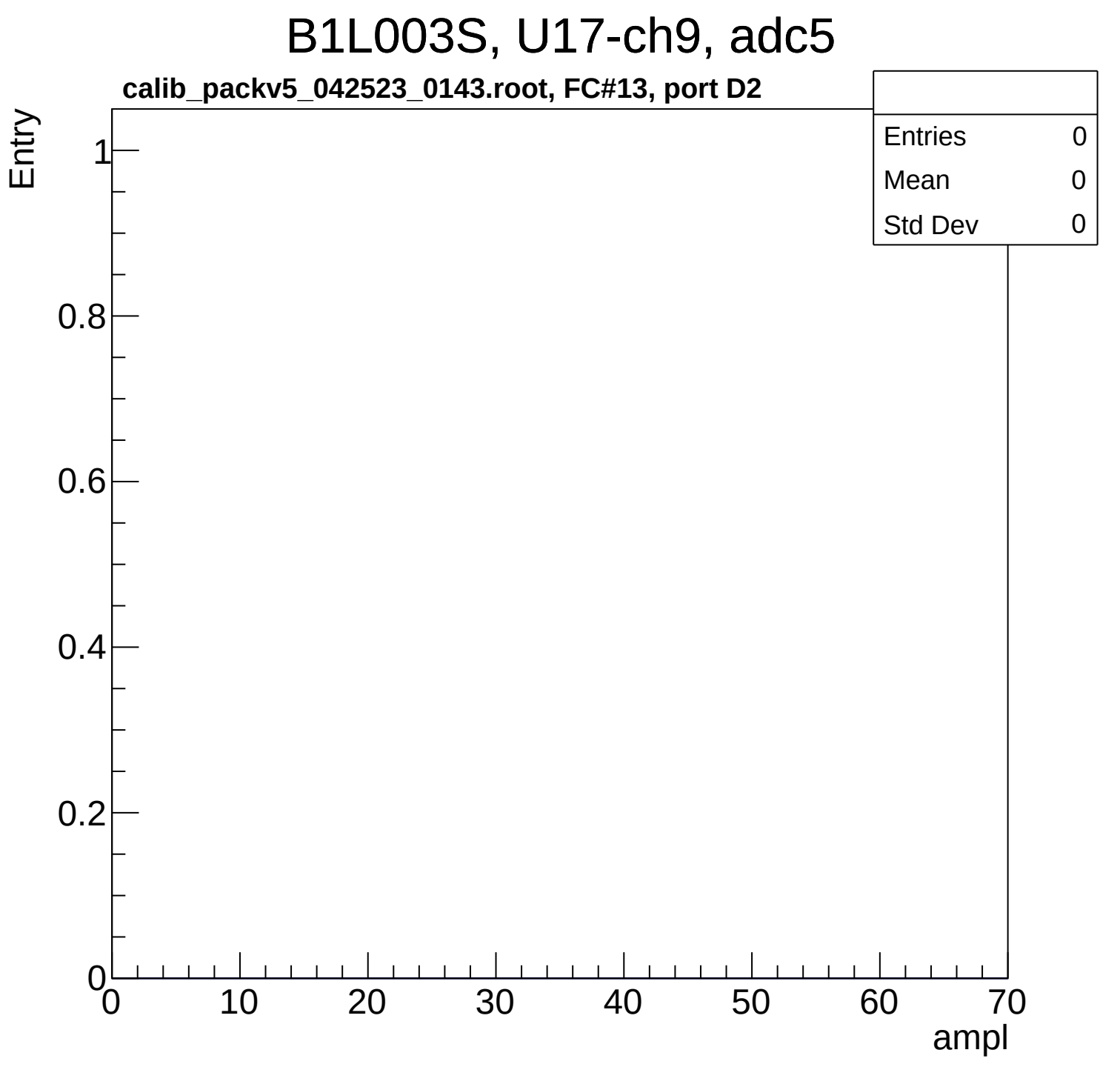
ampl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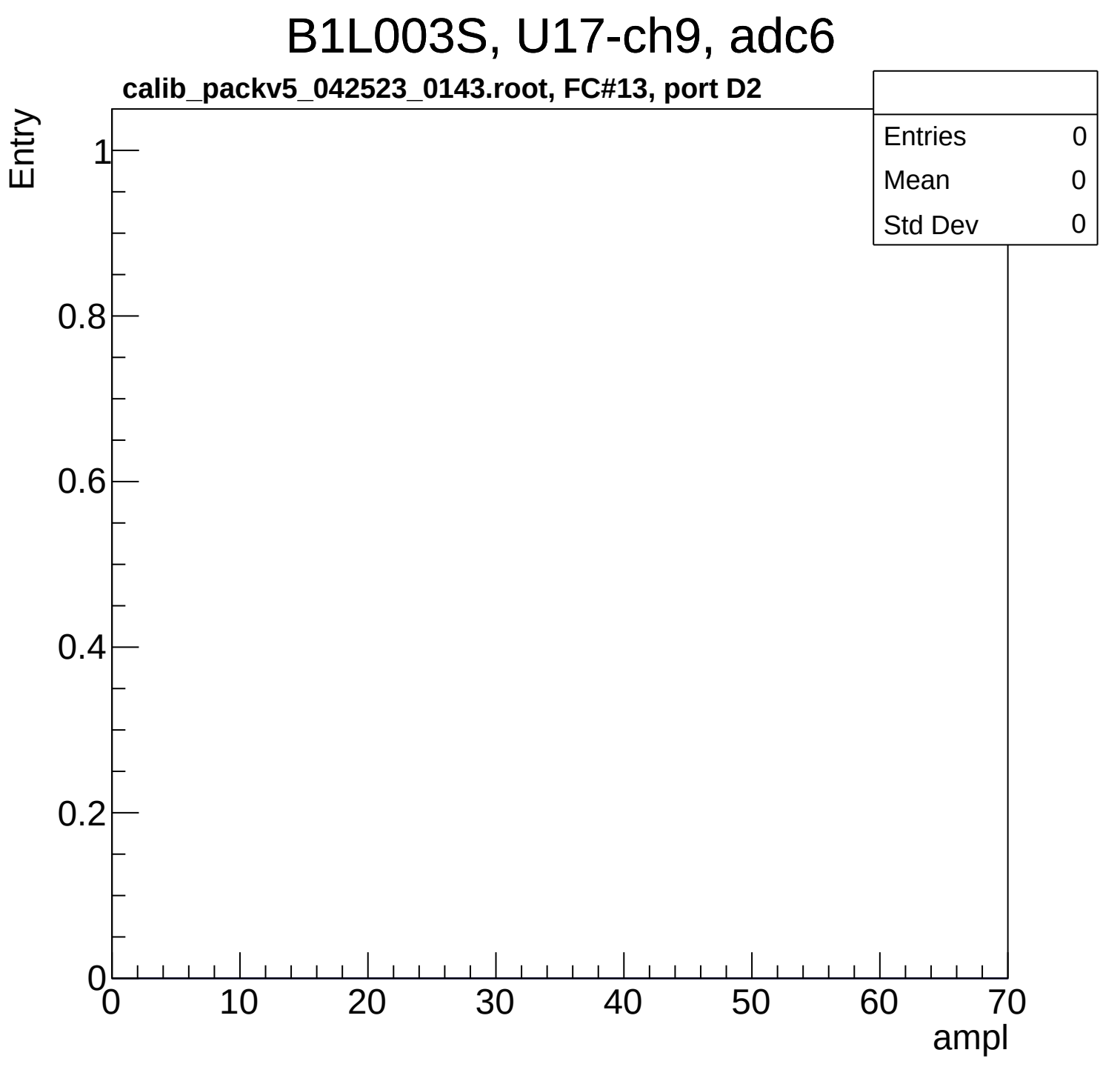
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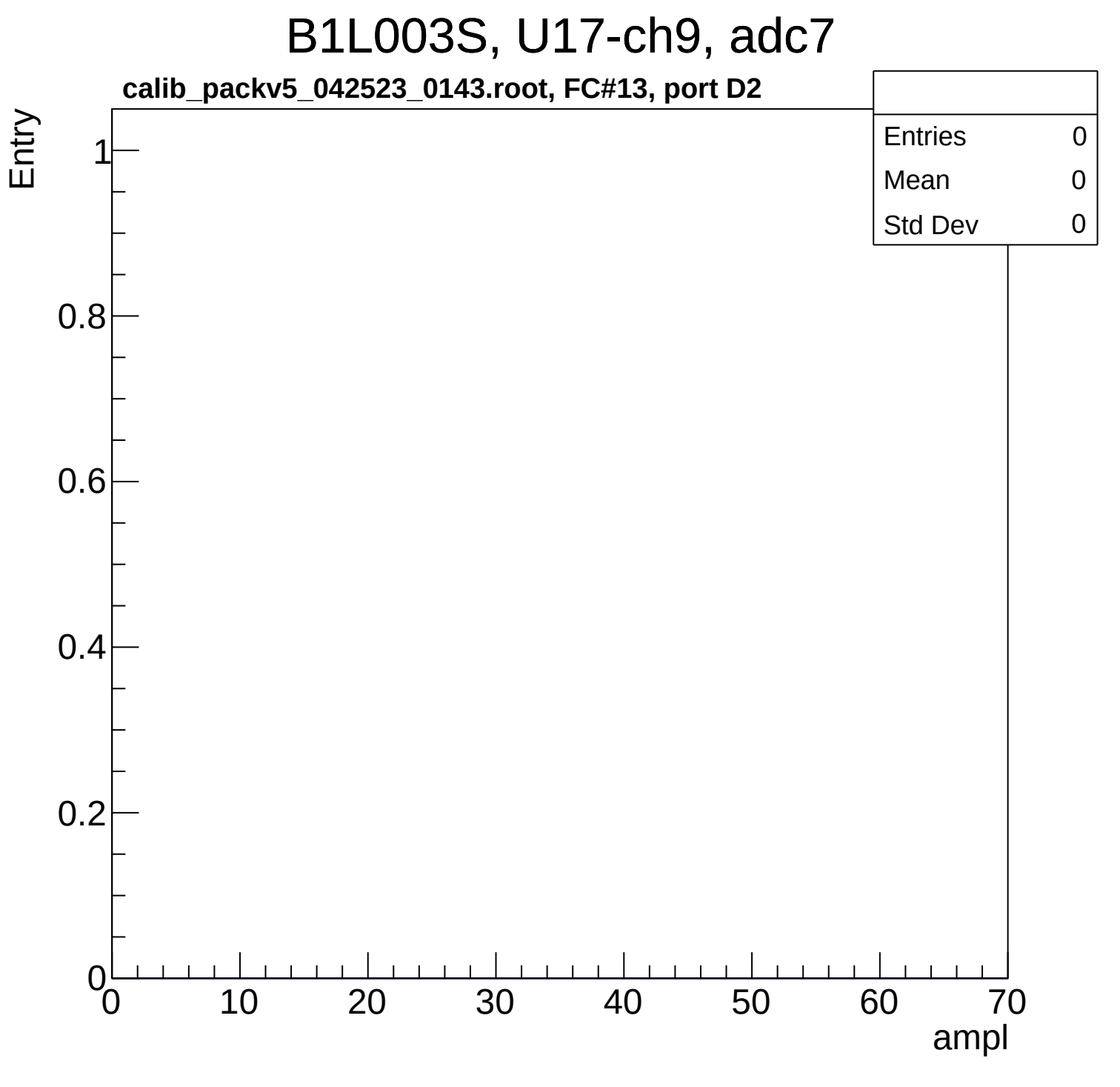
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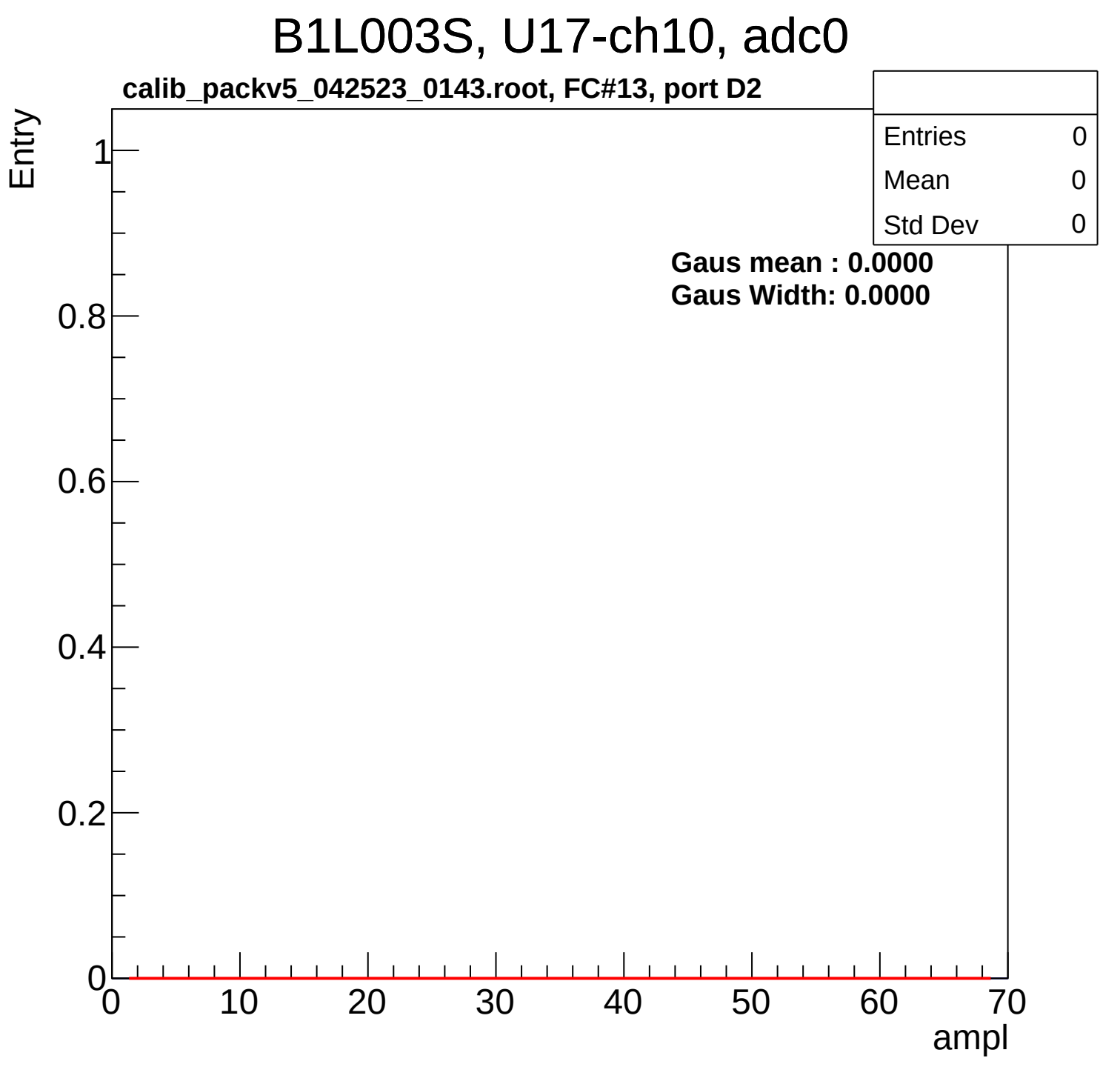
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Gaus Width: 0.0000
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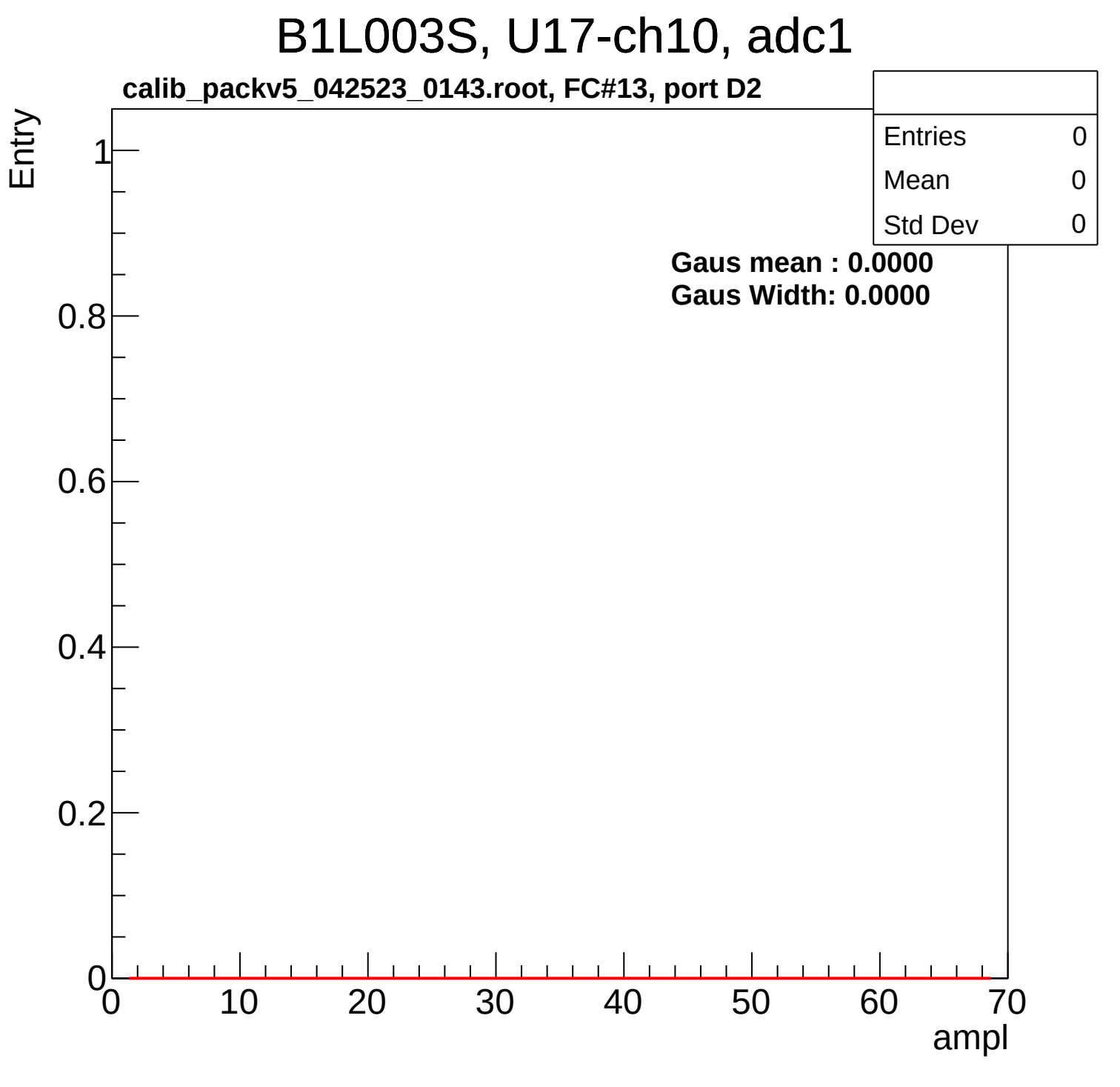
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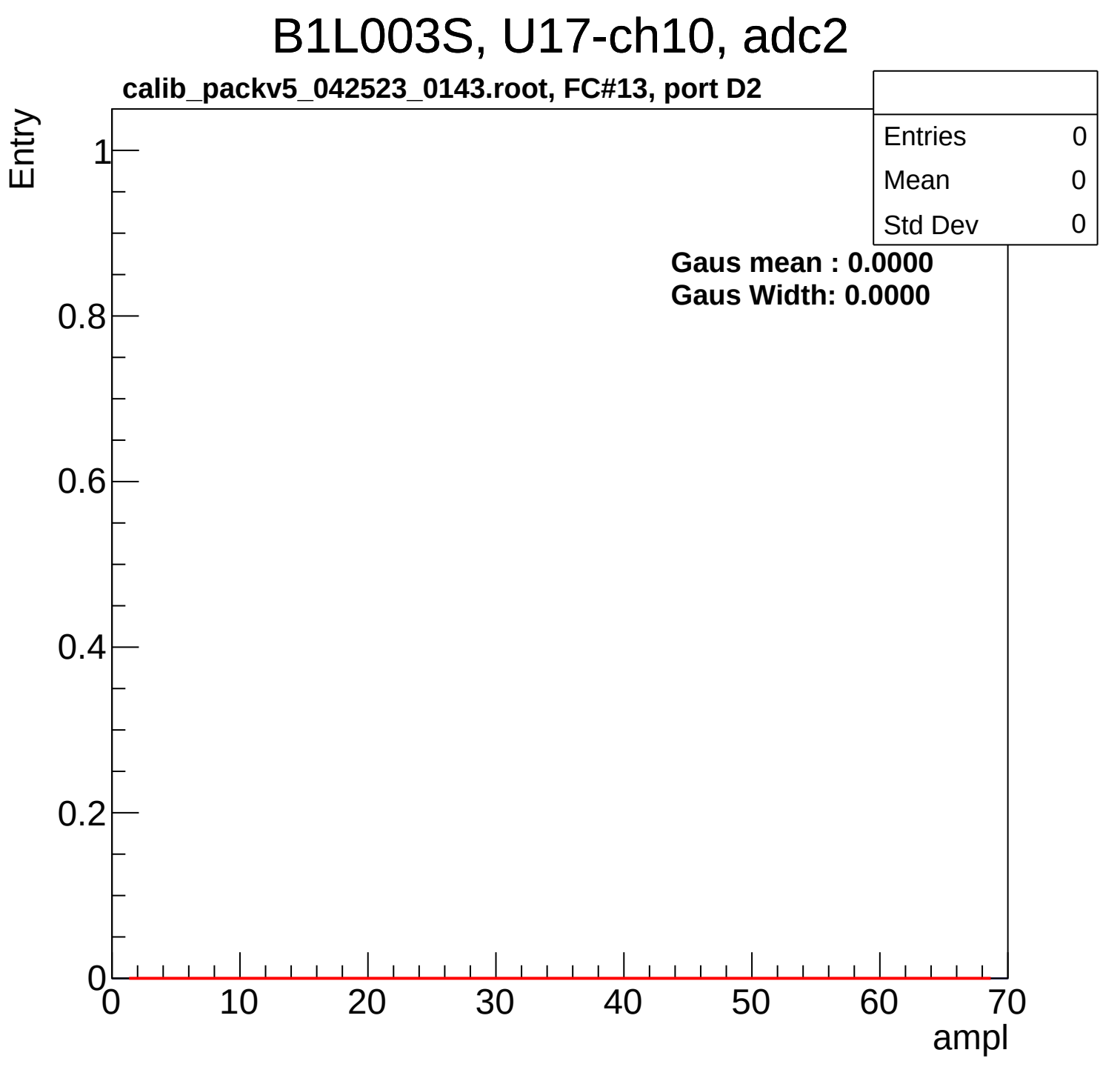
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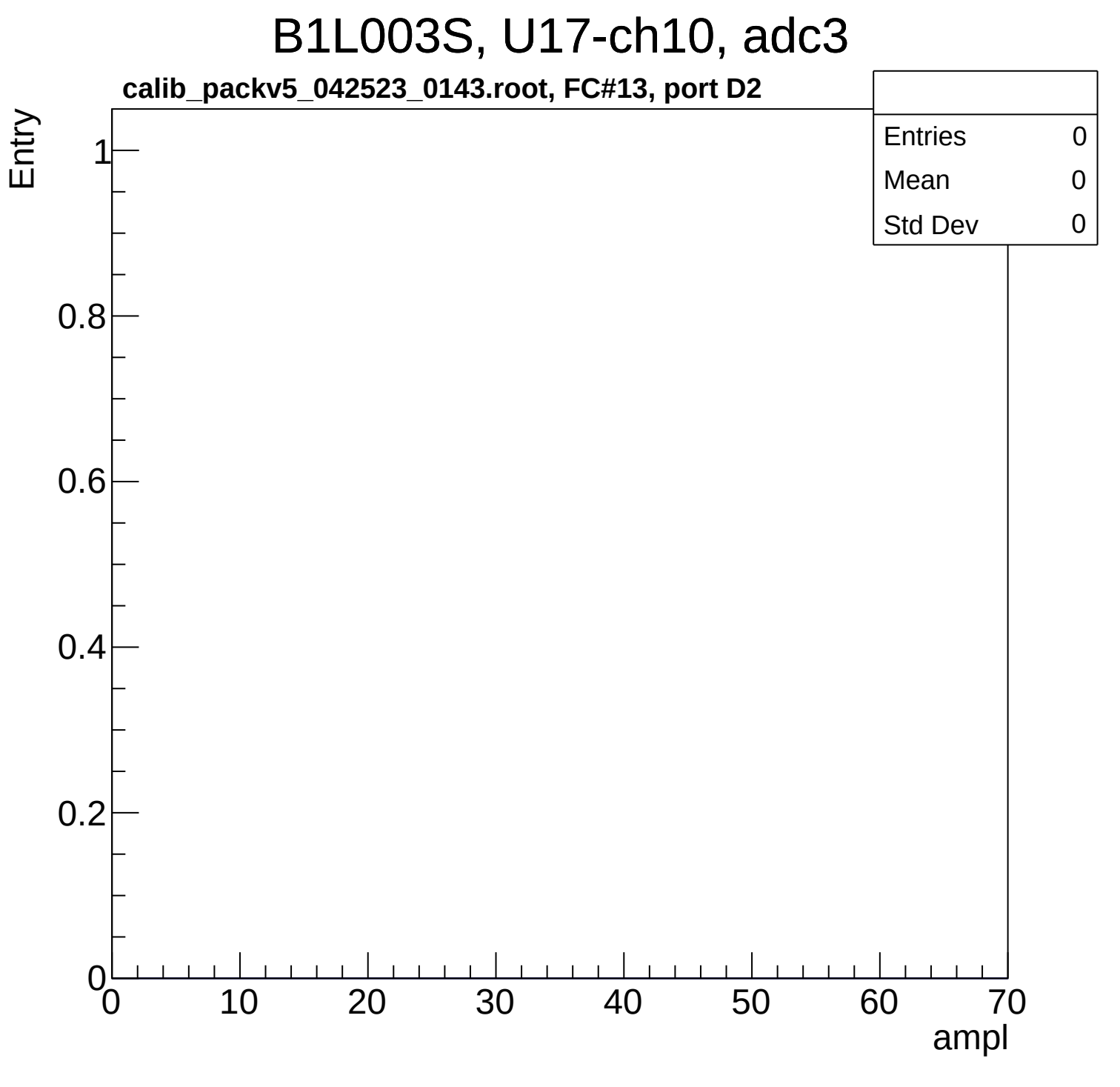
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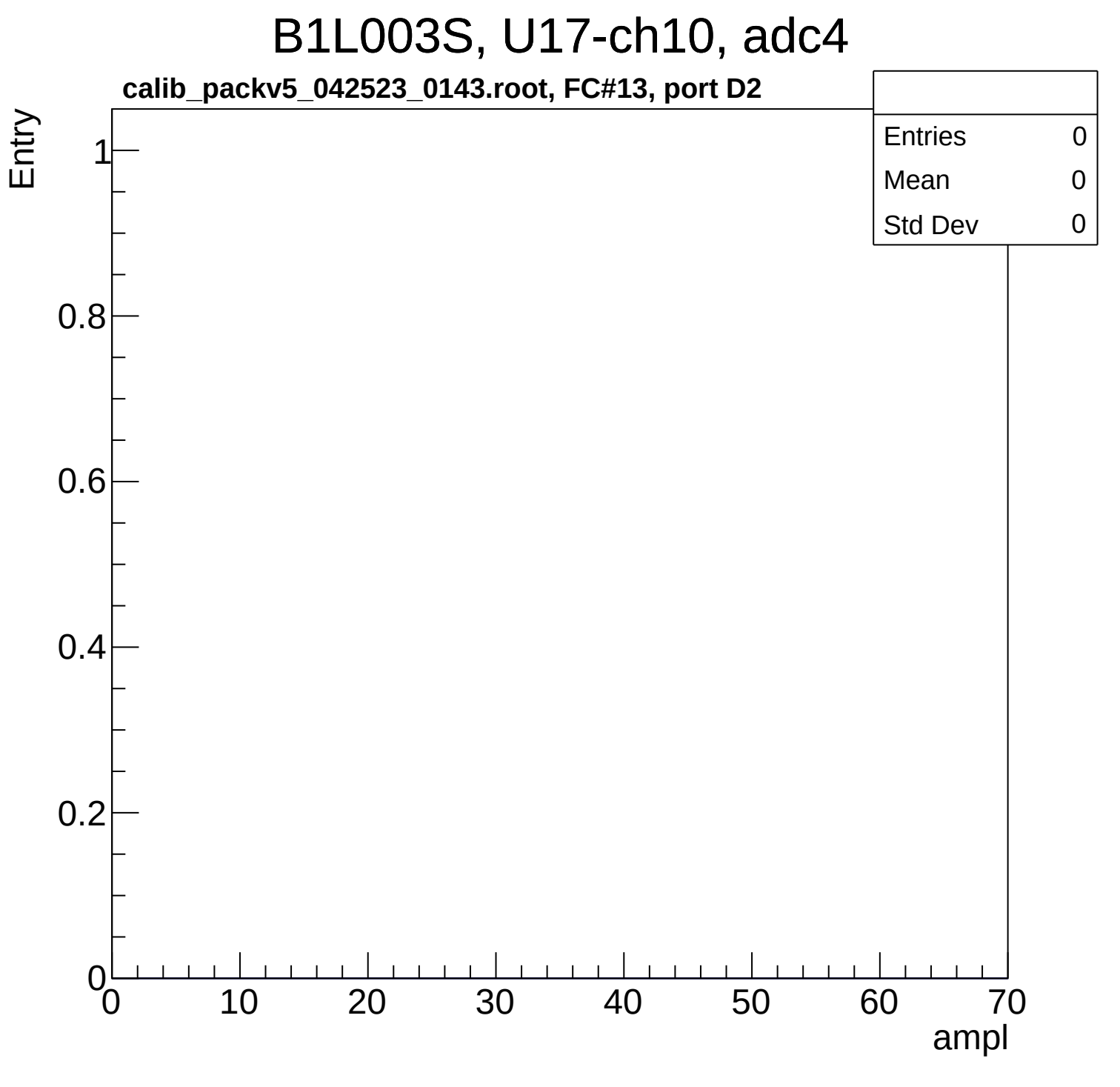
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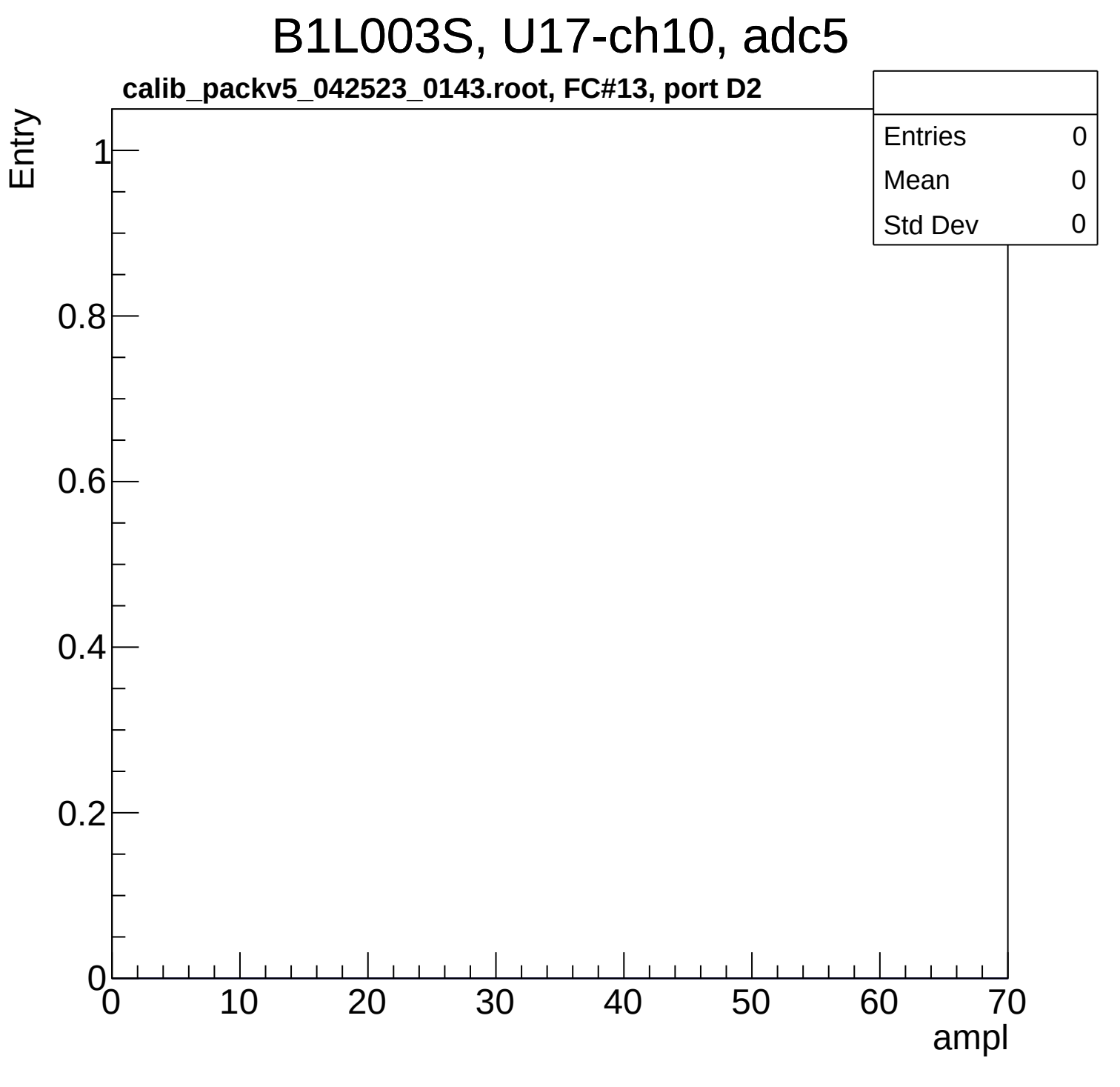
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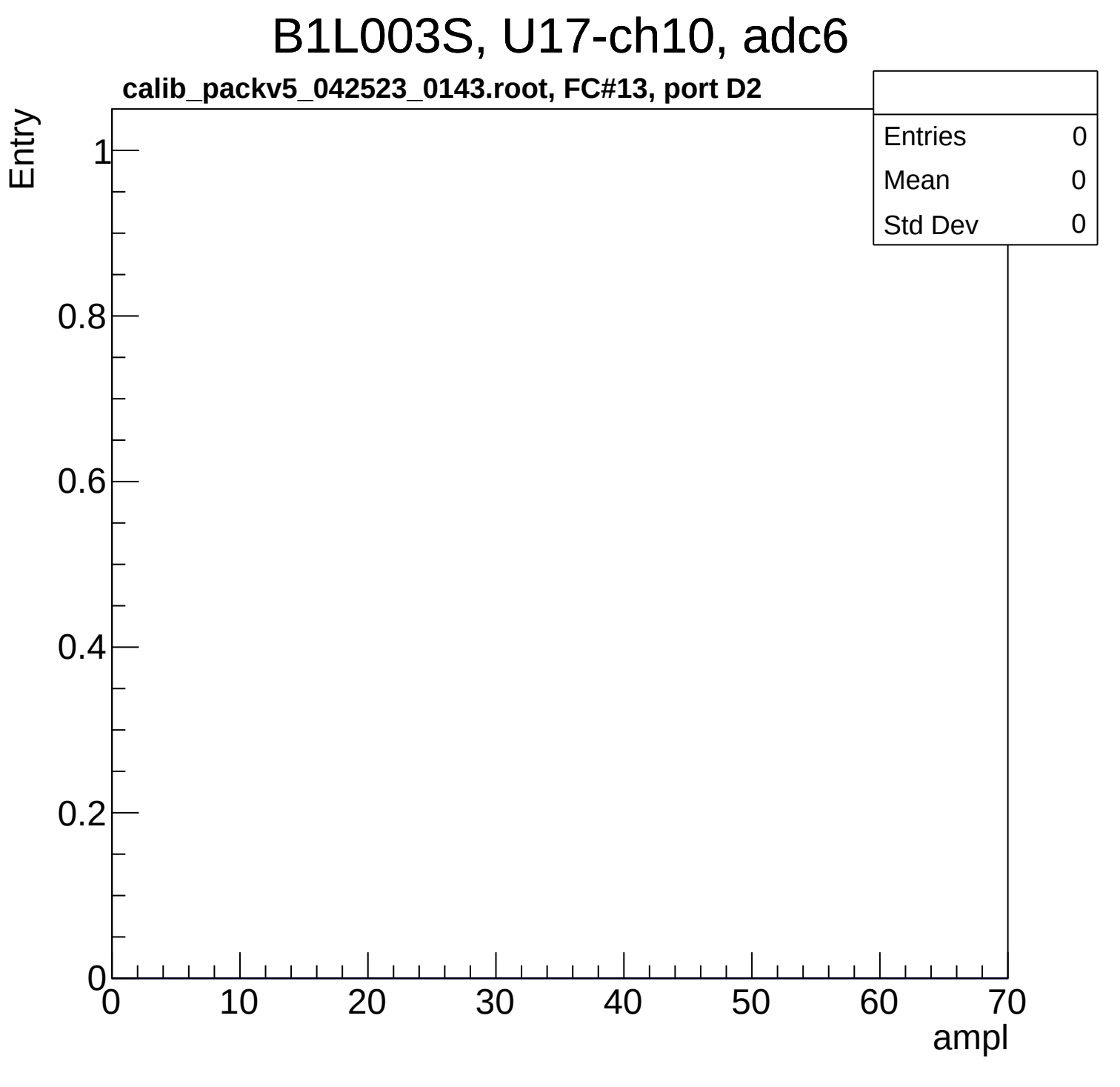
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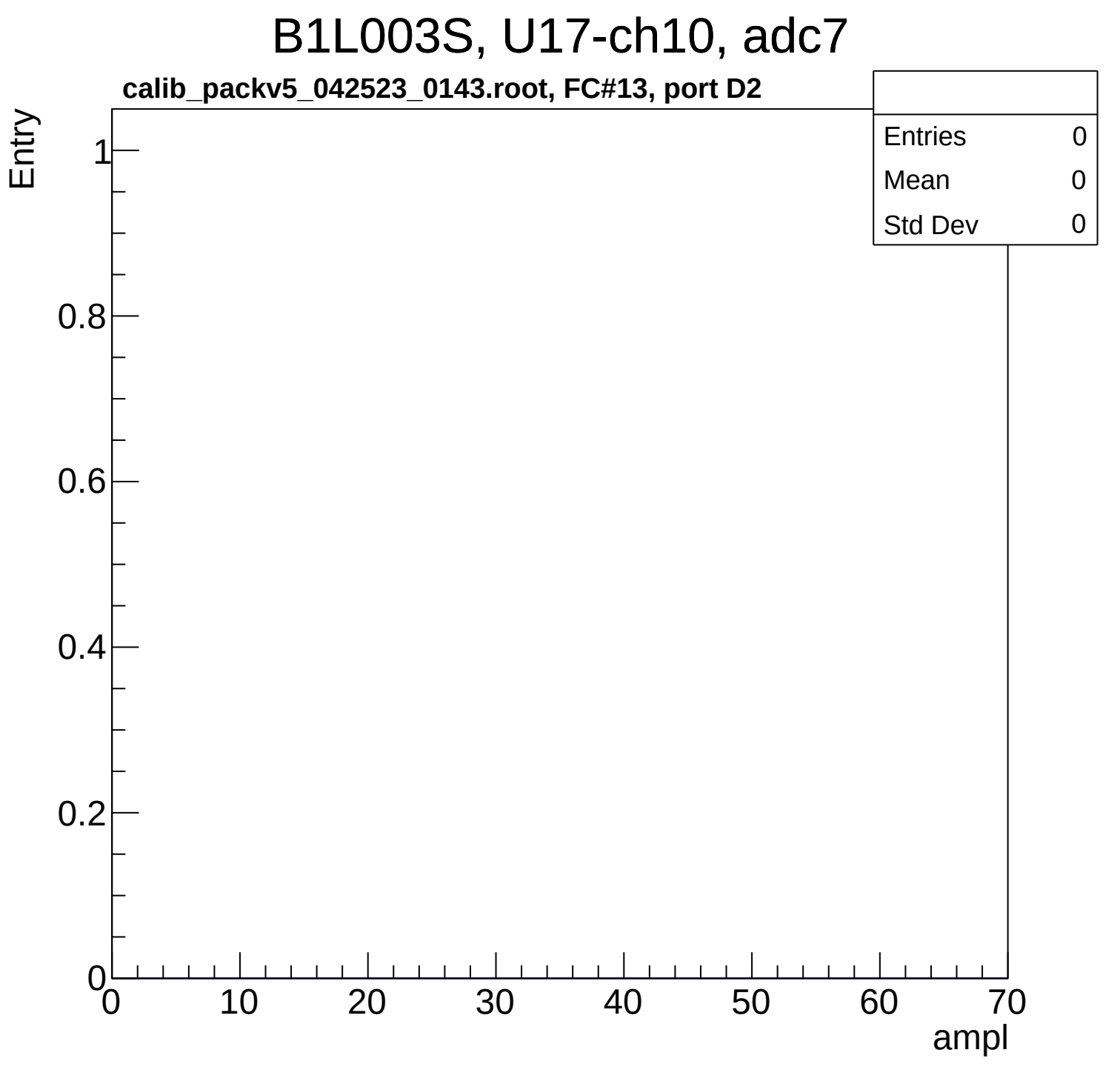
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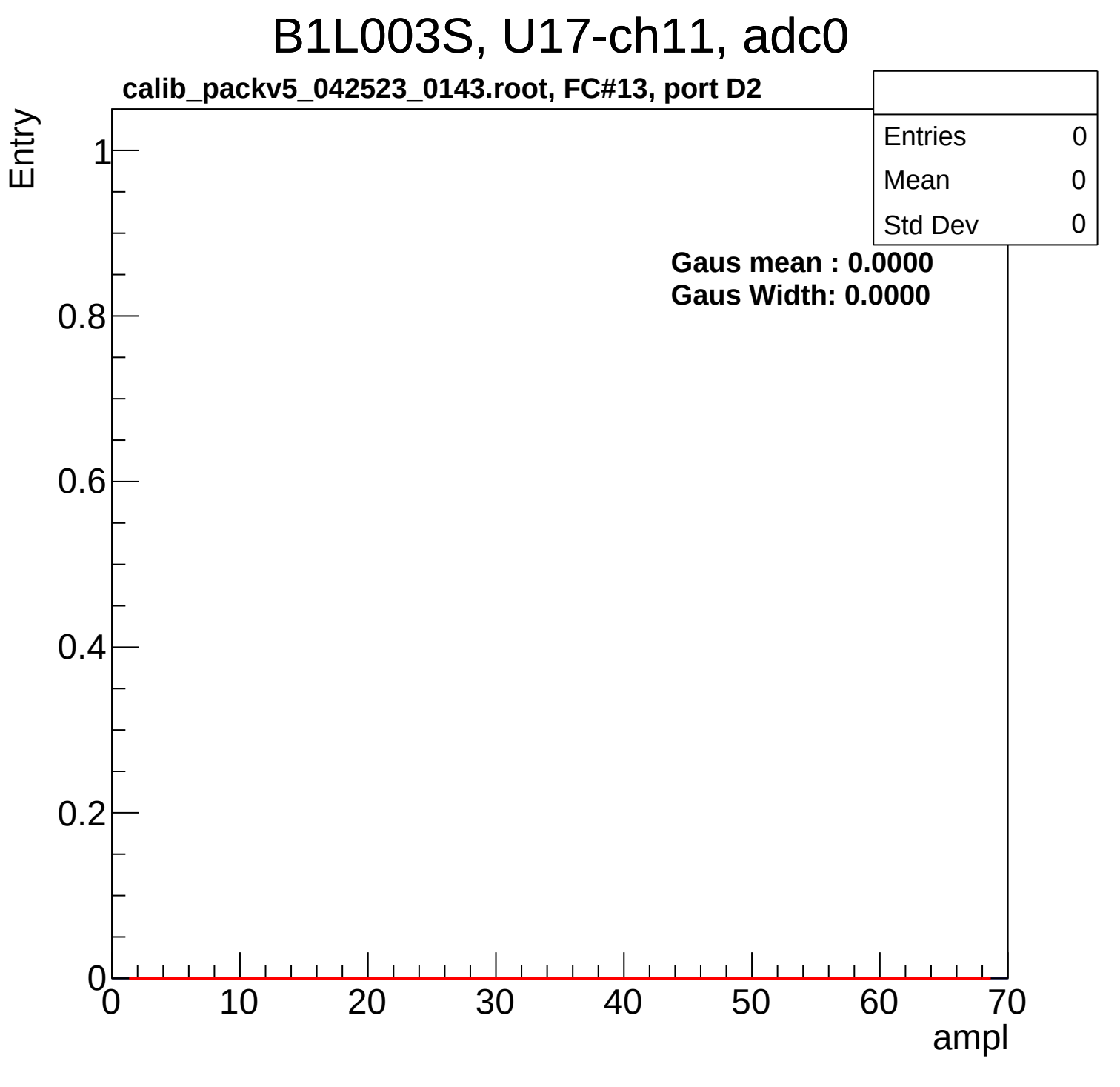
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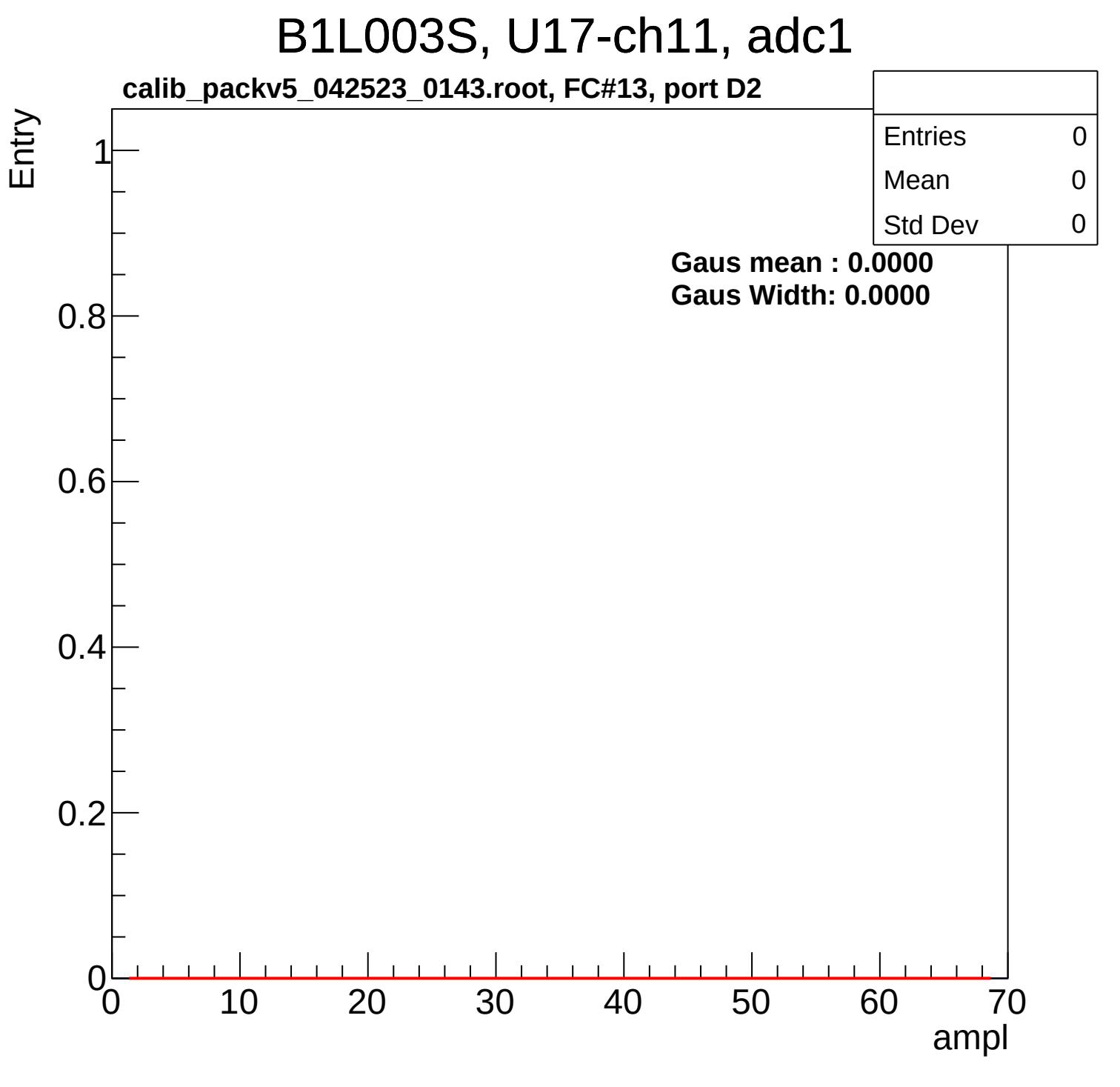
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Gaus Width: 0.0000
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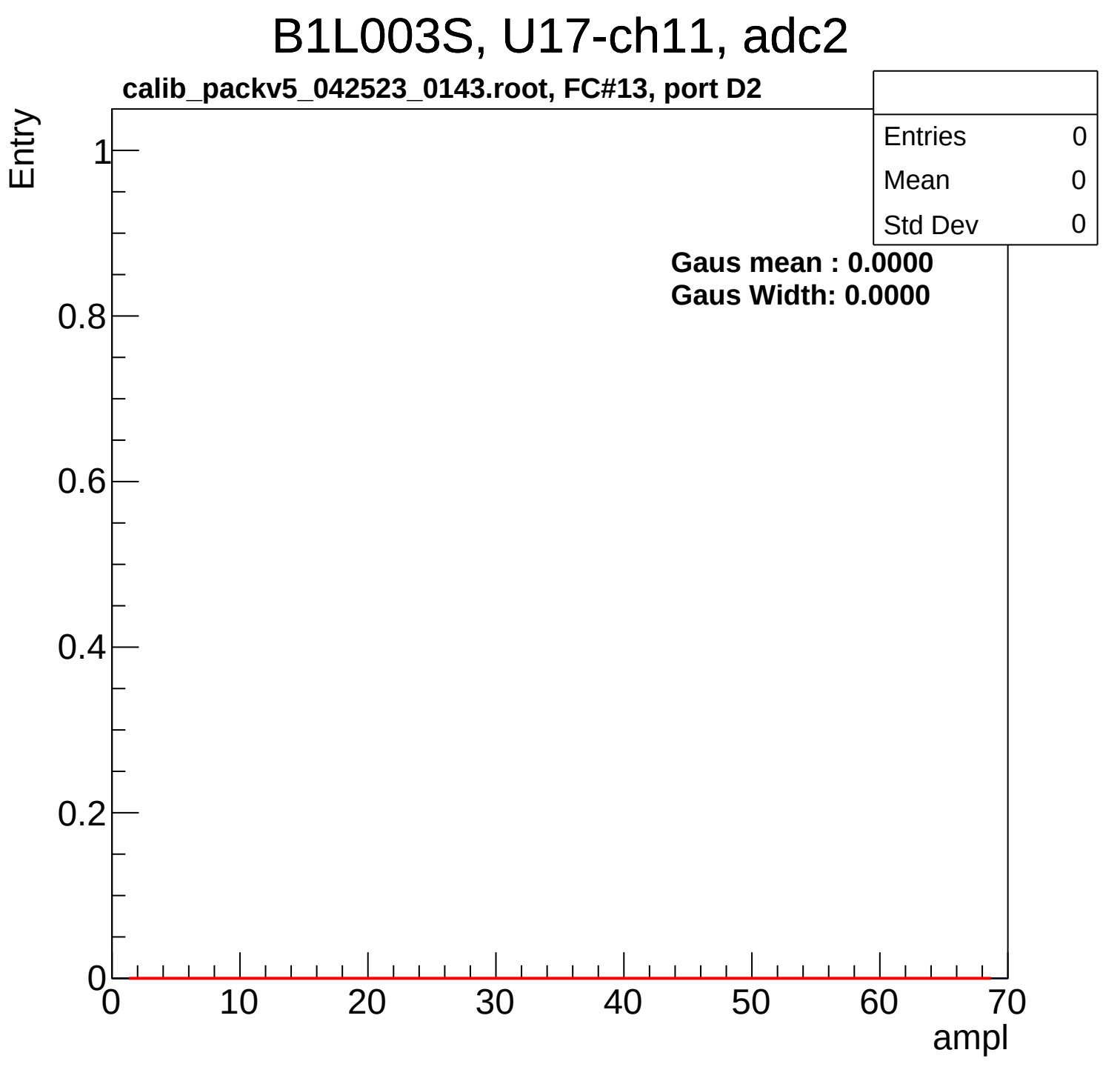
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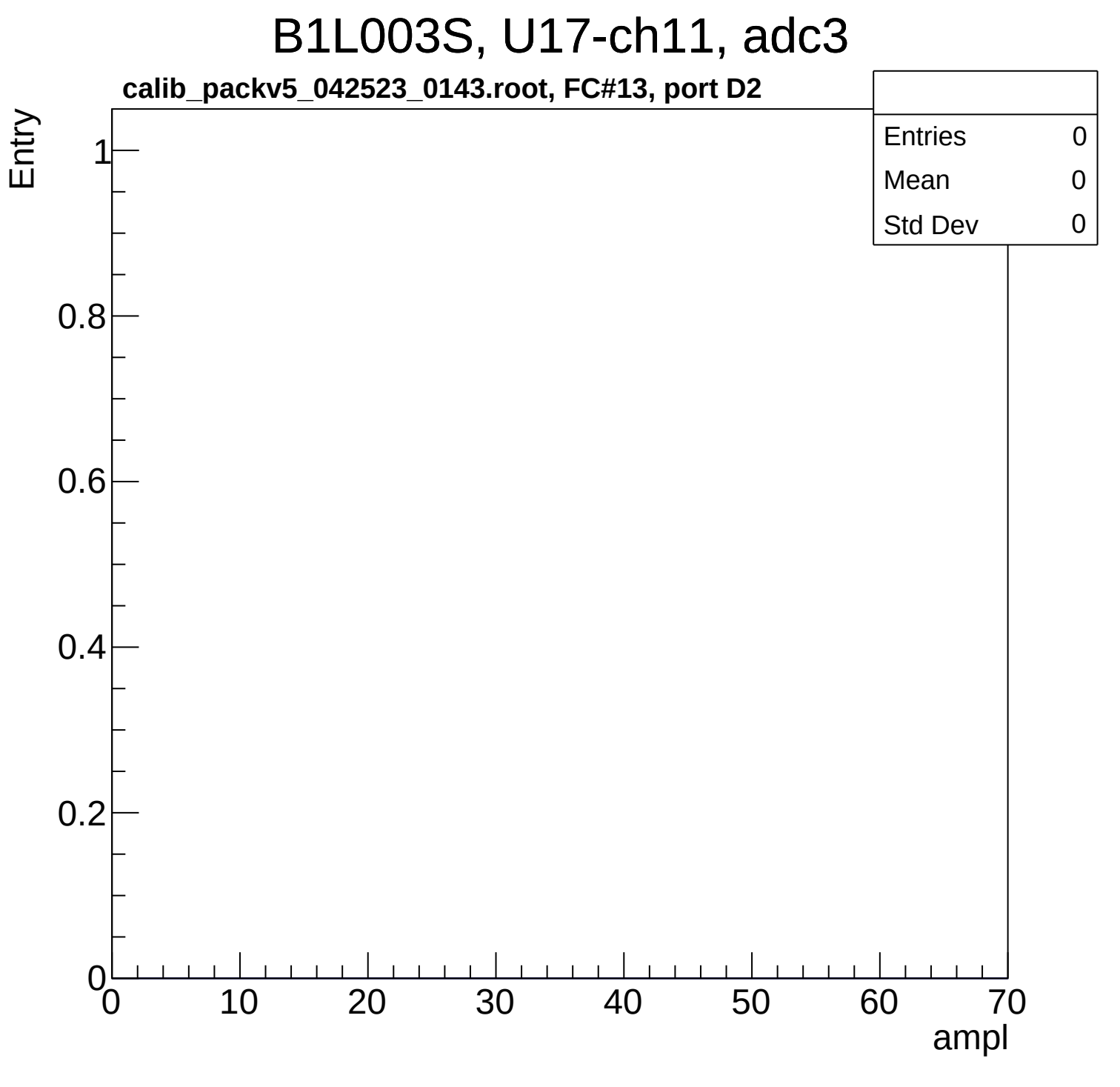
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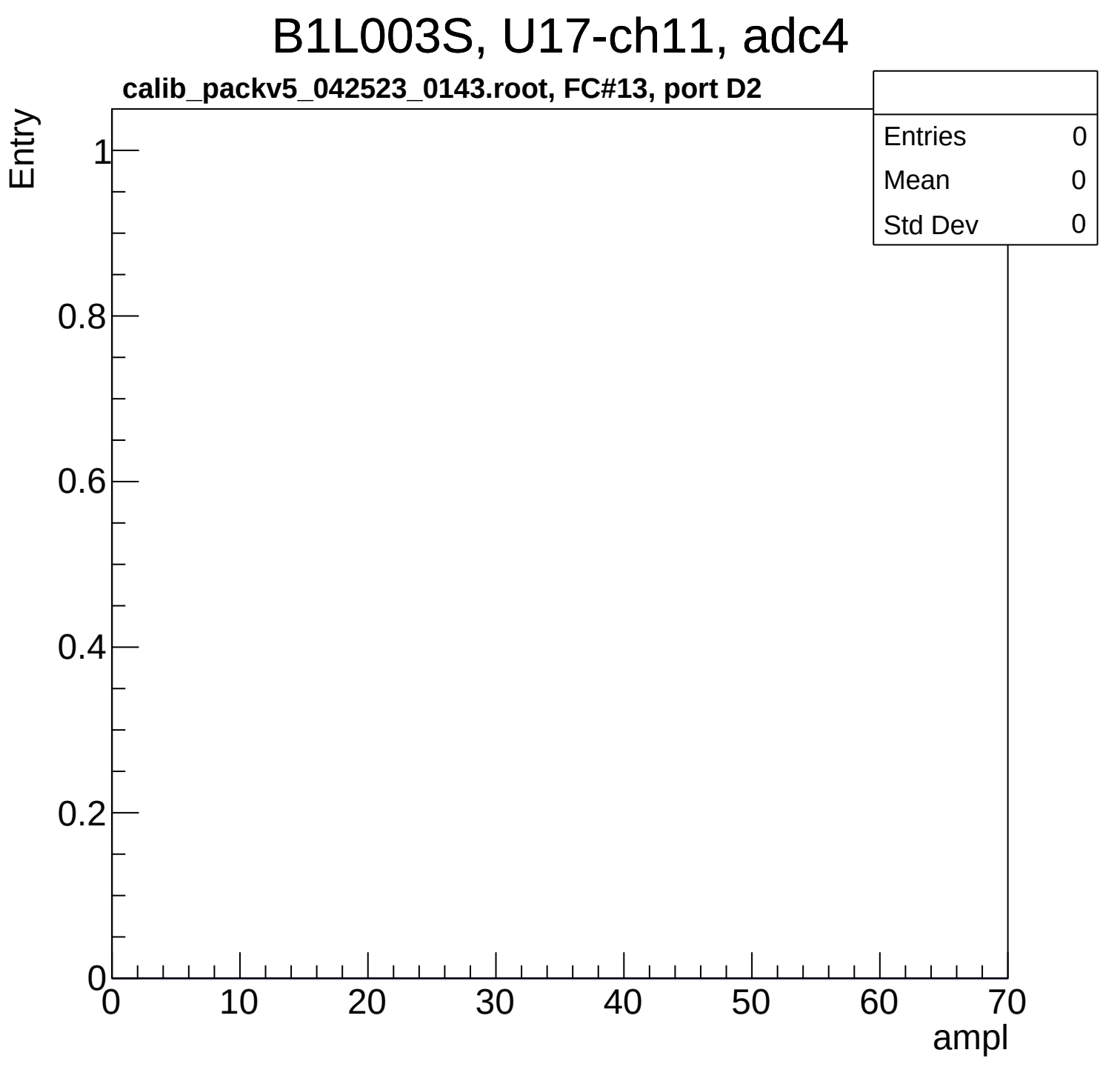
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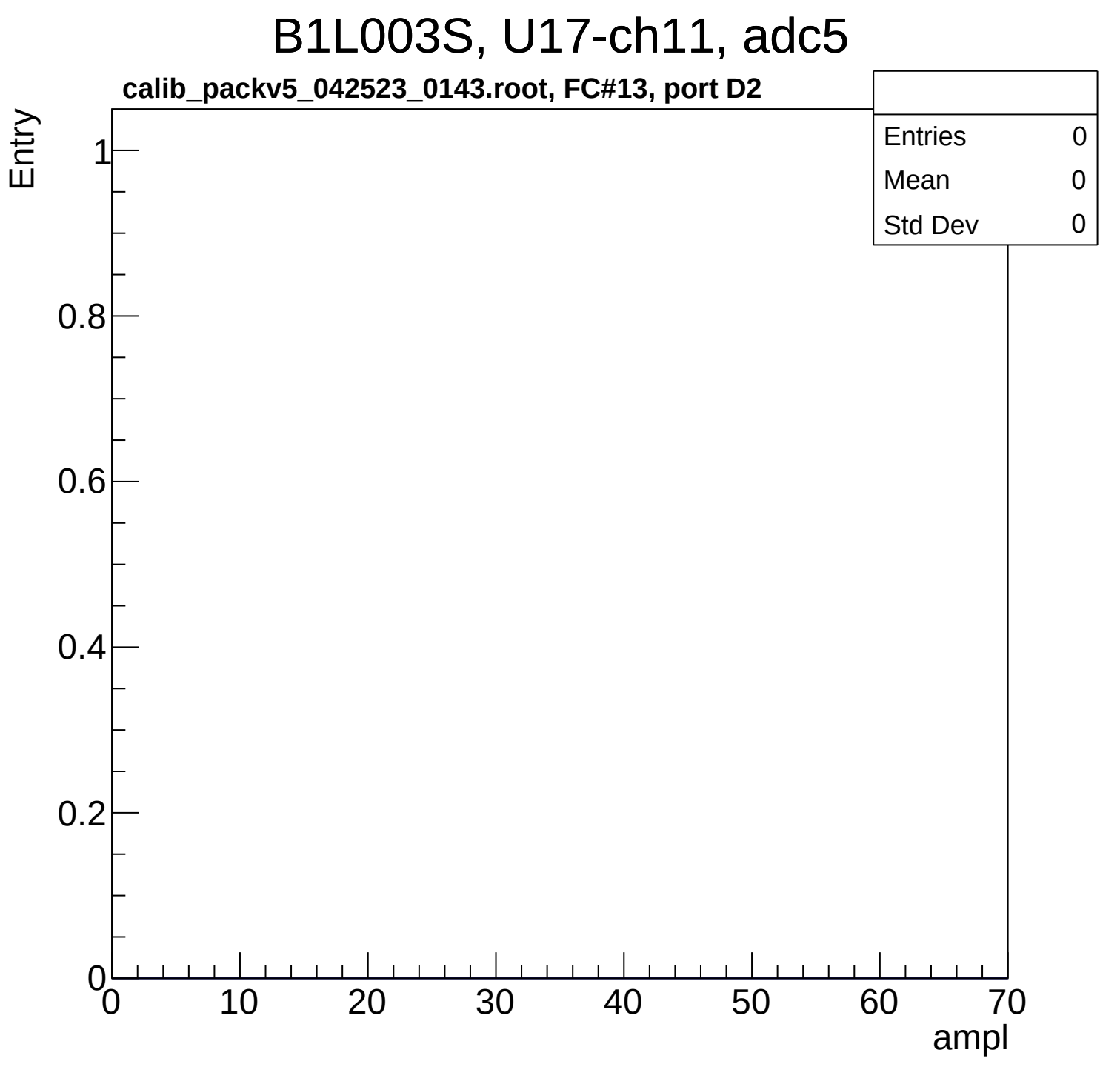
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Entries 0
Mean 0
Std Dev 0
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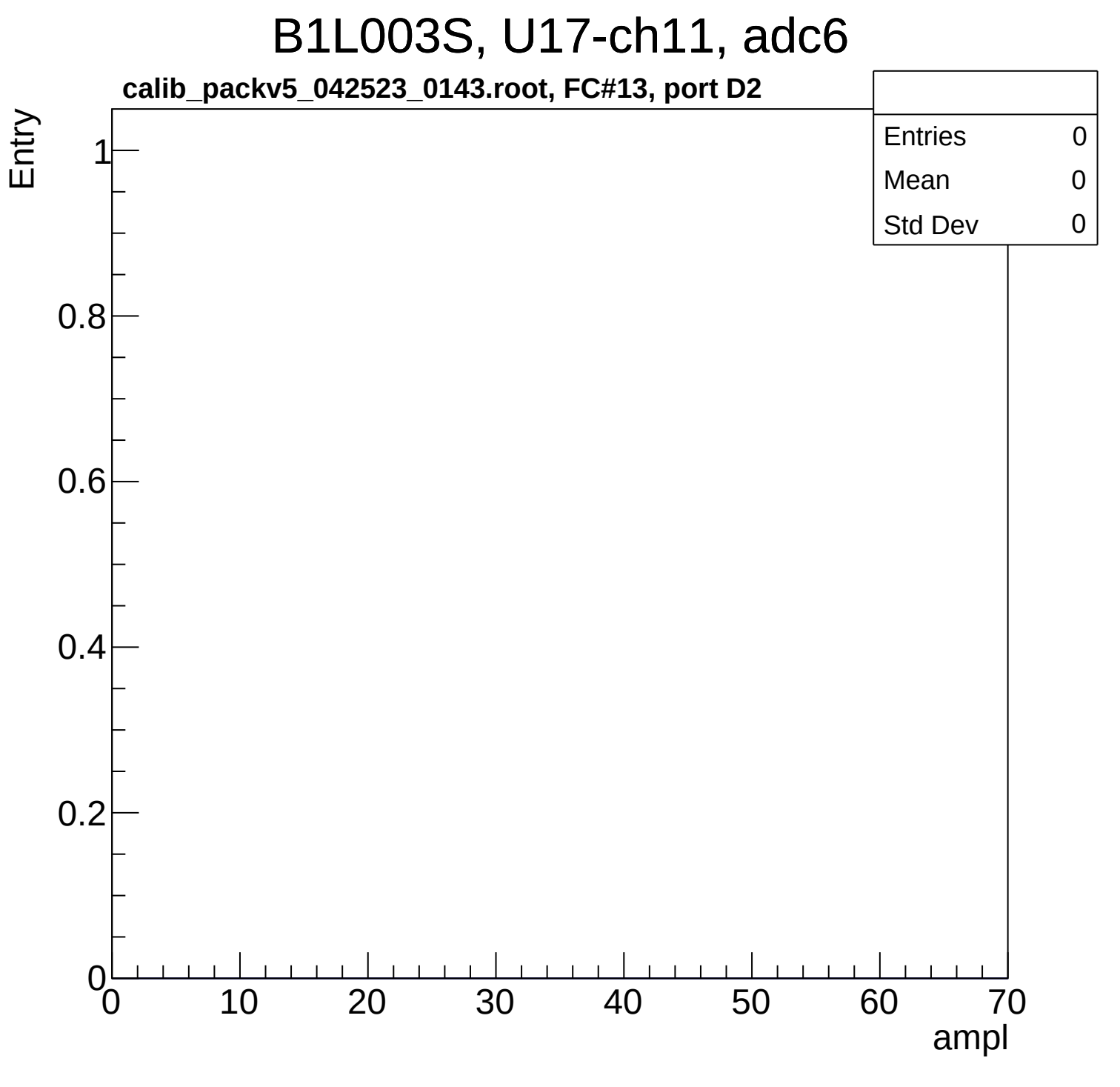
ampl


B1L003S, U17-ch11, adc6
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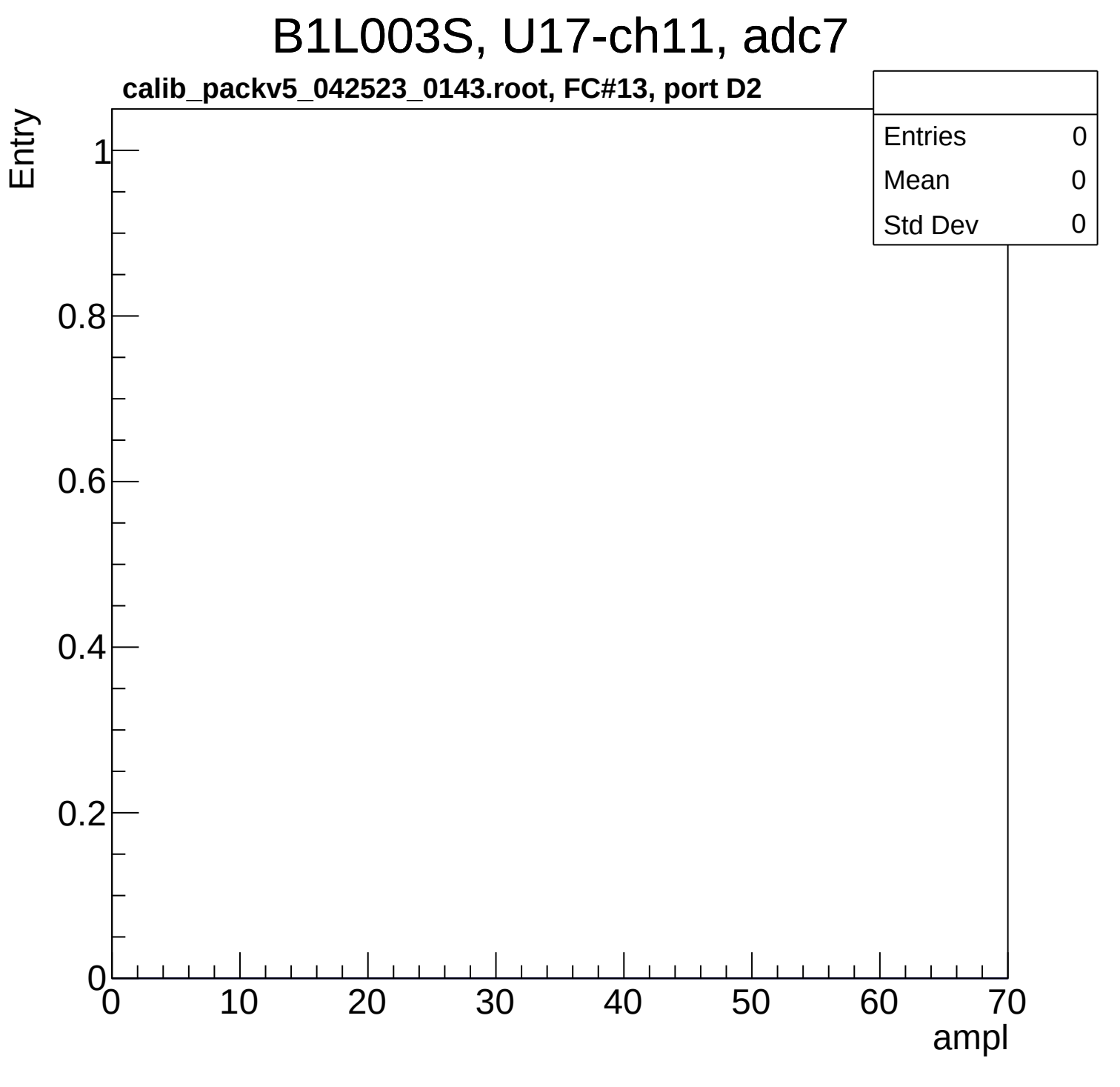
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Std Dev 0
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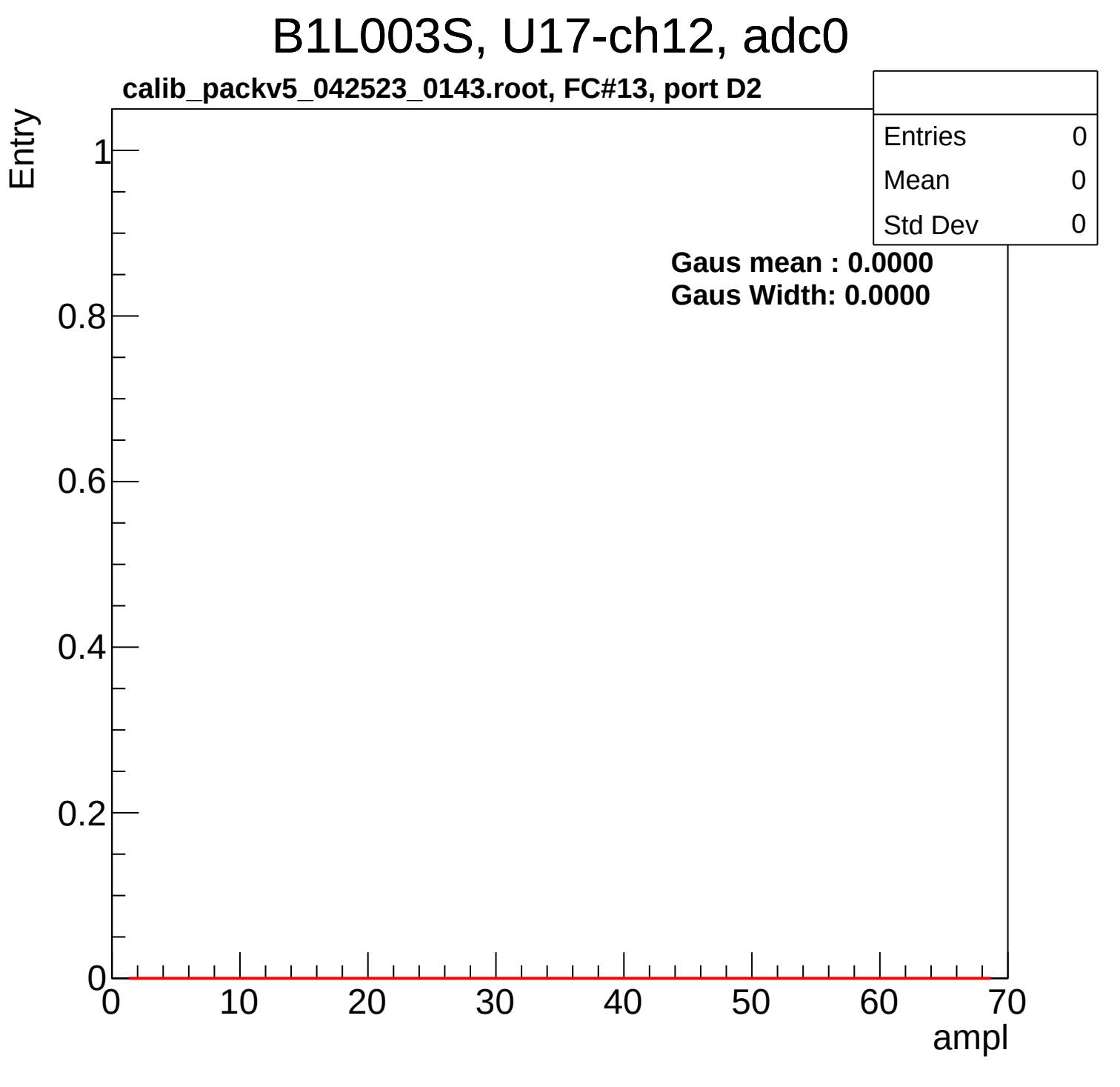
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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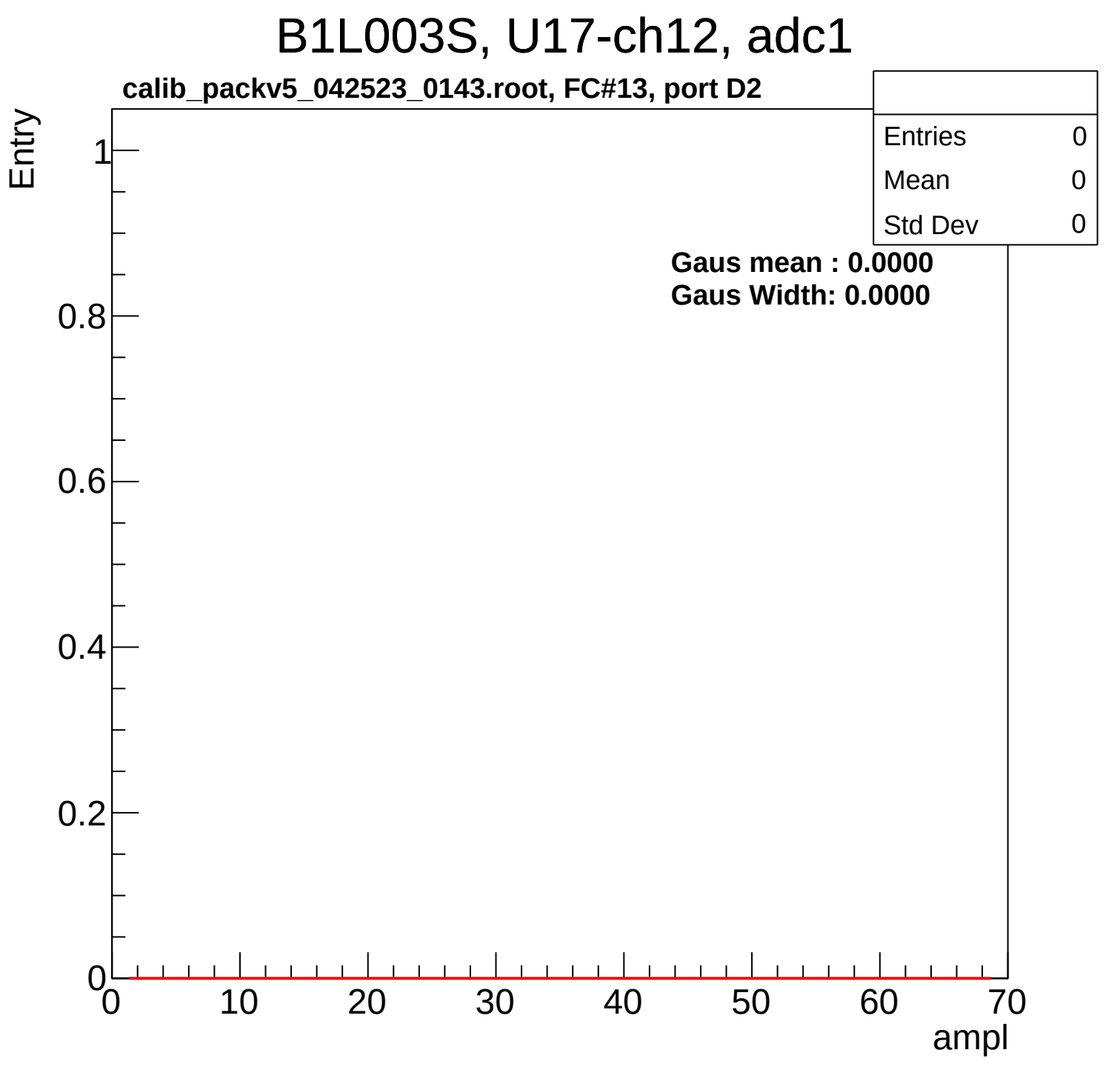
ampl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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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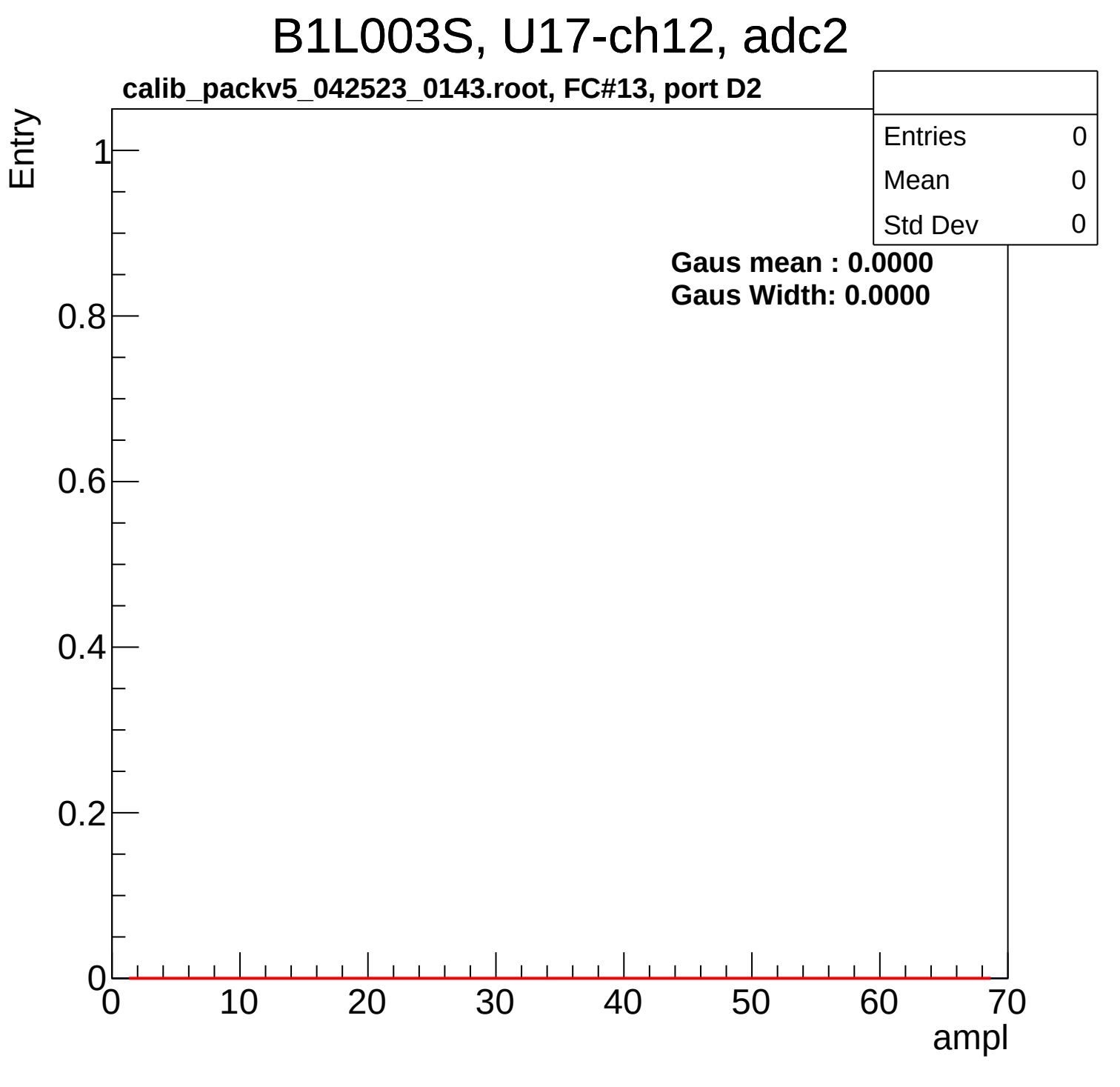
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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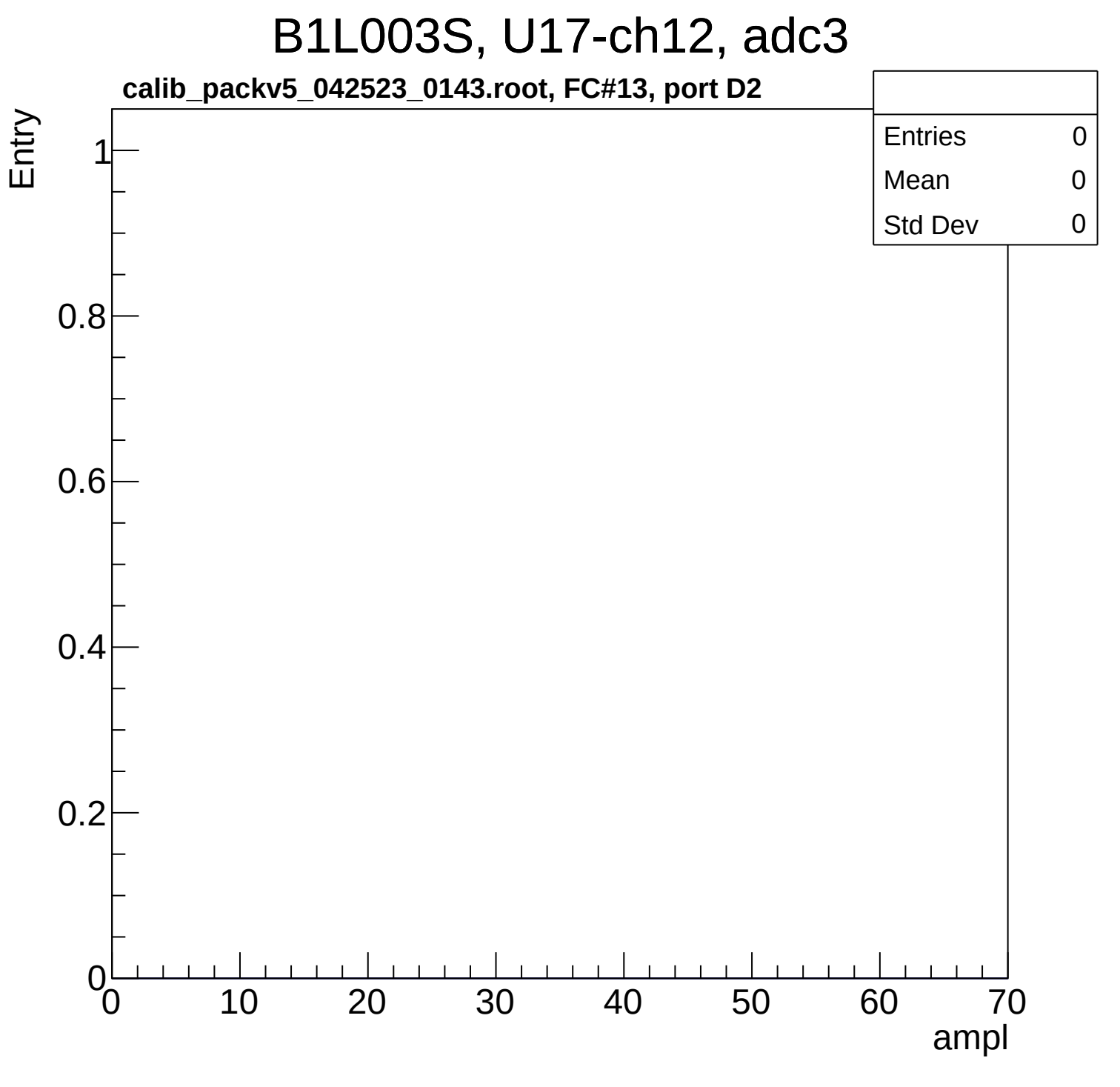
B1L003S, U17-ch12, adc3
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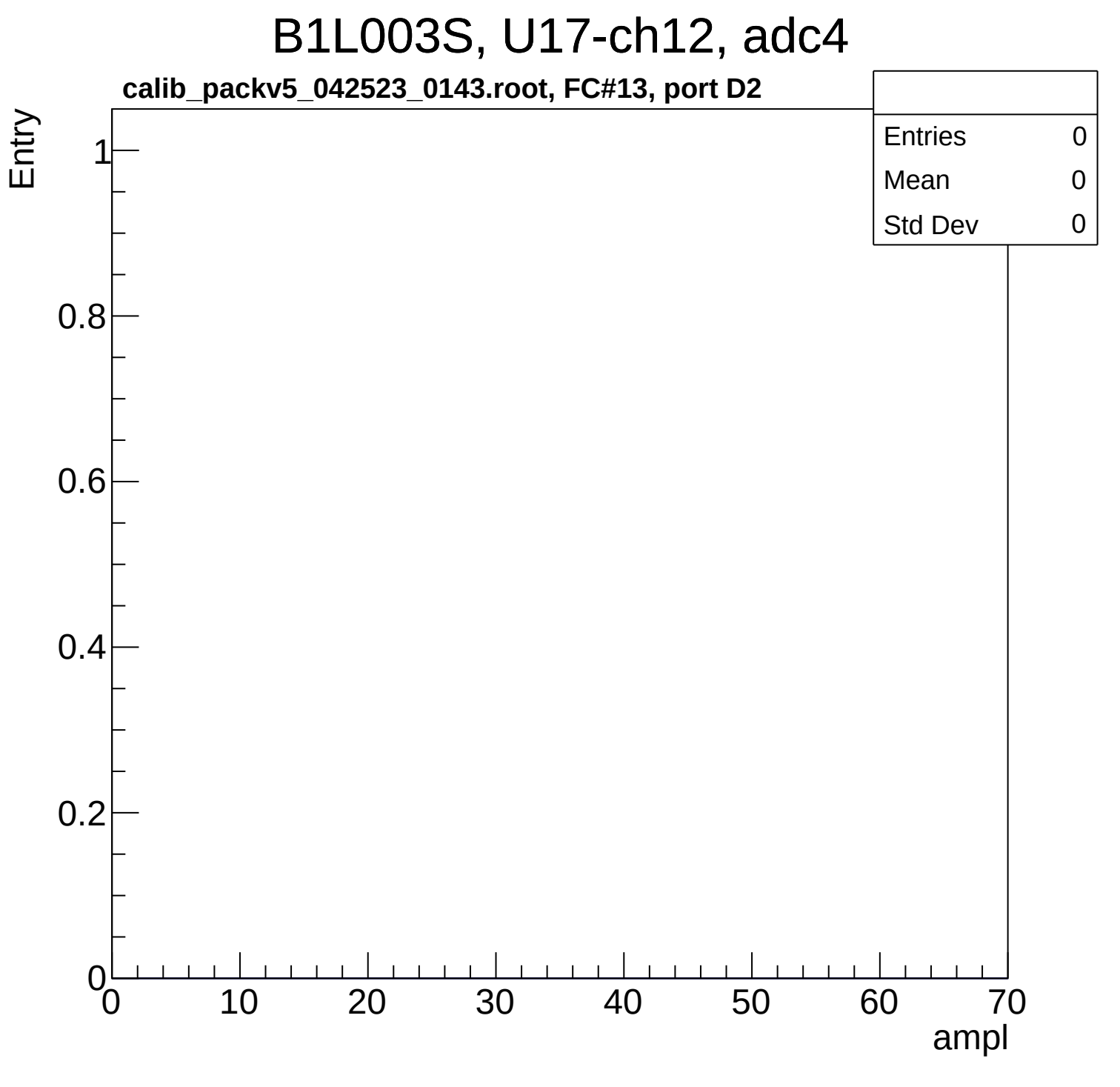
ampl


B1L003S, U17-ch12, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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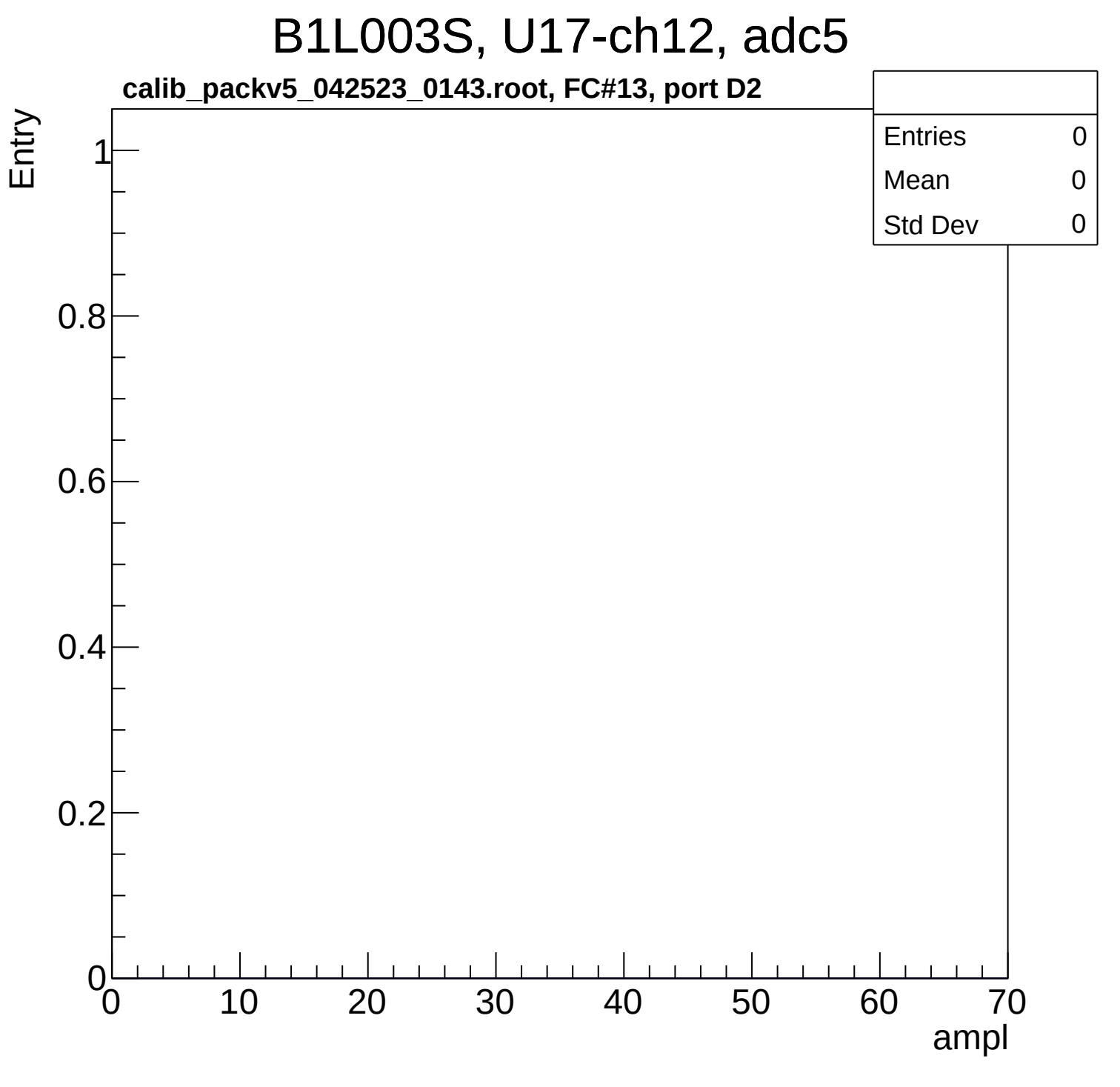
ampl


B1L003S, U17-ch12, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
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Std Dev 0
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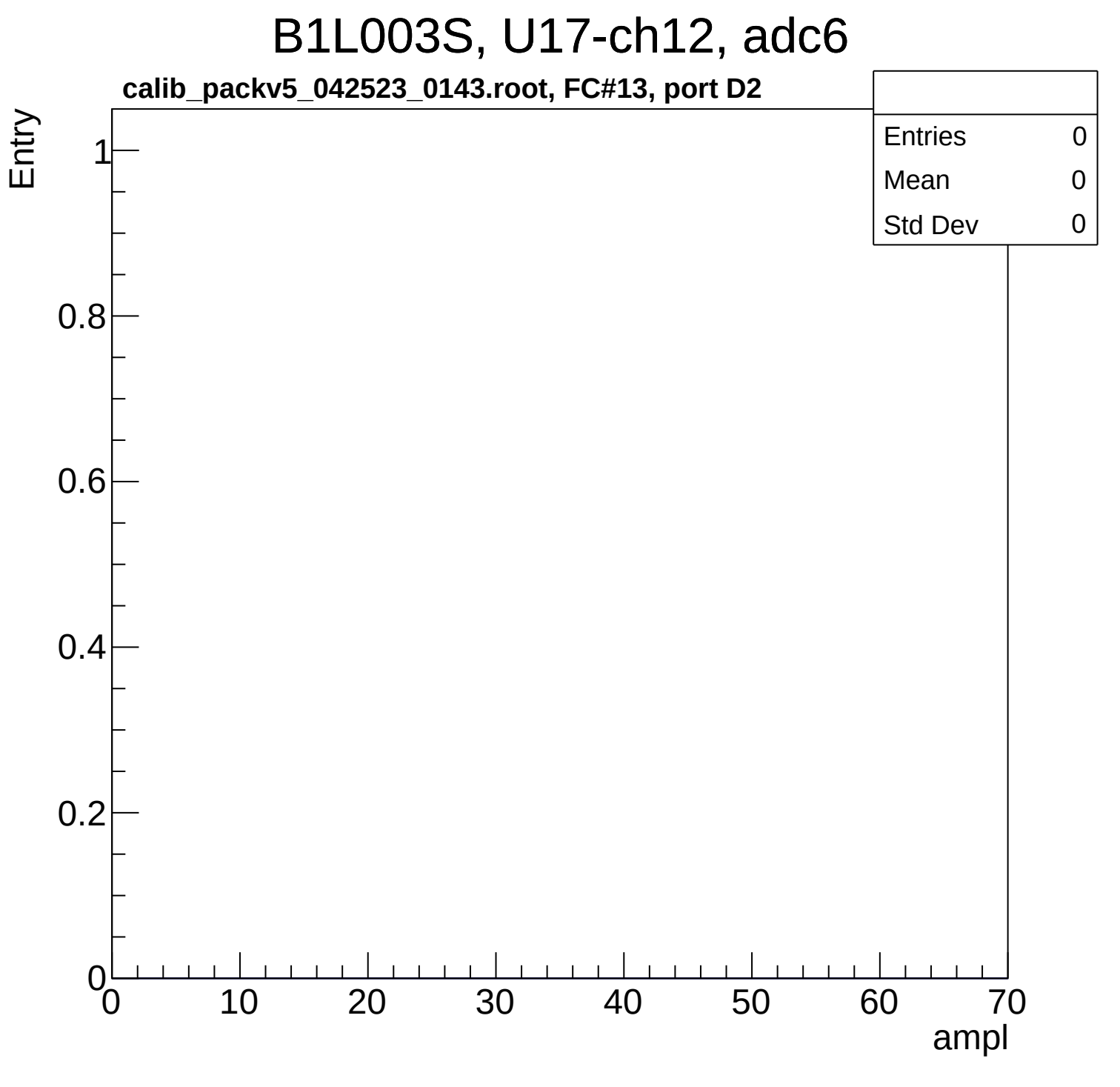
ampl


B1L003S, U17-ch12, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
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Std Dev 0
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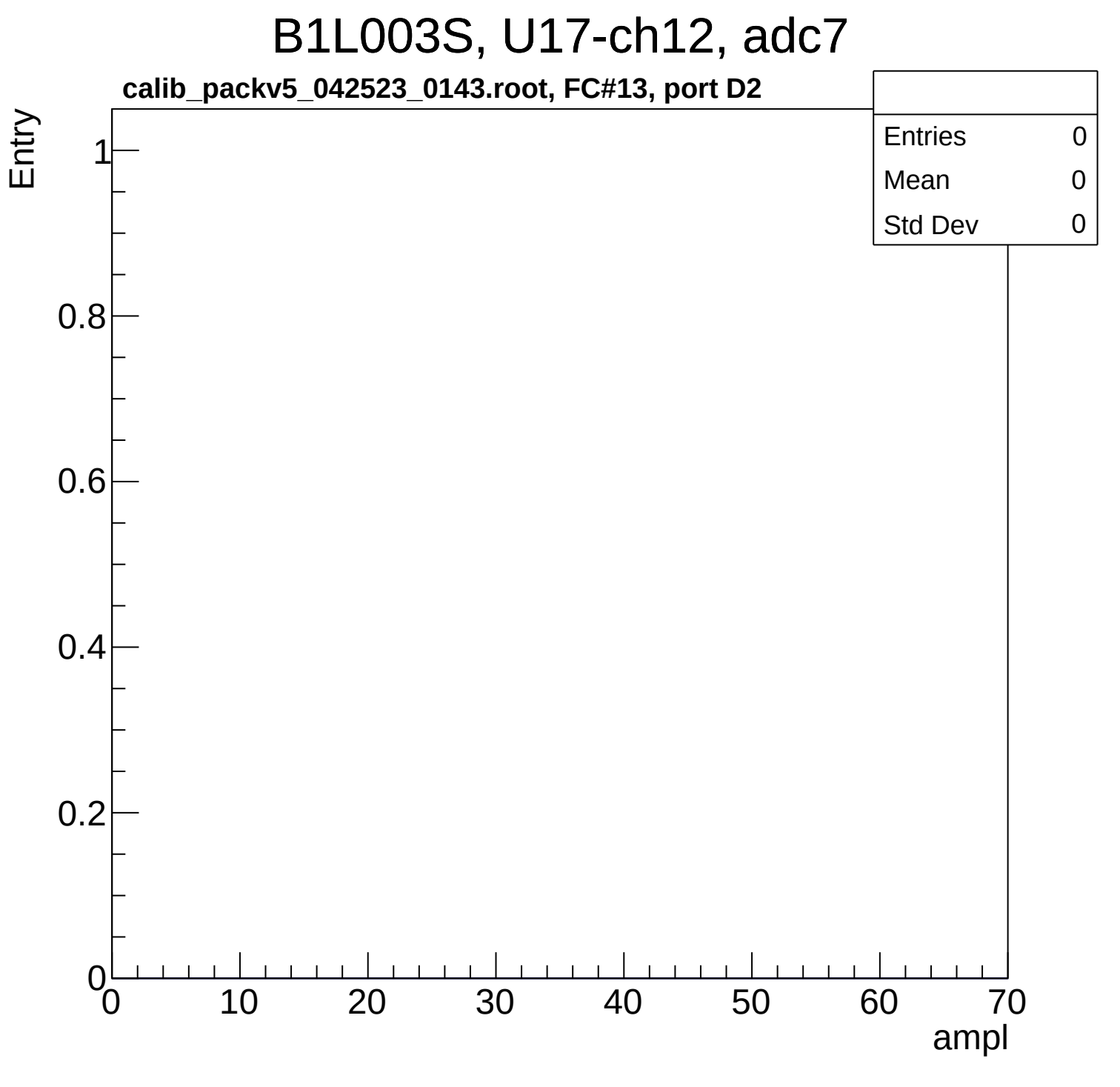
ampl


B1L003S, U17-ch12, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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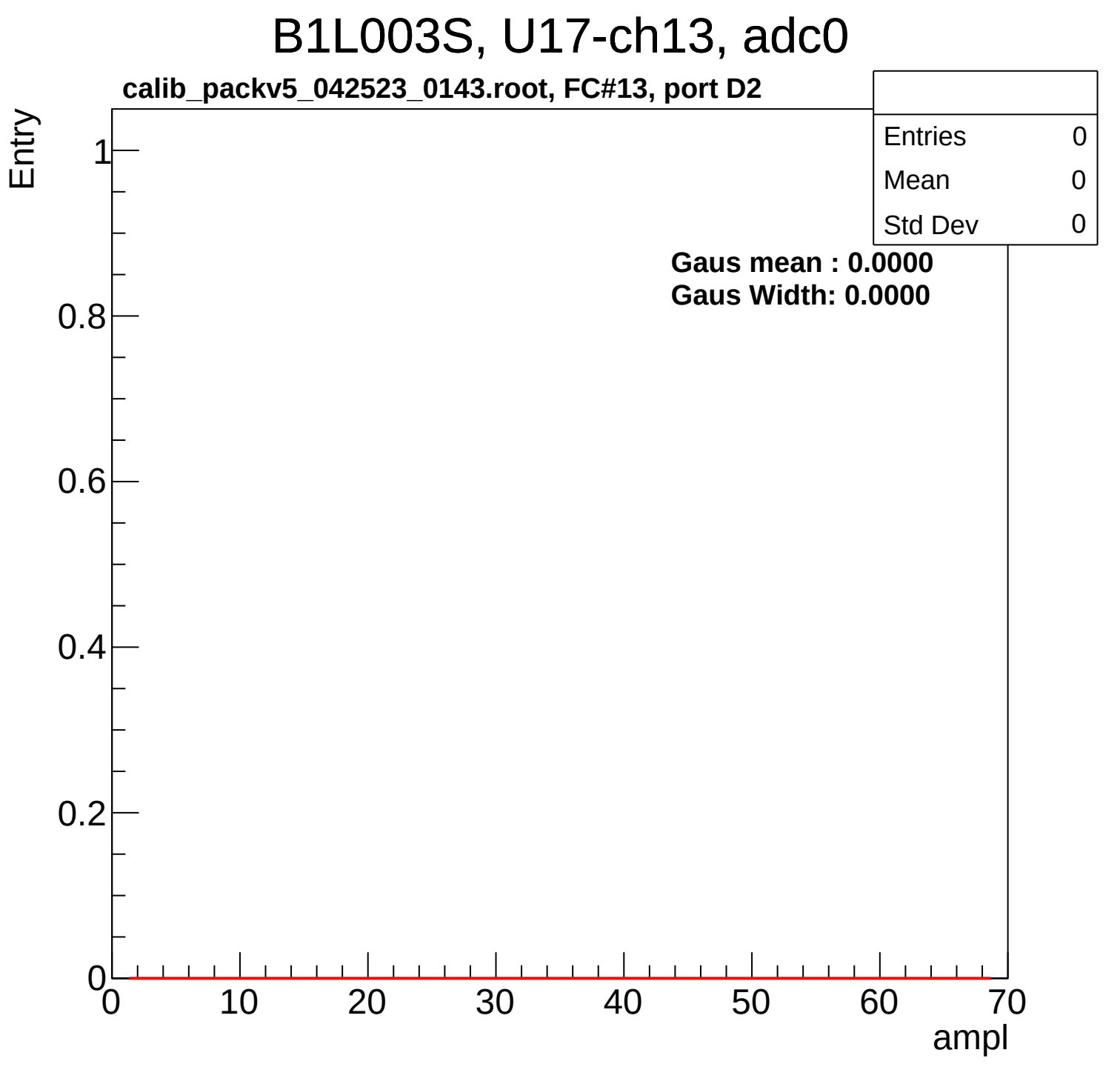
ampl


B1L003S, U17-ch13, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0.4
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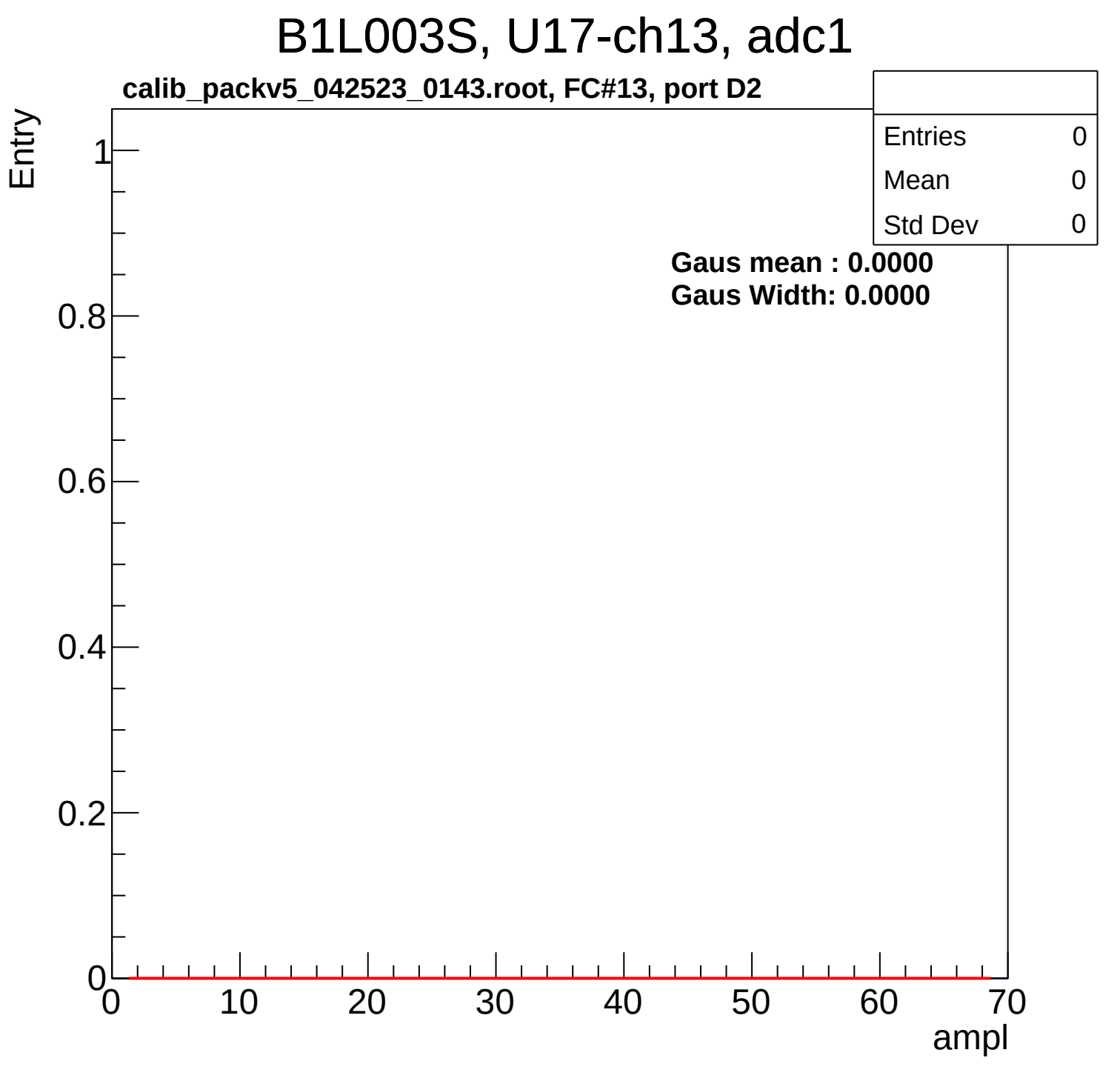
B1L003S, U17-ch13, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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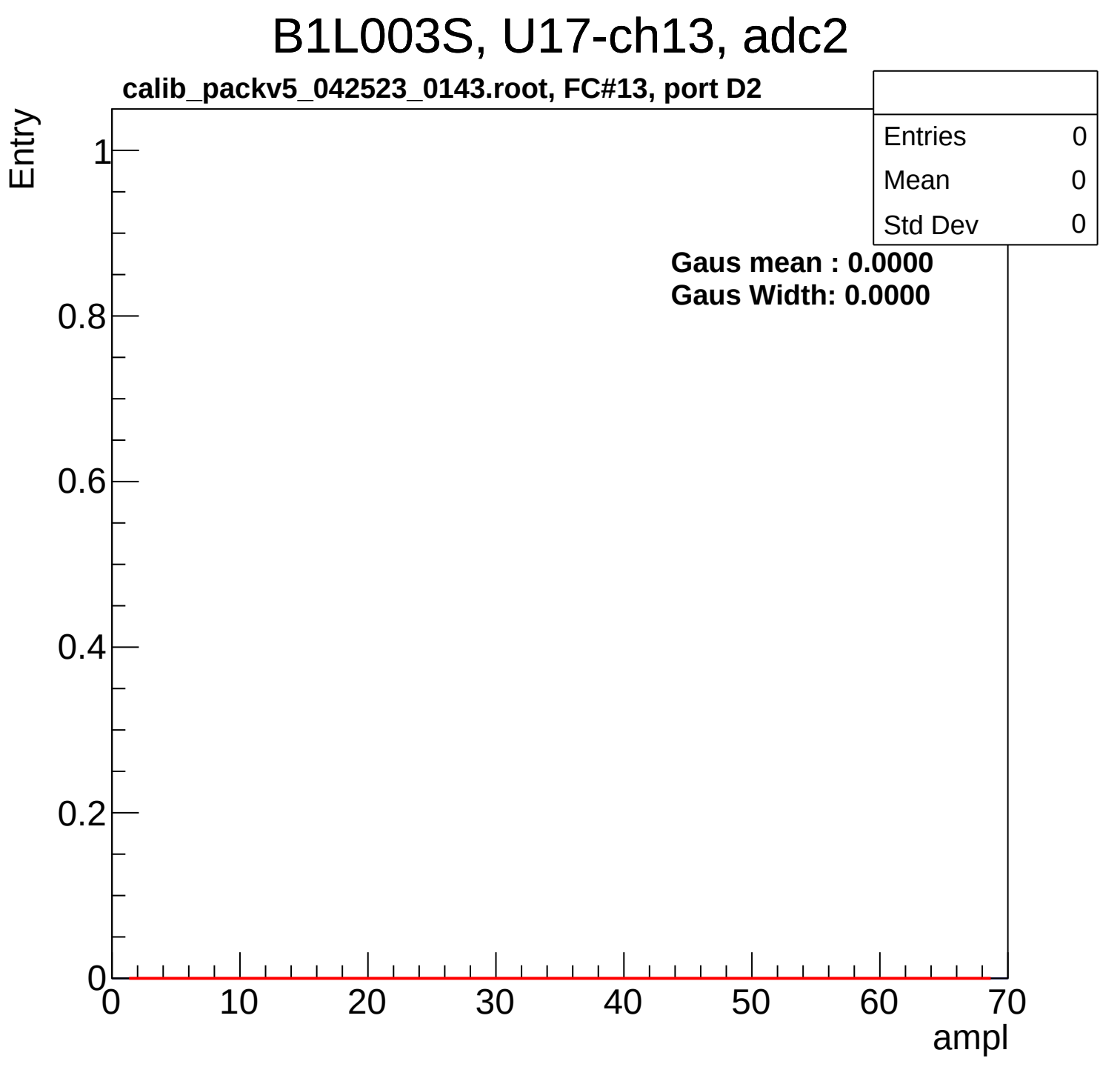
ampl


B1L003S, U17-ch13, adc2
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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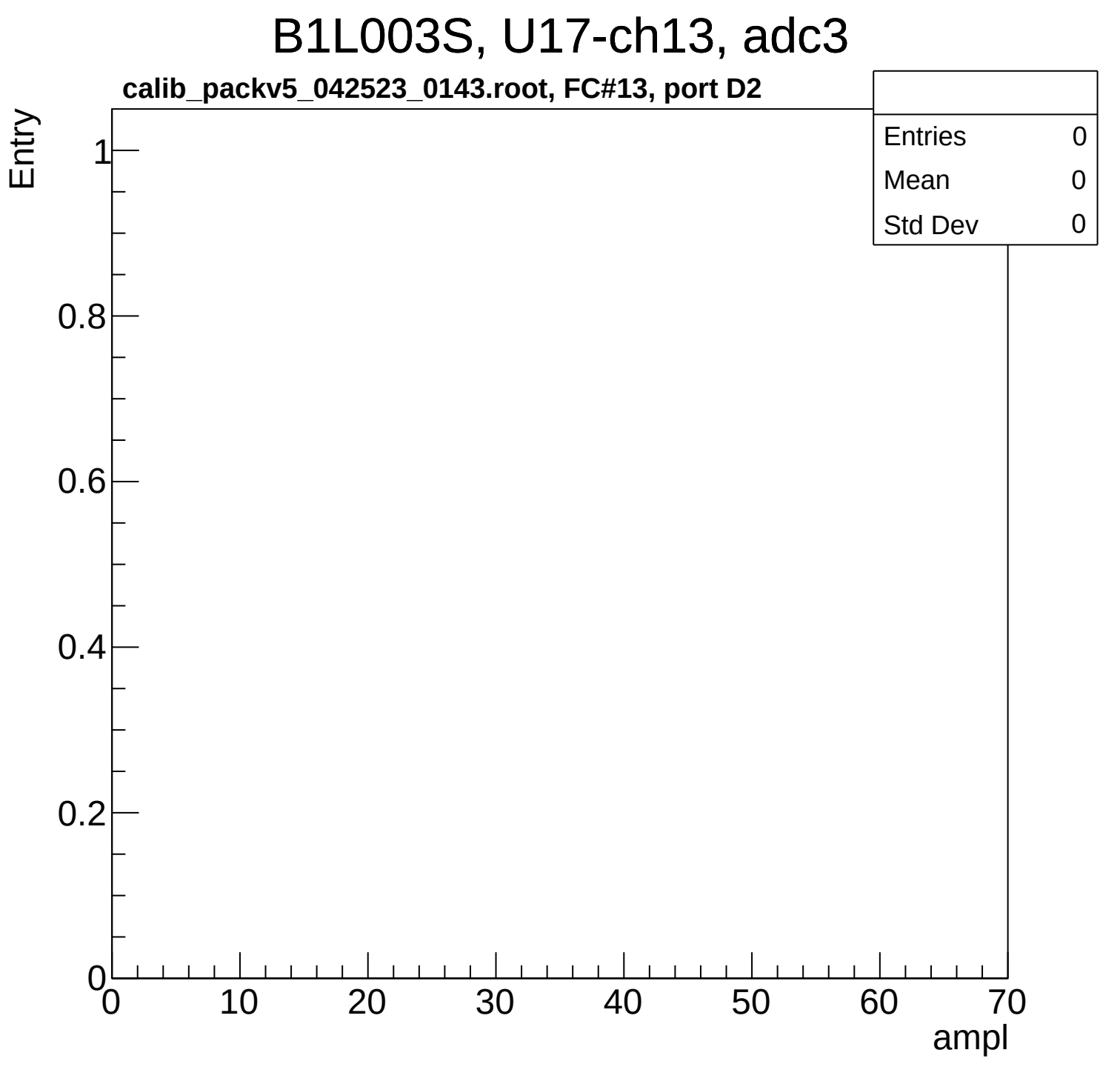
ampl


B1L003S, U17-ch13, adc3
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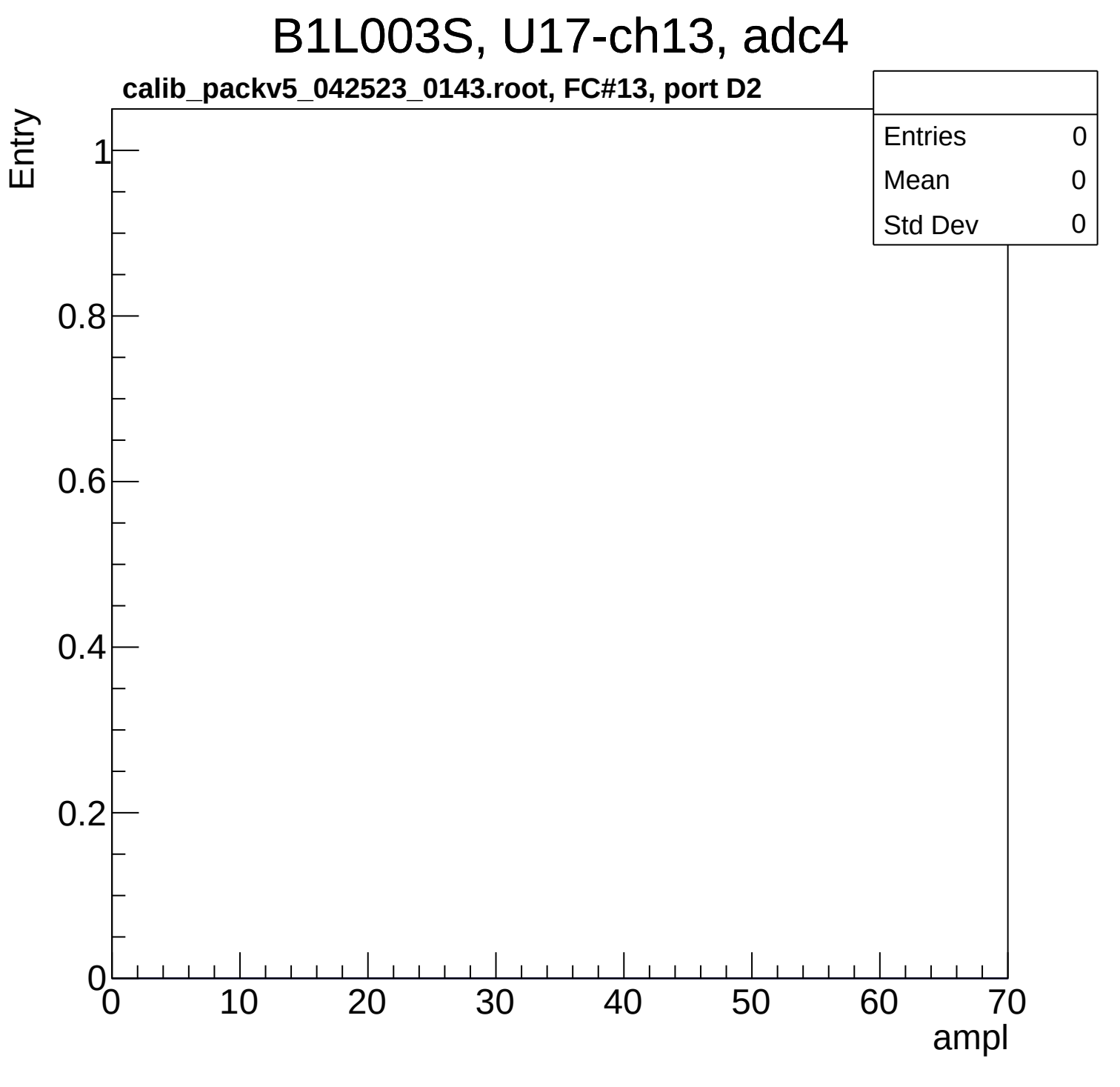
ampl


B1L003S, U17-ch13, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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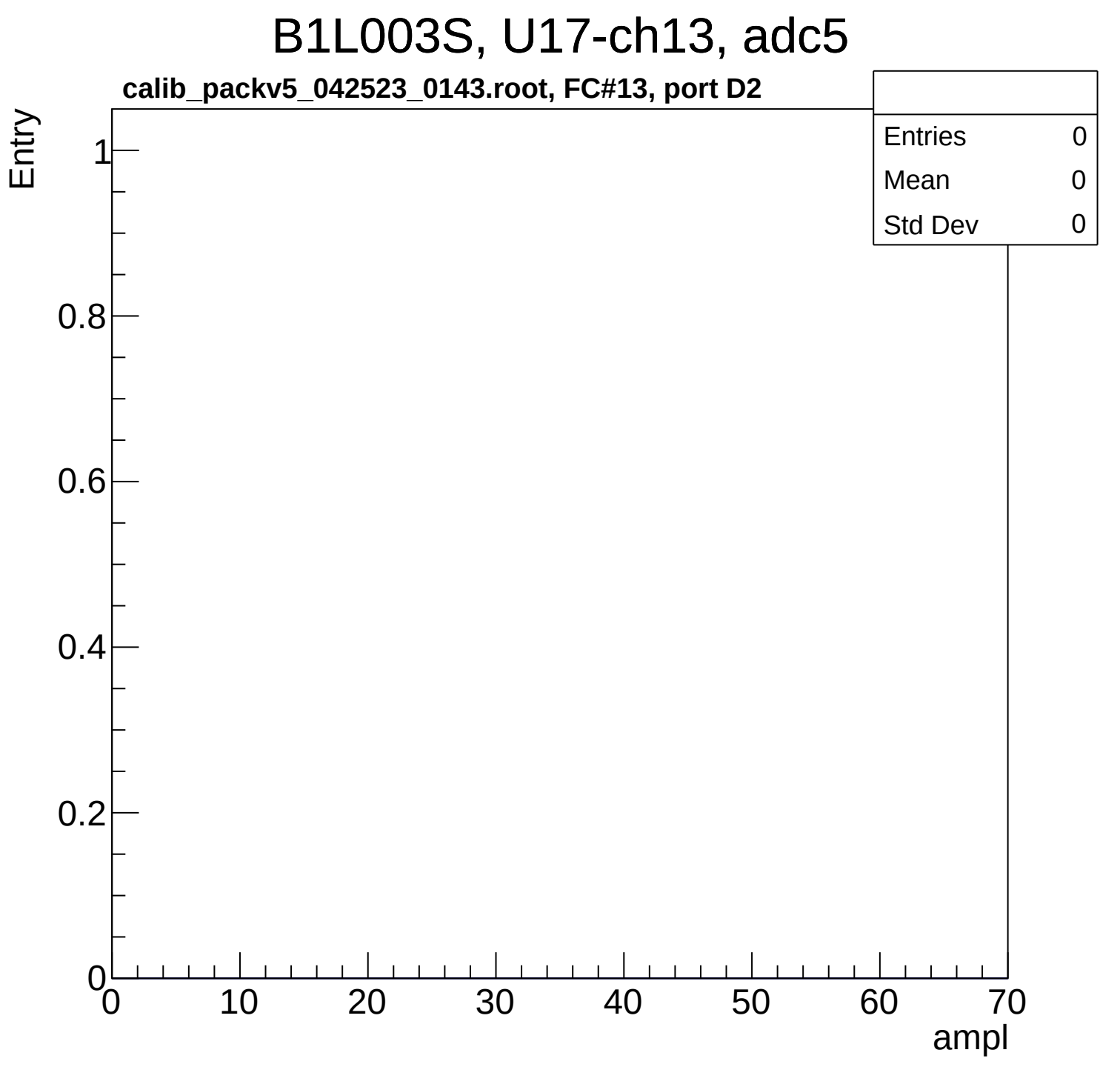
ampl


B1L003S, U17-ch13, adc5
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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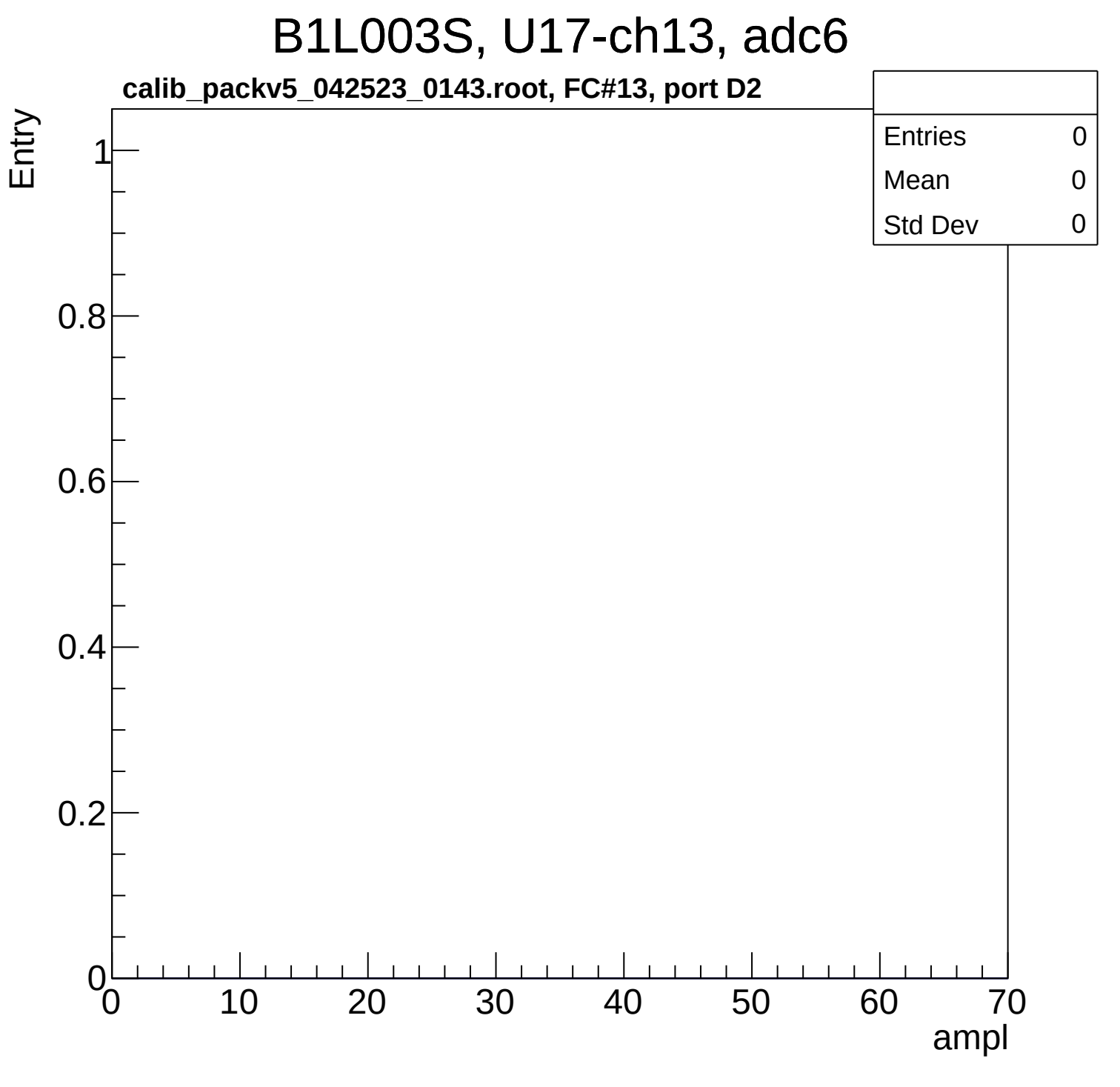
B1L003S, U17-ch13, adc6
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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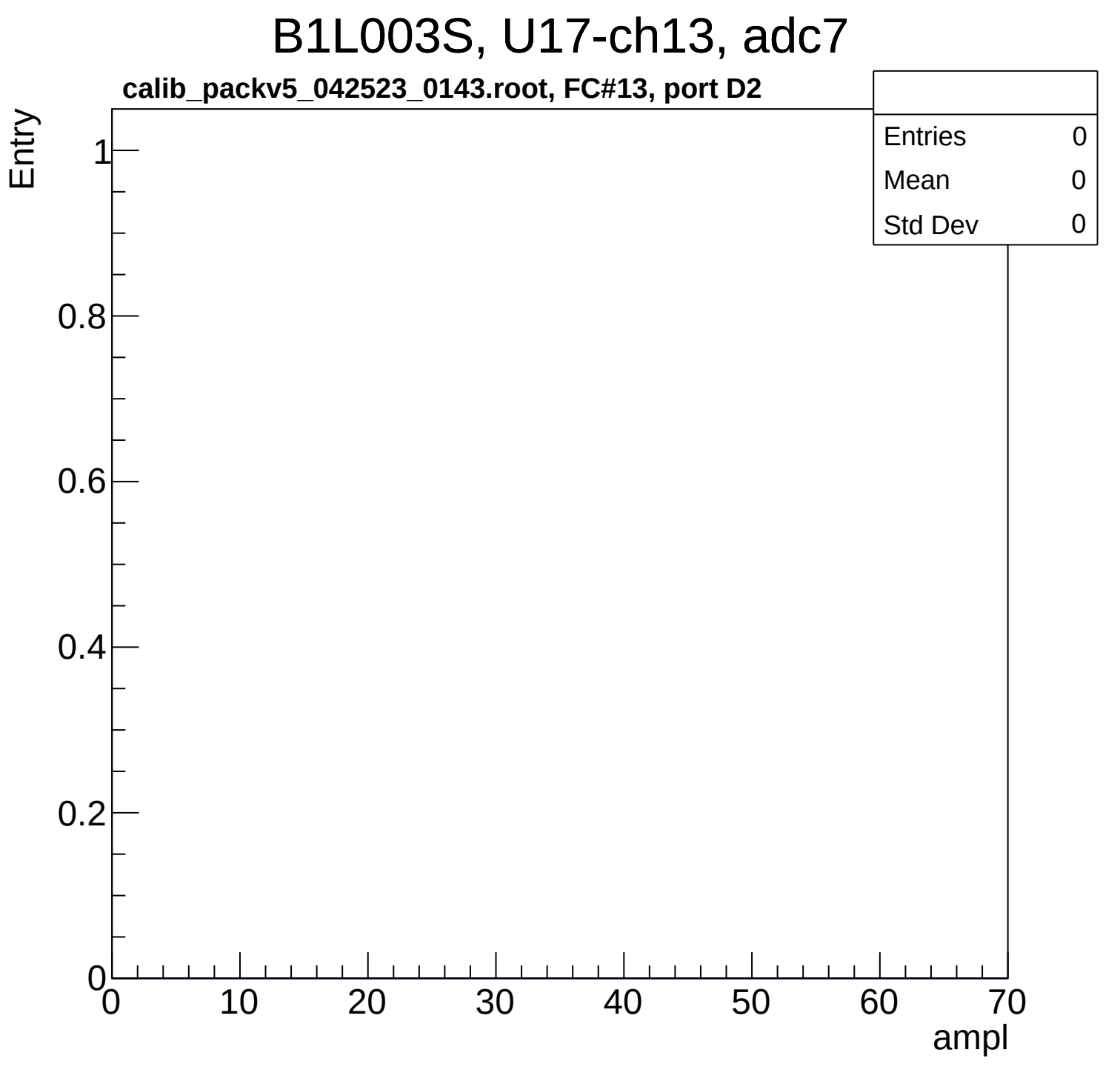
ampl


B1L003S, U17-ch13, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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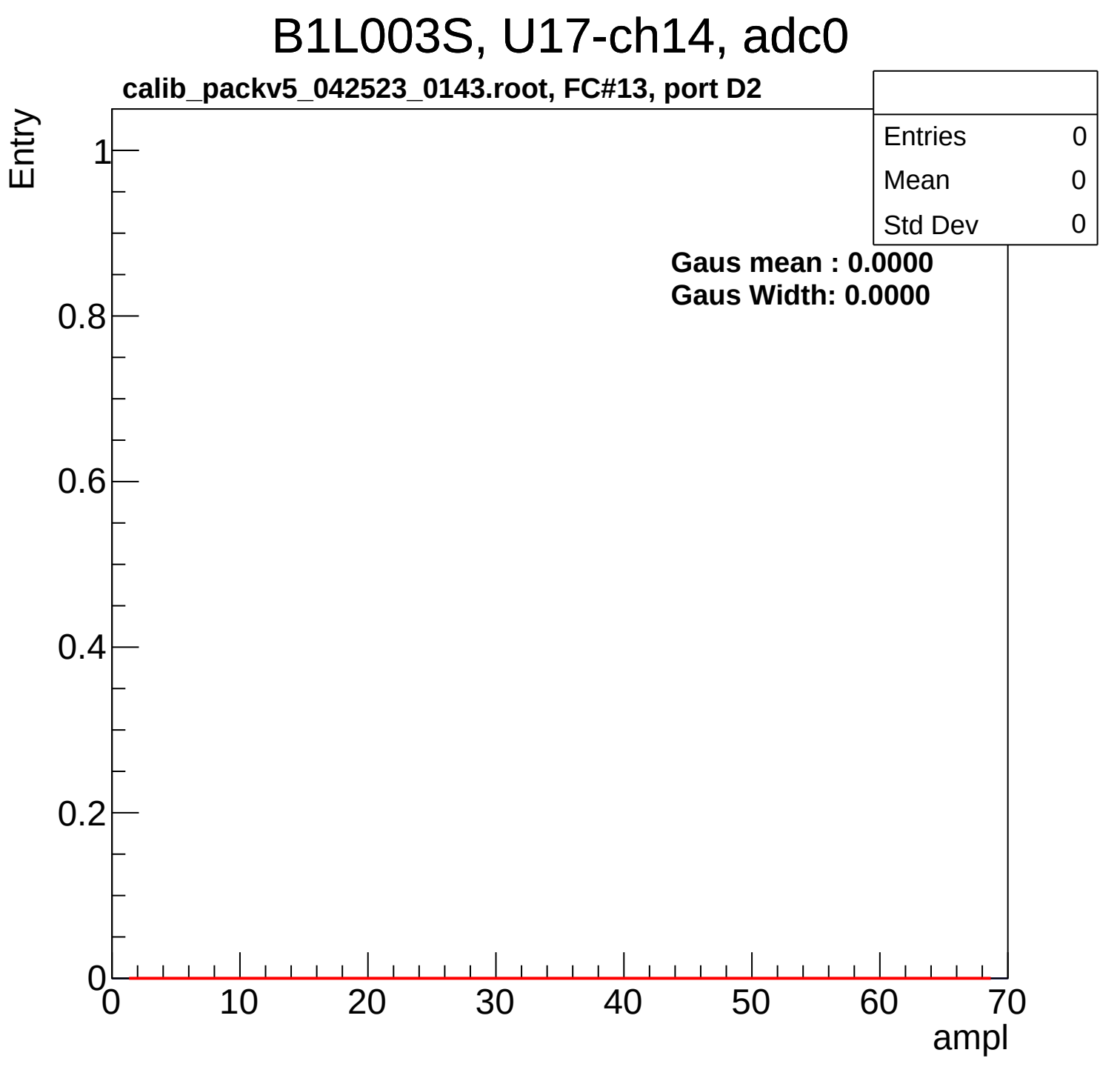
ampl


B1L003S, U17-ch14, adc0
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Std Dev 0
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Gaus Width: 0.0000
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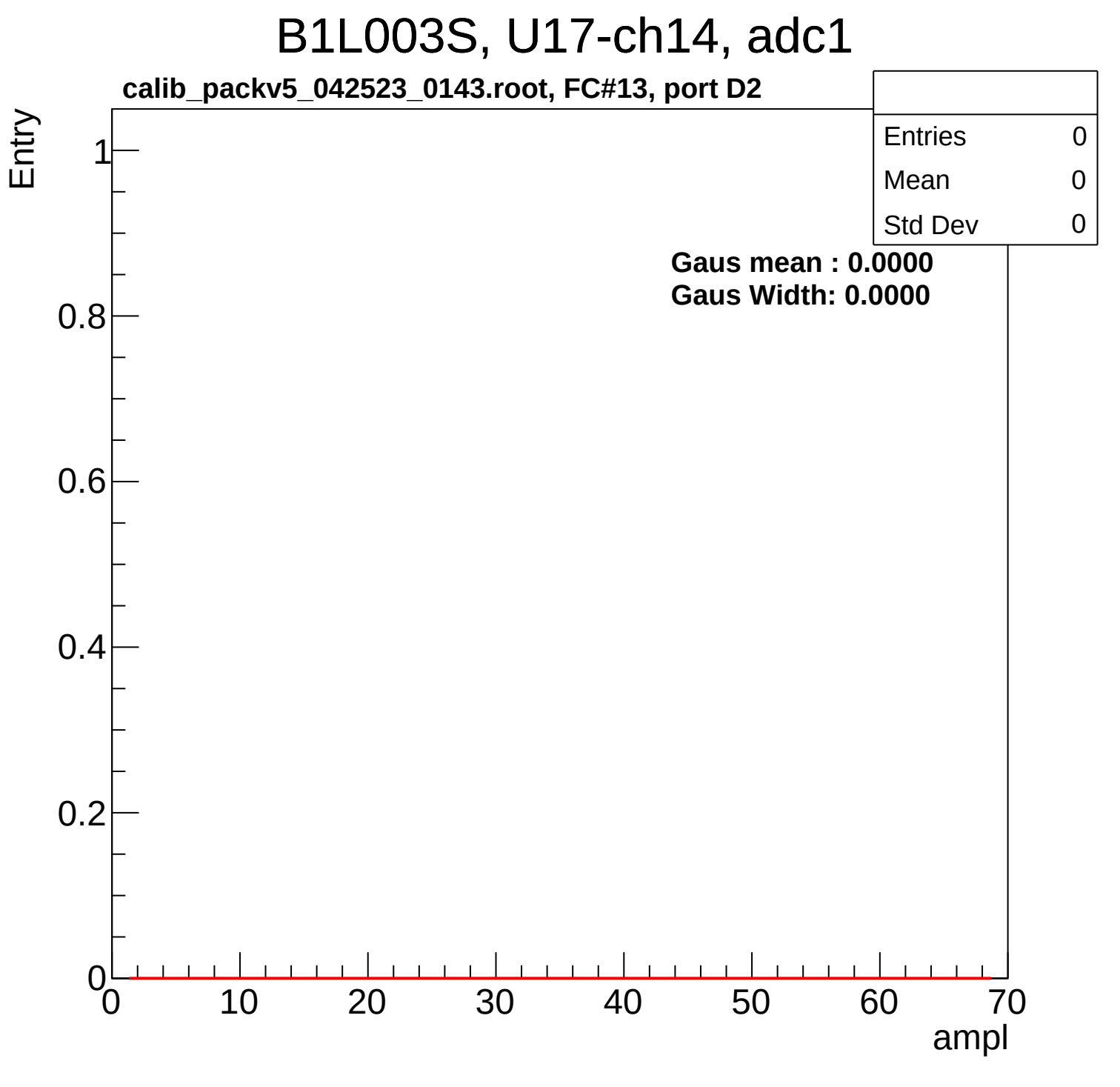
B1L003S, U17-ch14, adc1
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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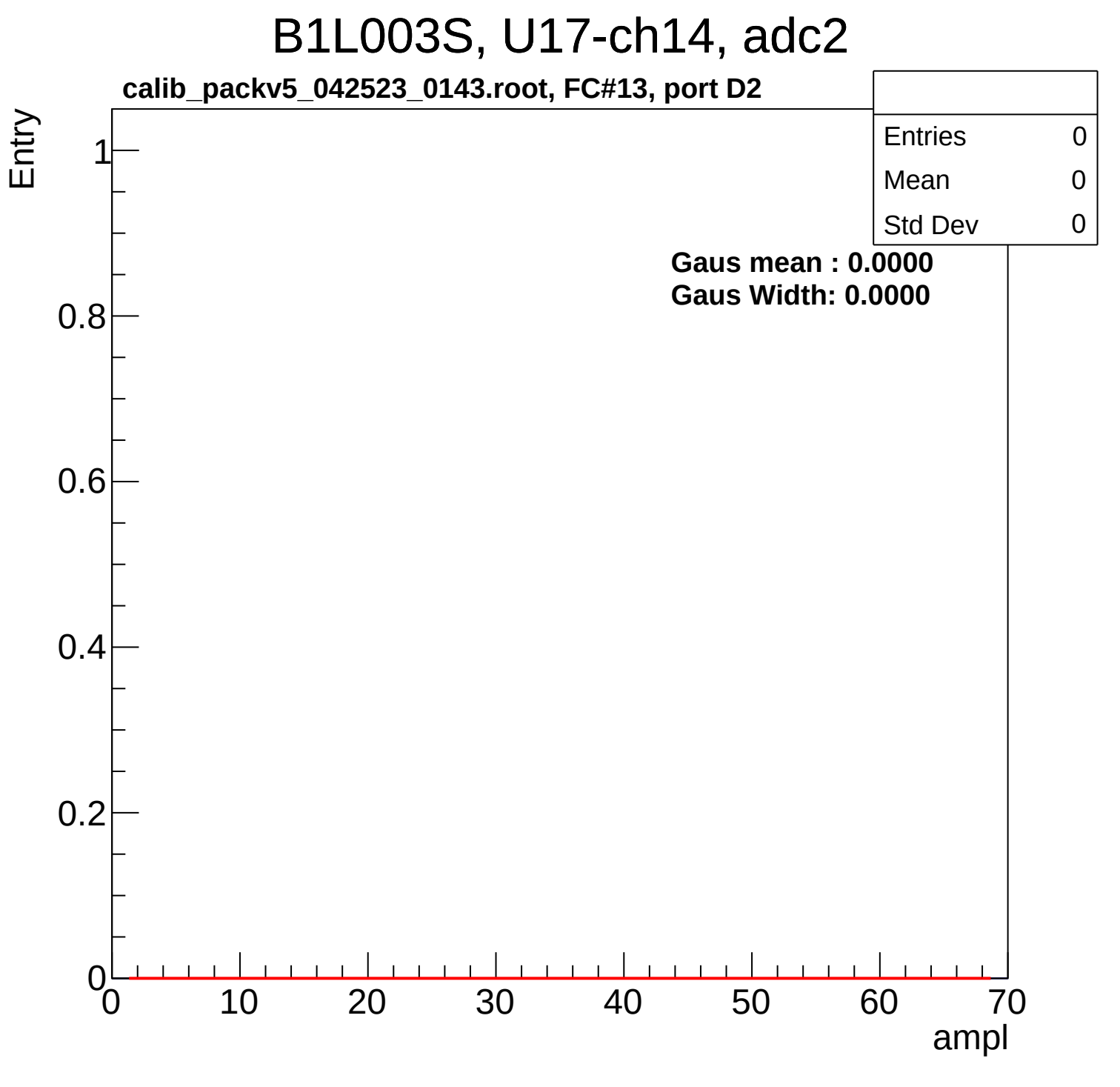
B1L003S, U17-ch14, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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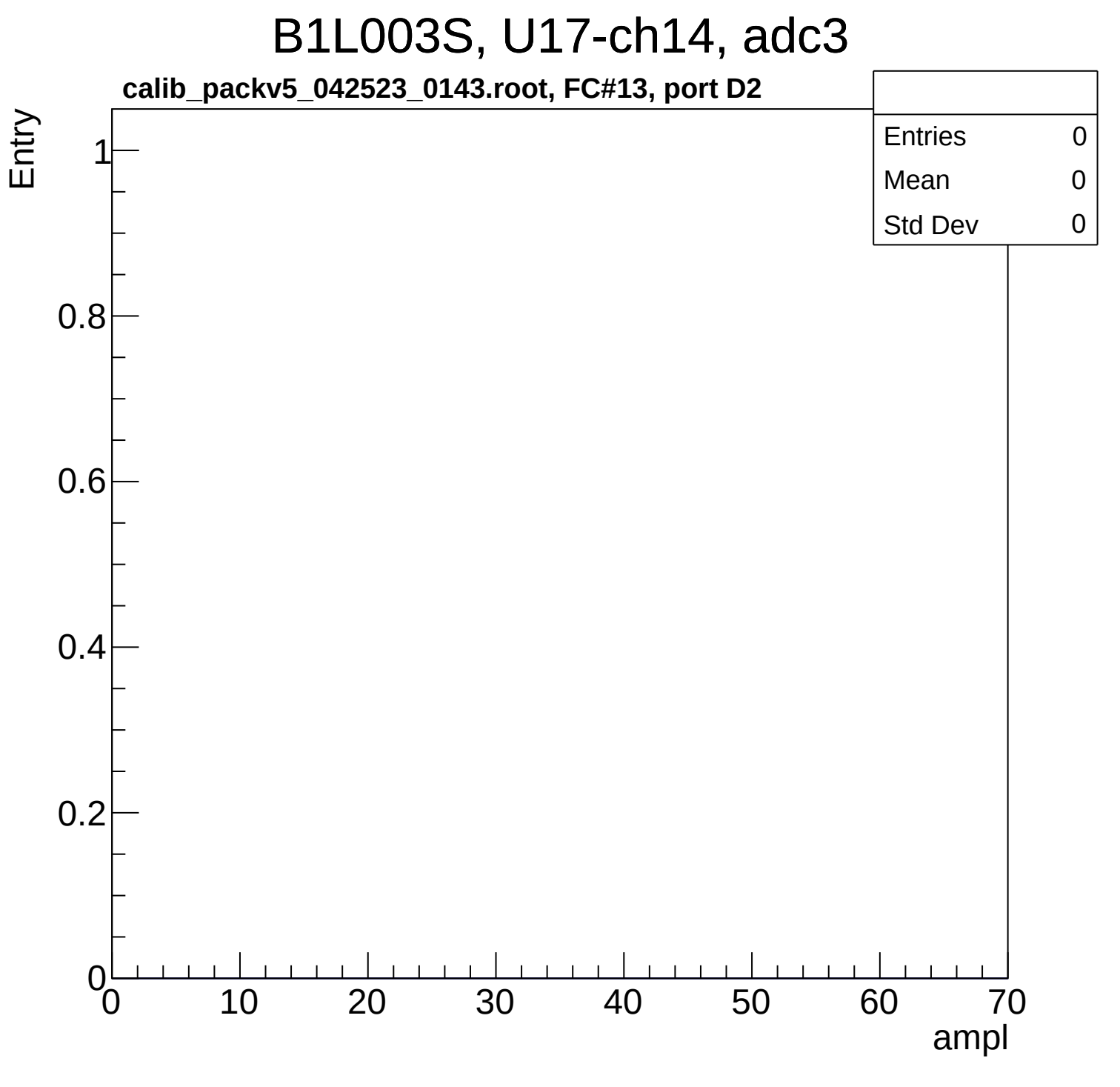
B1L003S, U17-ch14, adc3
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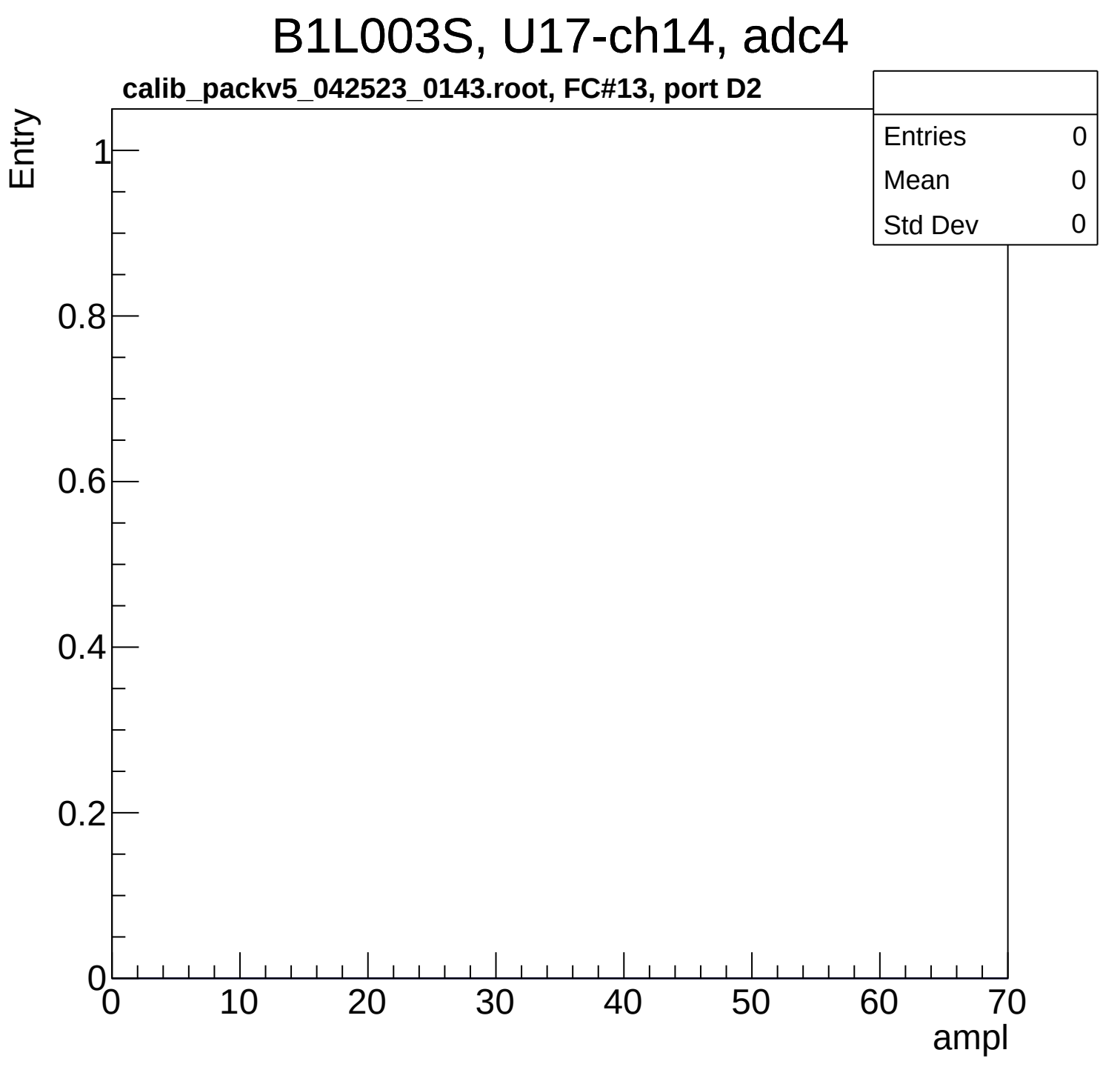
B1L003S, U17-ch14, adc4
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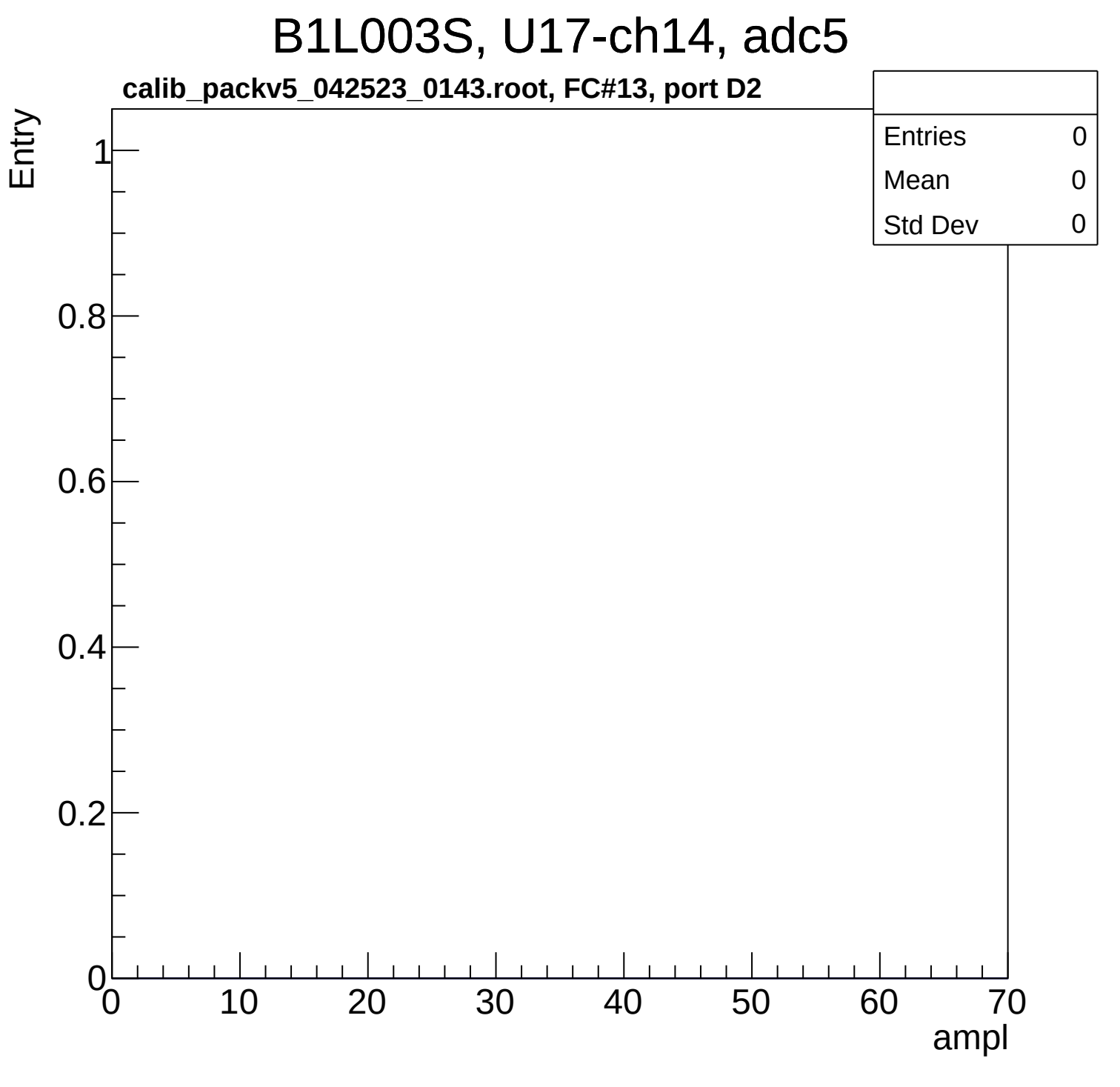
B1L003S, U17-ch14, adc5
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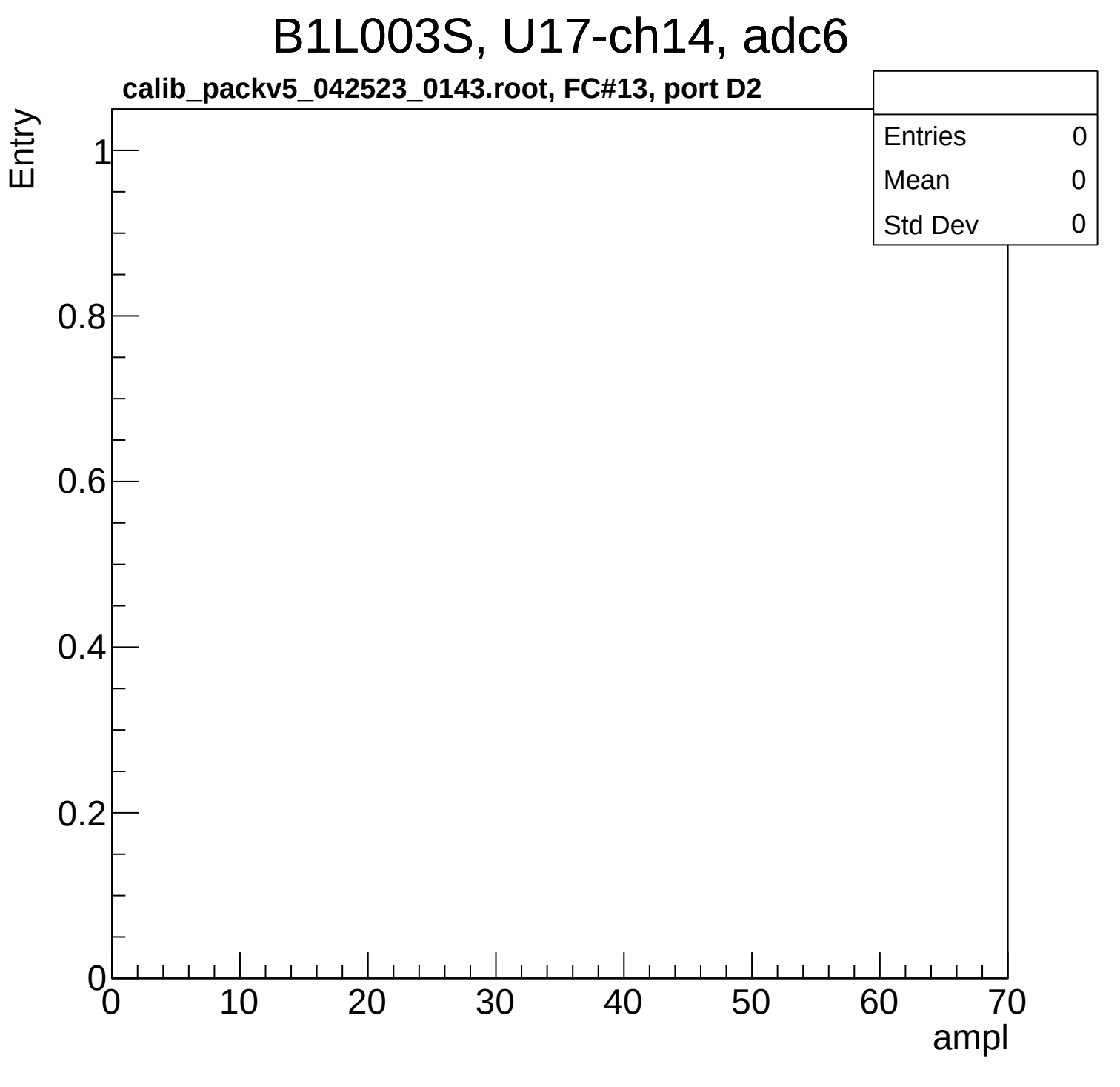
B1L003S, U17-ch14, adc6
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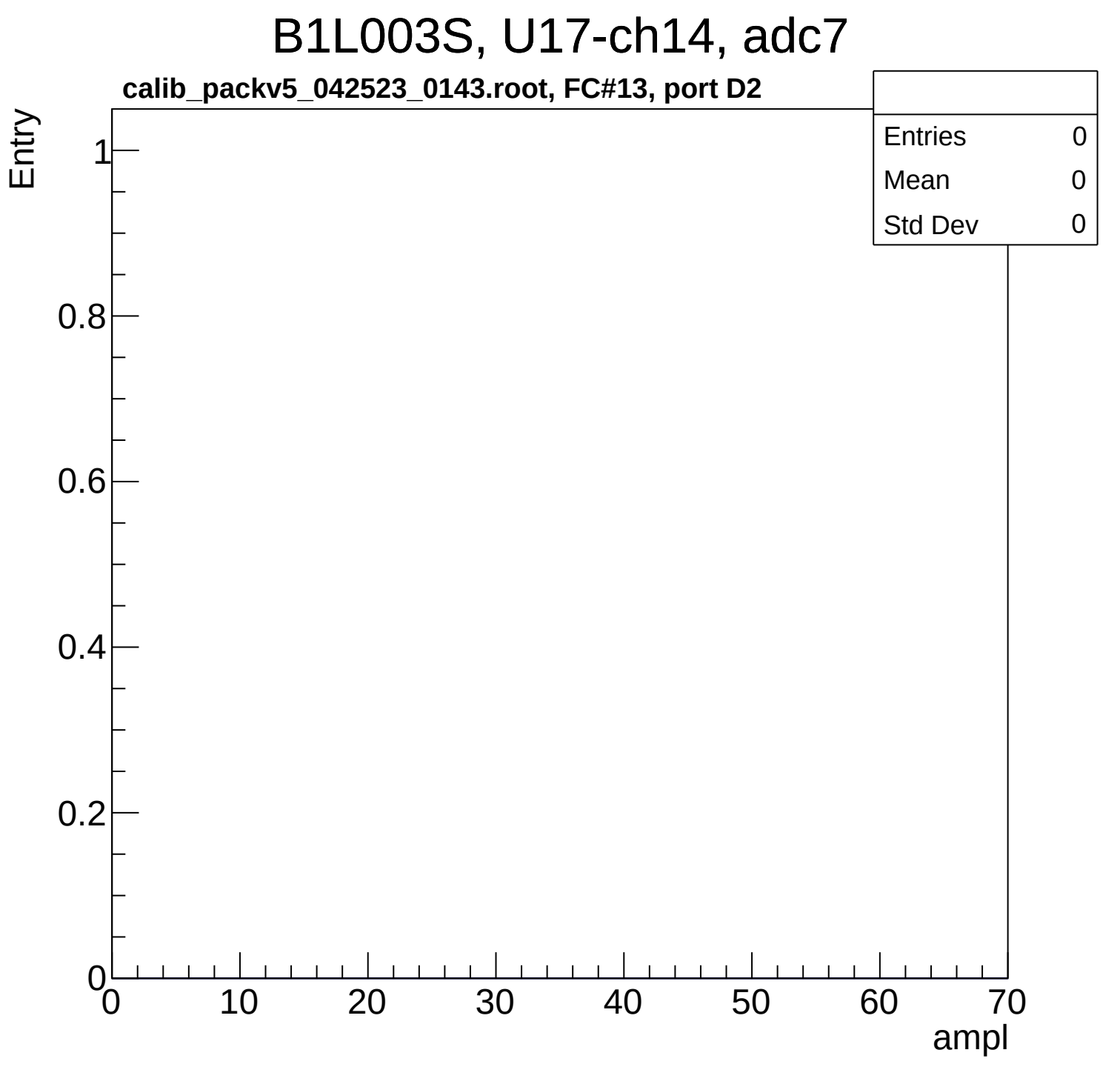
B1L003S, U17-ch14, adc7
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Entries 0
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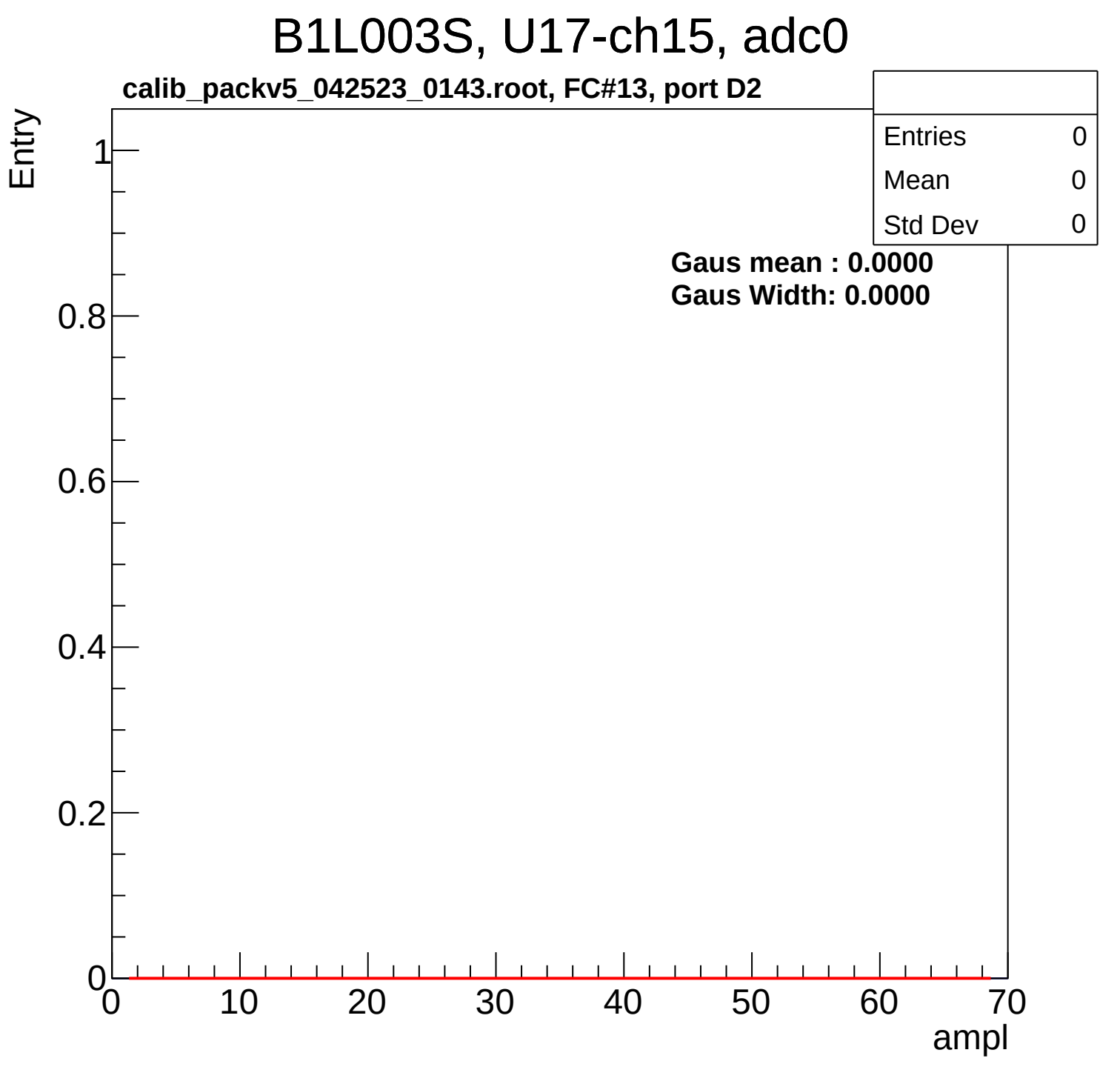
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Std Dev 0
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Gaus Width: 0.0000
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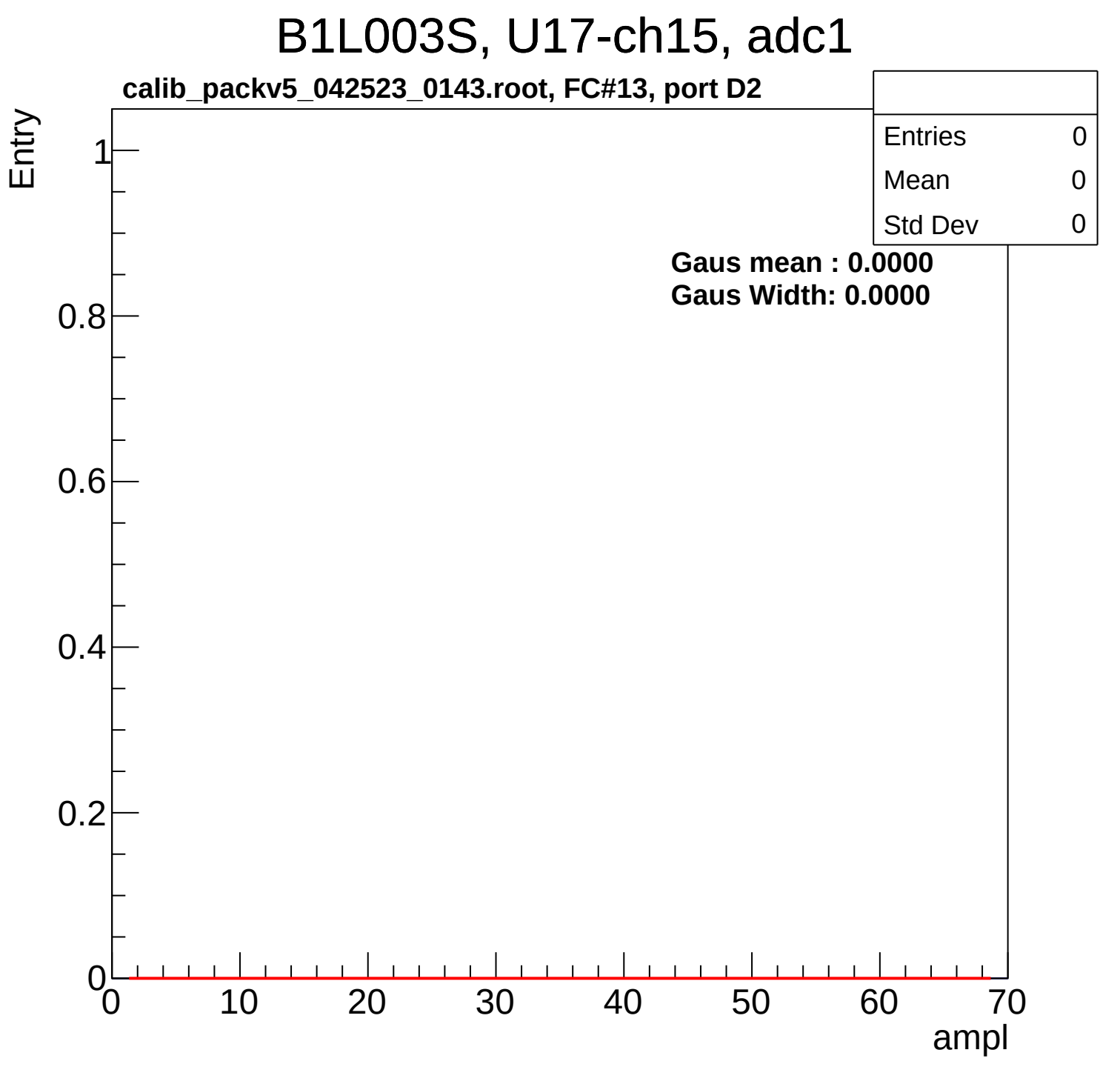
B1L003S, U17-ch15, adc1
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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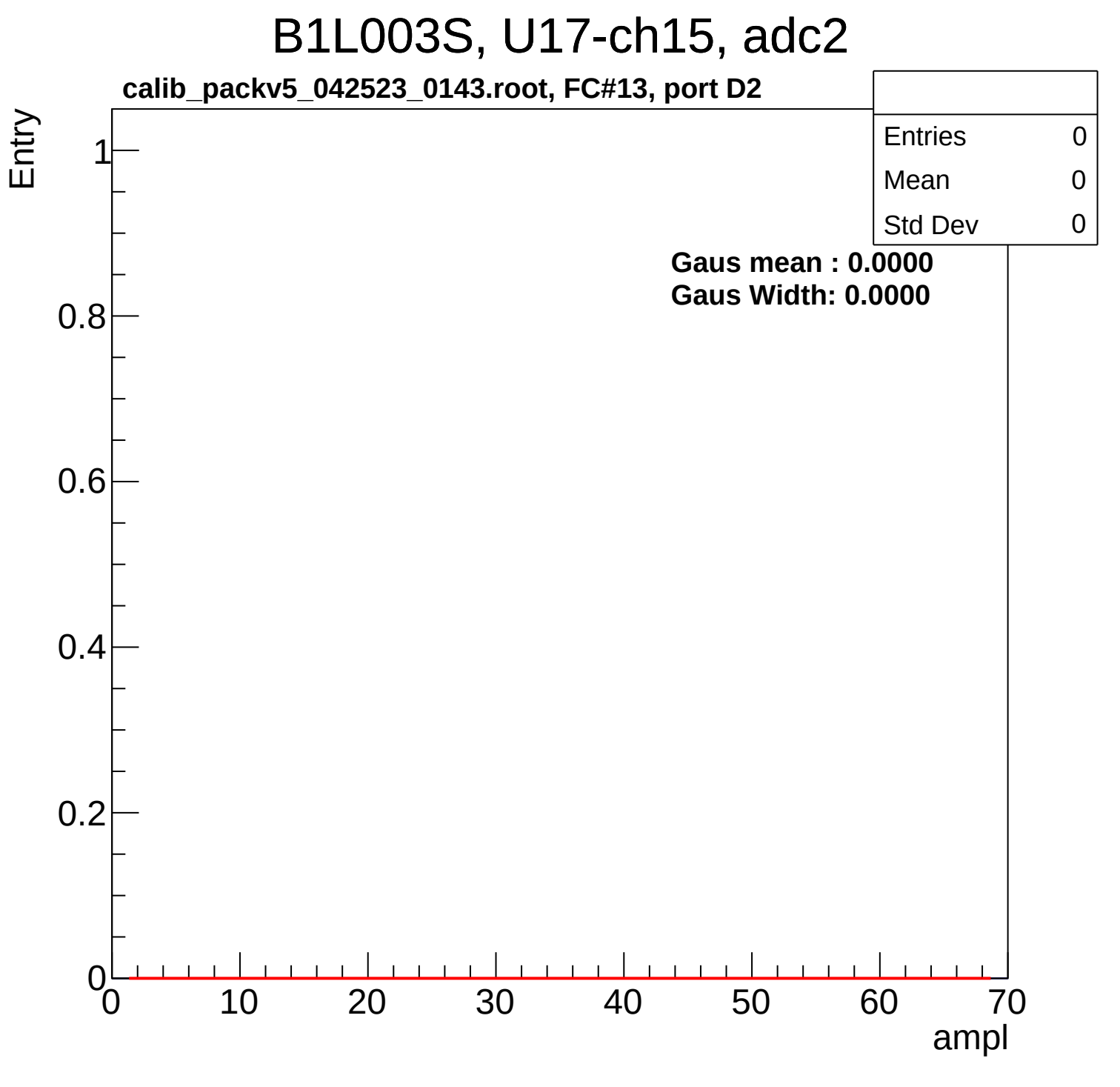
B1L003S, U17-ch15, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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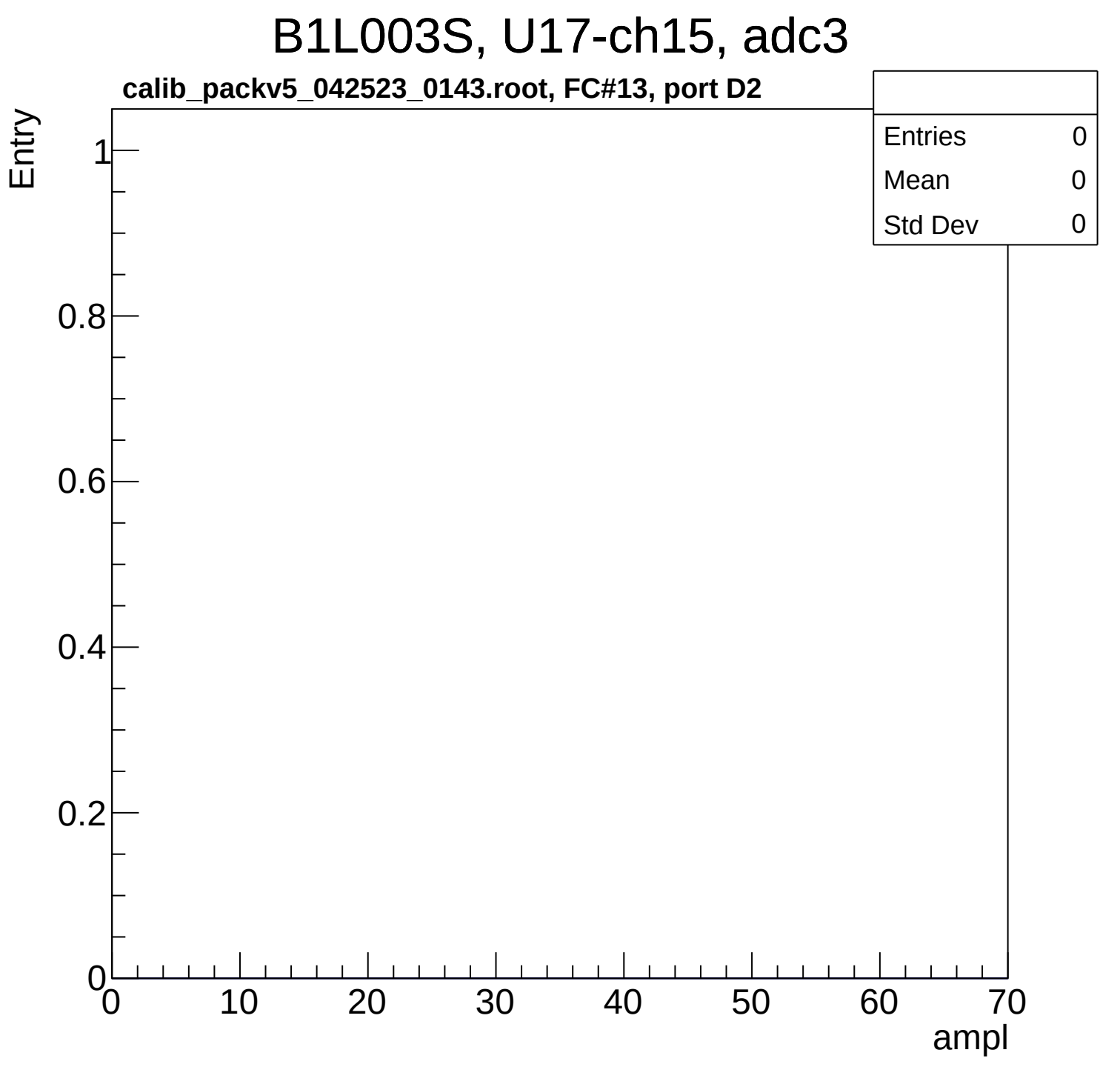
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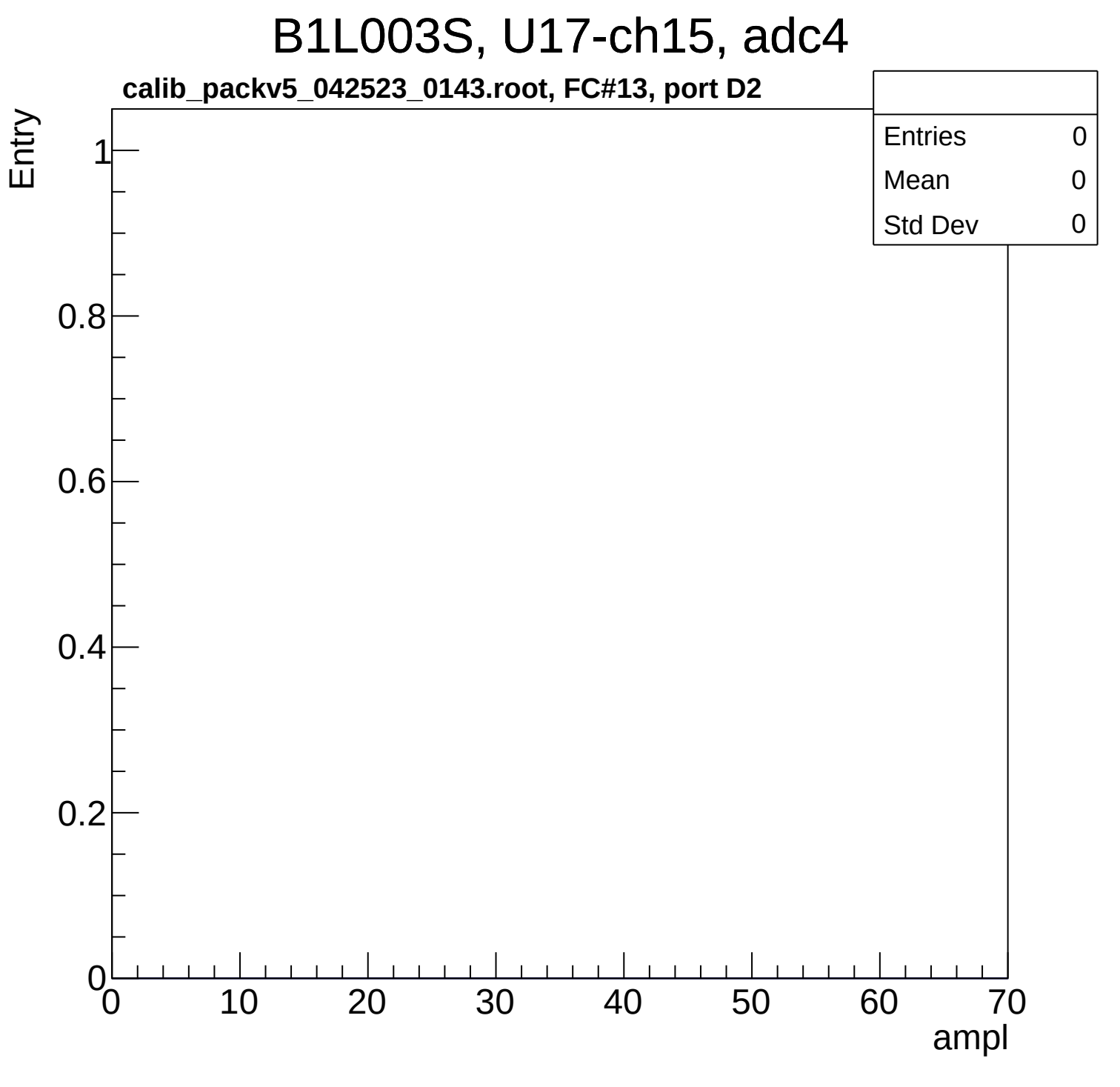
B1L003S, U17-ch15, adc4
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Entries 0
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Std Dev 0
Entry
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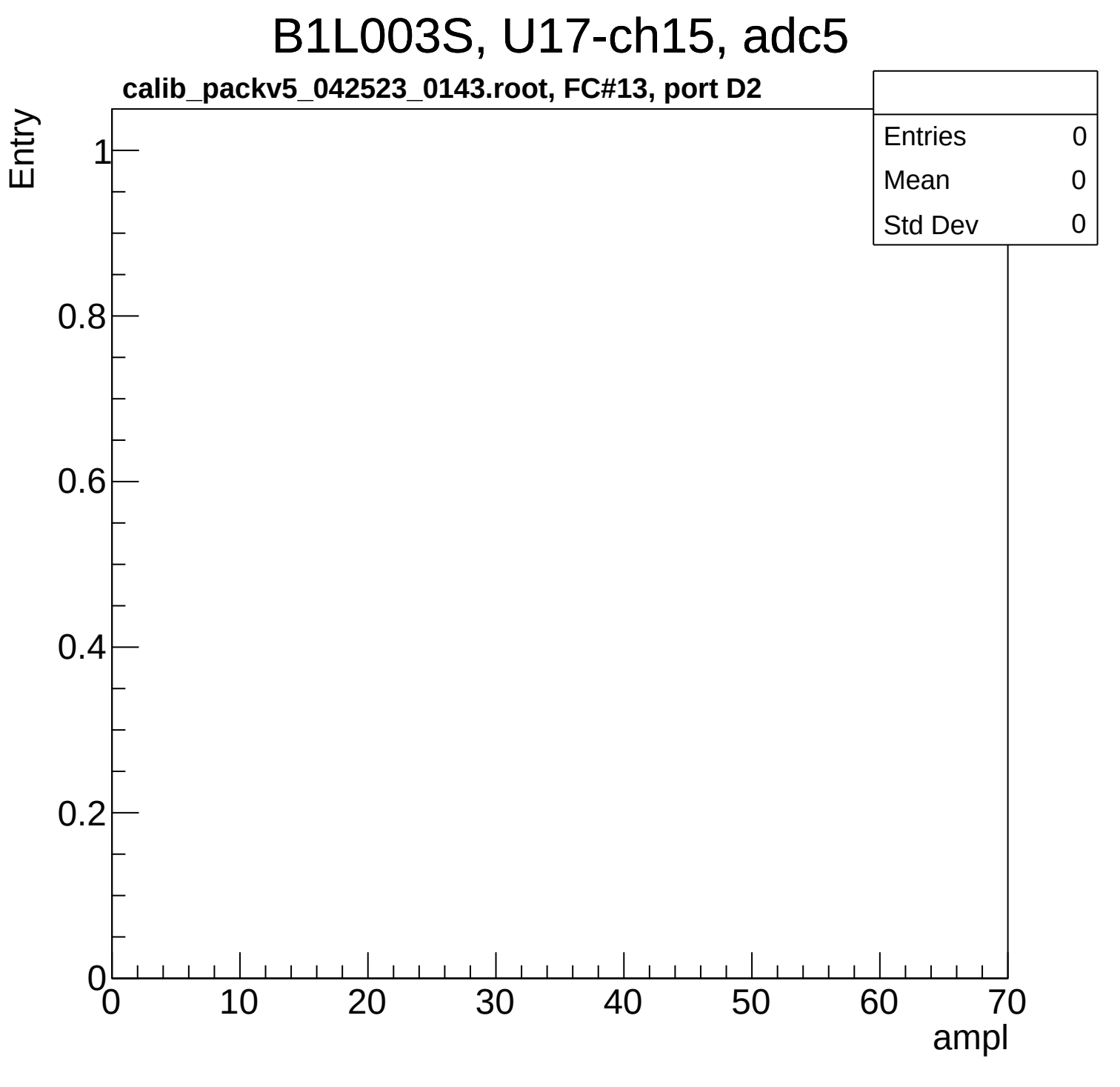
ampl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Std Dev 0
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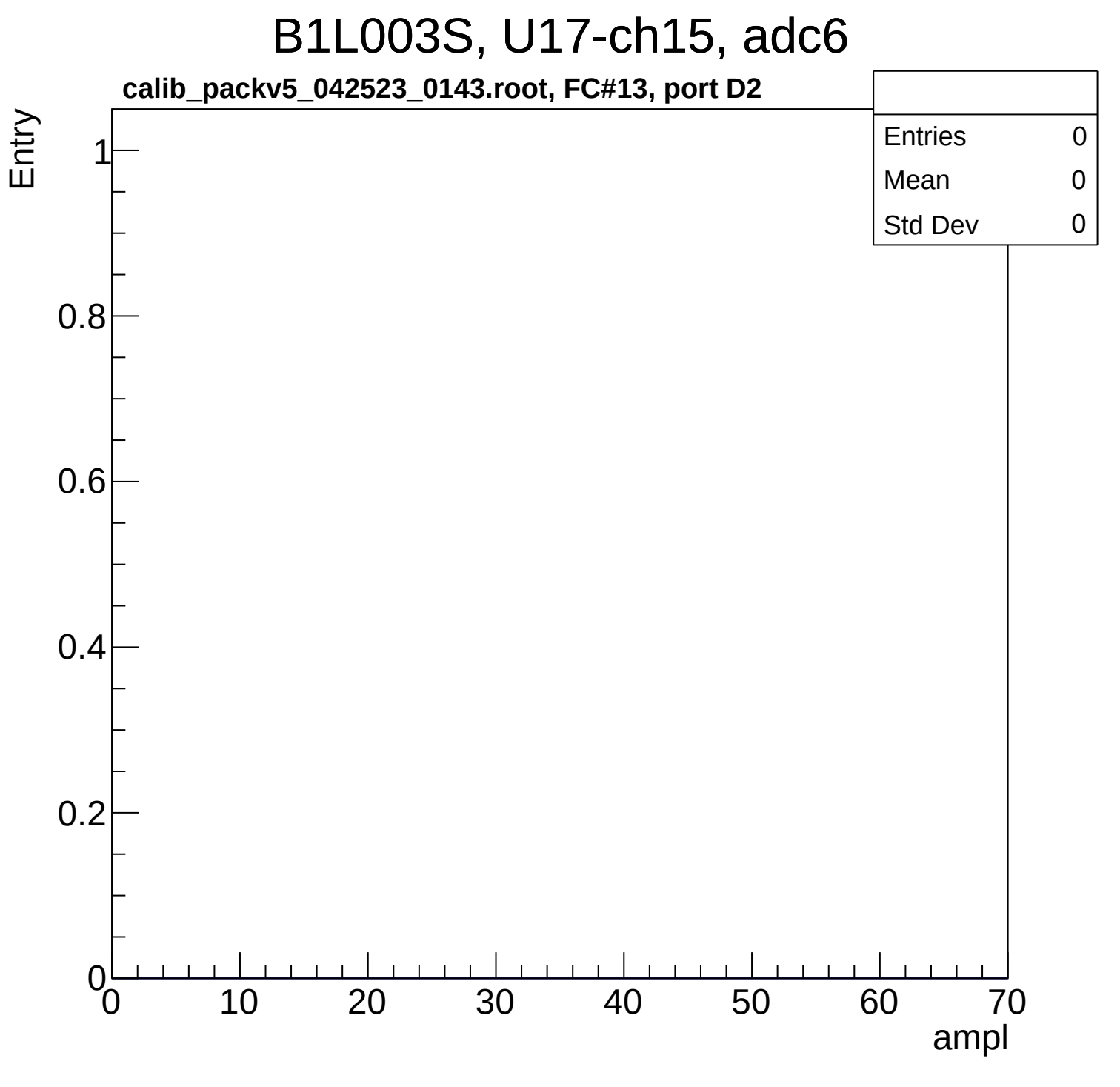
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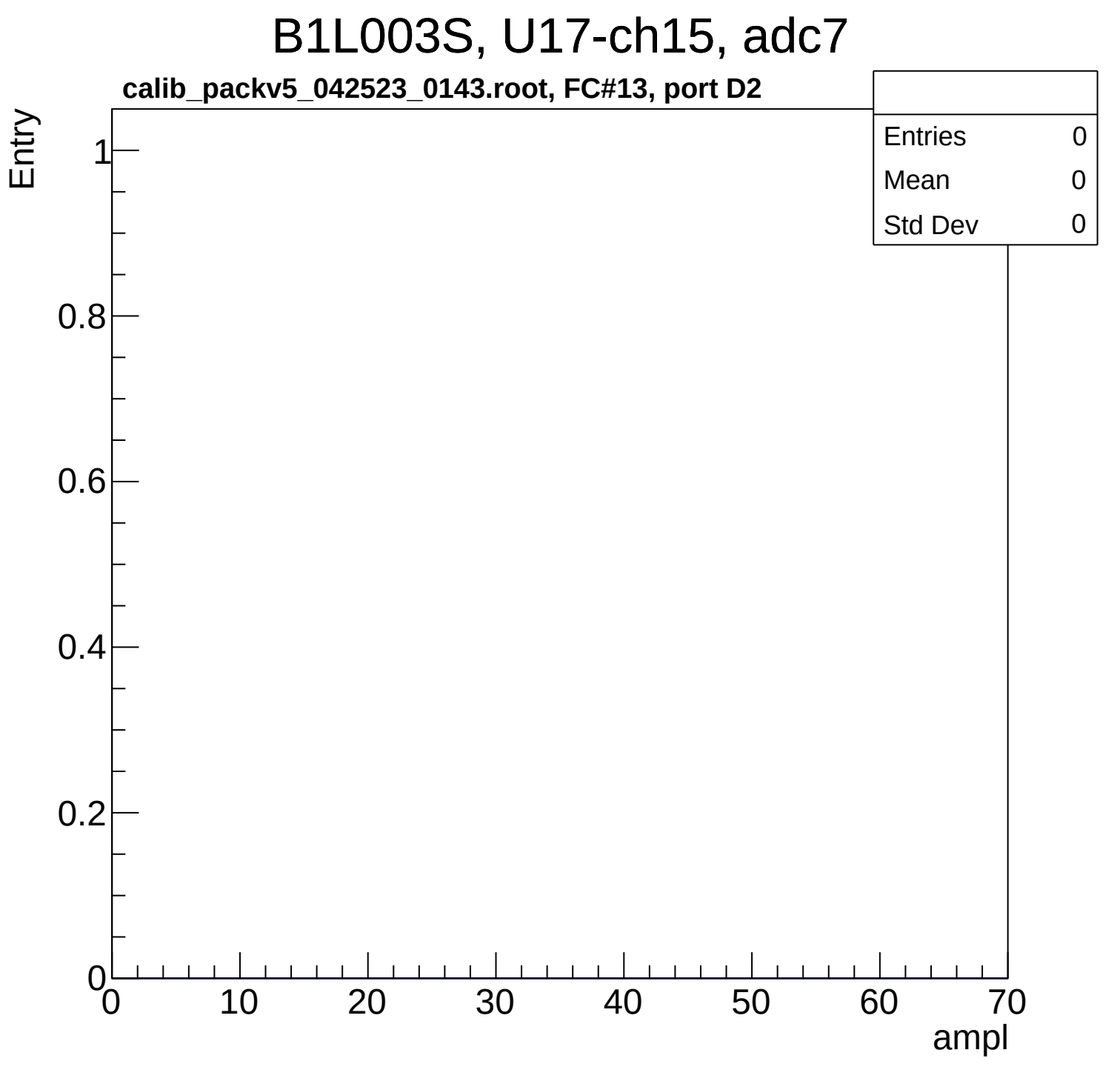
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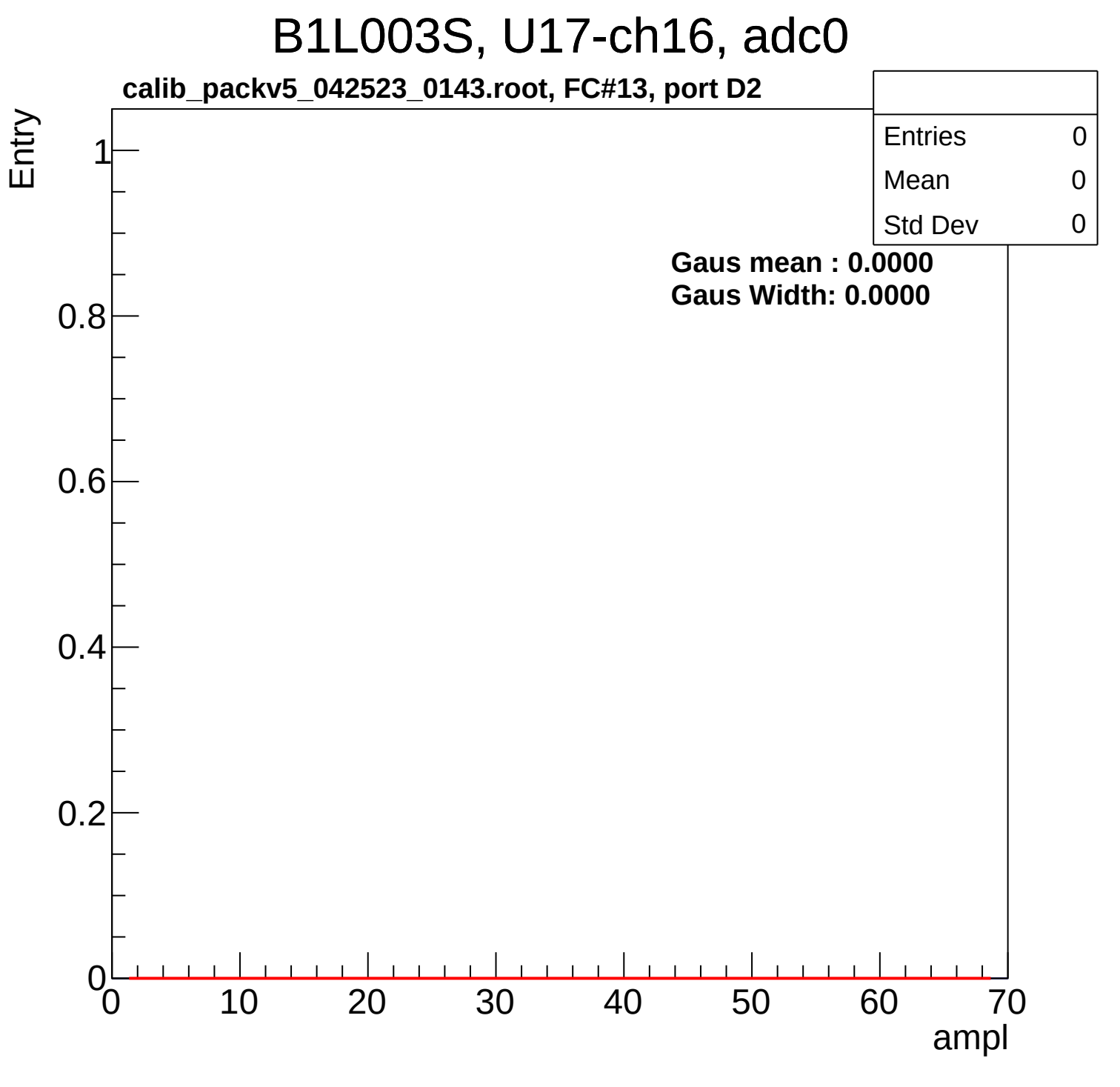
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Std Dev 0
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Gaus Width: 0.0000
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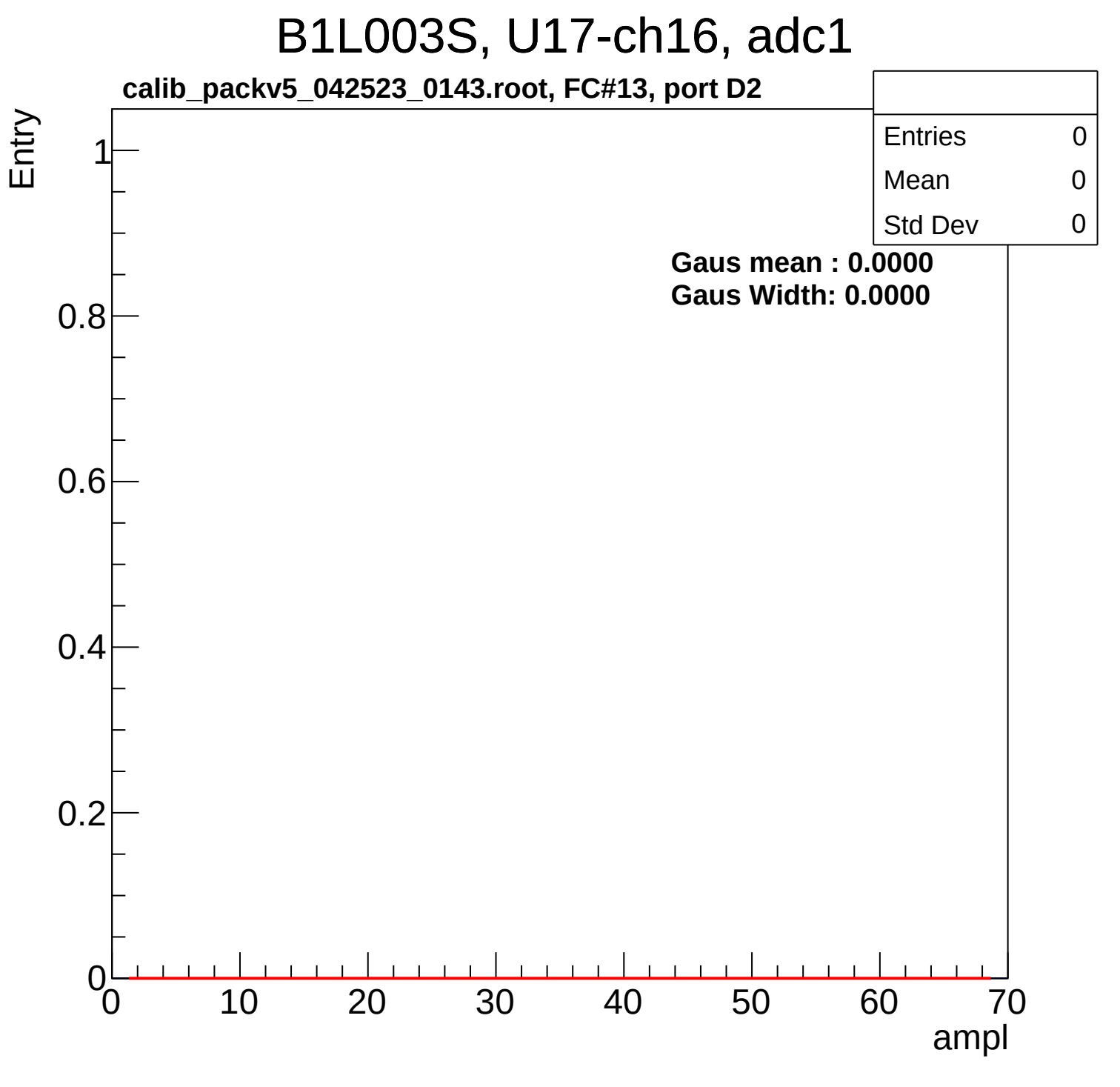
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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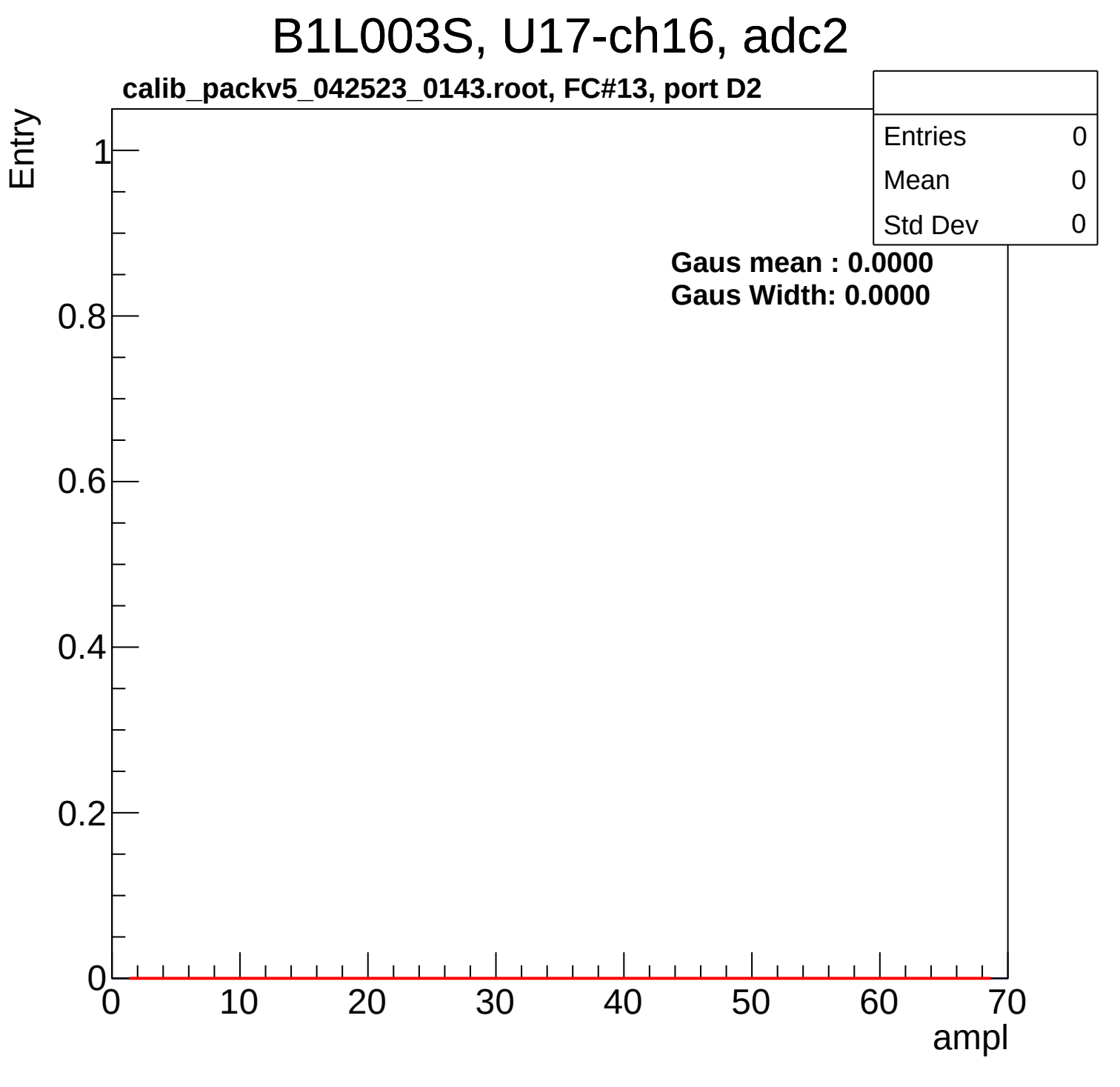
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Gaus Width: 0.0000
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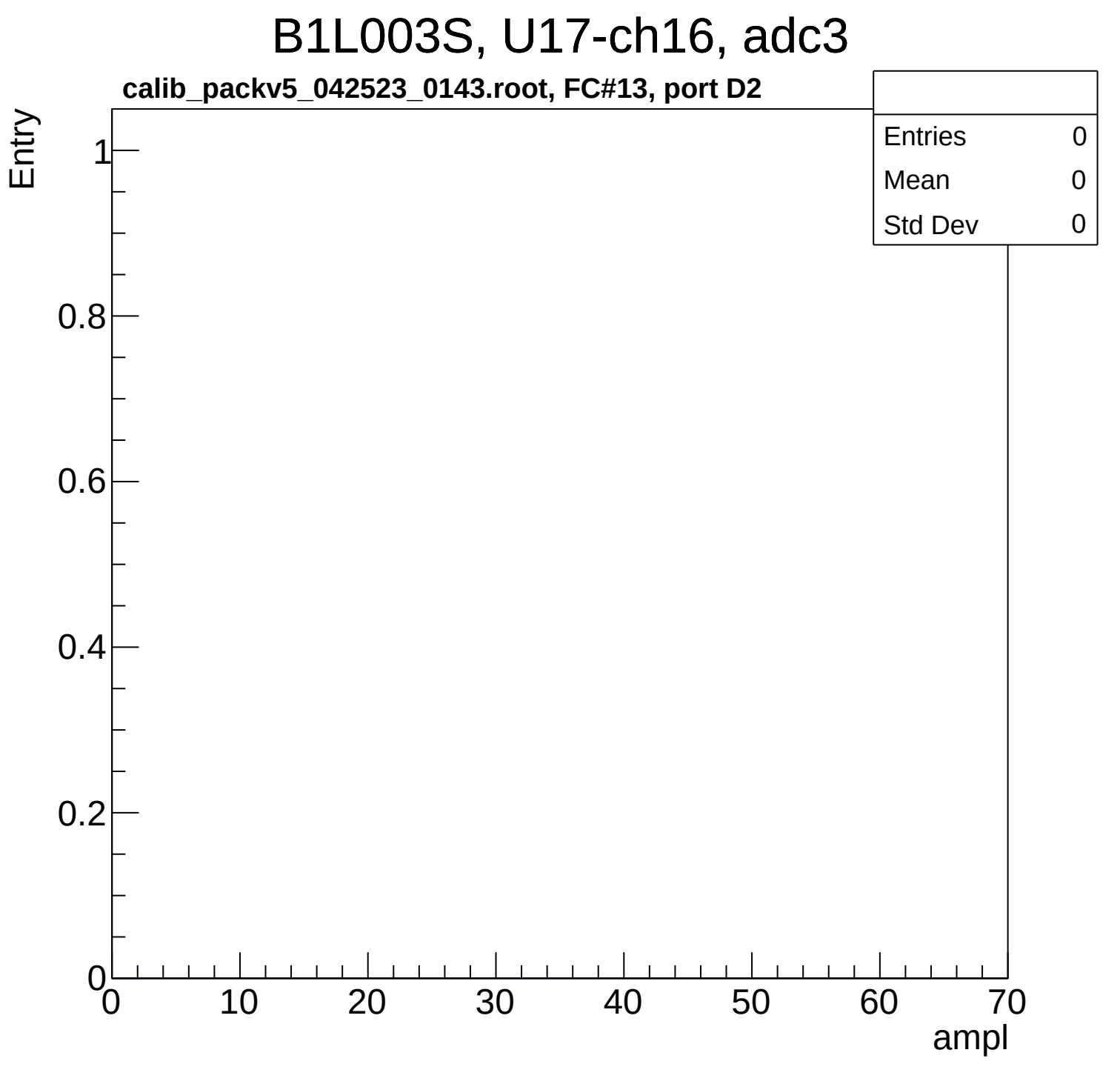
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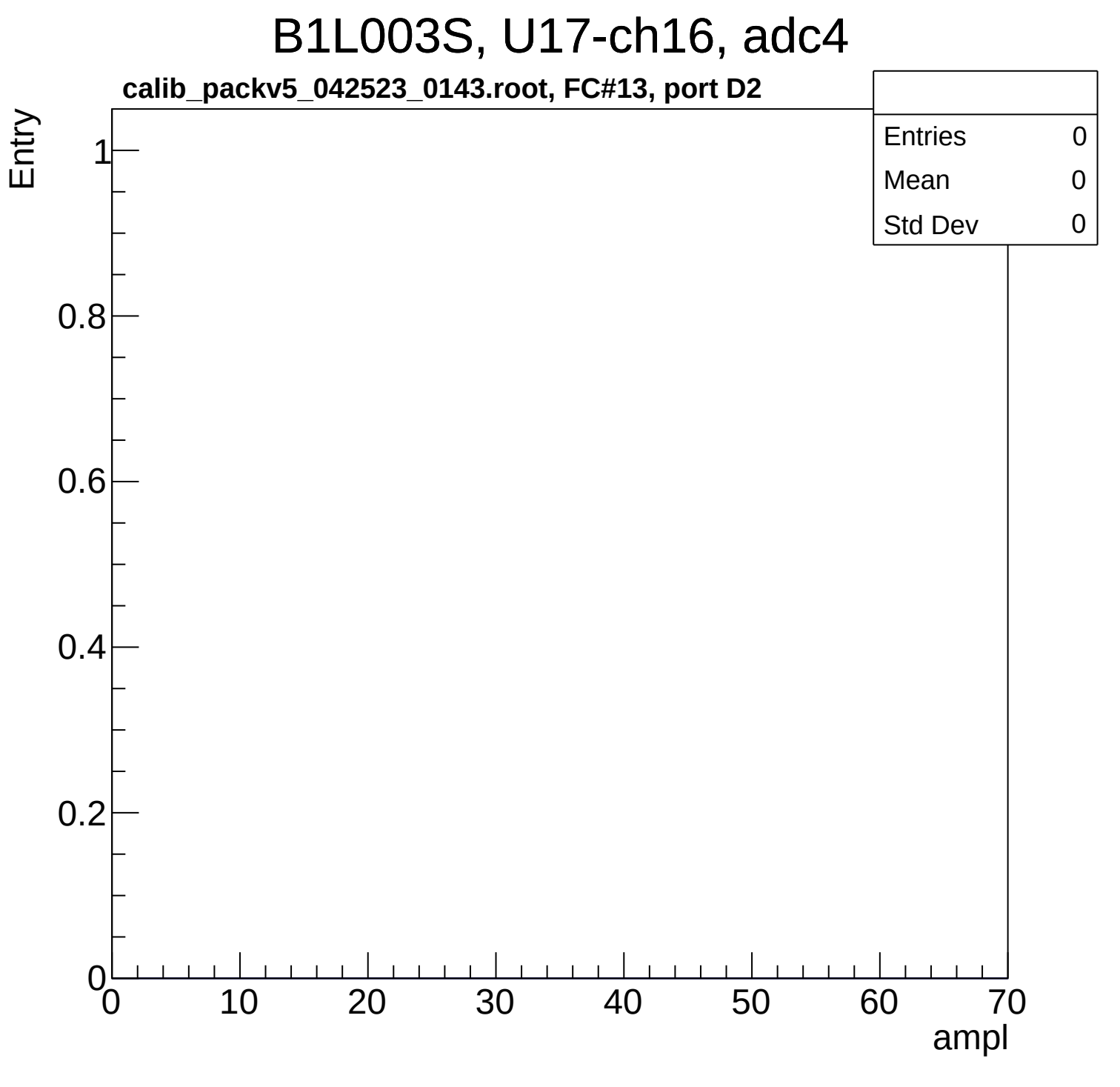
ampl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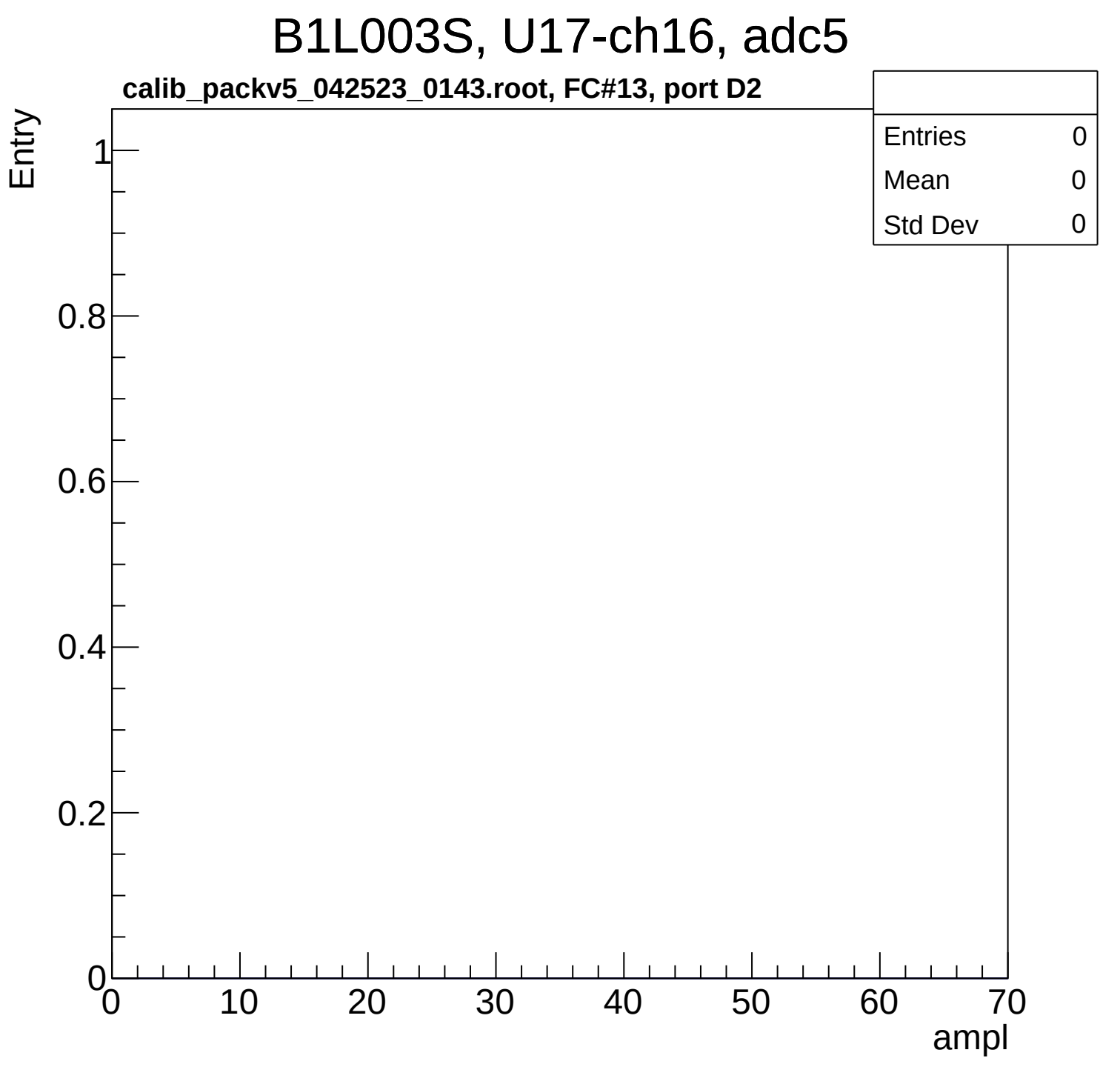
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calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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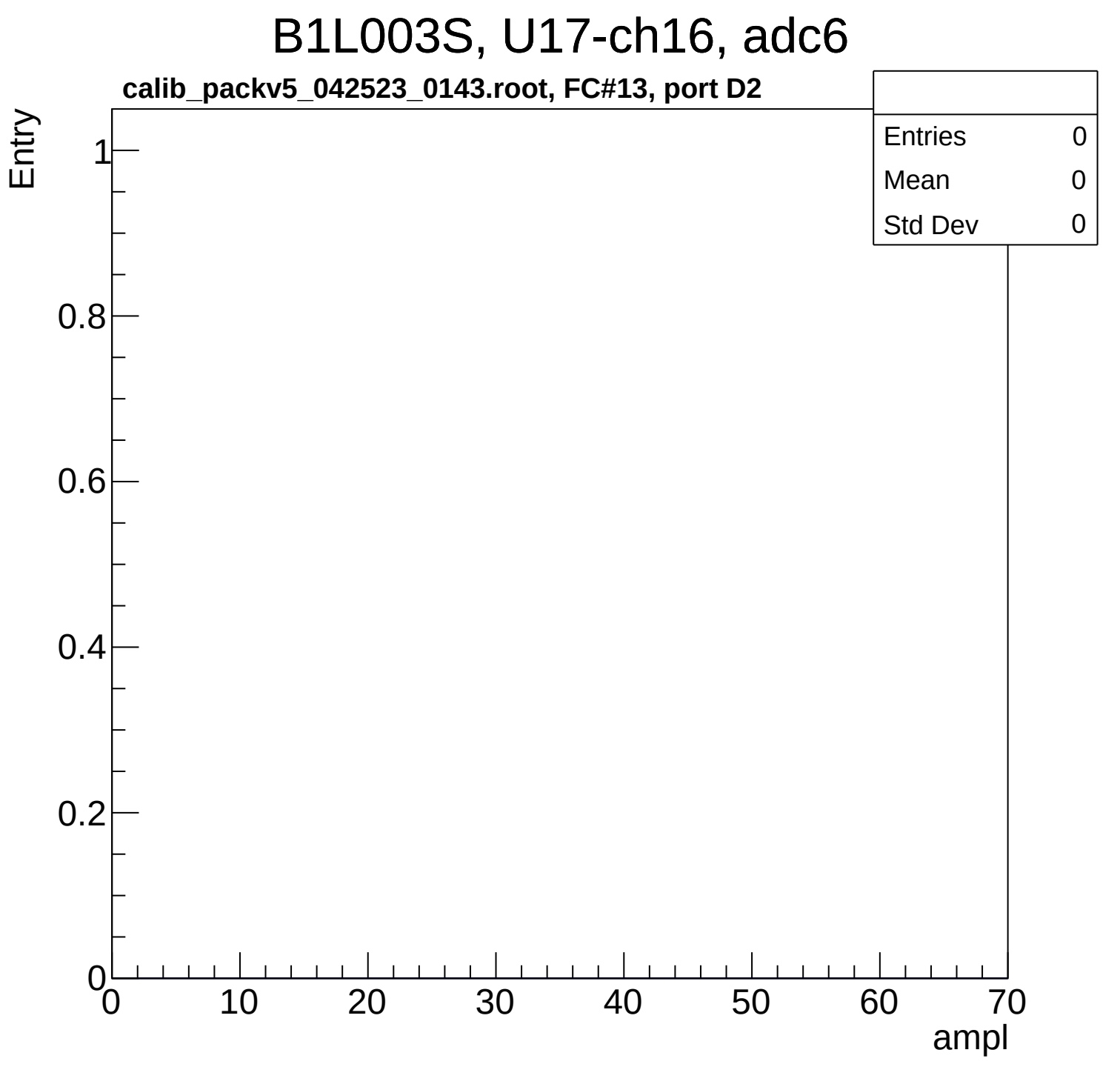
B1L003S, U17-ch16, adc6
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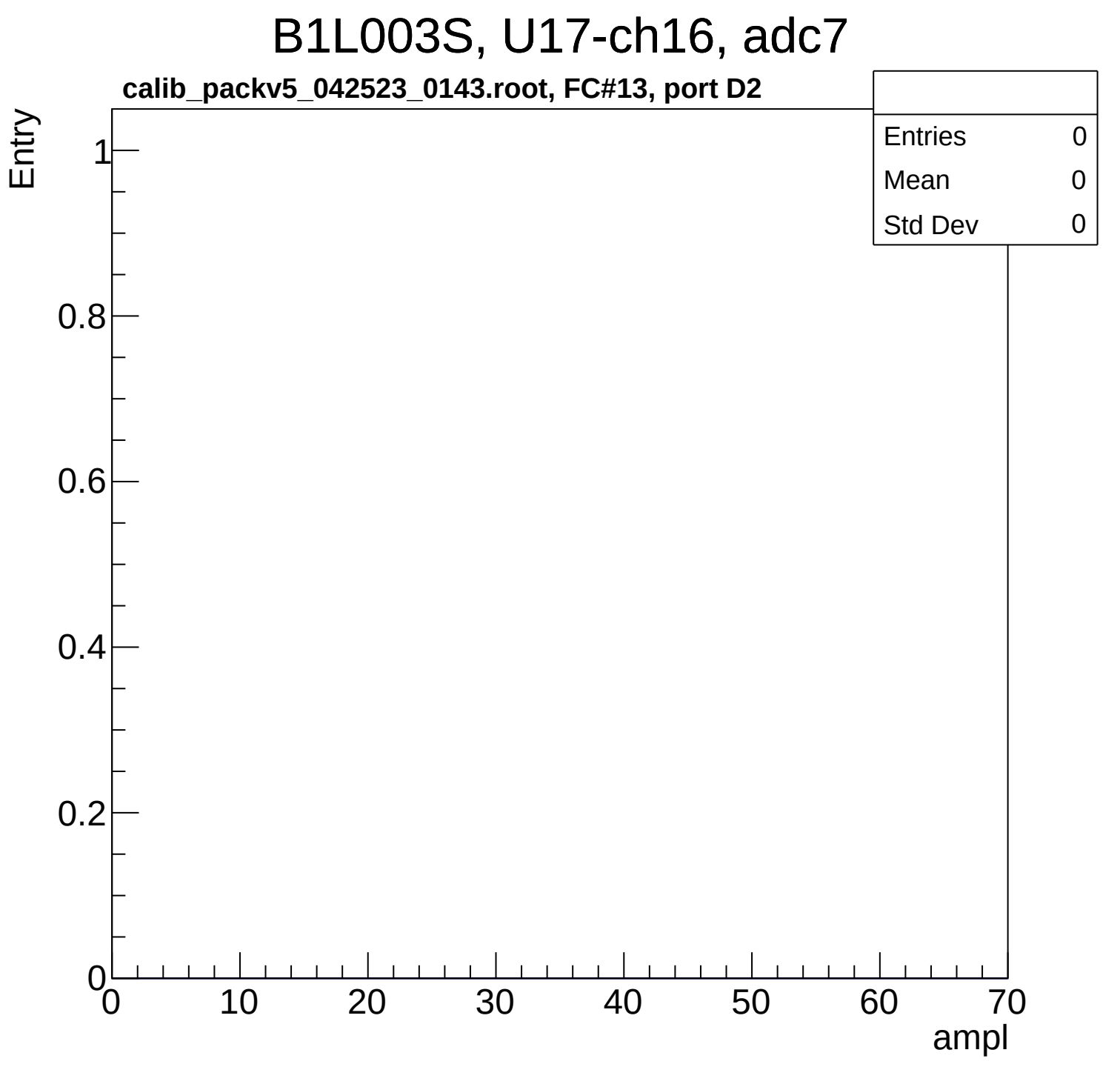
ampl


B1L003S, U17-ch16, adc7
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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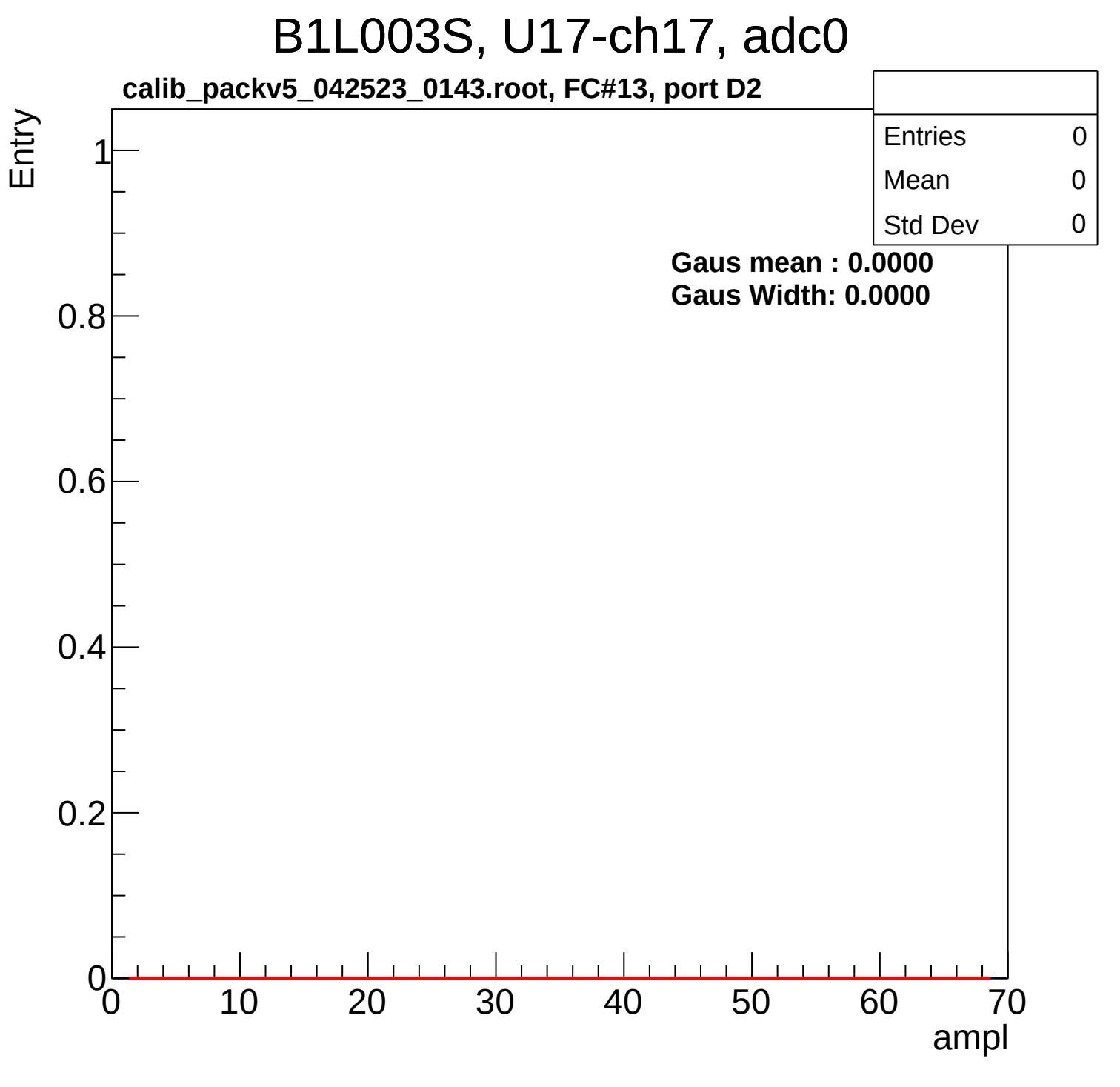
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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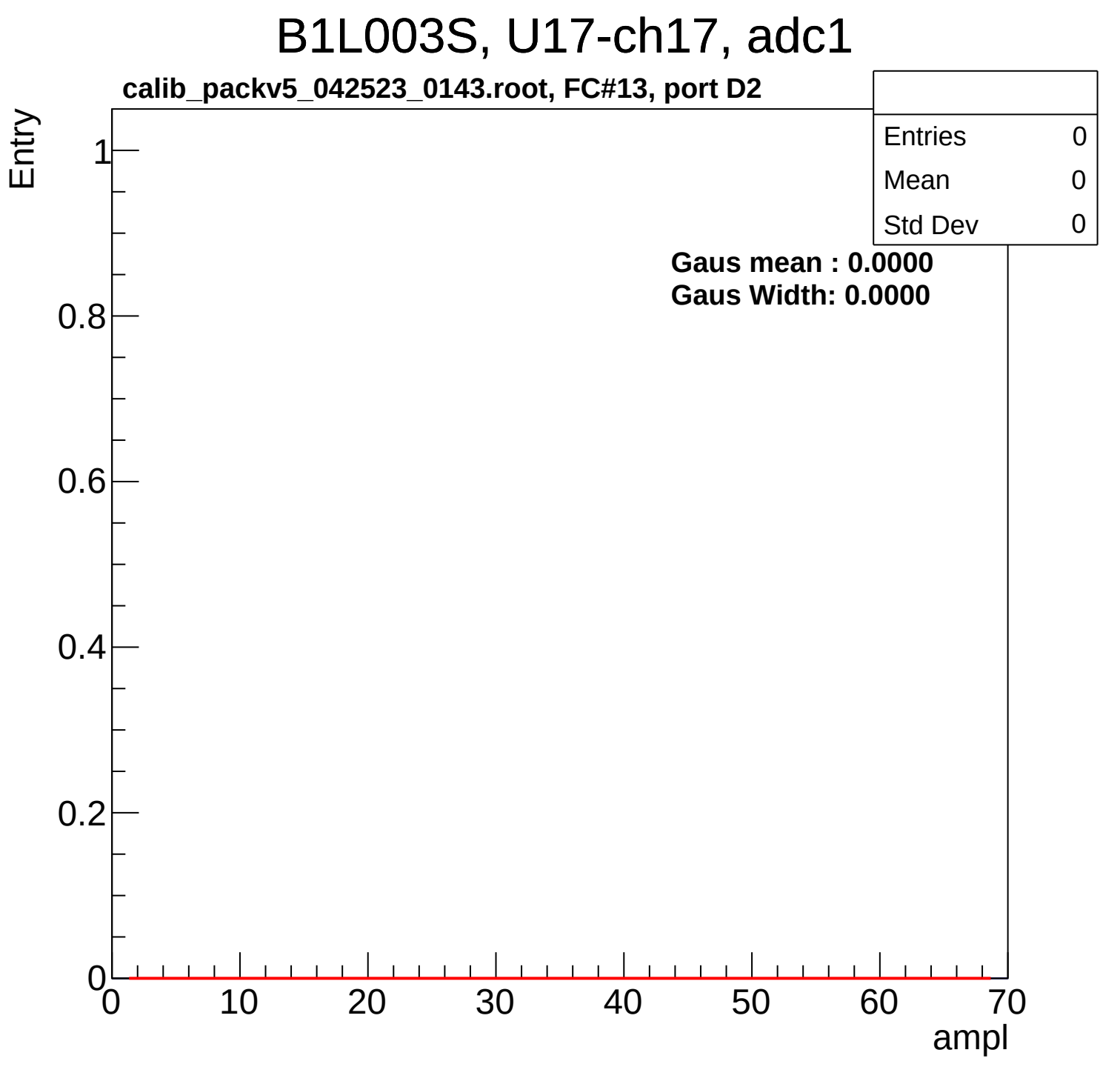
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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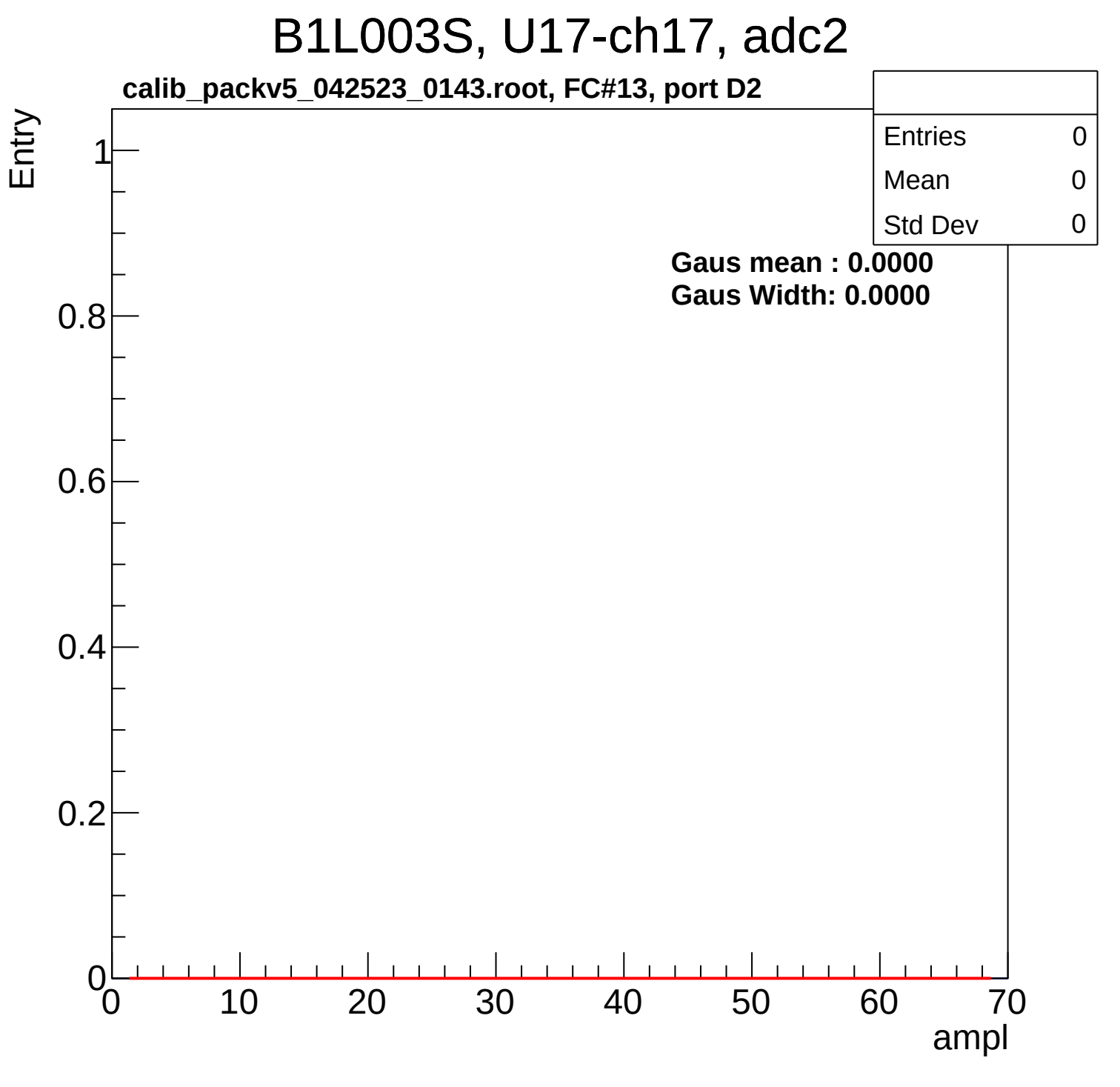
ampl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Gaus Width: 0.0000
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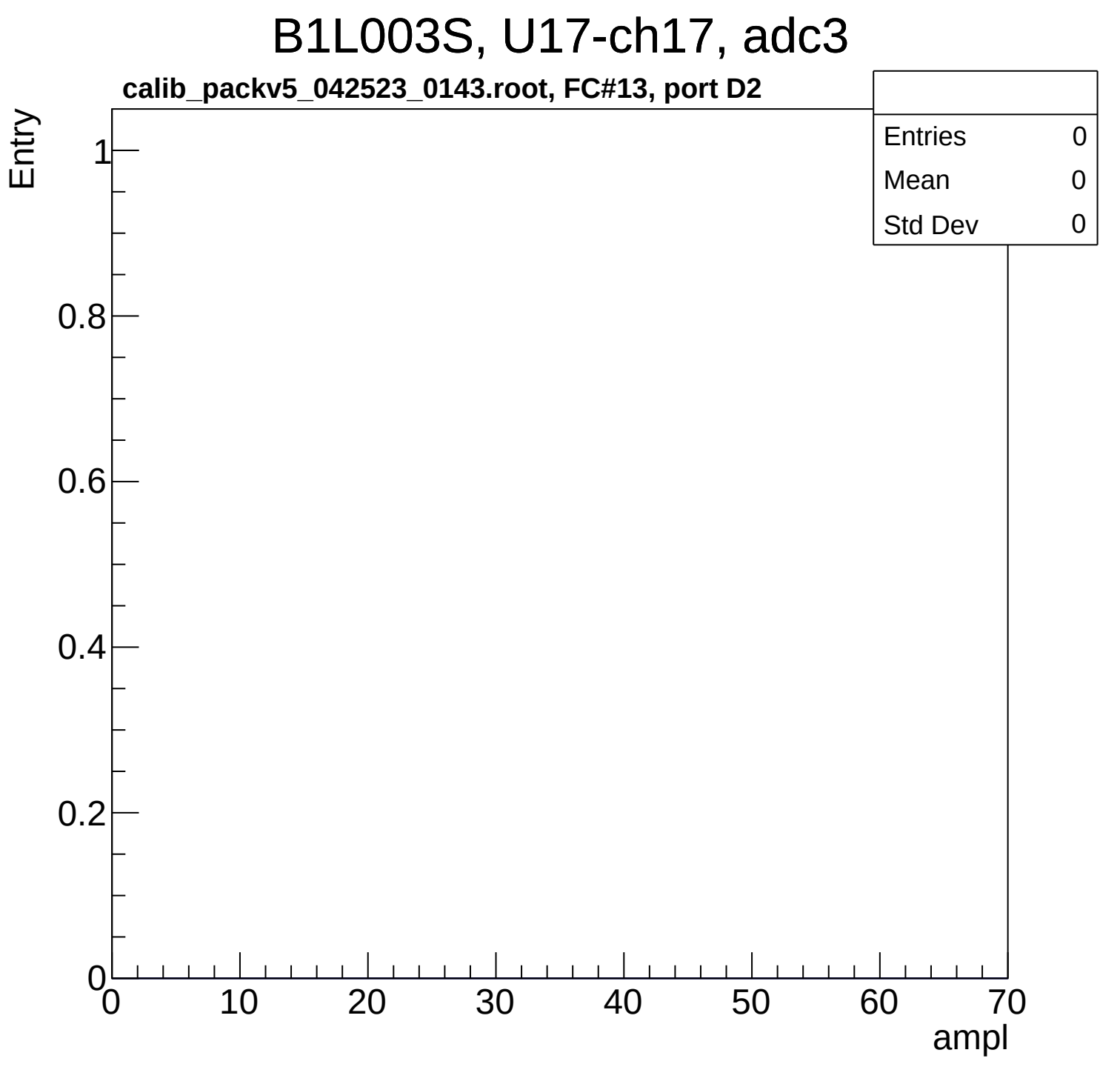
ampl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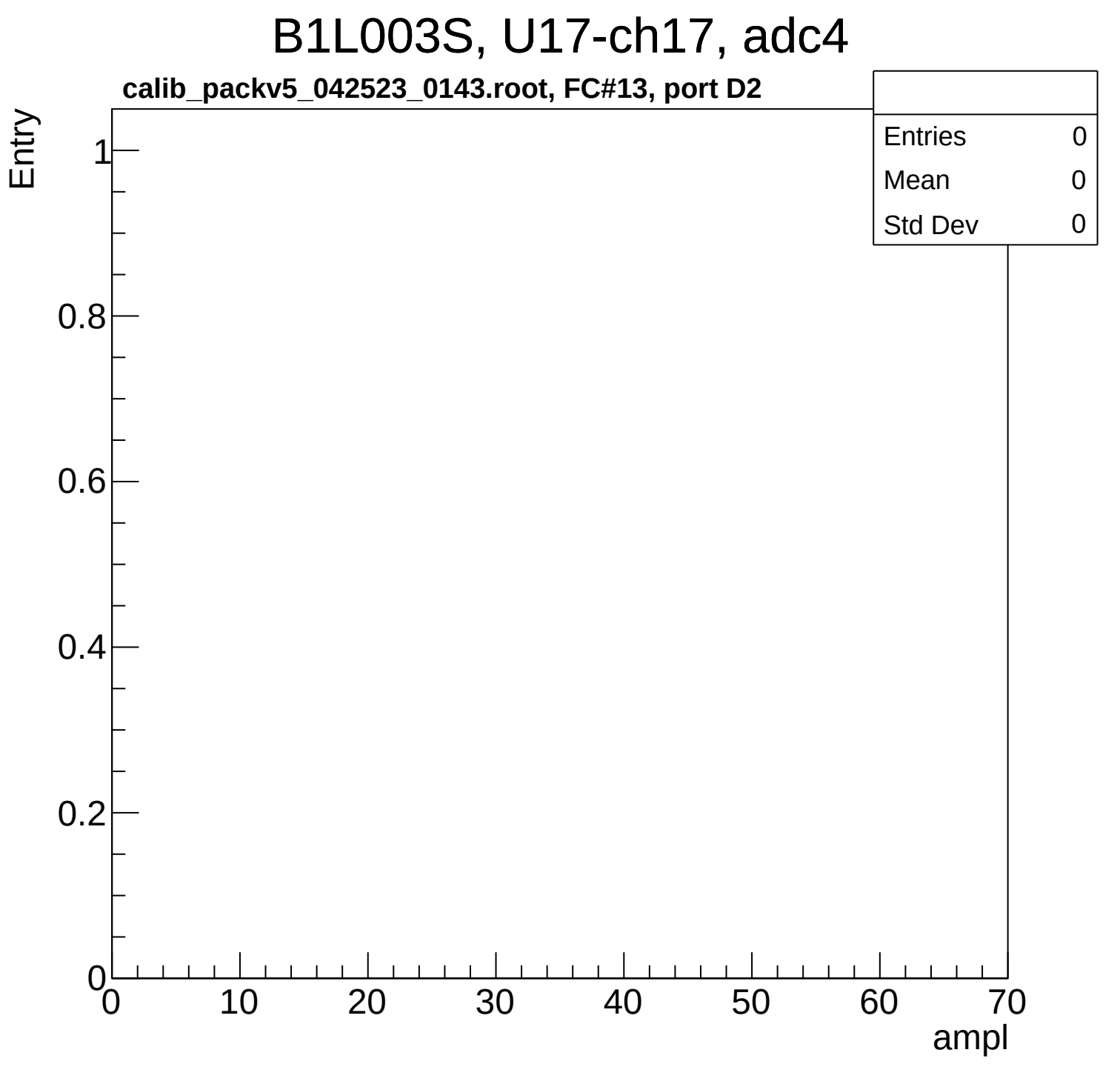
ampl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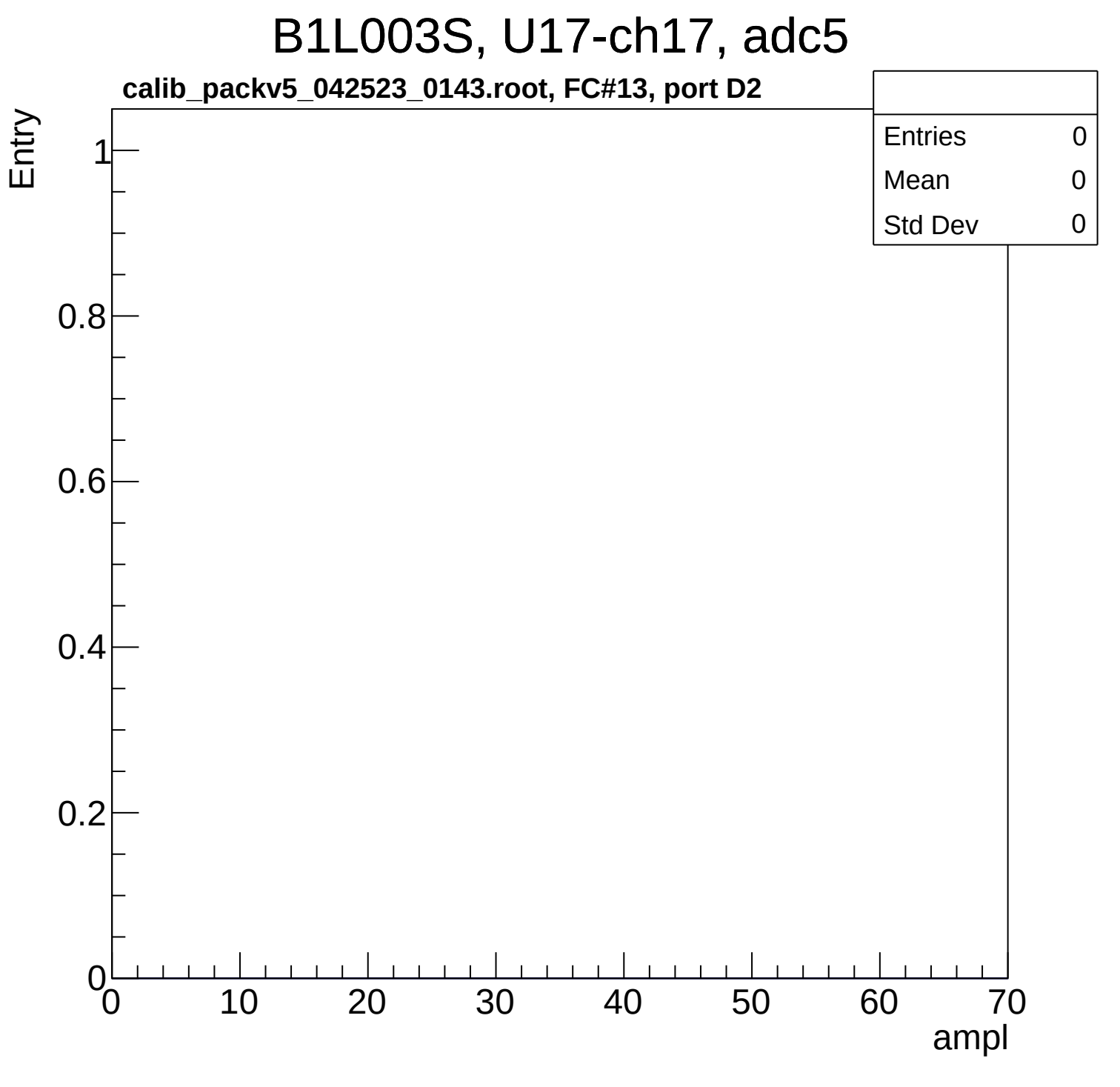
ampl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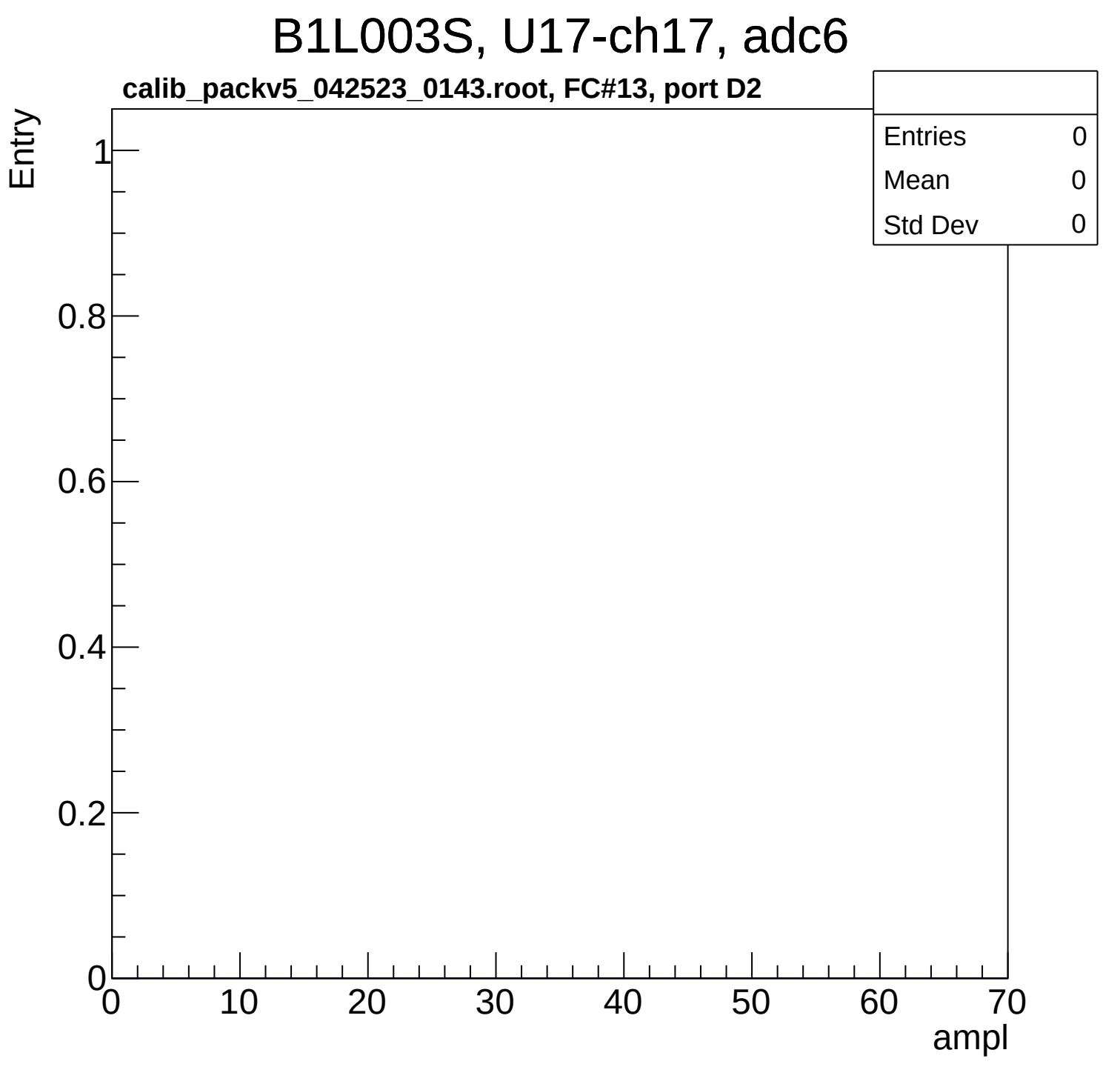
ampl


B1L003S, U17-ch17, adc6
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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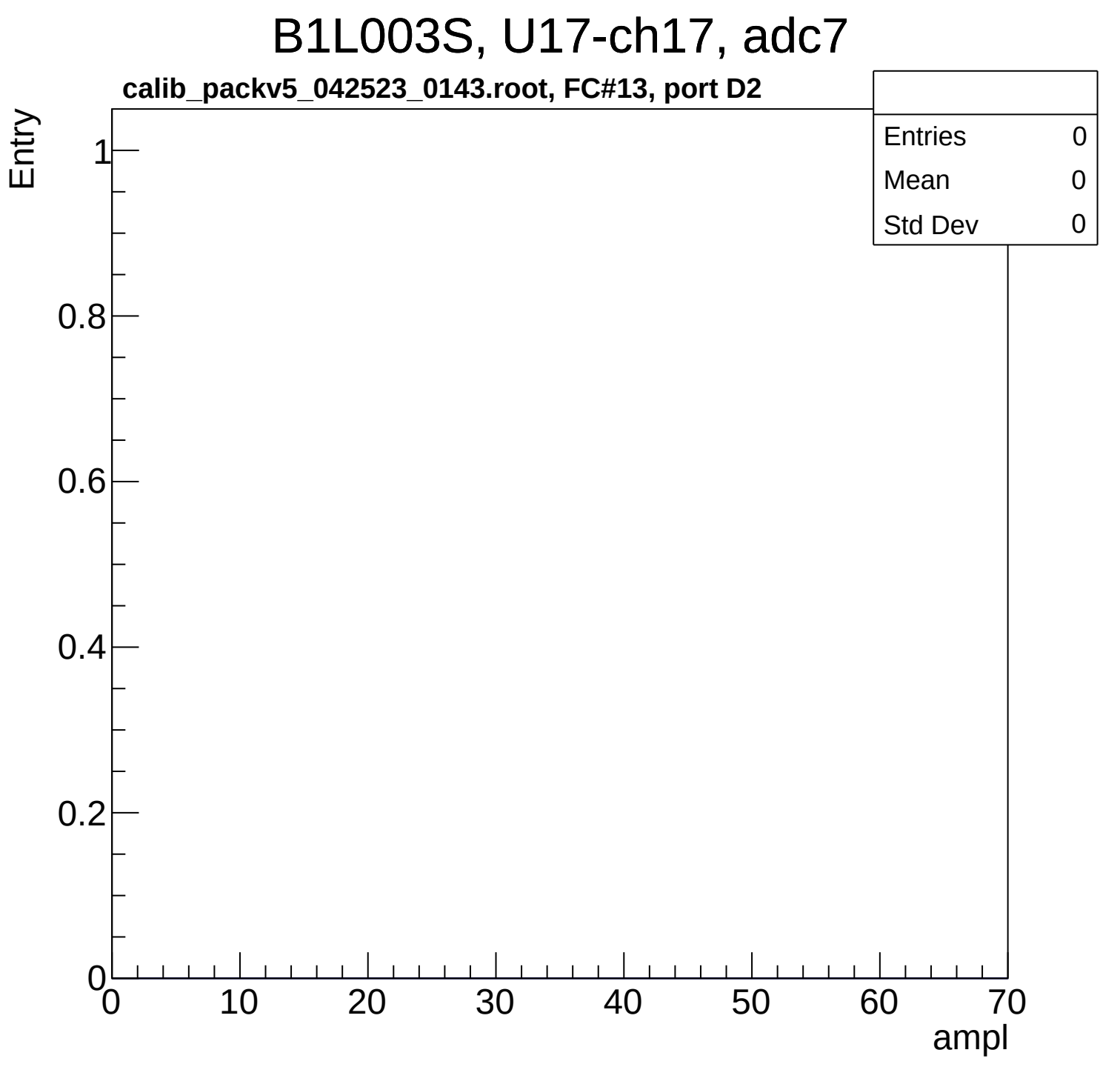
ampl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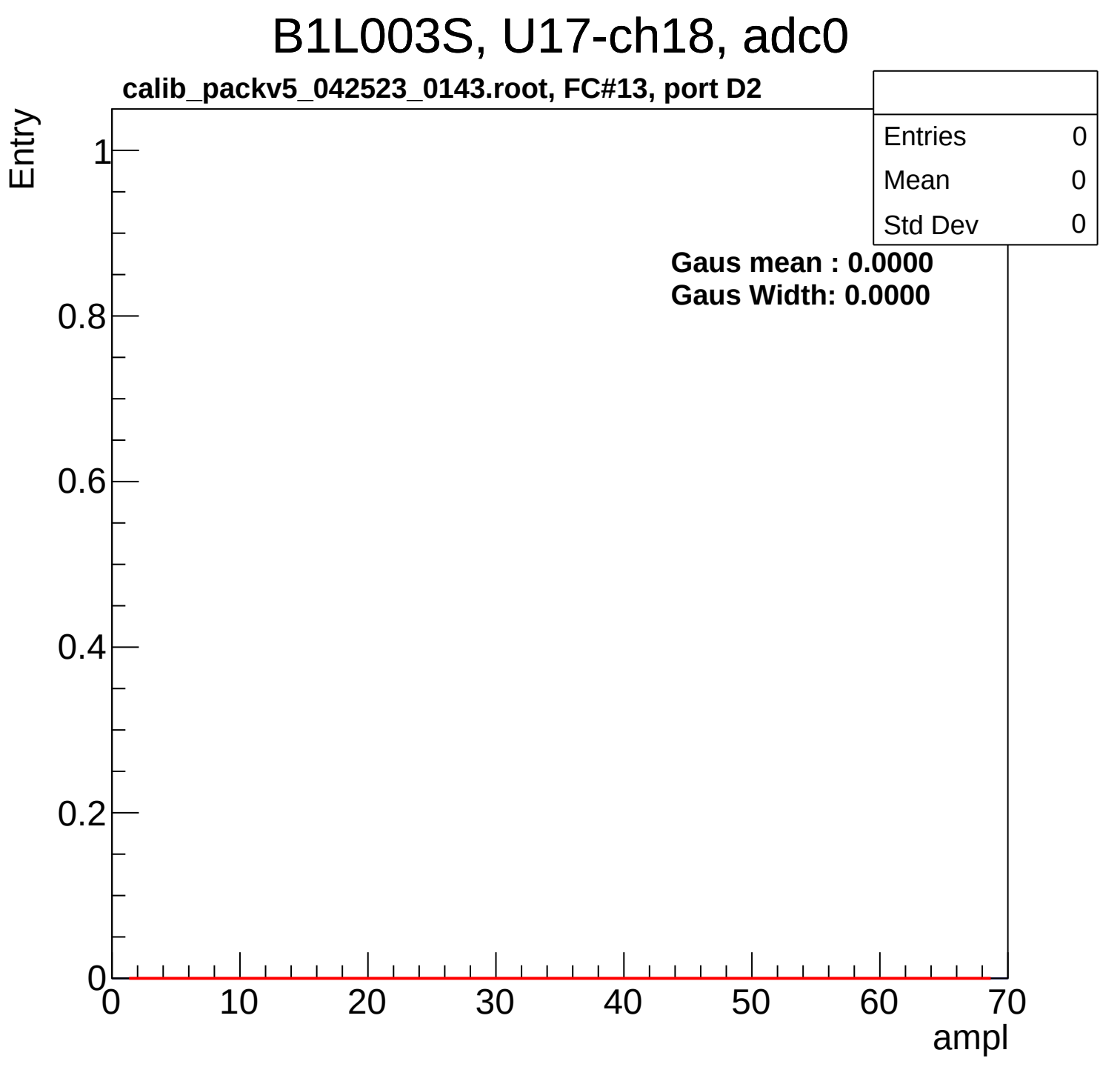
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Std Dev 0
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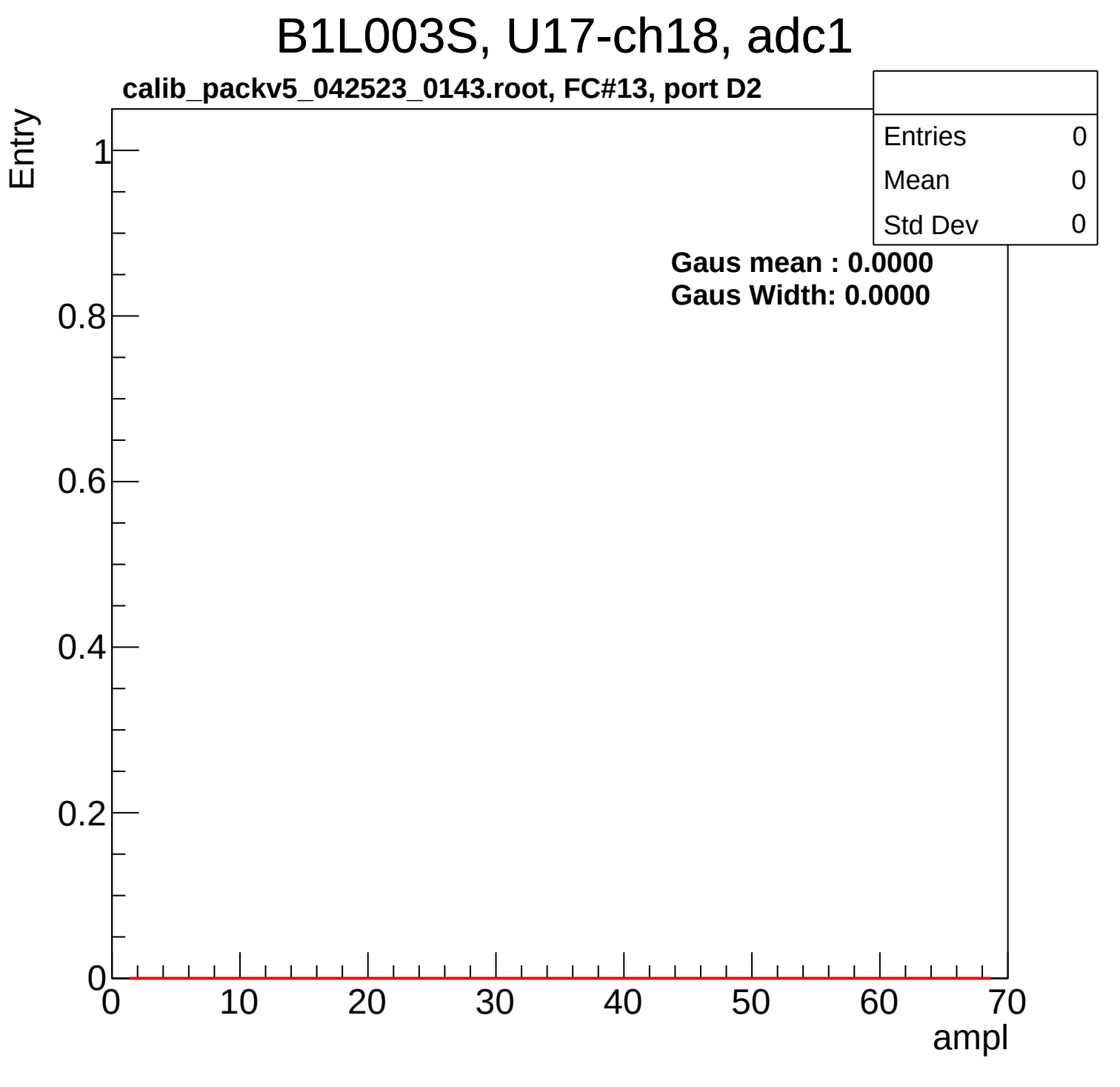
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Gaus mean : 0.0000
Gaus Width: 0.0000
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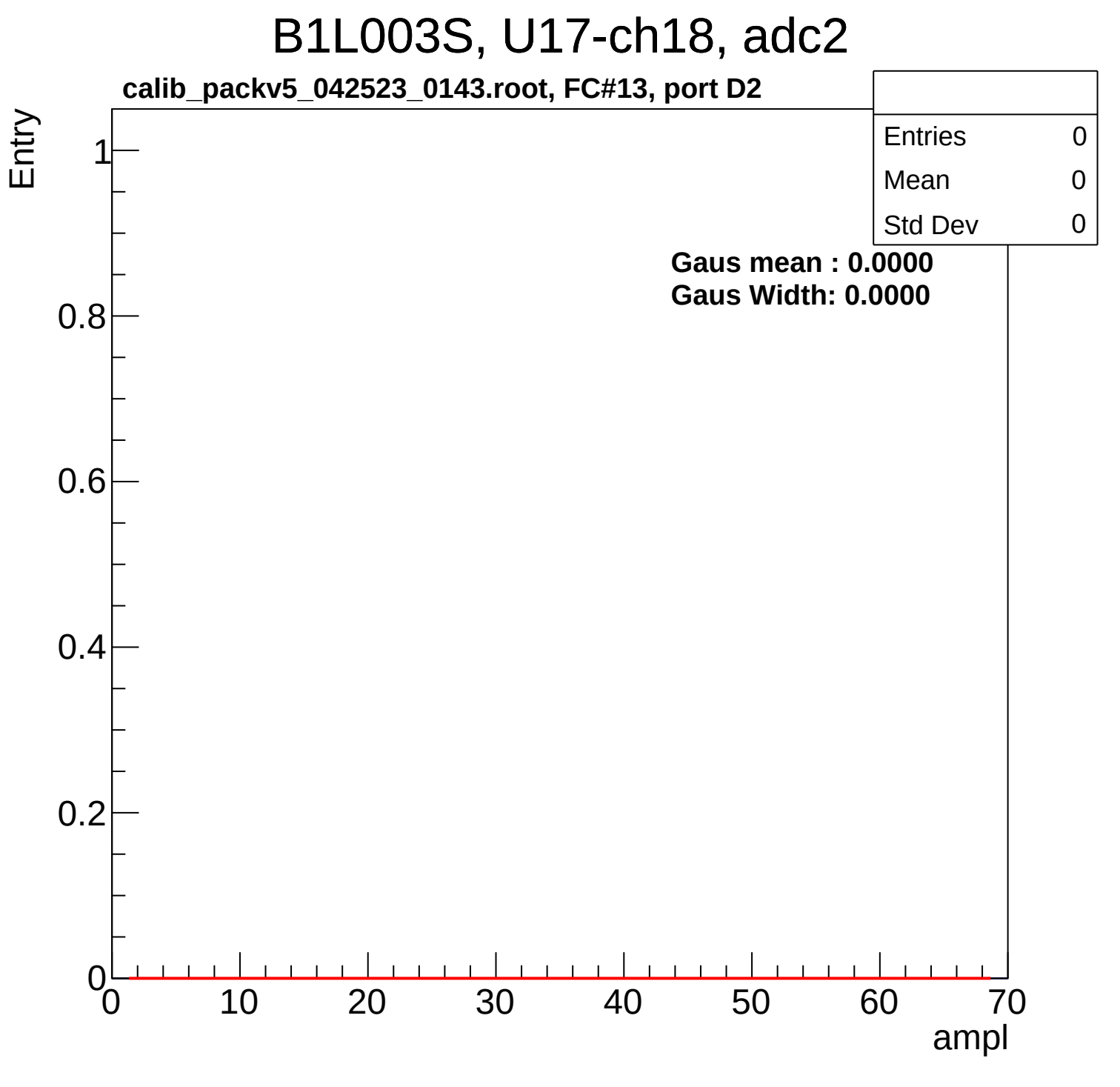
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Std Dev 0
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Gaus Width: 0.0000
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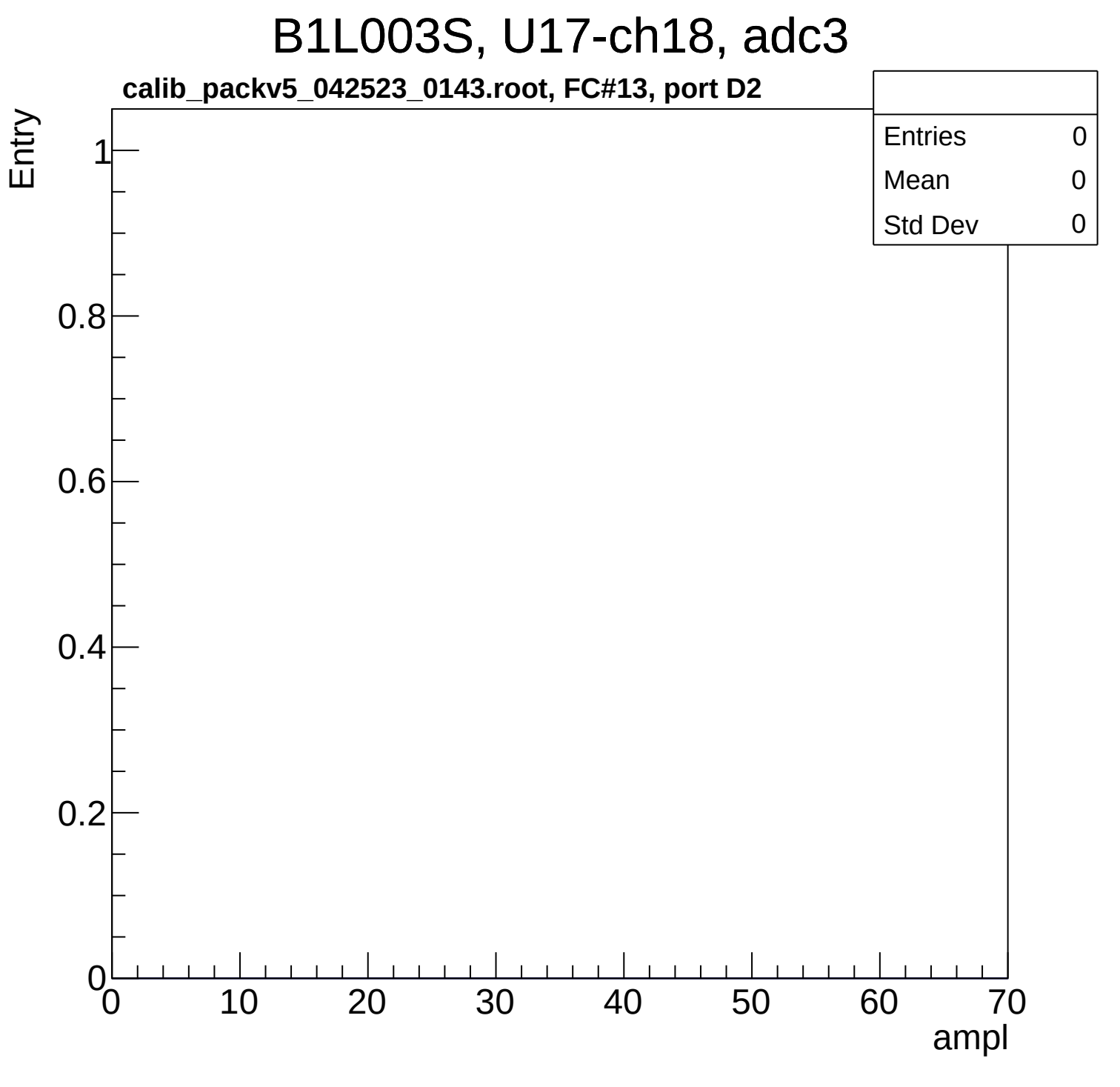
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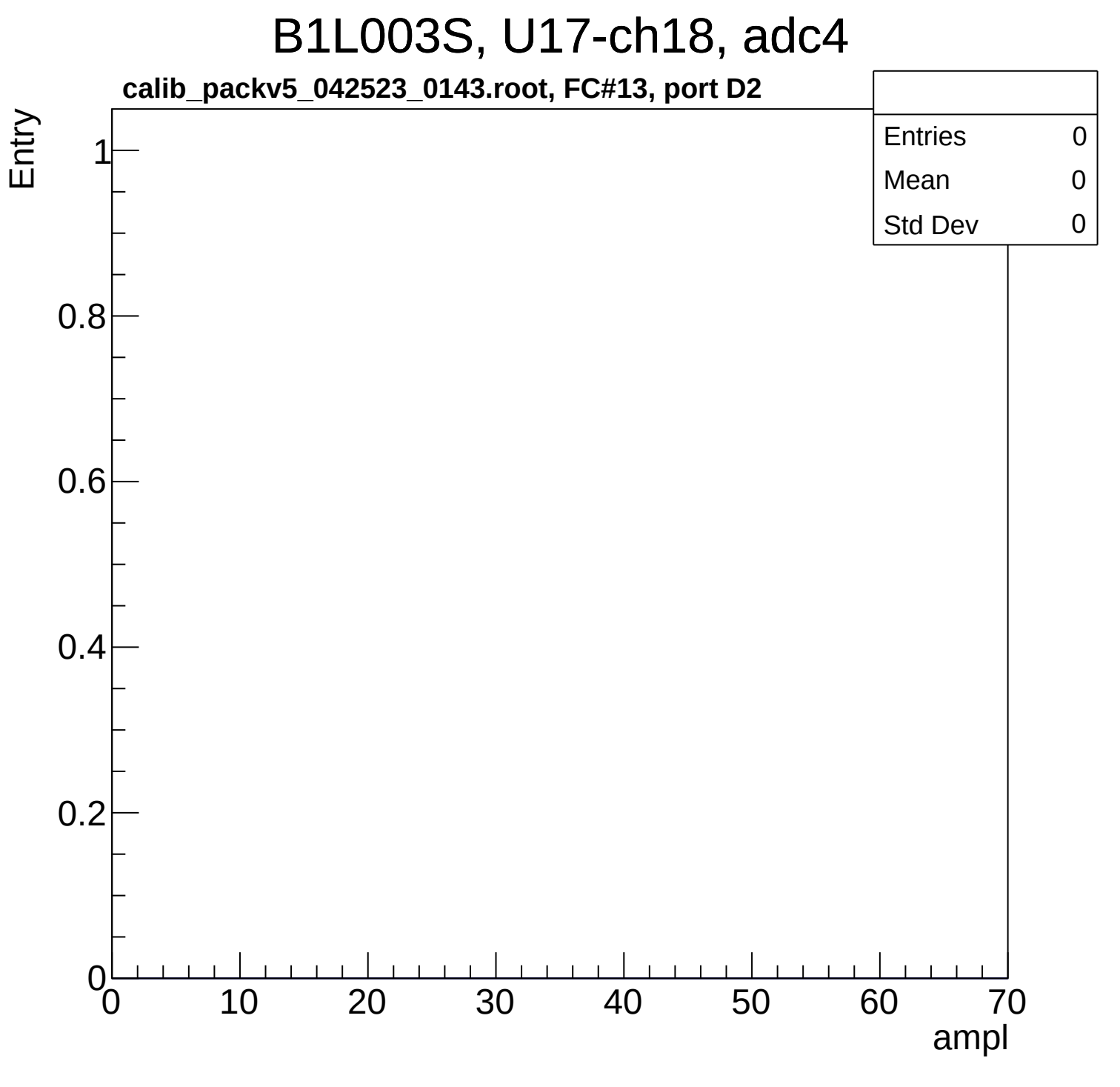
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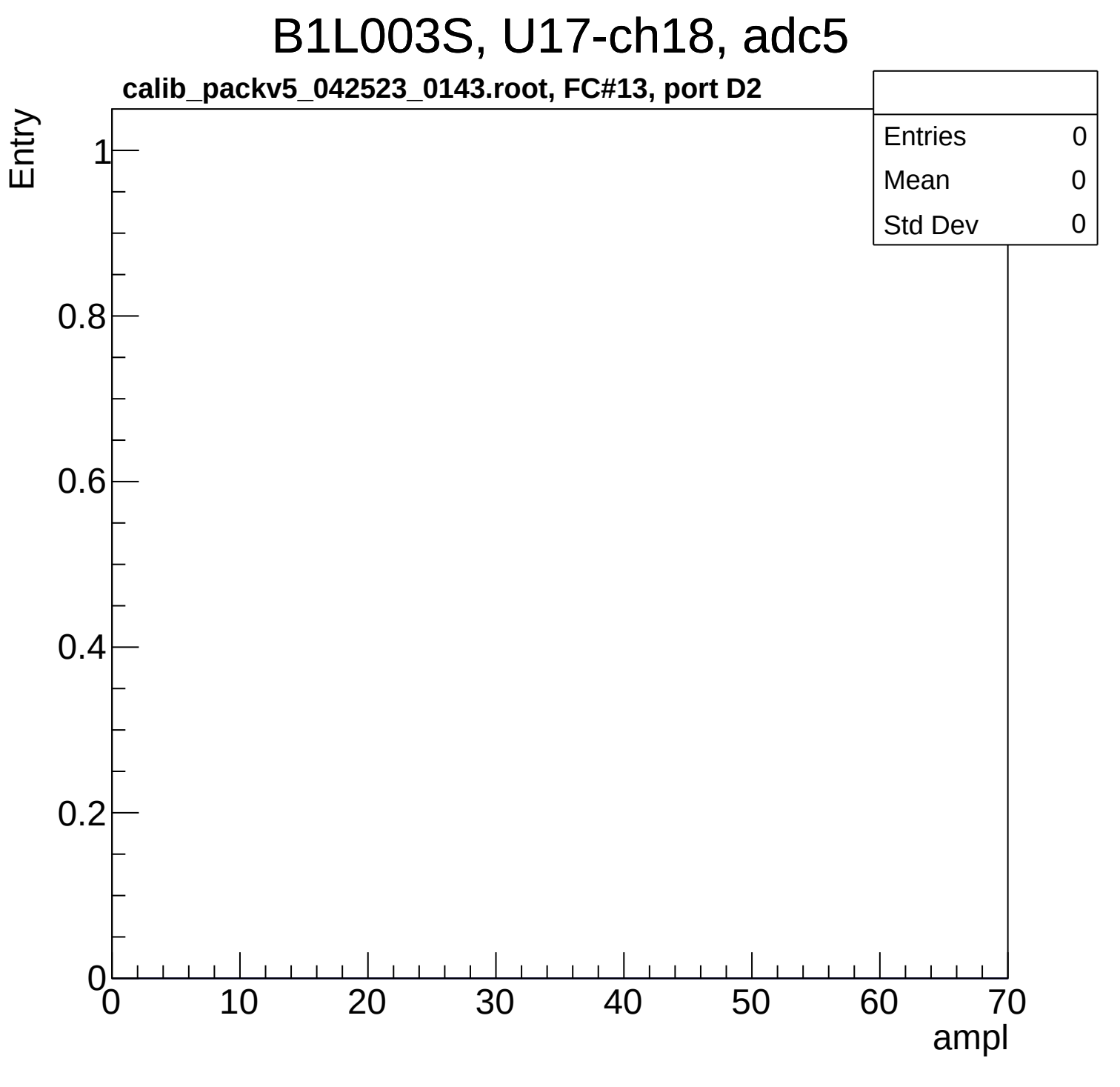
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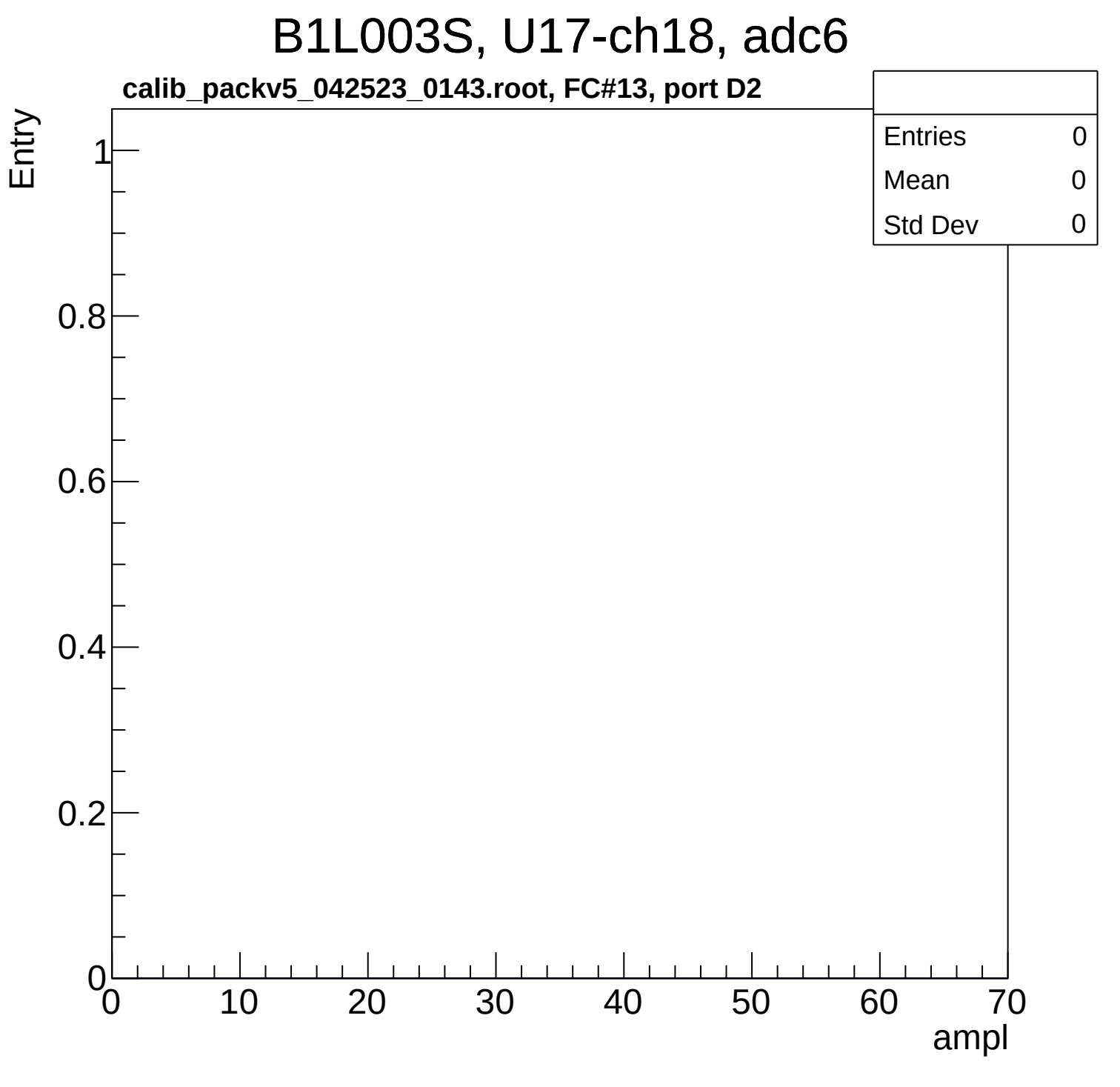
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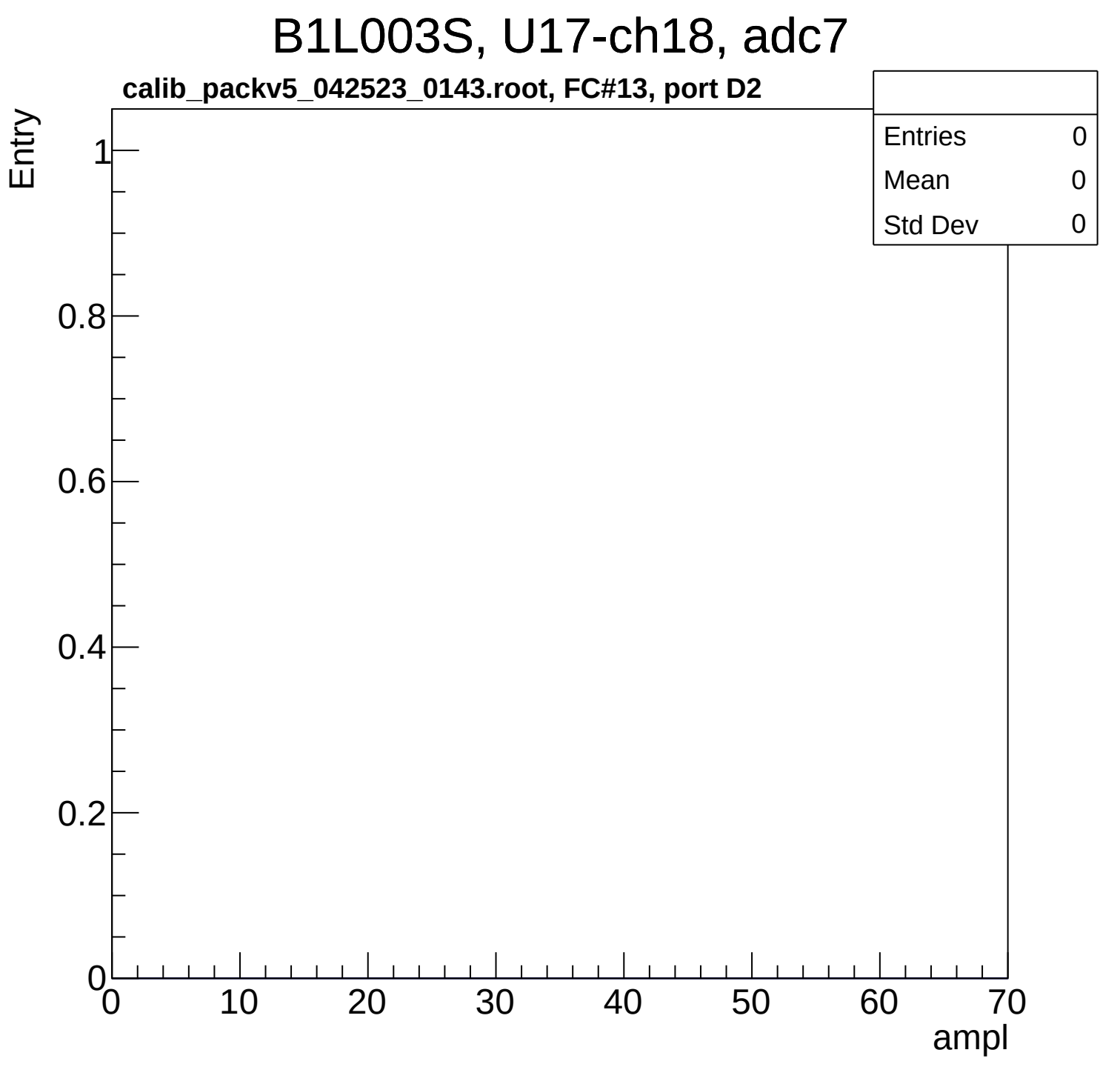
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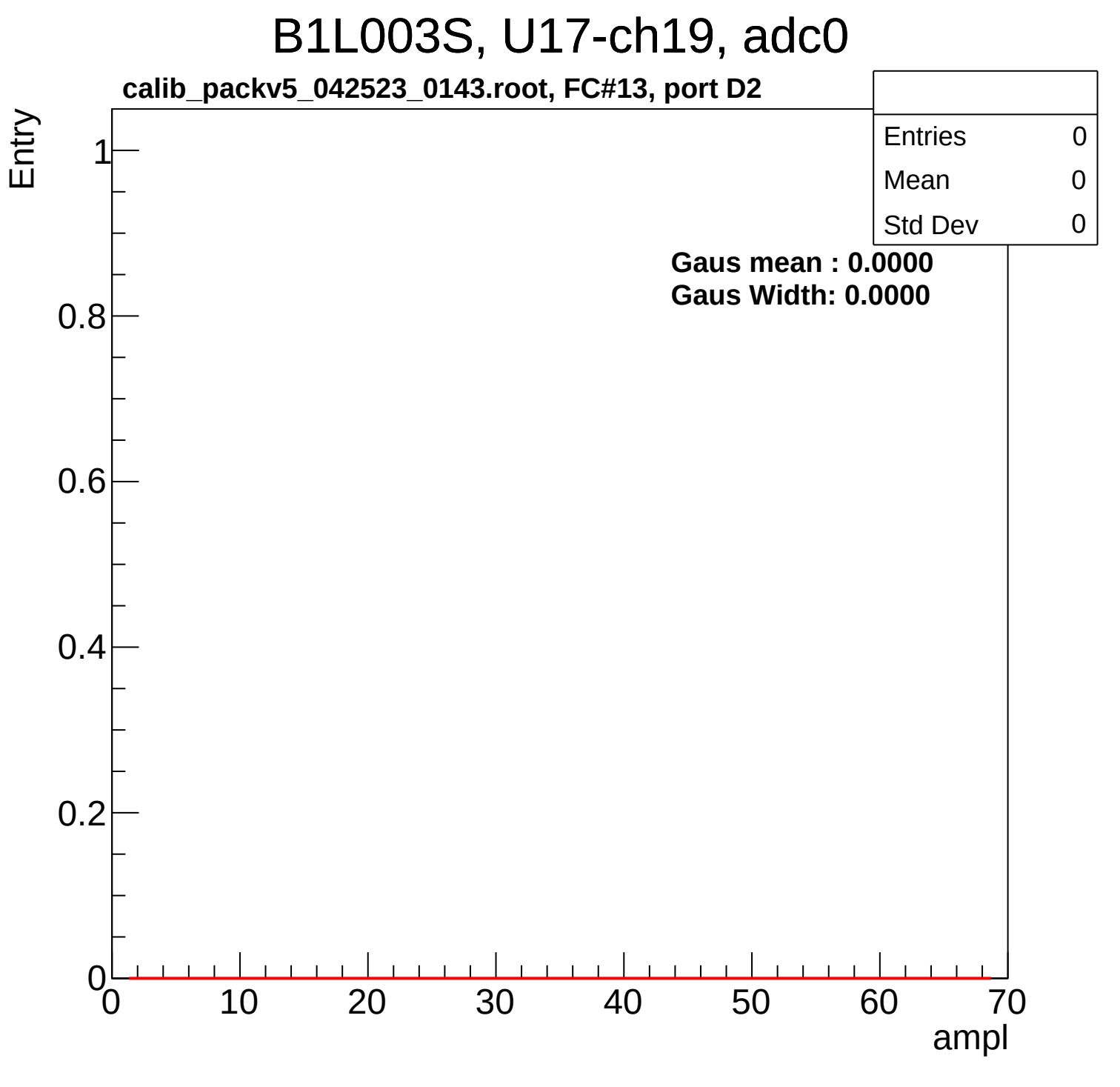
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Gaus Width: 0.0000
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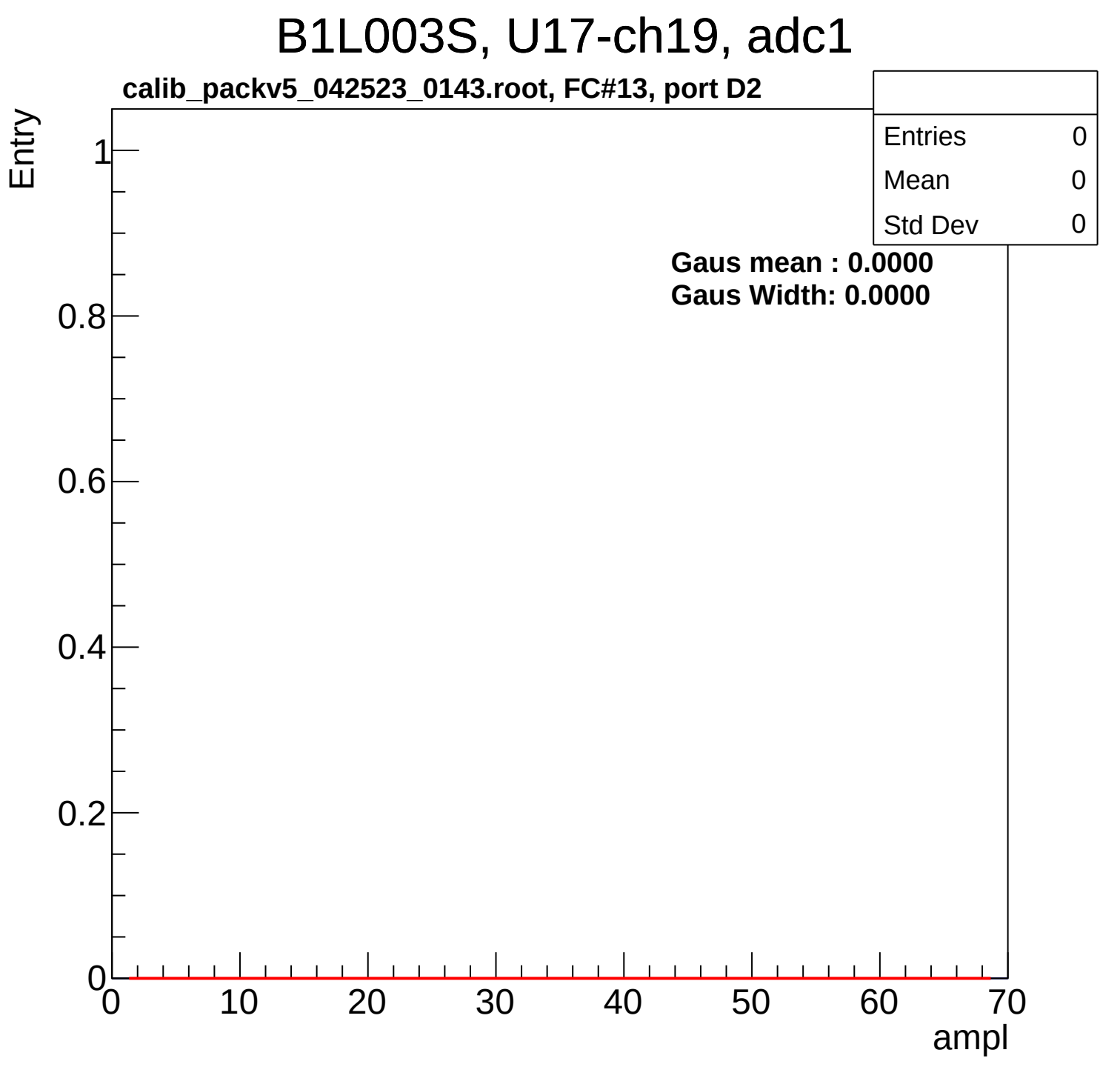
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Gaus Width: 0.0000
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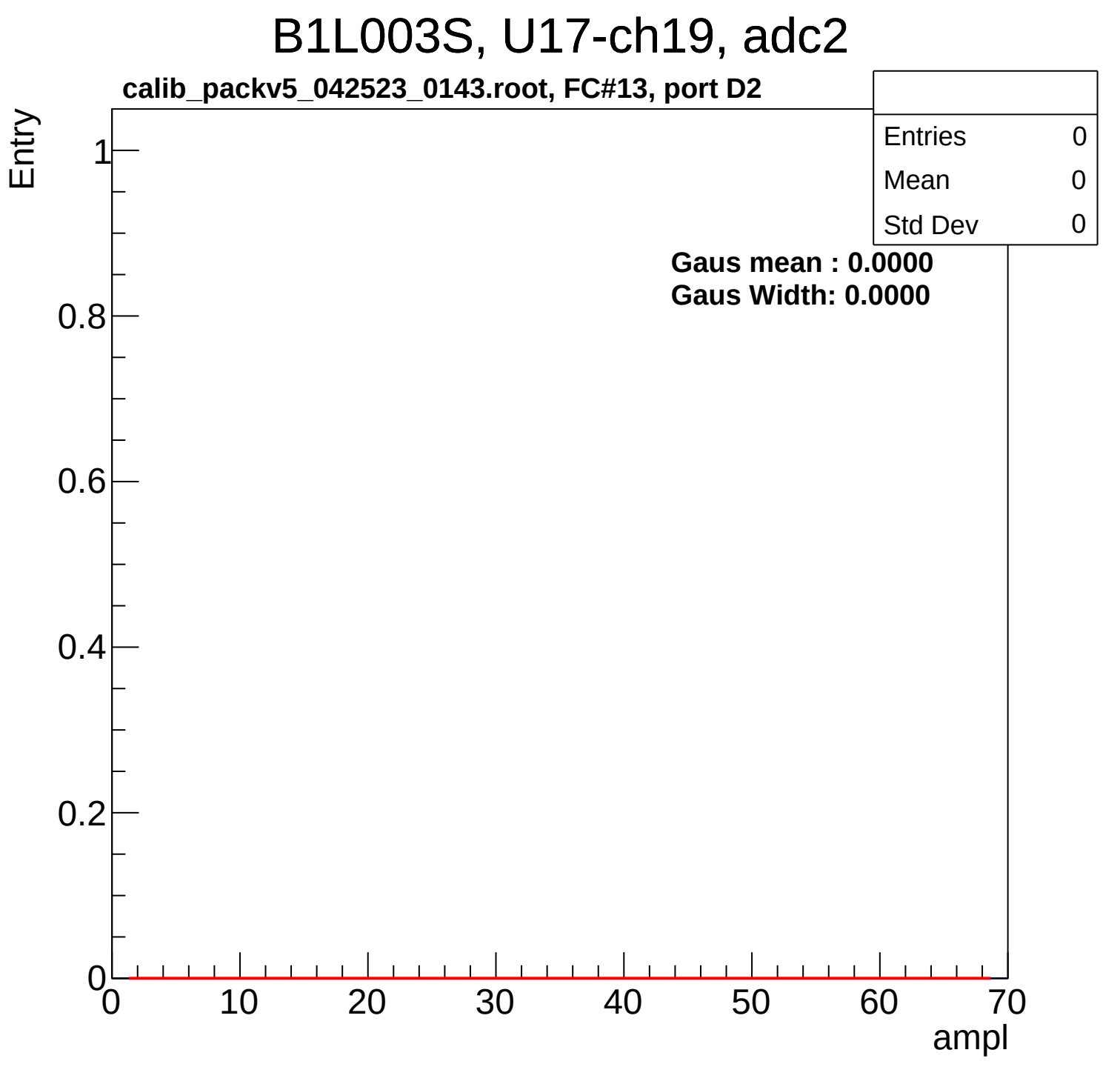
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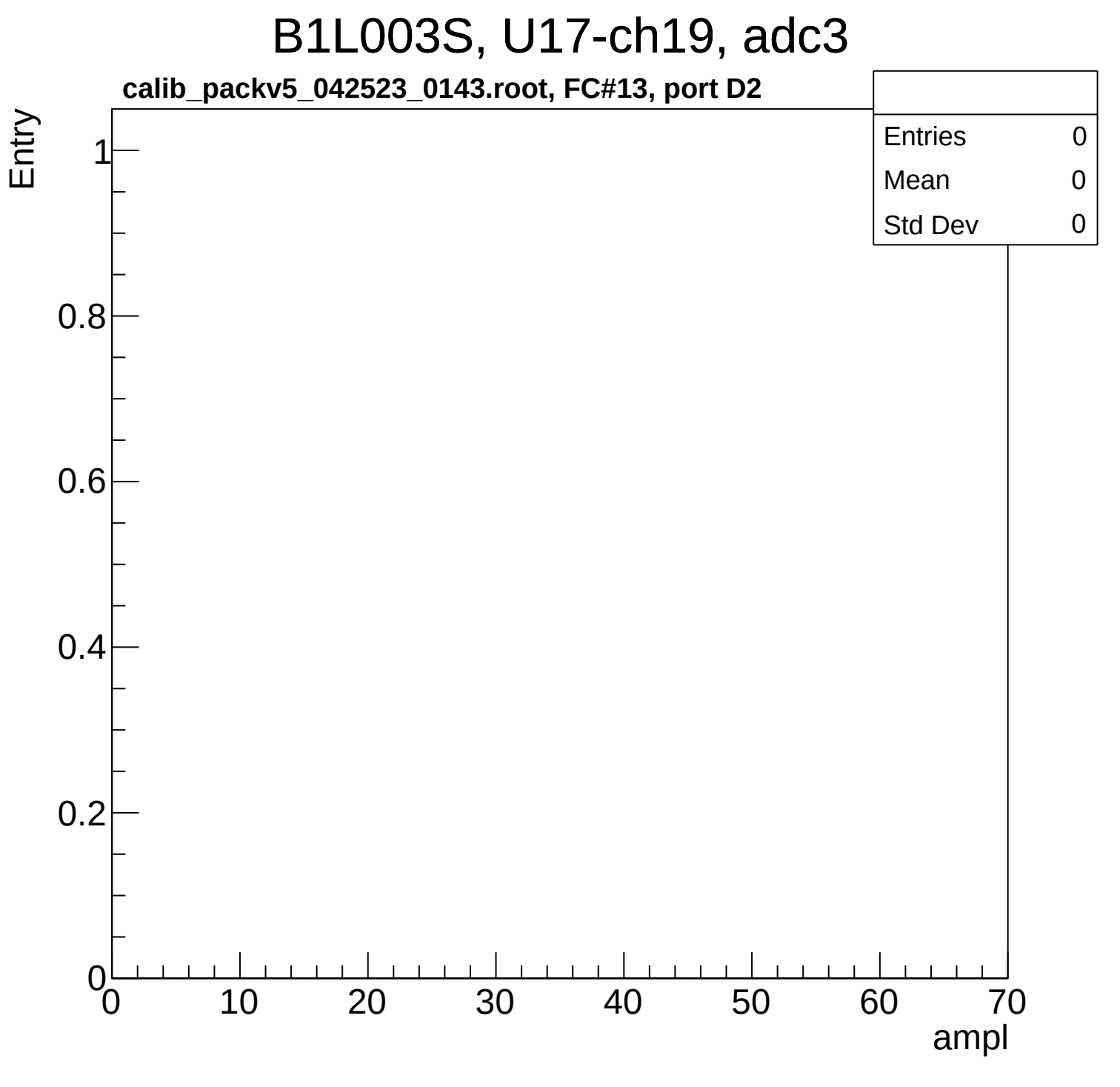
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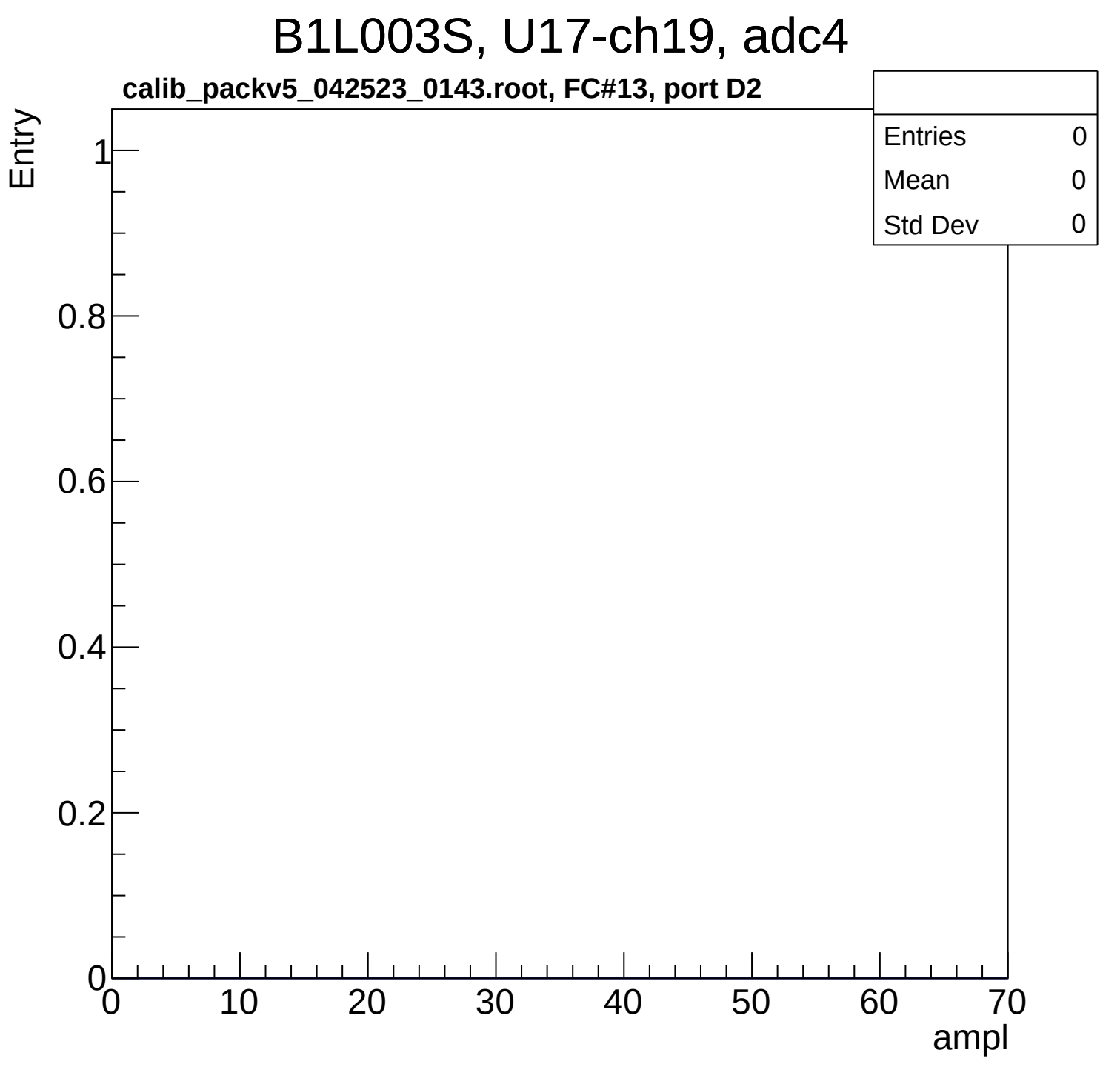
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Mean 0
Std Dev 0
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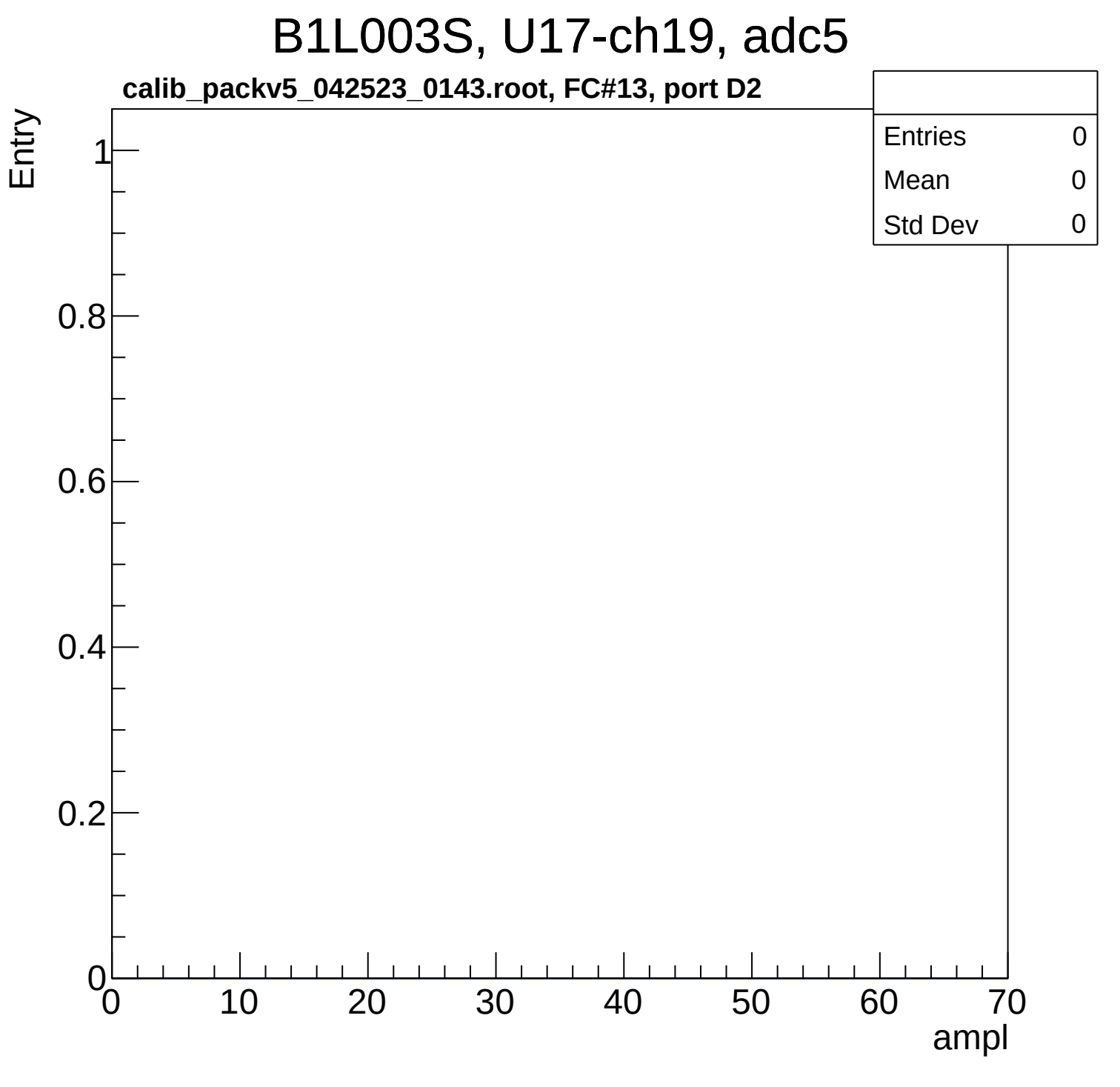
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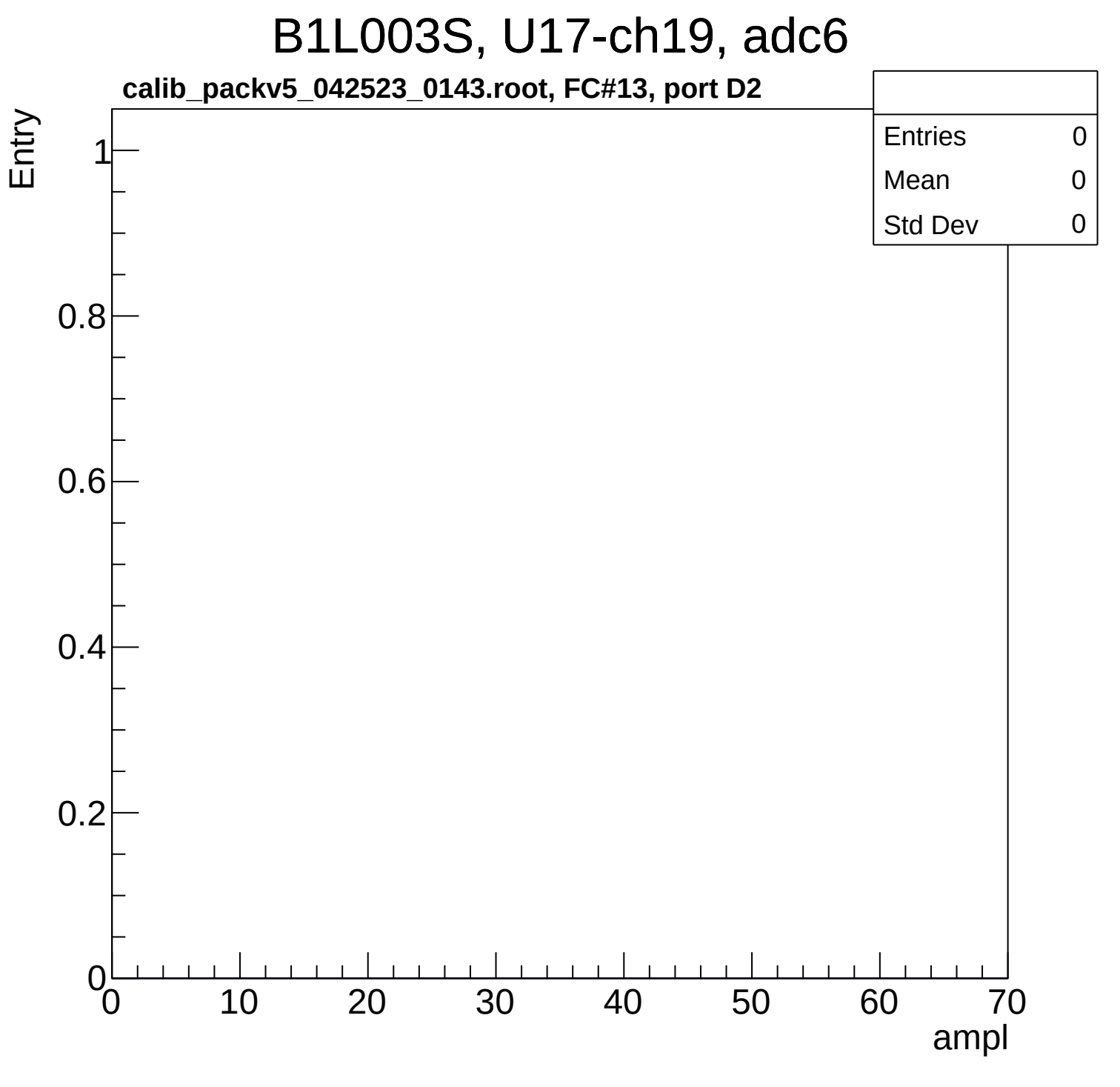
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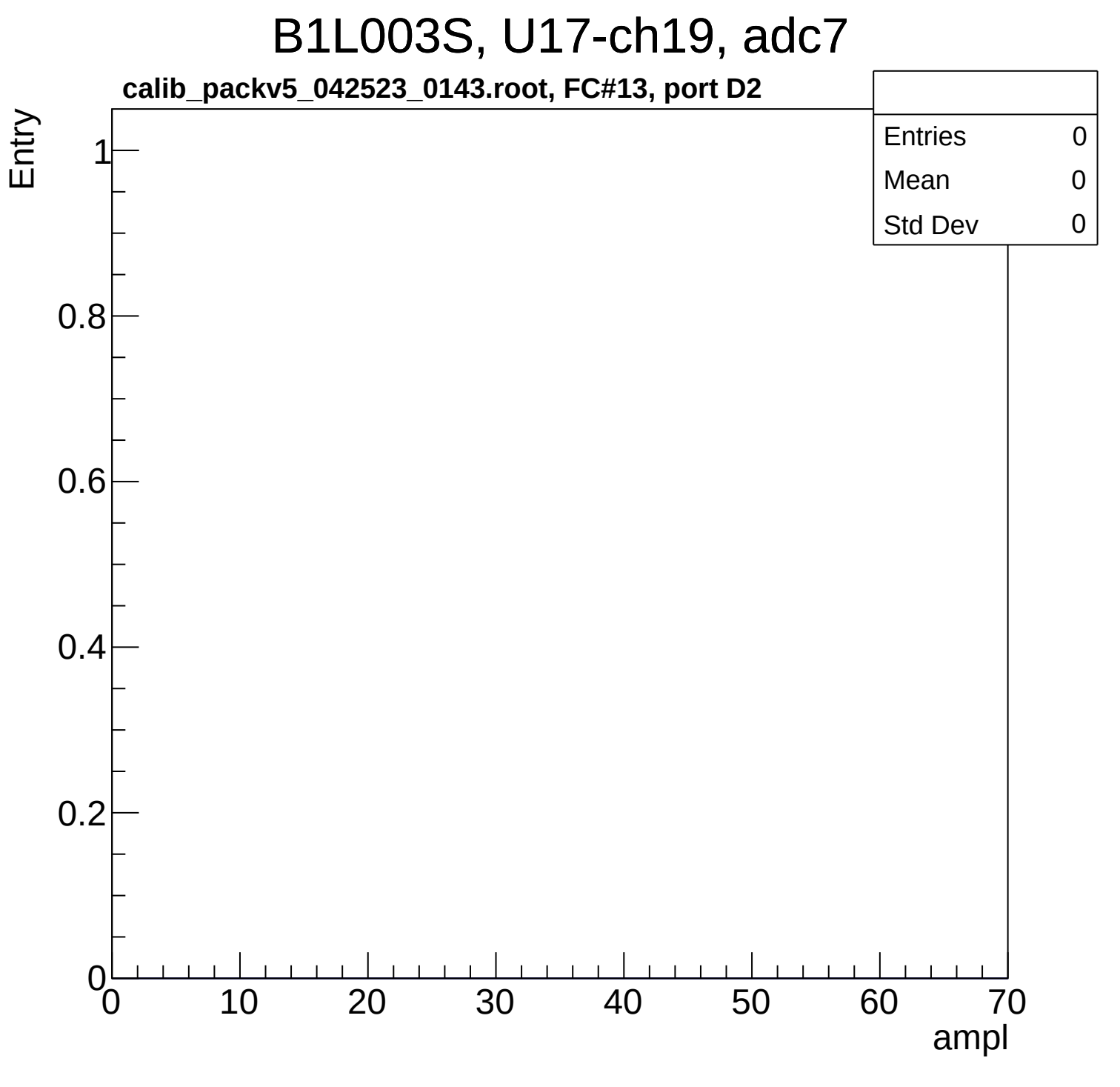
B1L003S, U17-ch19, adc7
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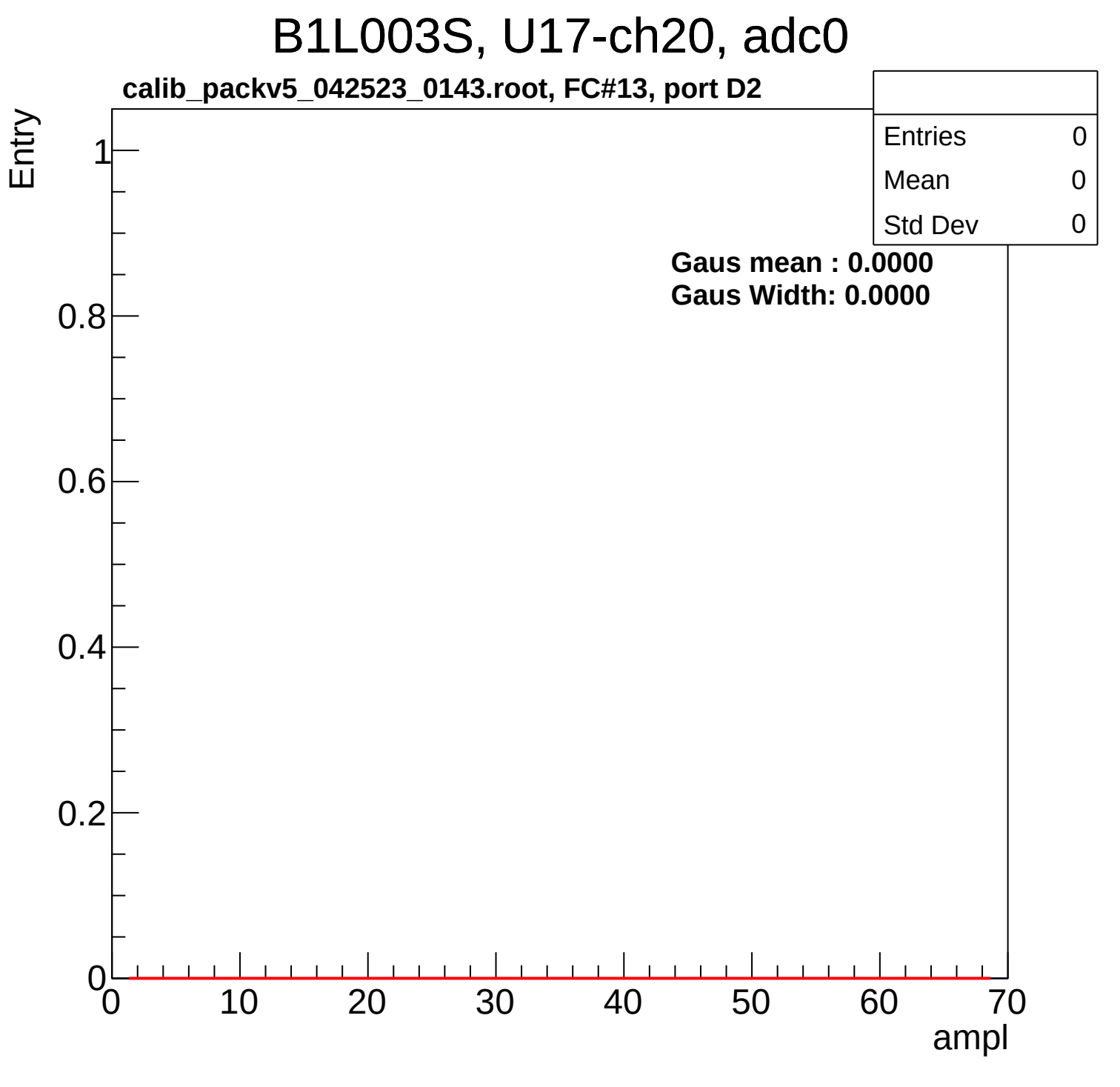
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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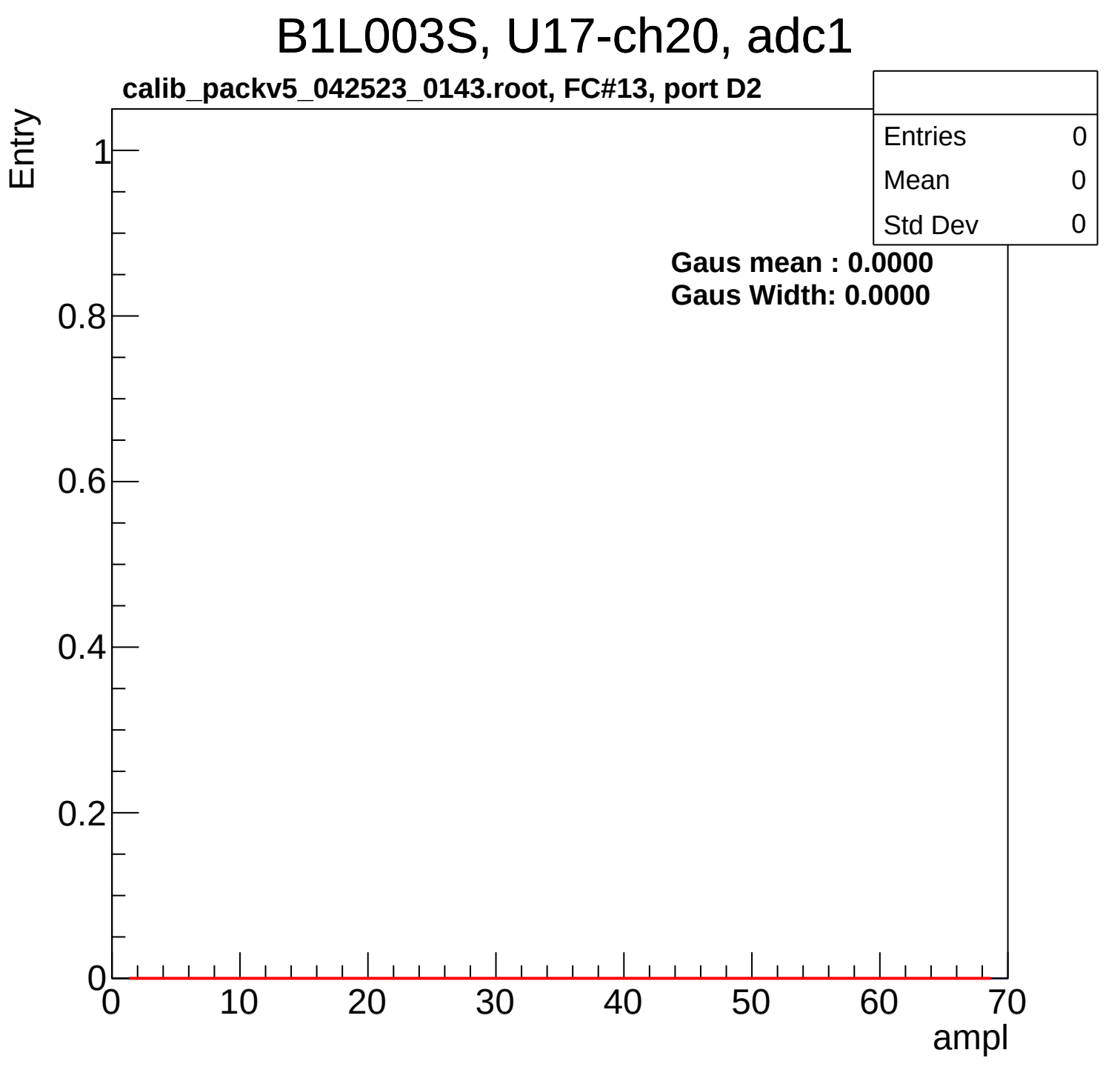
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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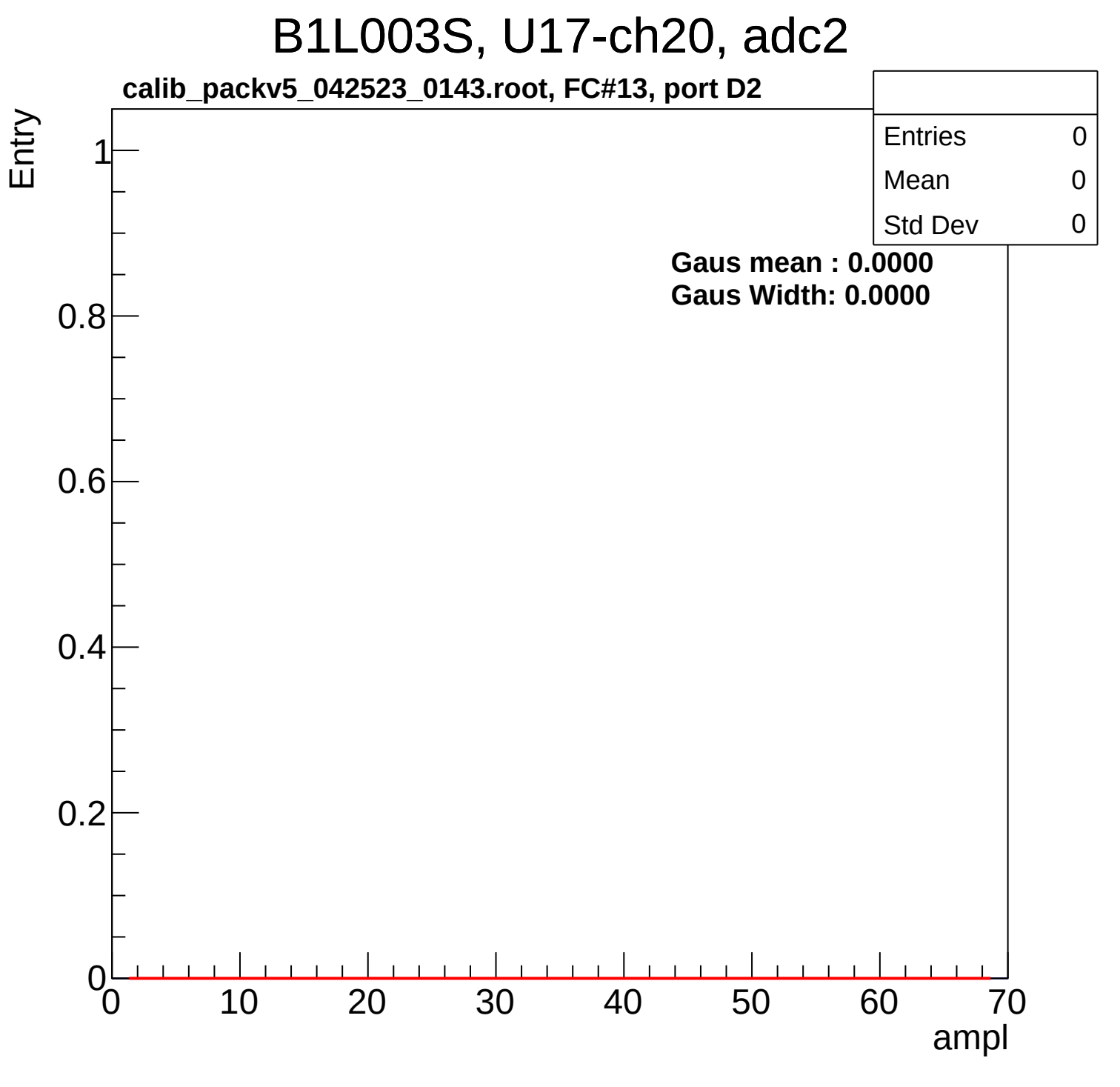
ampl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Std Dev 0
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Gaus Width: 0.0000
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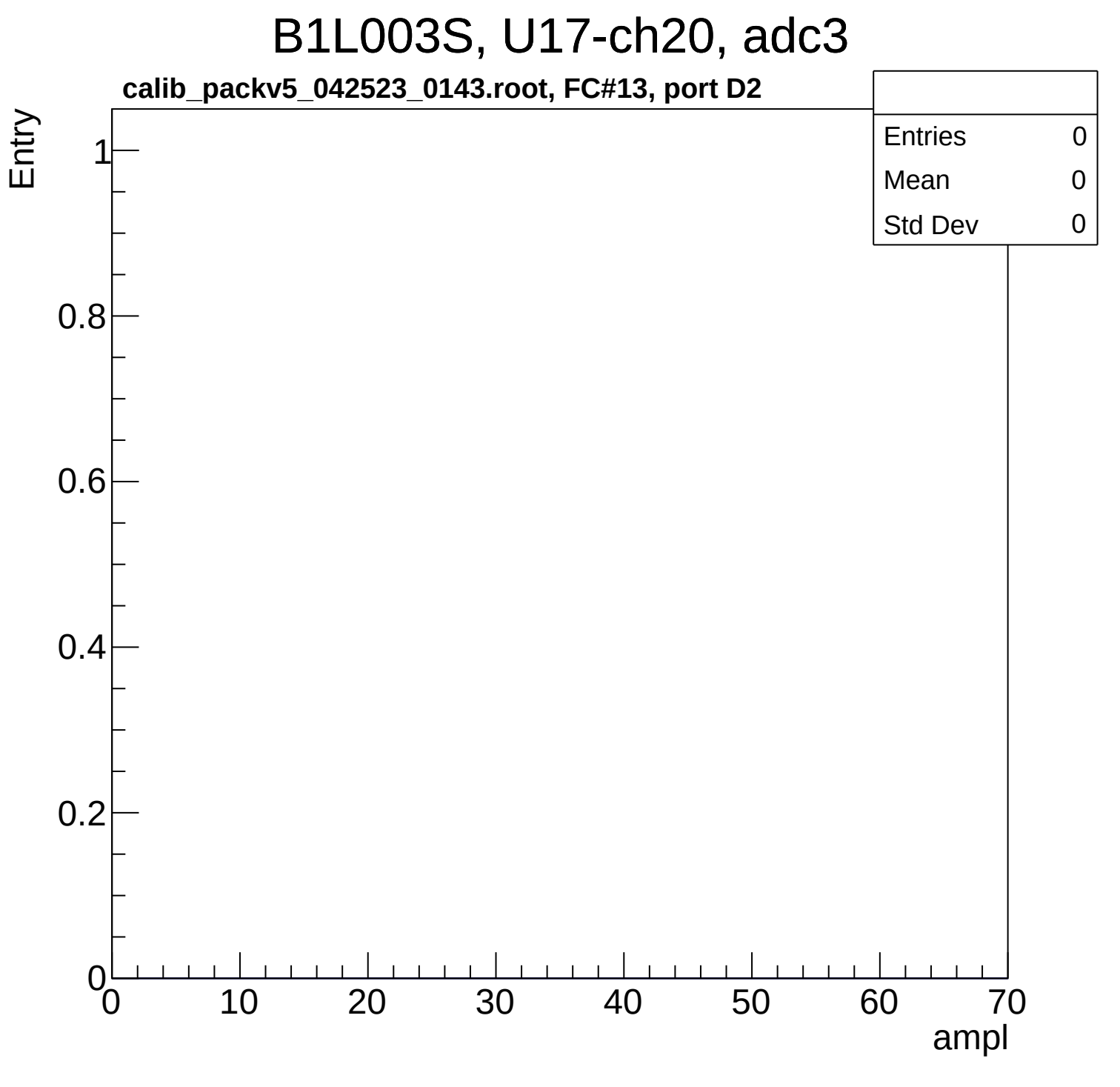
ampl


B1L003S, U17-ch20, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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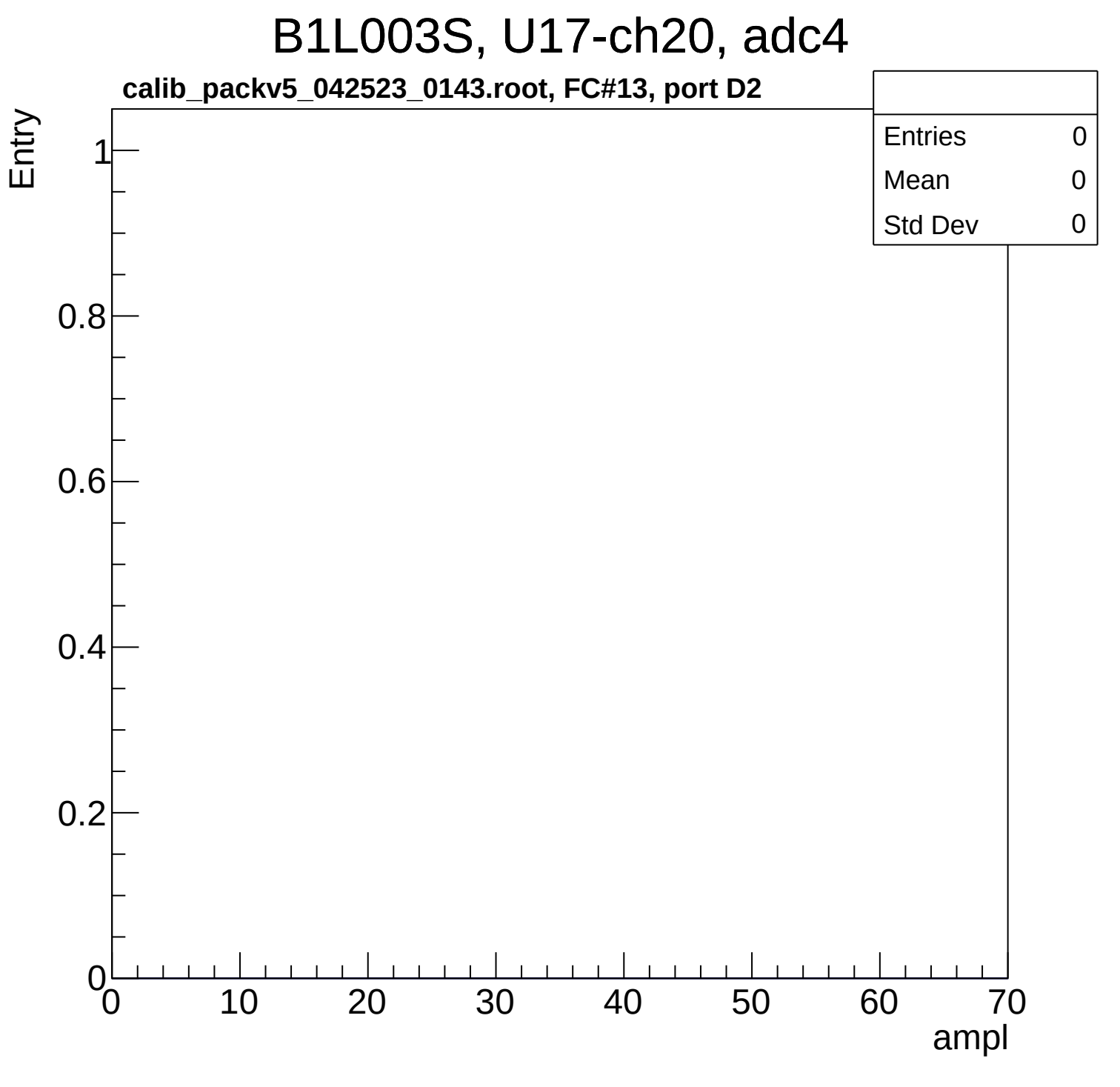
ampl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Std Dev 0
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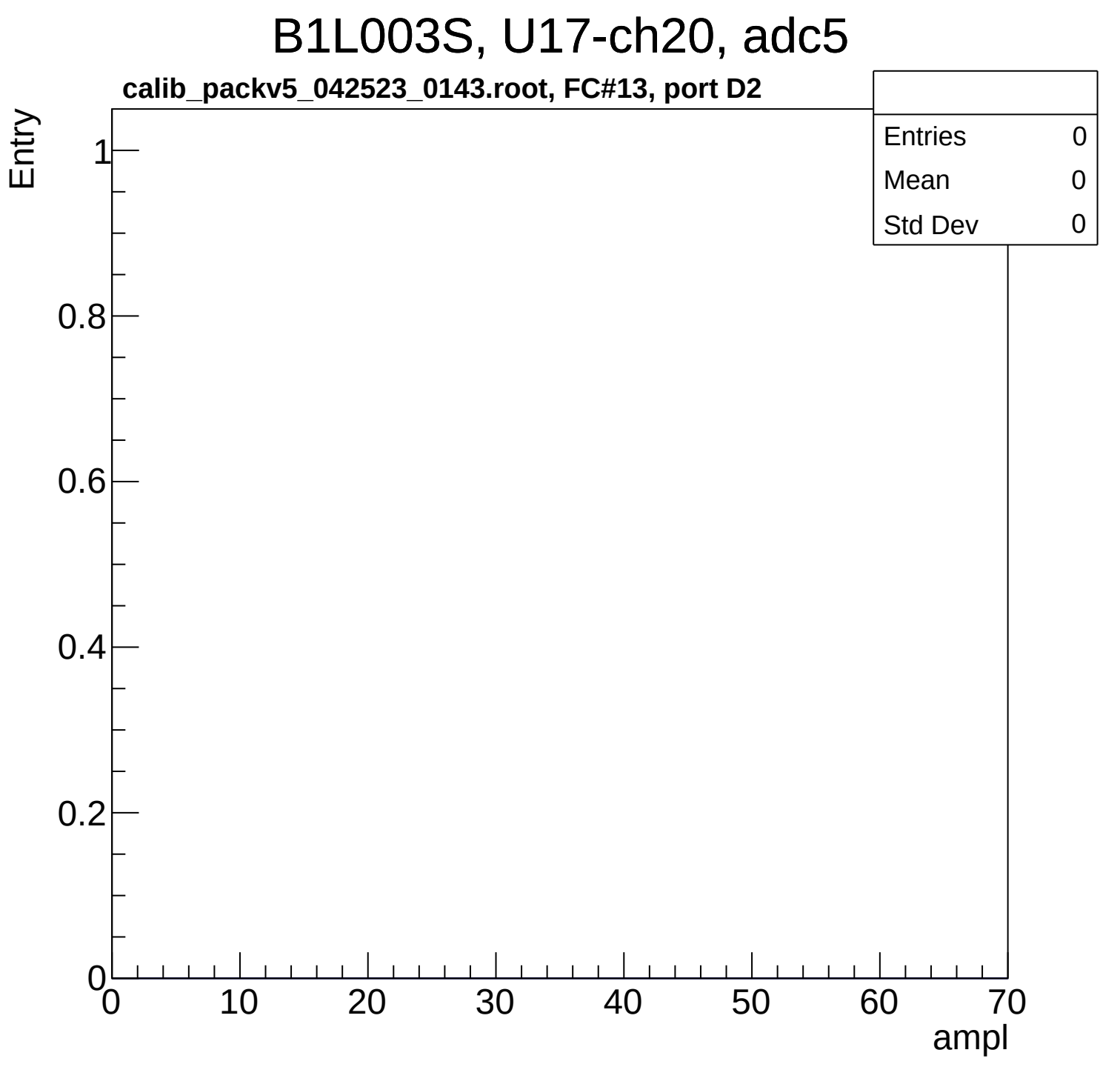
ampl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calib_packv5_042523_0143.root, FC#13, port D2
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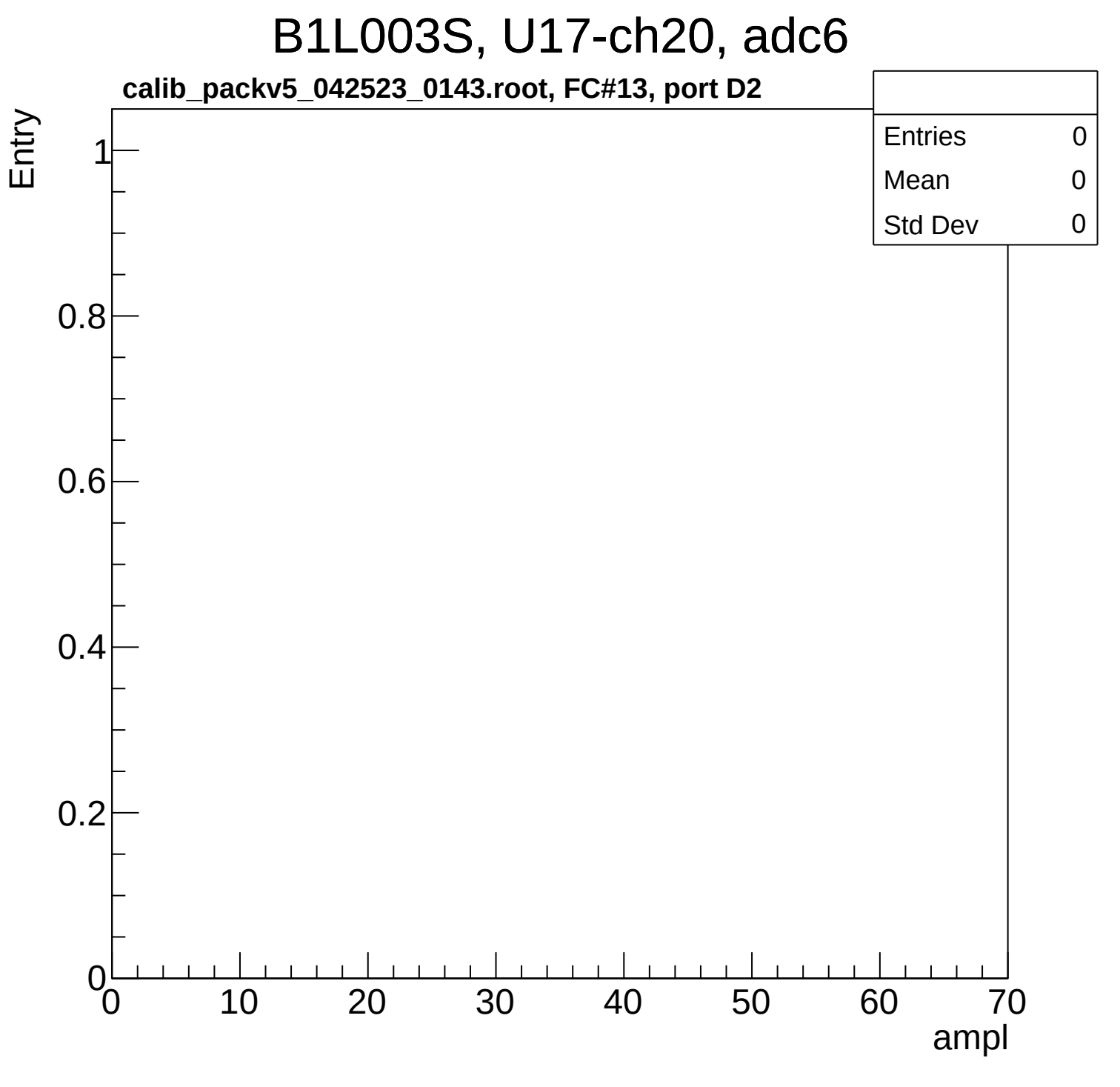
ampl


B1L003S, U17-ch20, adc6
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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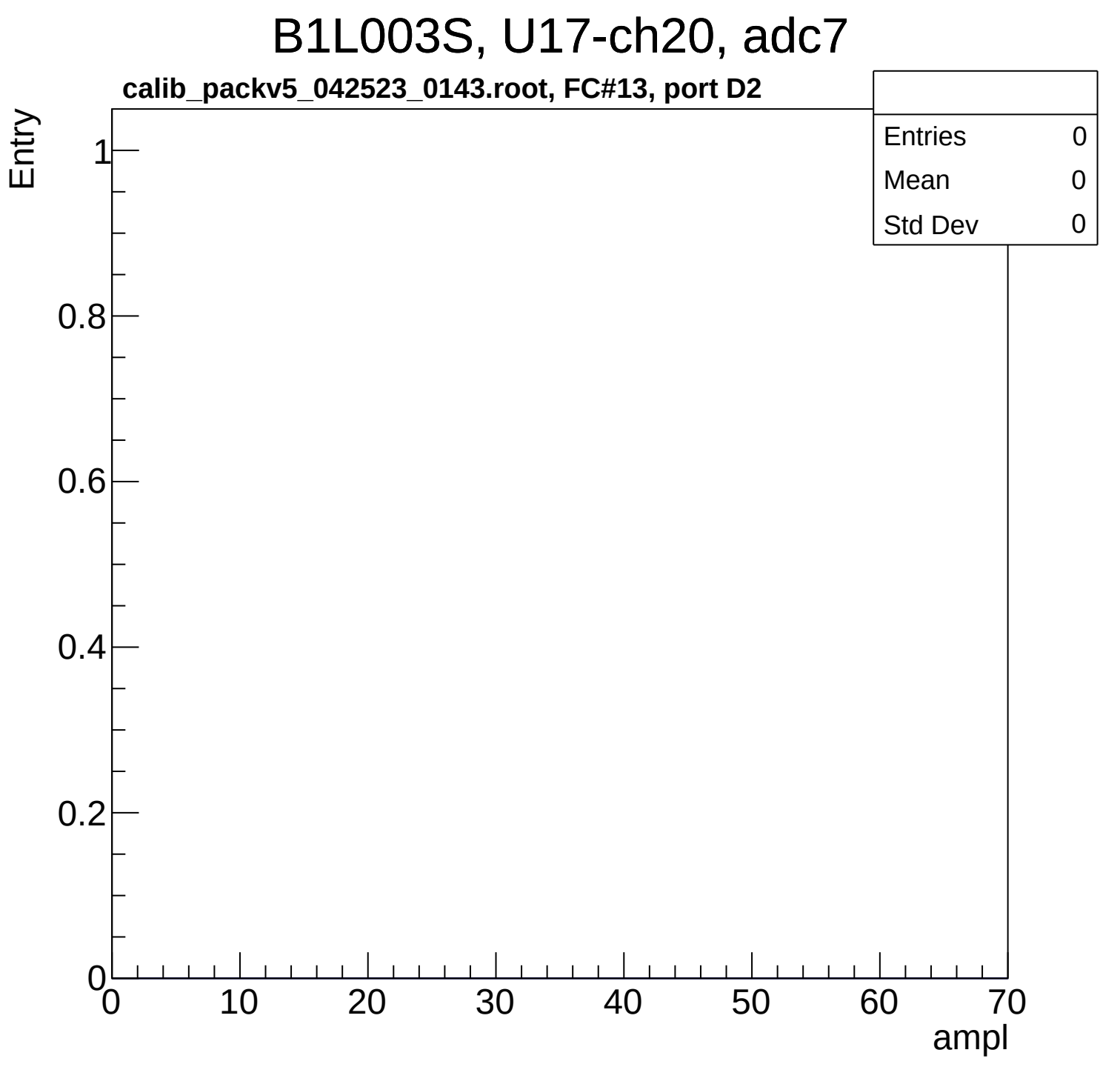
ampl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calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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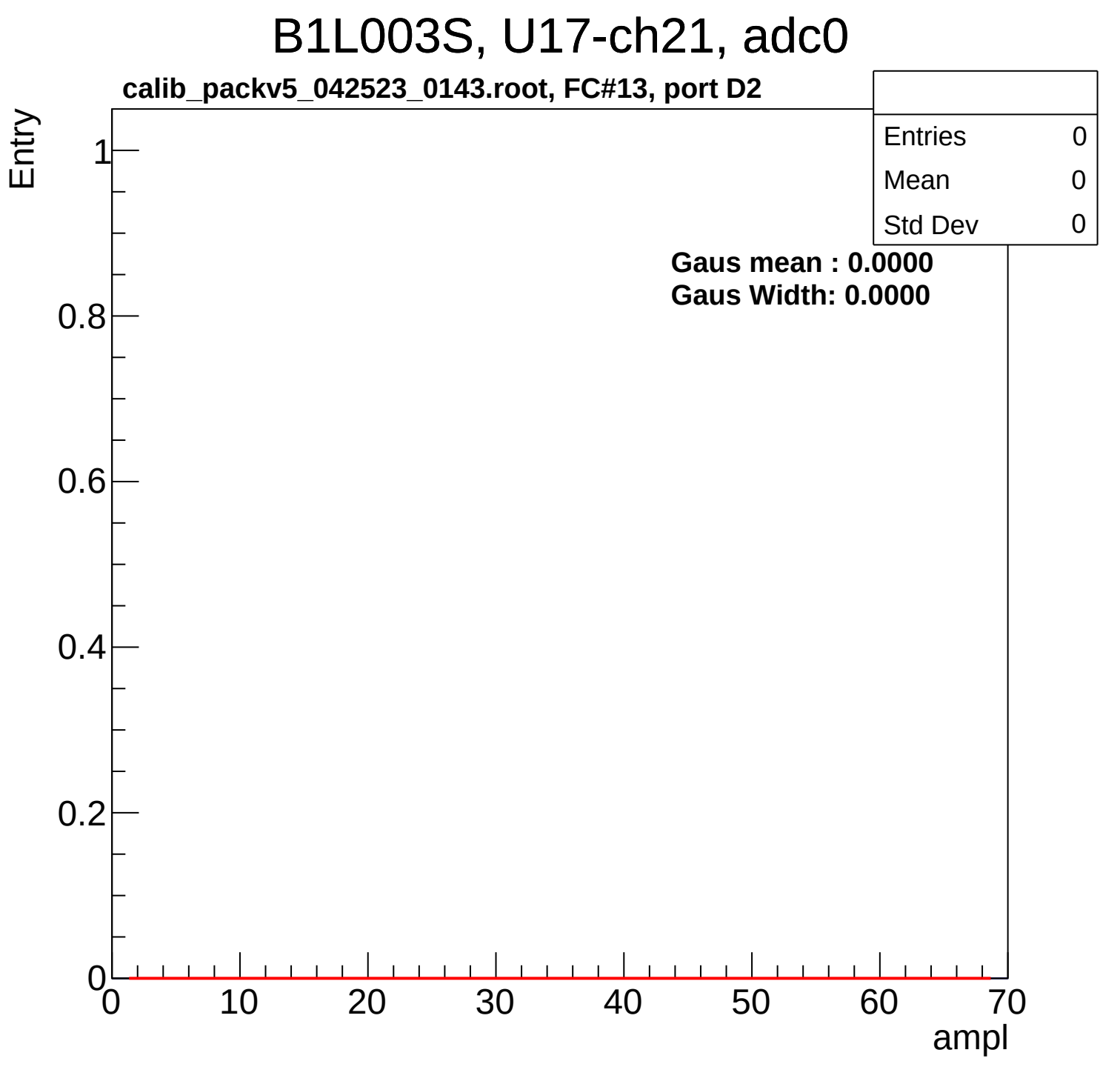
ampl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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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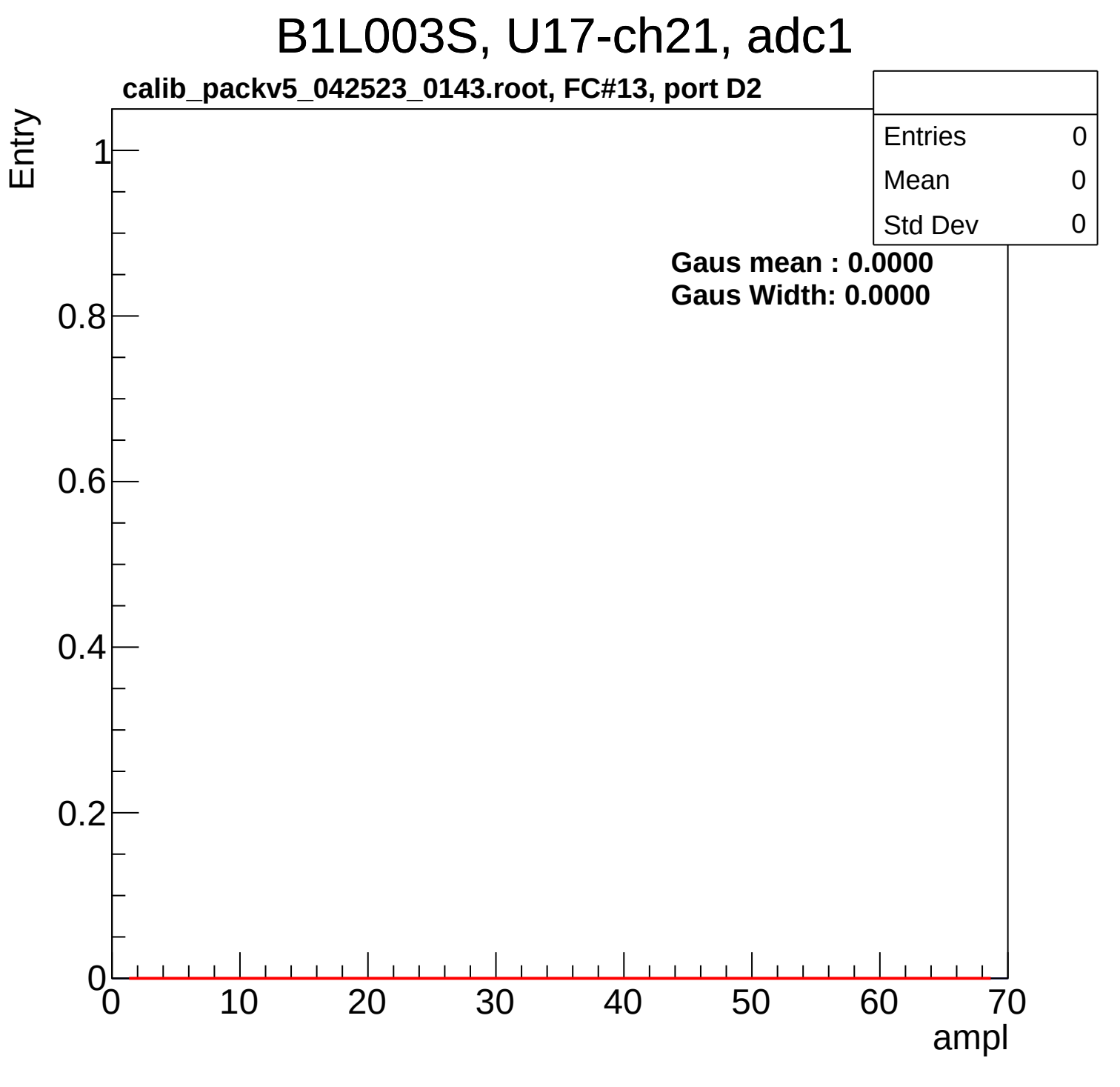
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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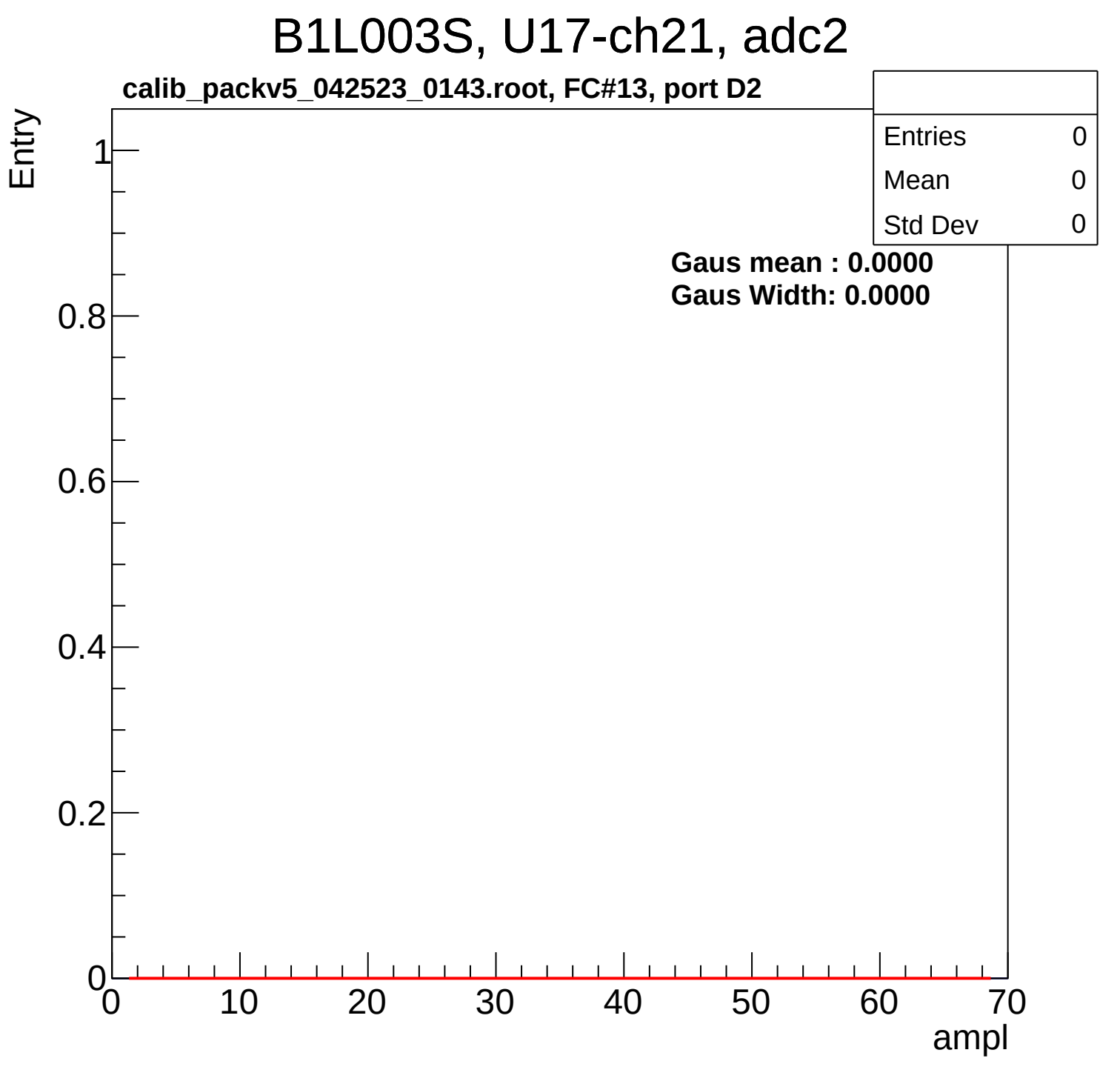
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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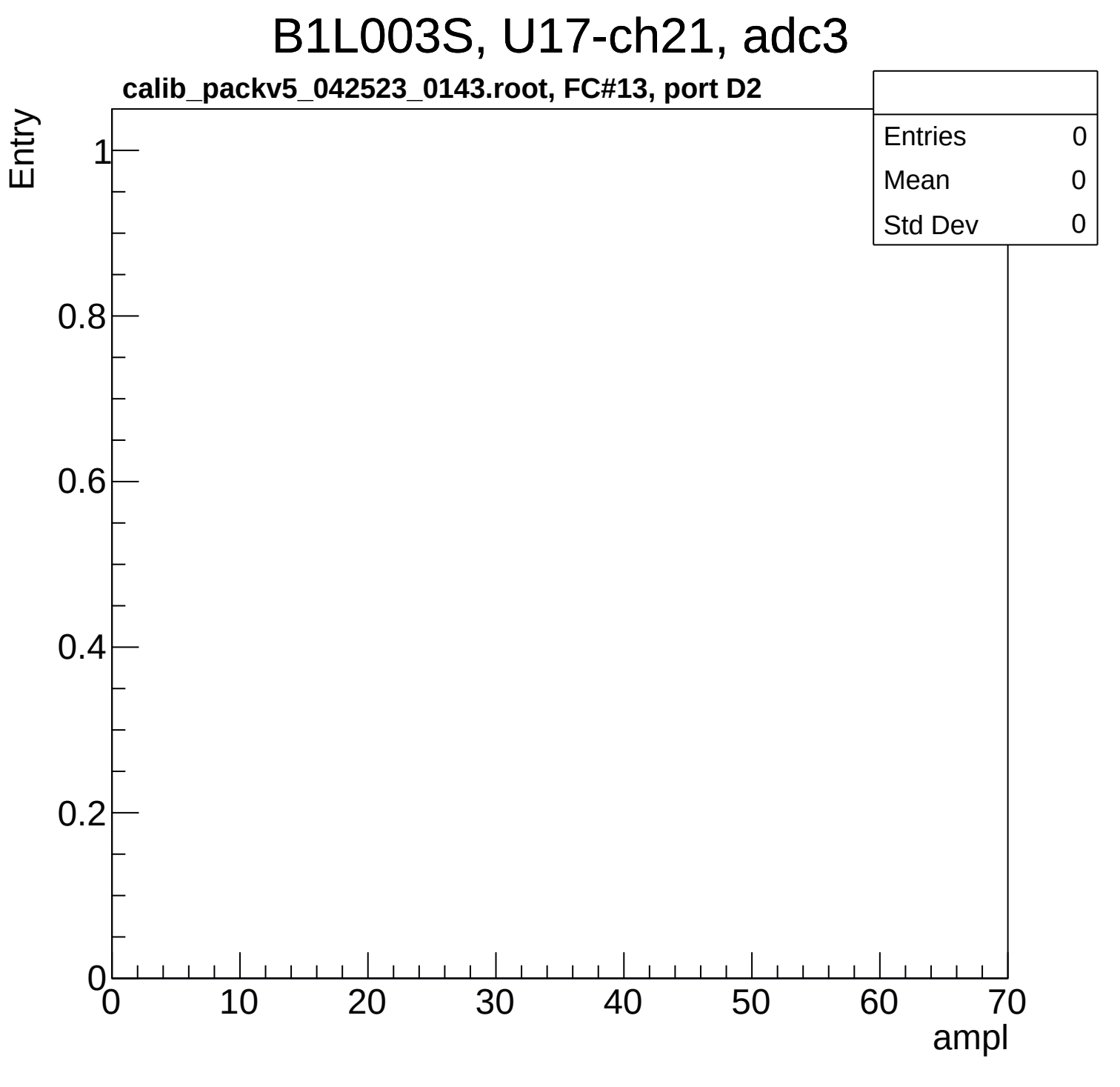
ampl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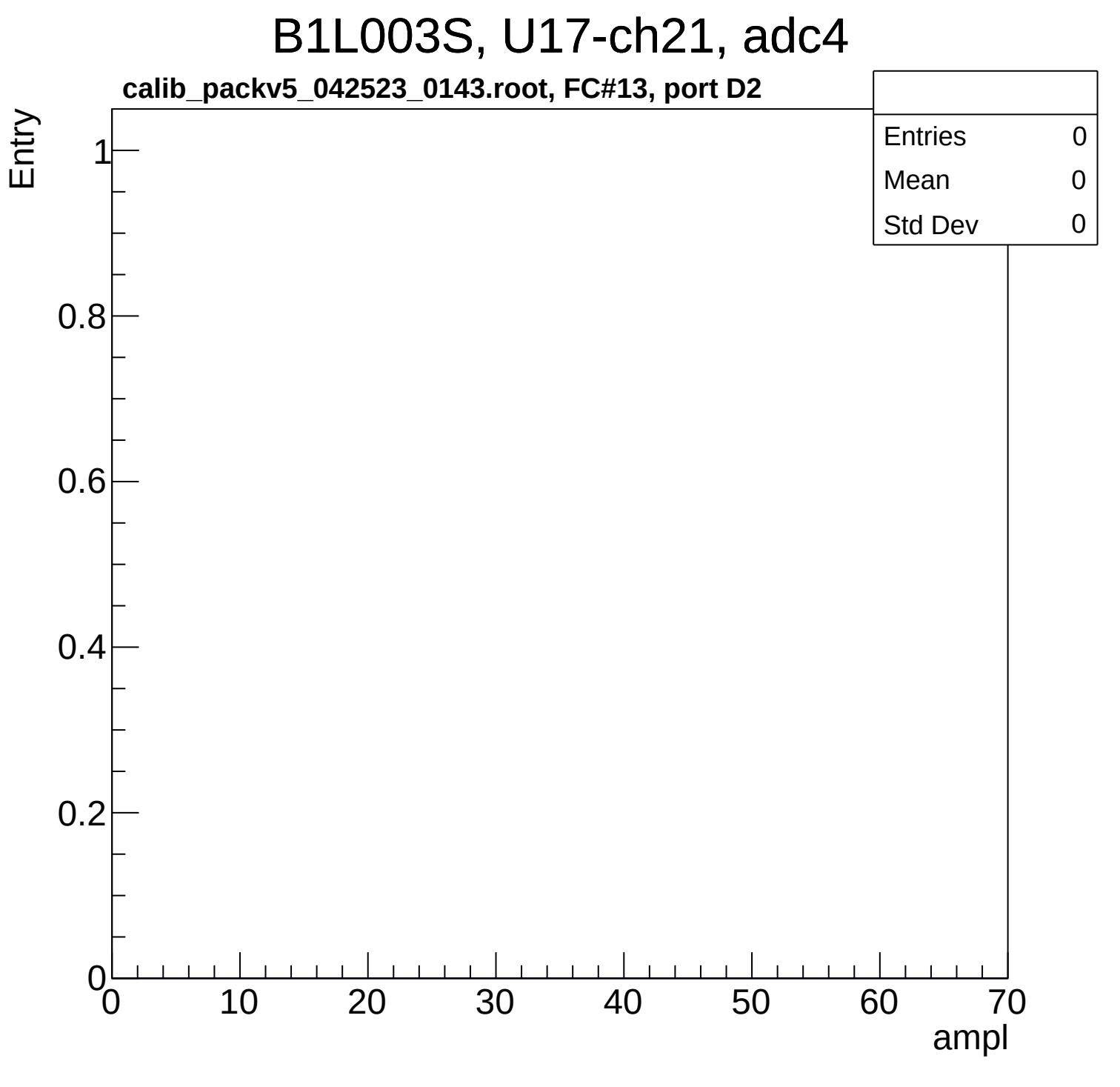
ampl


B1L003S, U17-ch21, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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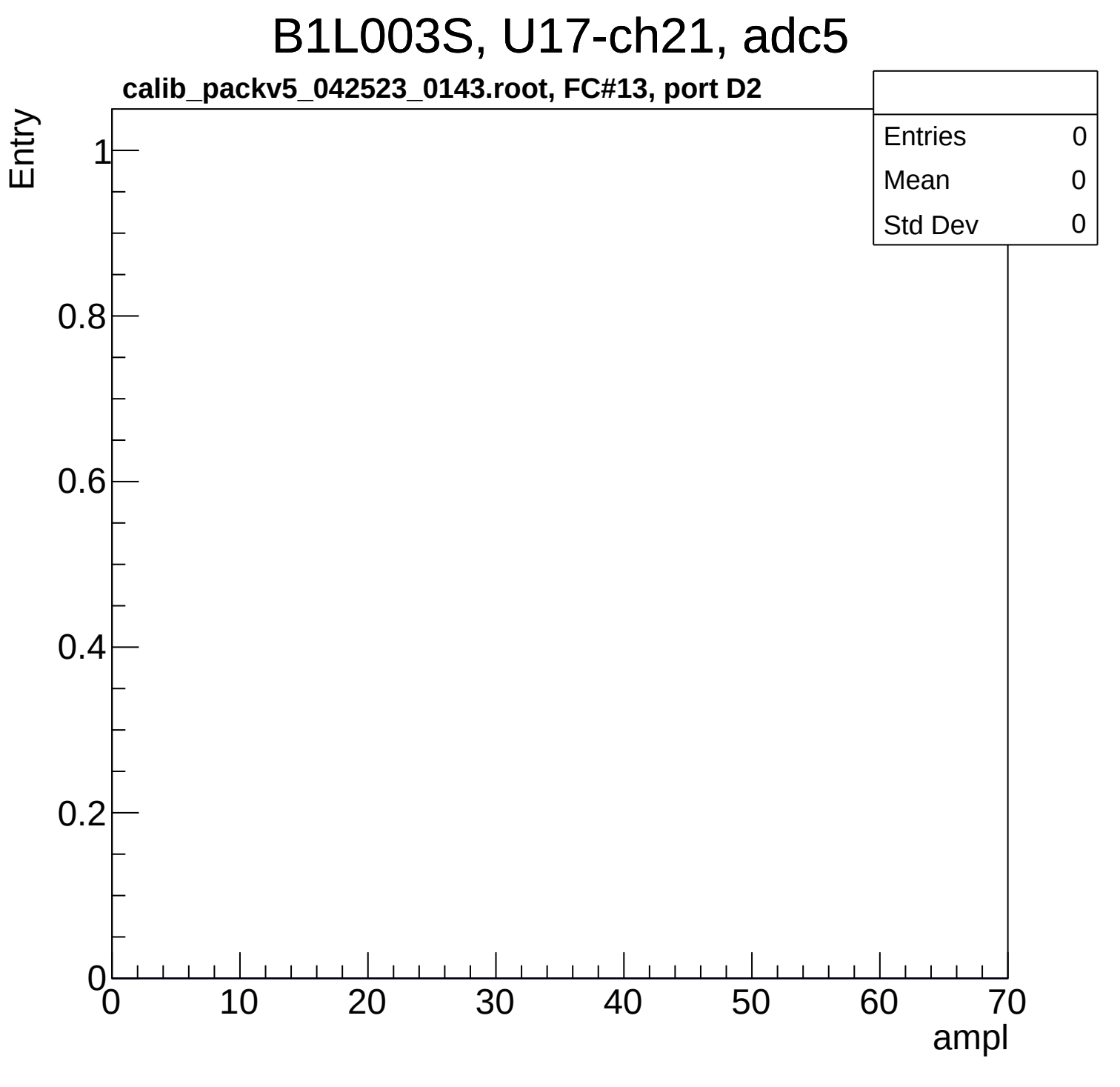
ampl


B1L003S, U17-ch21, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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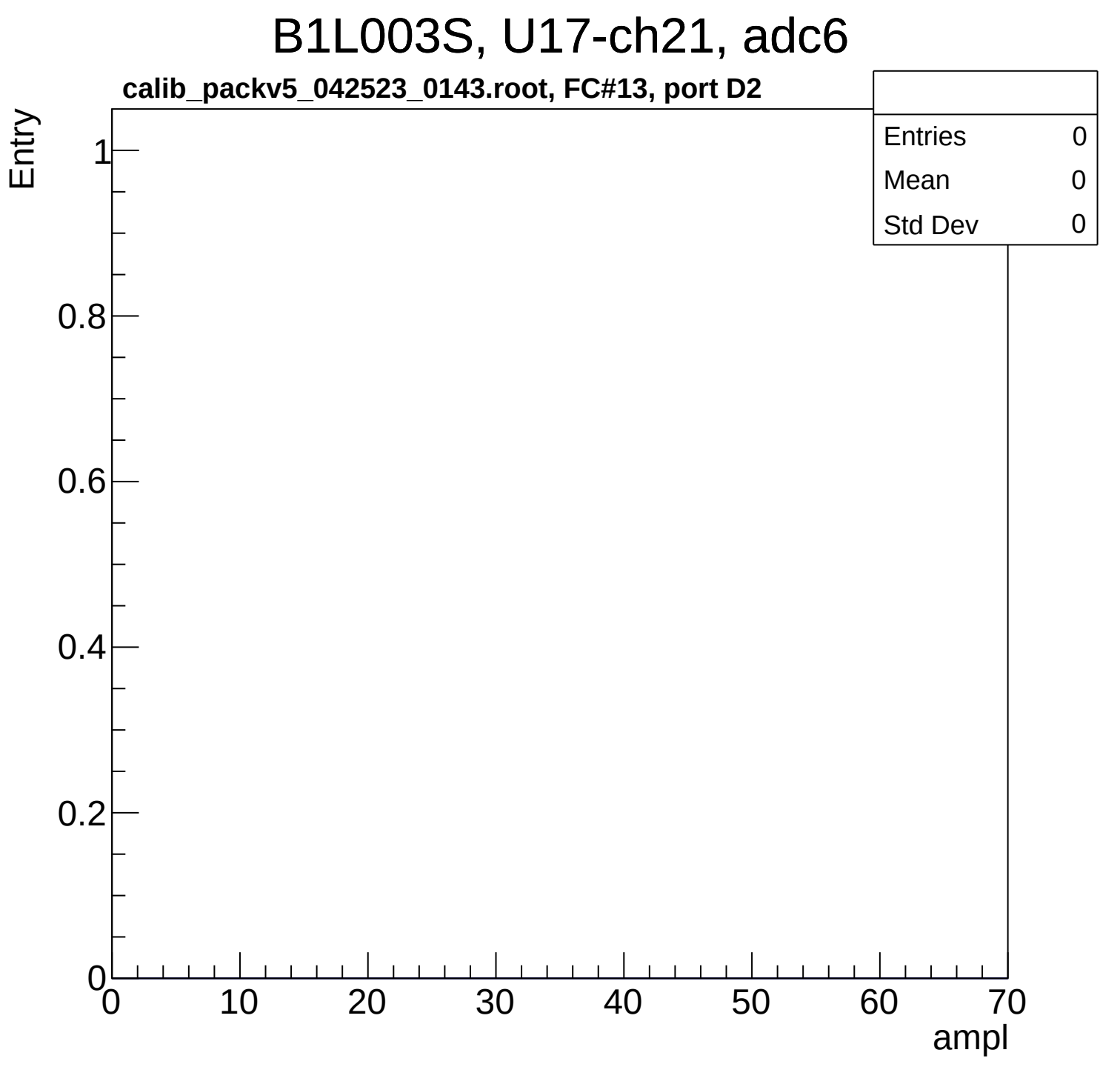
B1L003S, U17-ch21, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
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Std Dev 0
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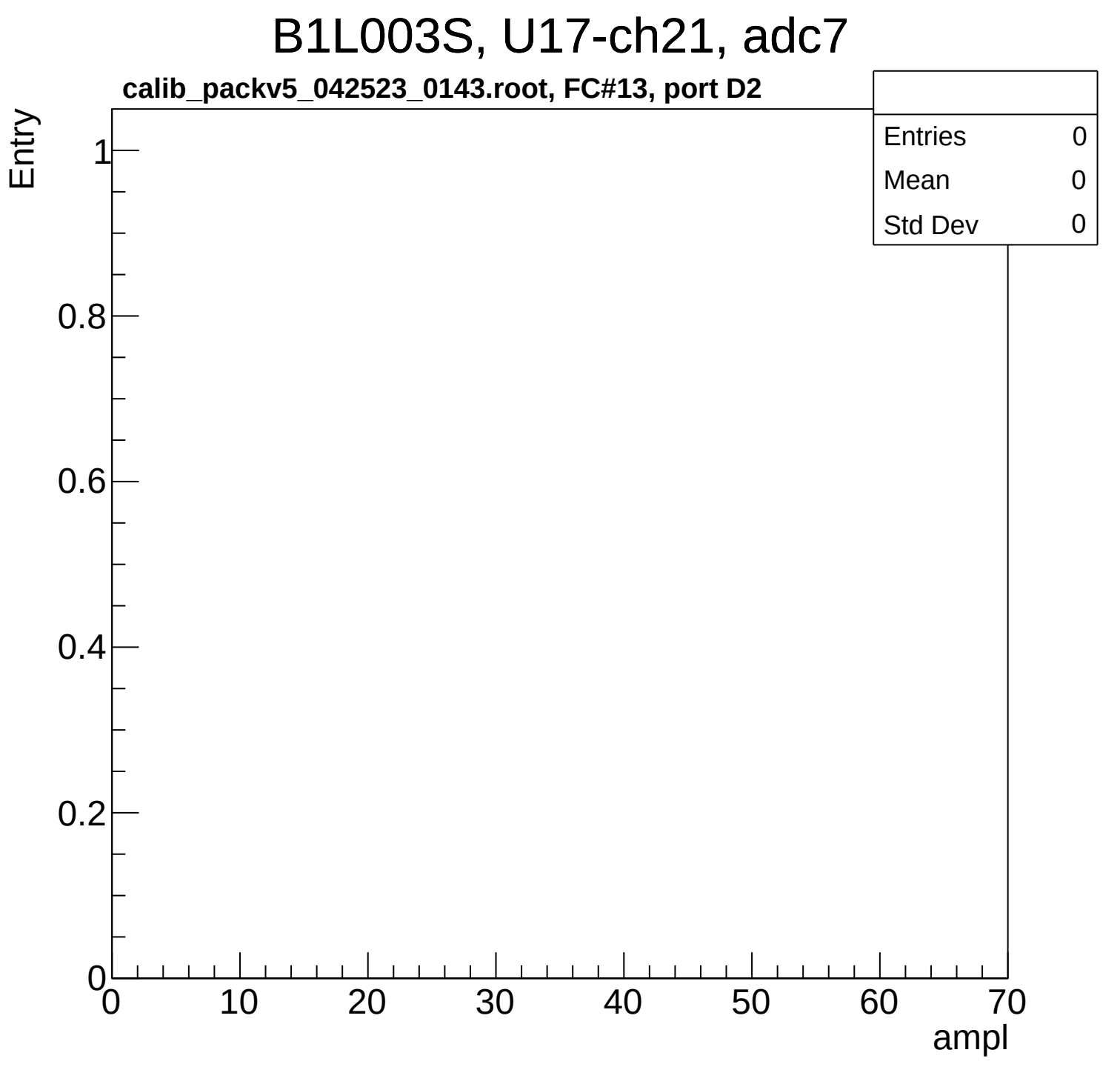
B1L003S, U17-ch21, adc7
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Std Dev 0
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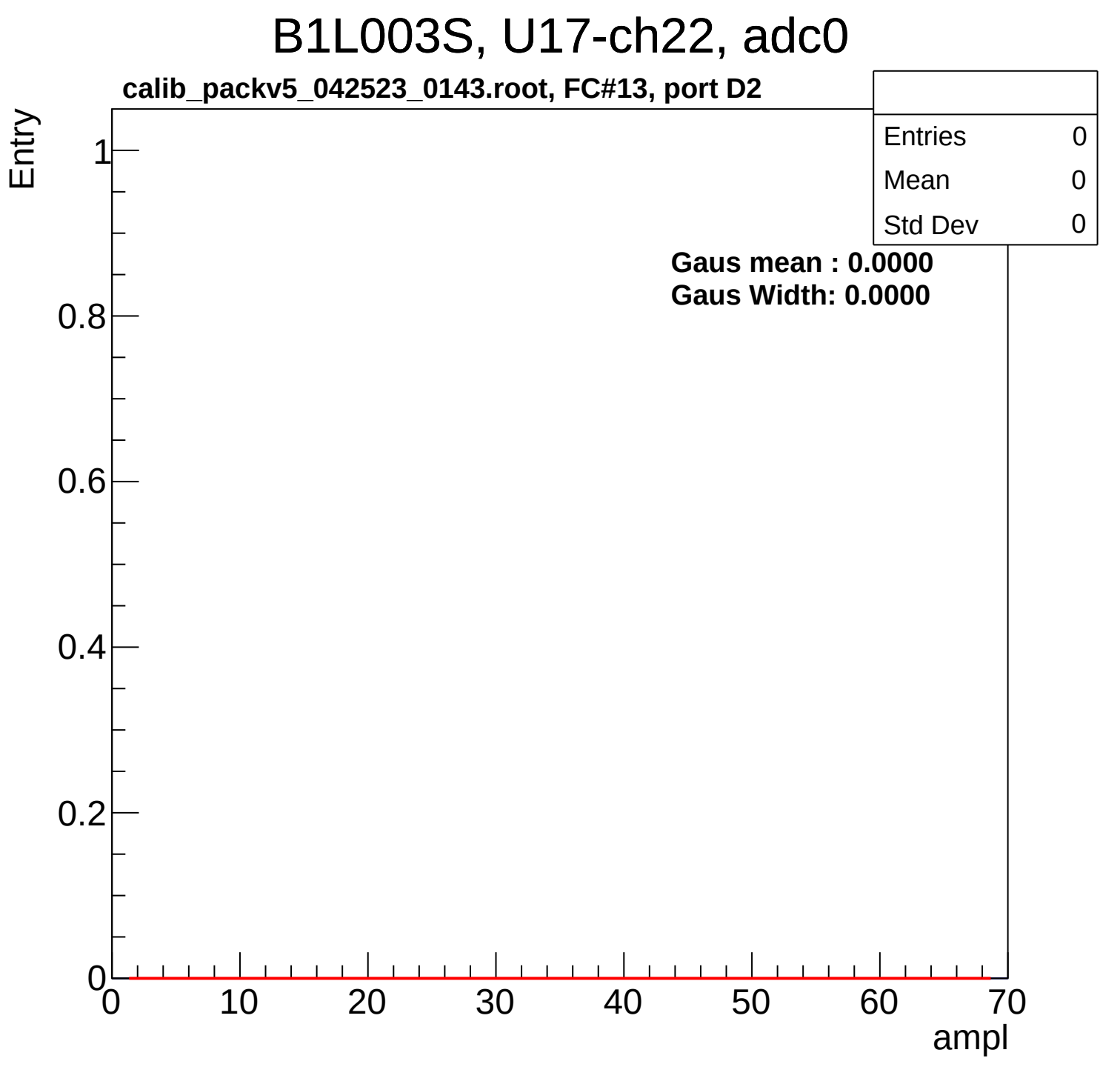
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Std Dev 0
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Gaus Width: 0.0000
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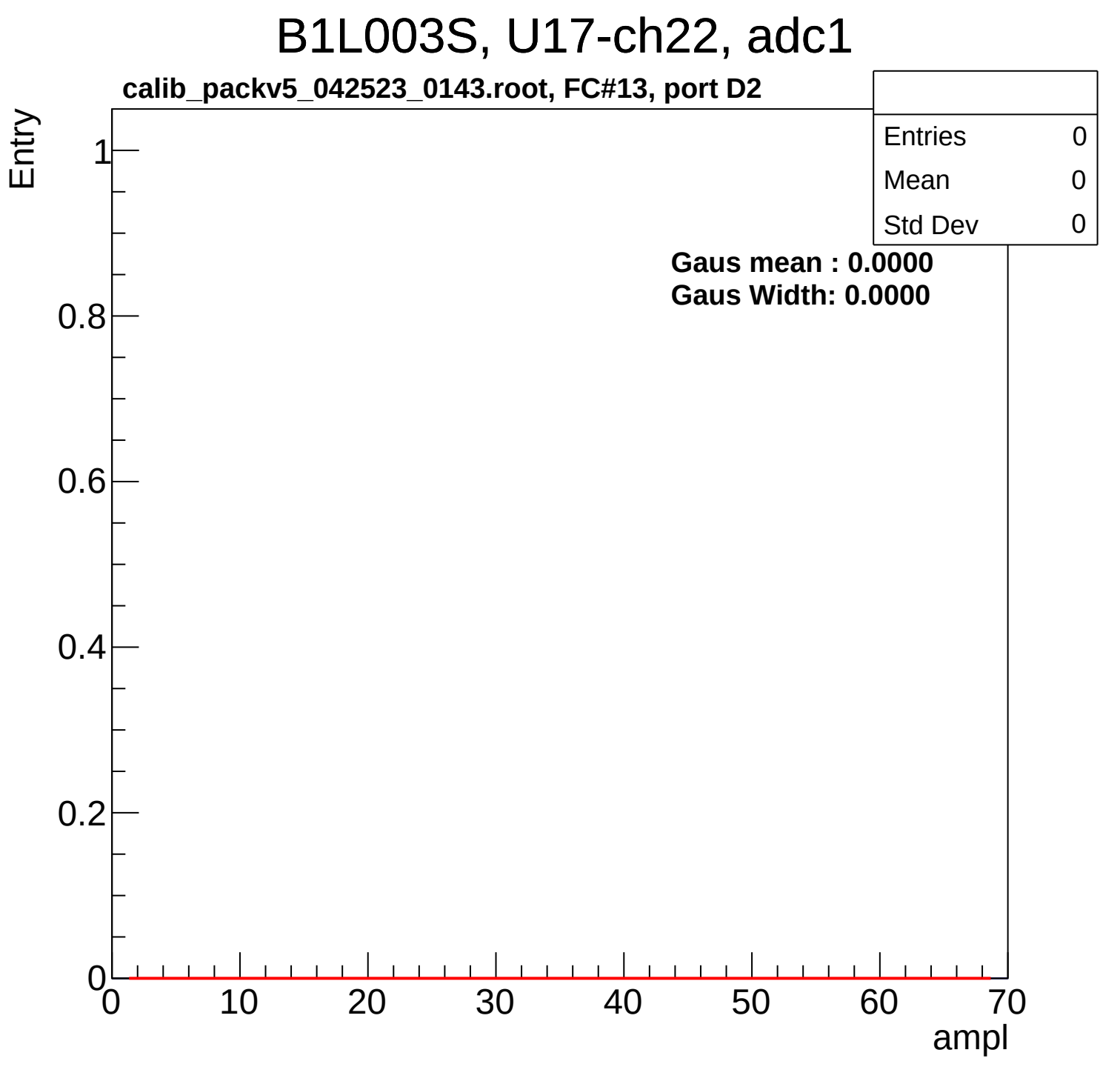
B1L003S, U17-ch22, adc1
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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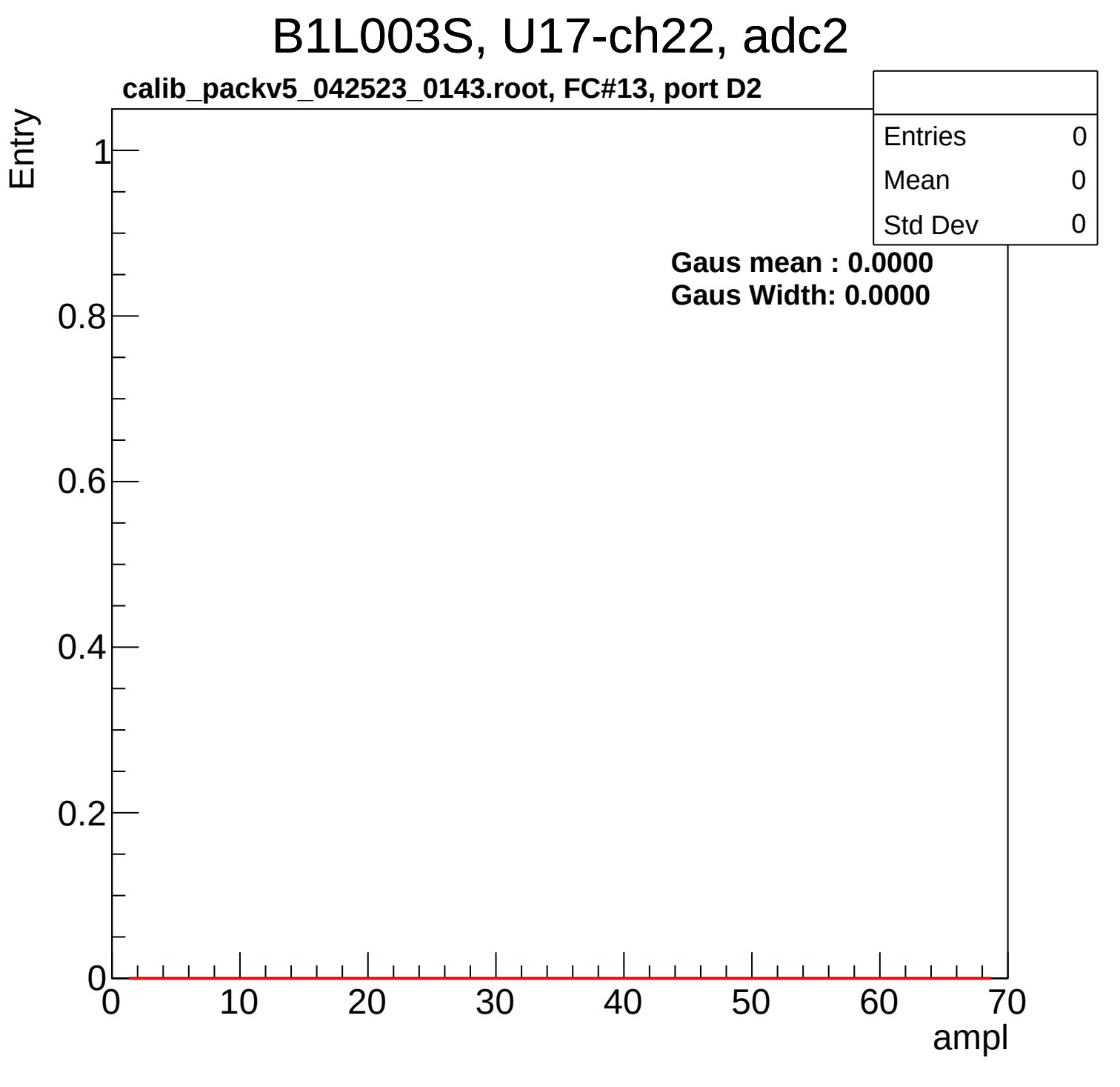
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Std Dev 0
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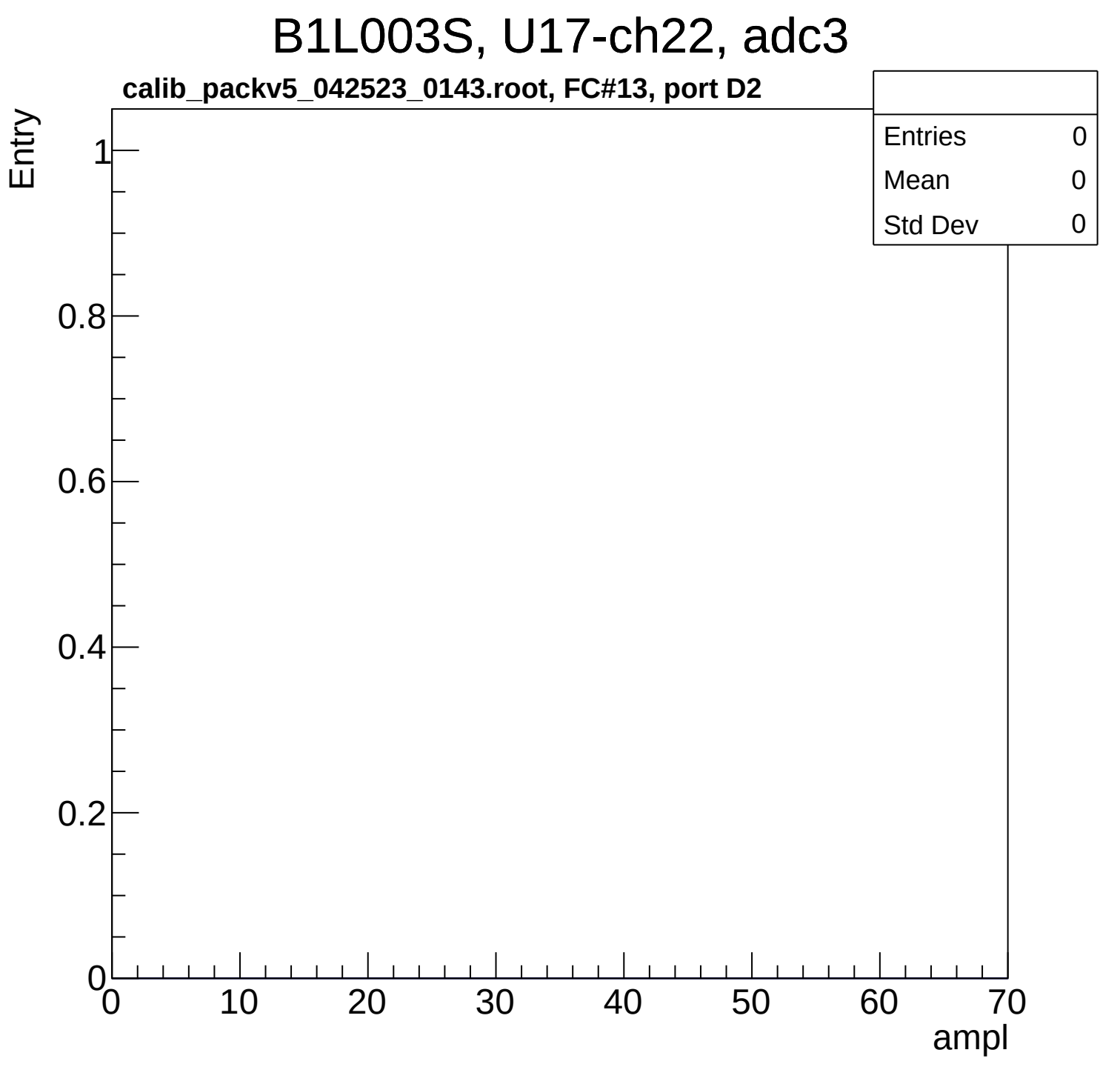
B1L003S, U17-ch22, adc3
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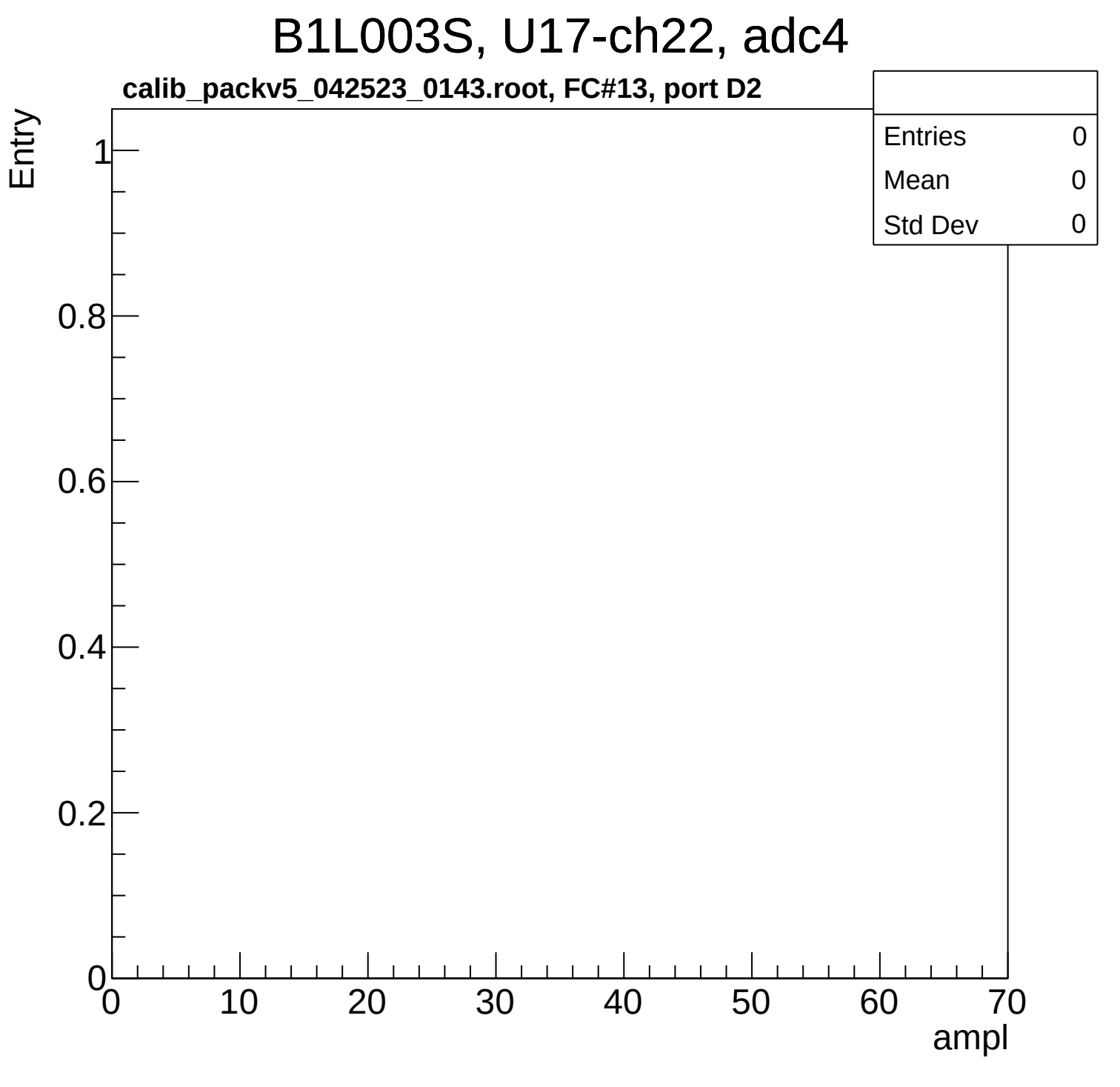
B1L003S, U17-ch22, adc4
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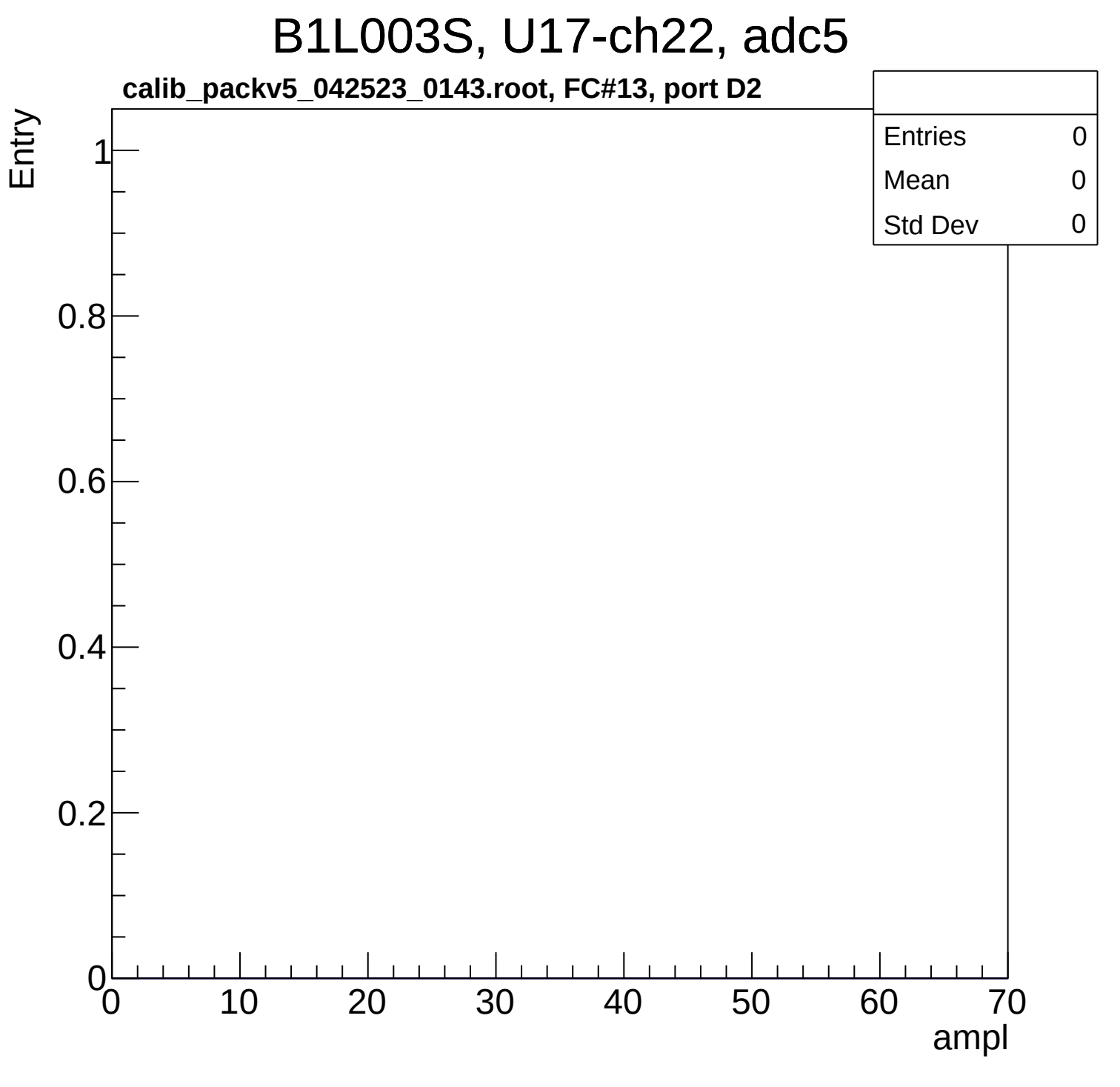
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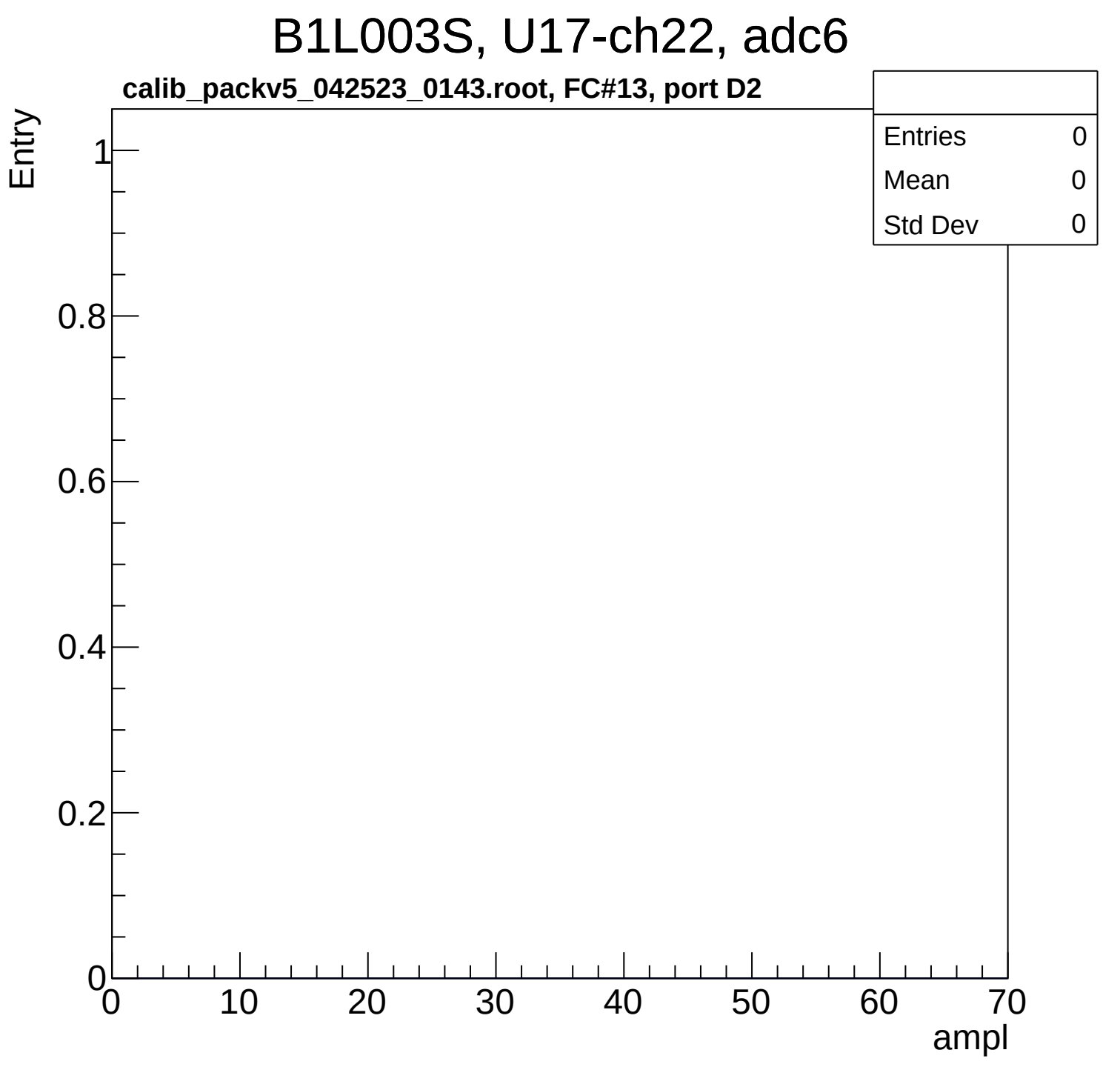
B1L003S, U17-ch22, adc6
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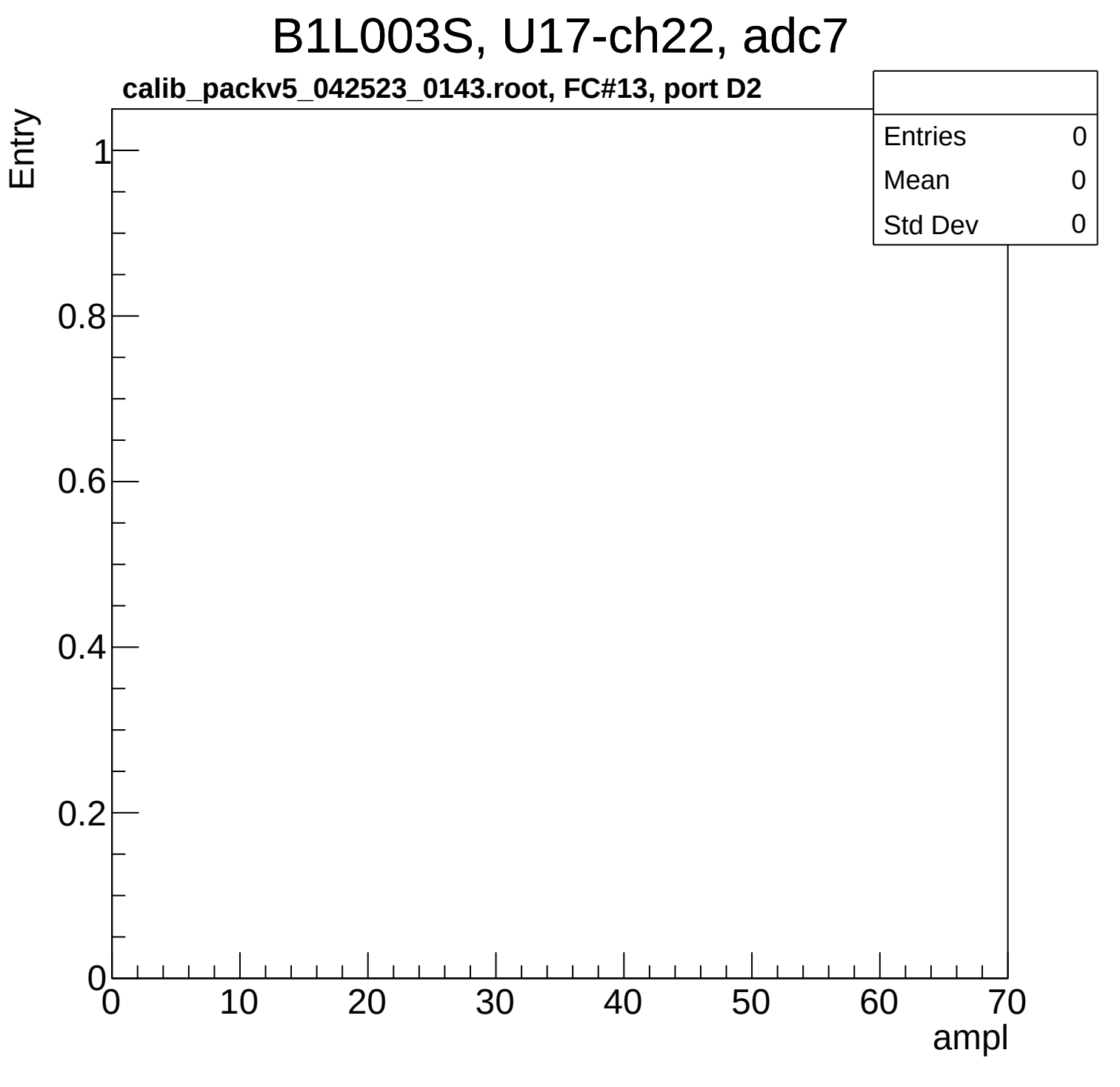
B1L003S, U17-ch22, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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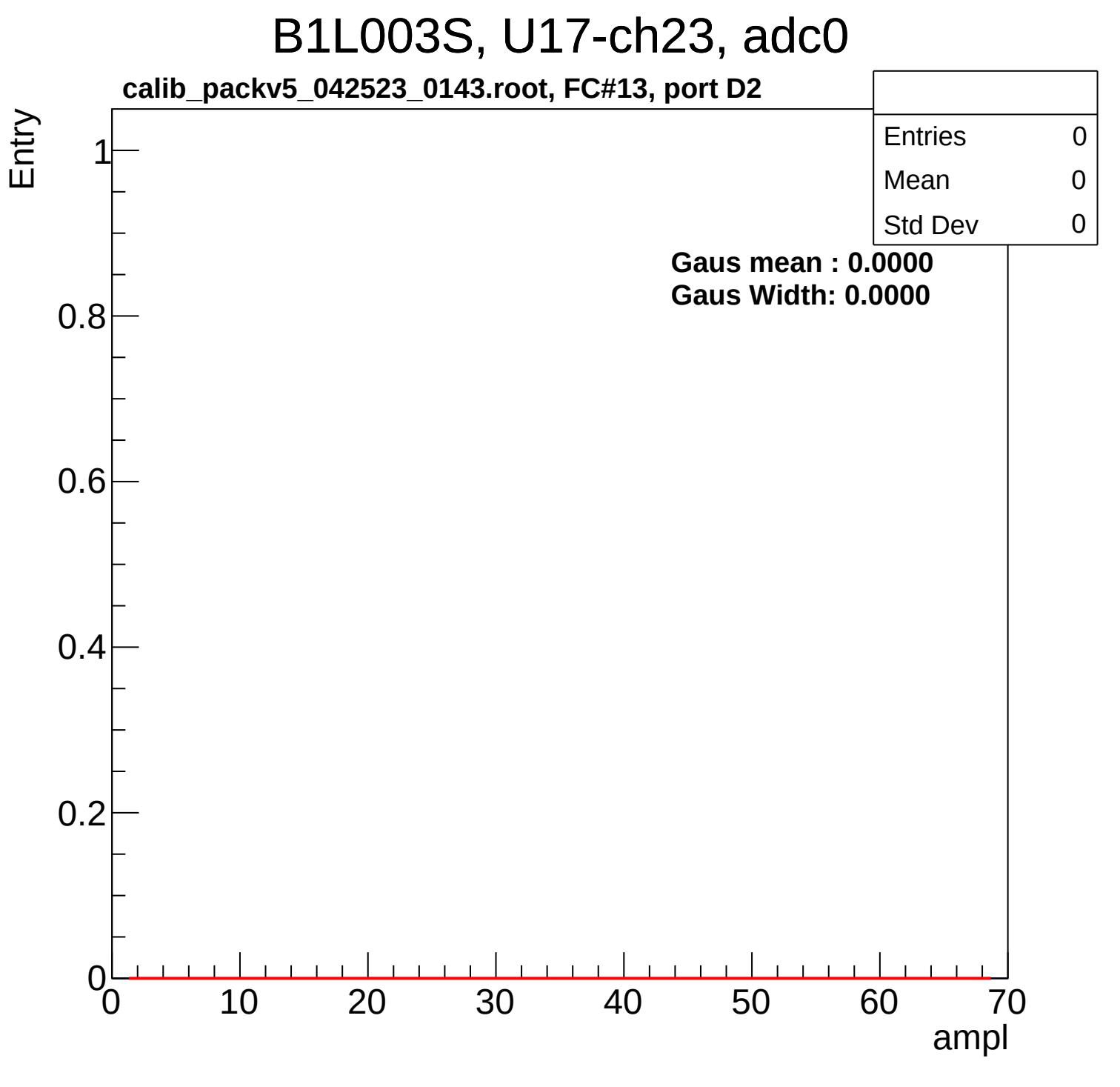
ampl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Std Dev 0
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Gaus Width: 0.0000
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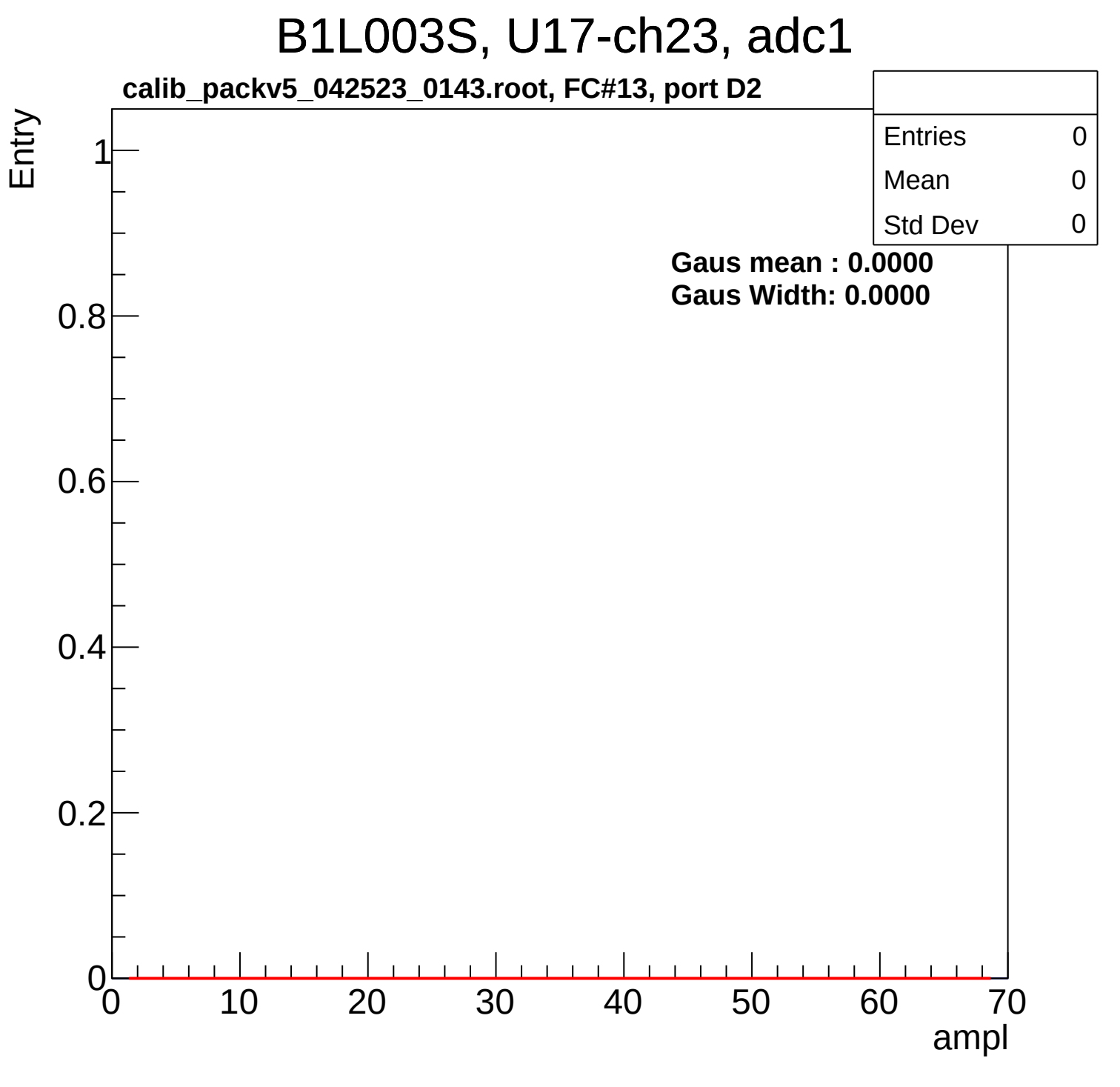
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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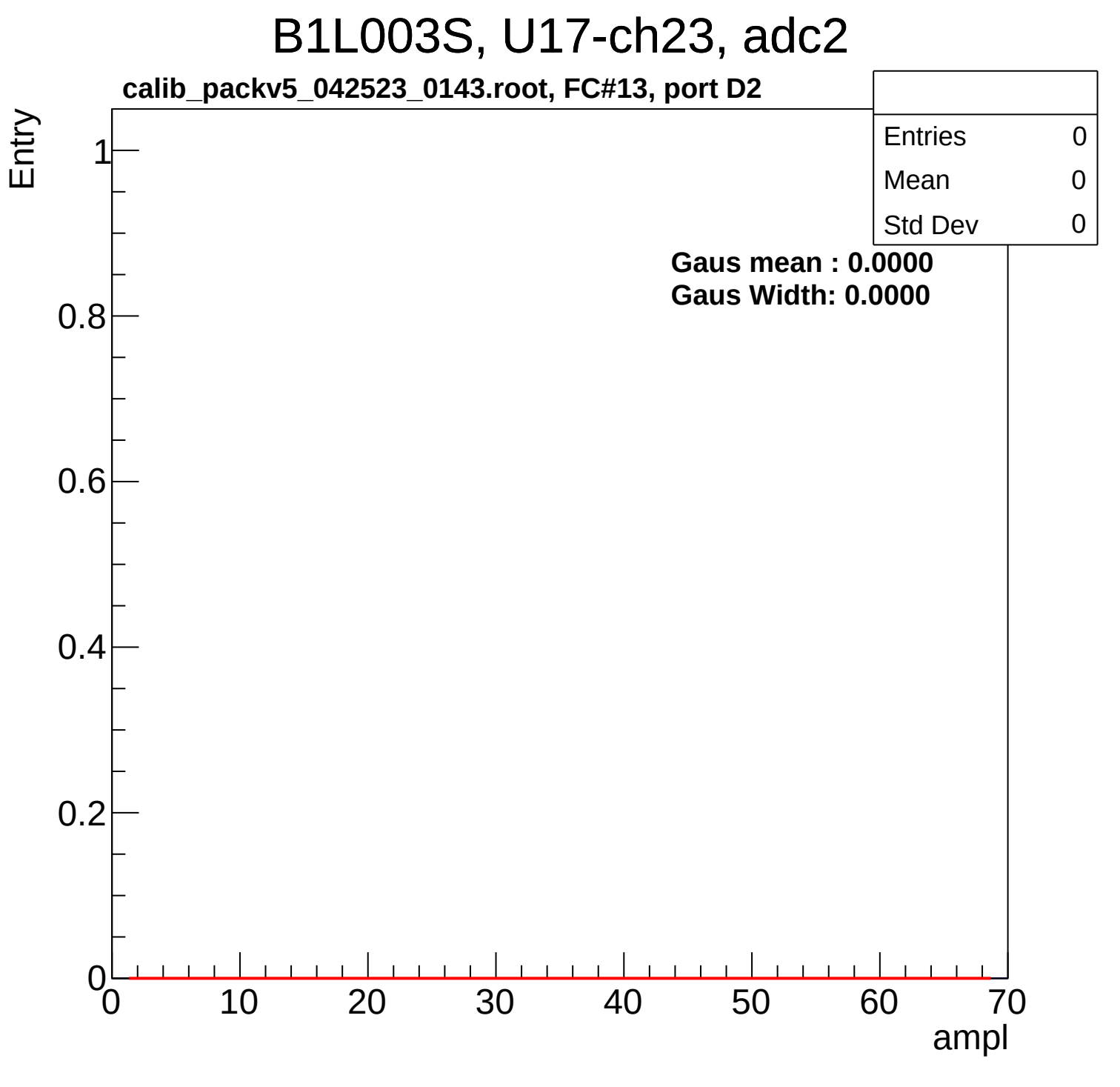
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Std Dev 0
Gaus mean : 0.0000
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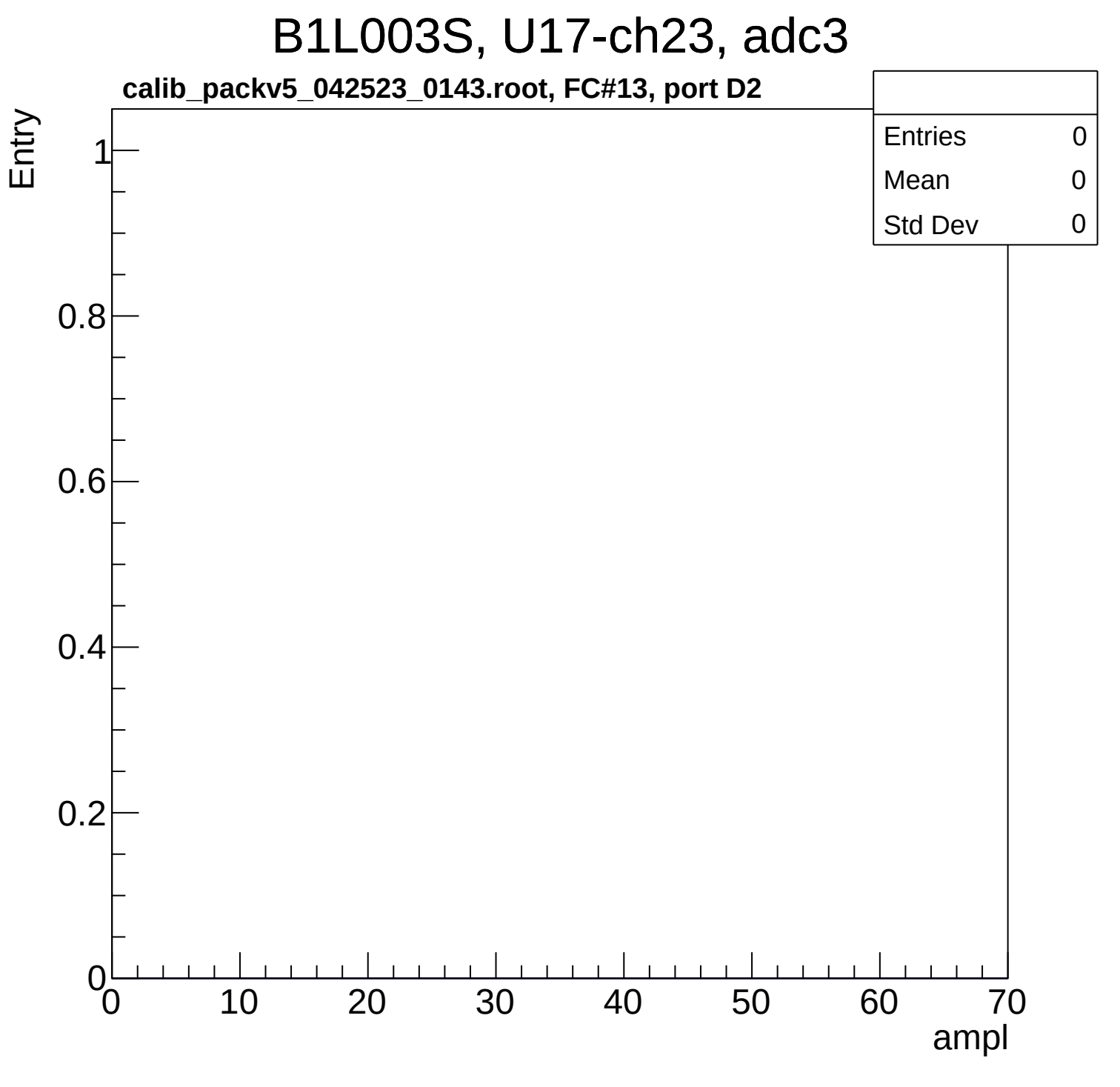
B1L003S, U17-ch23, adc3
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Entries 0
Mean 0
Std Dev 0
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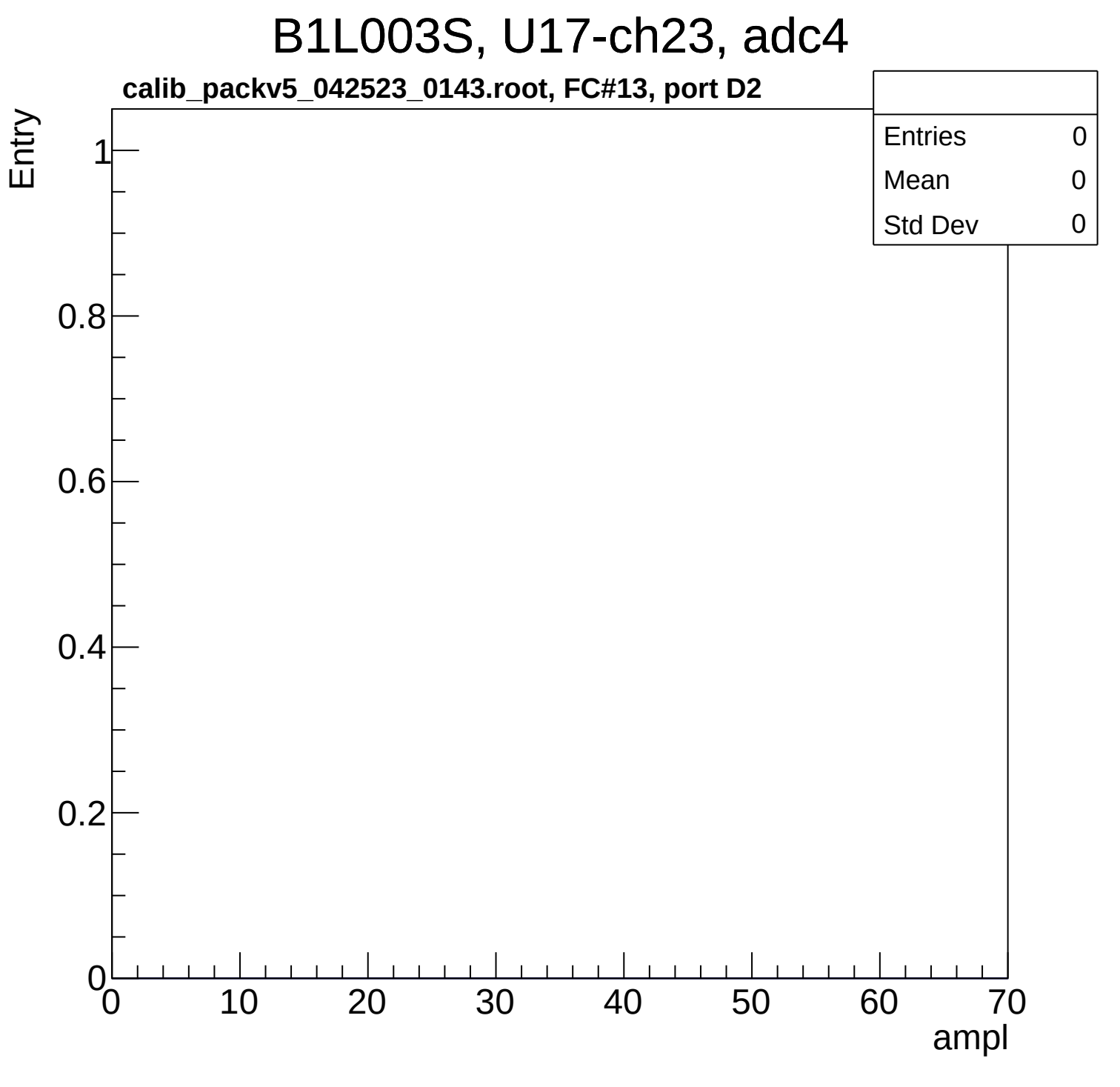
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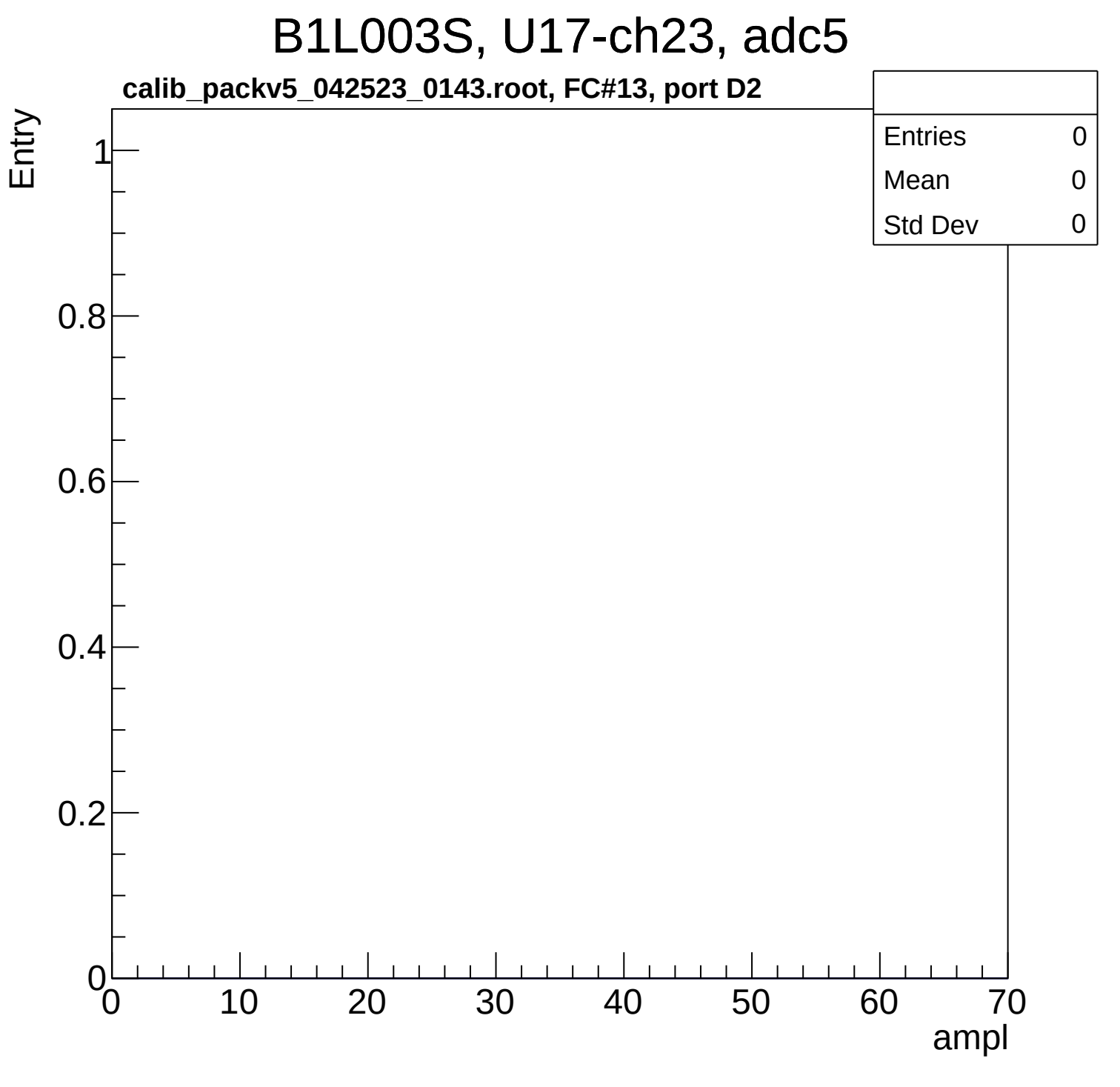
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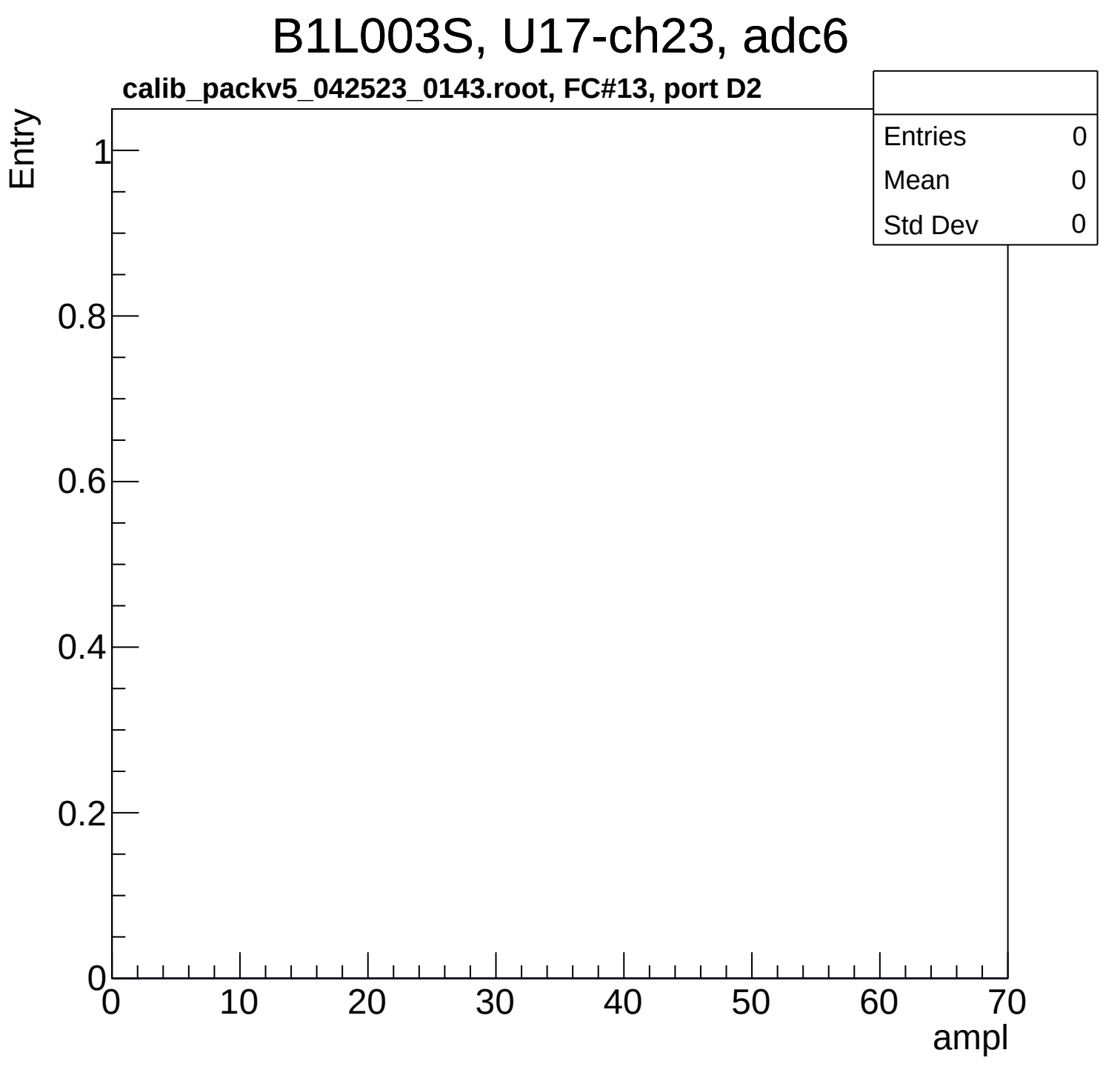
B1L003S, U17-ch23, adc6
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Std Dev 0
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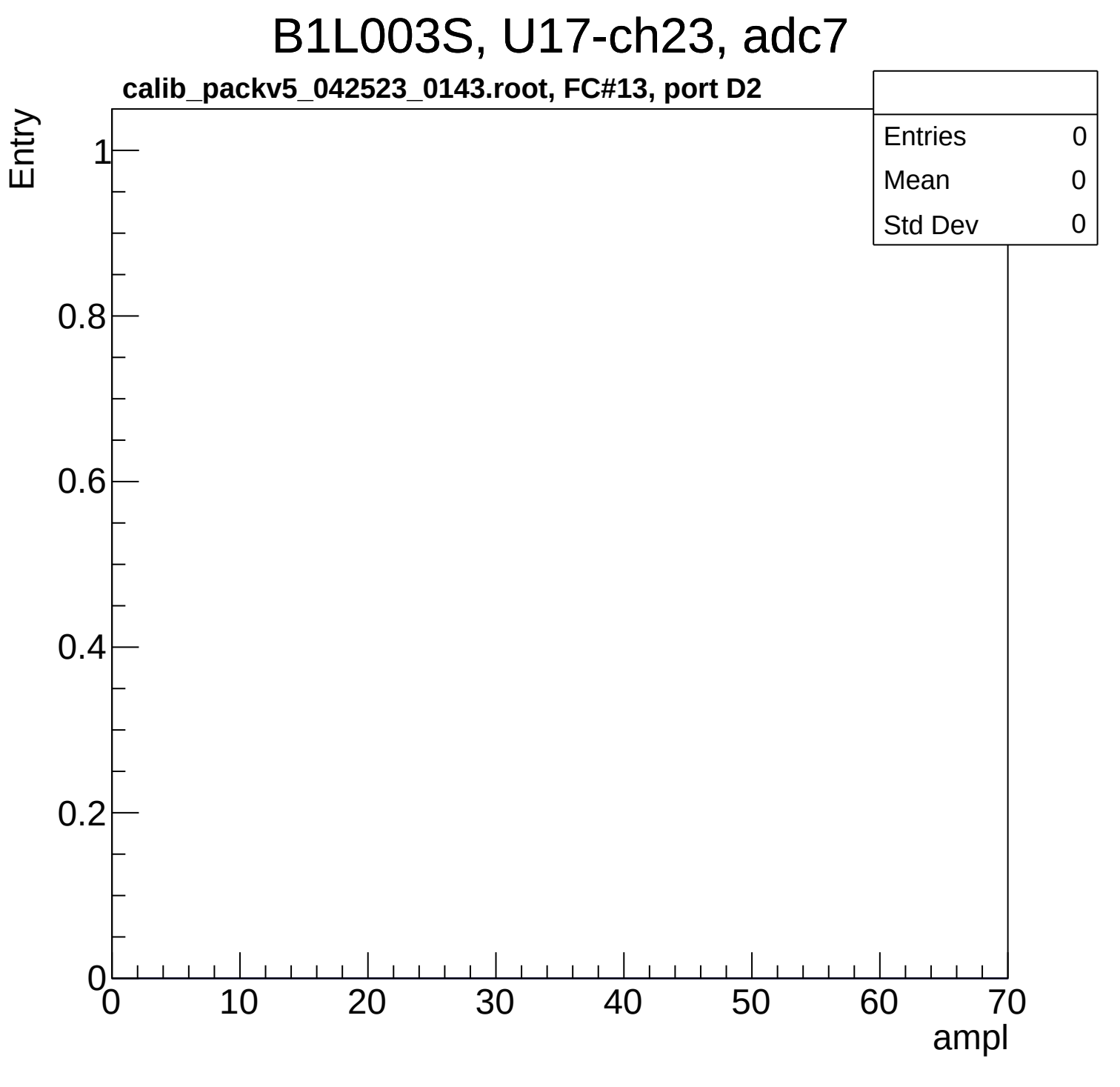
ampl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calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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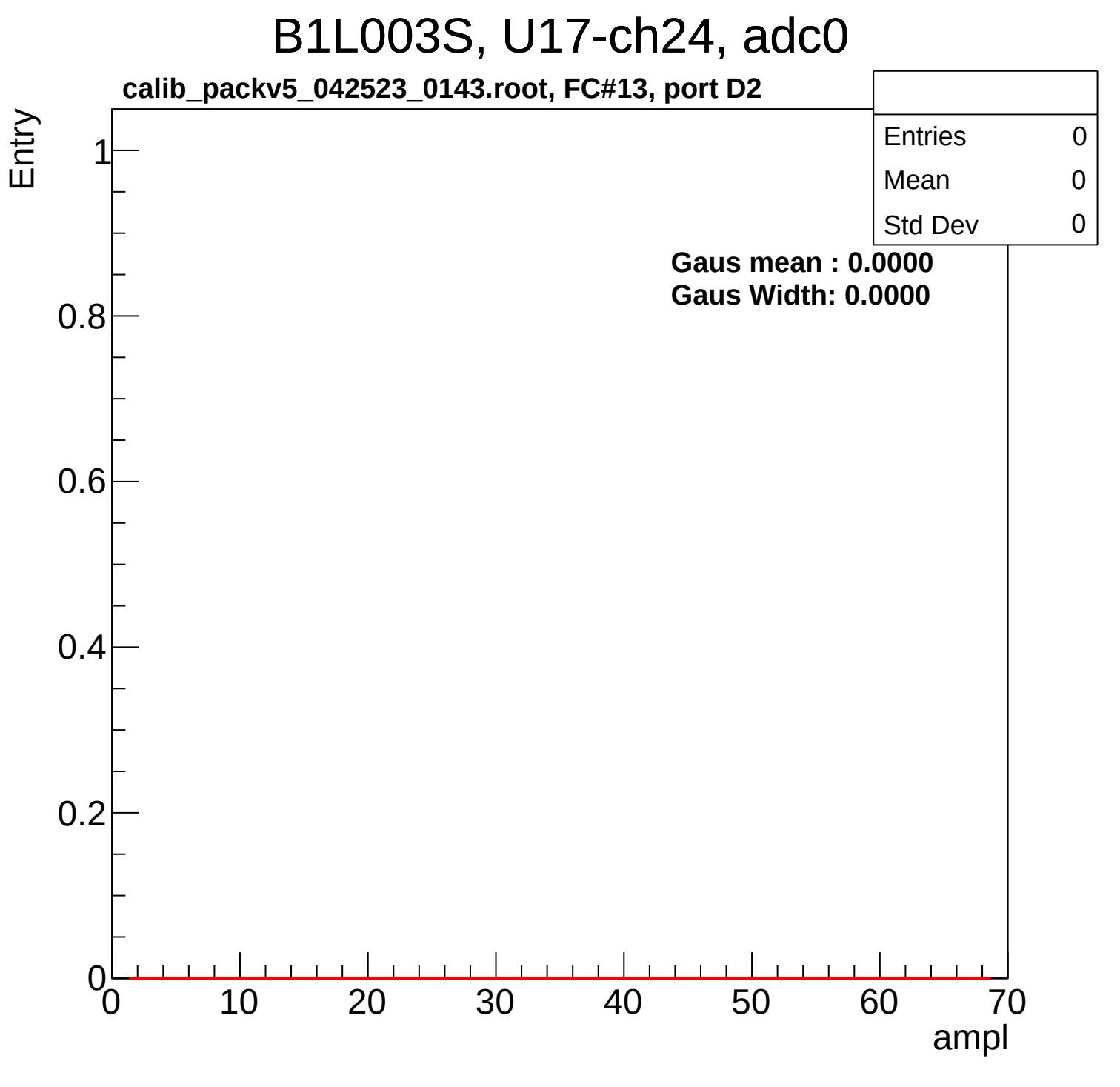
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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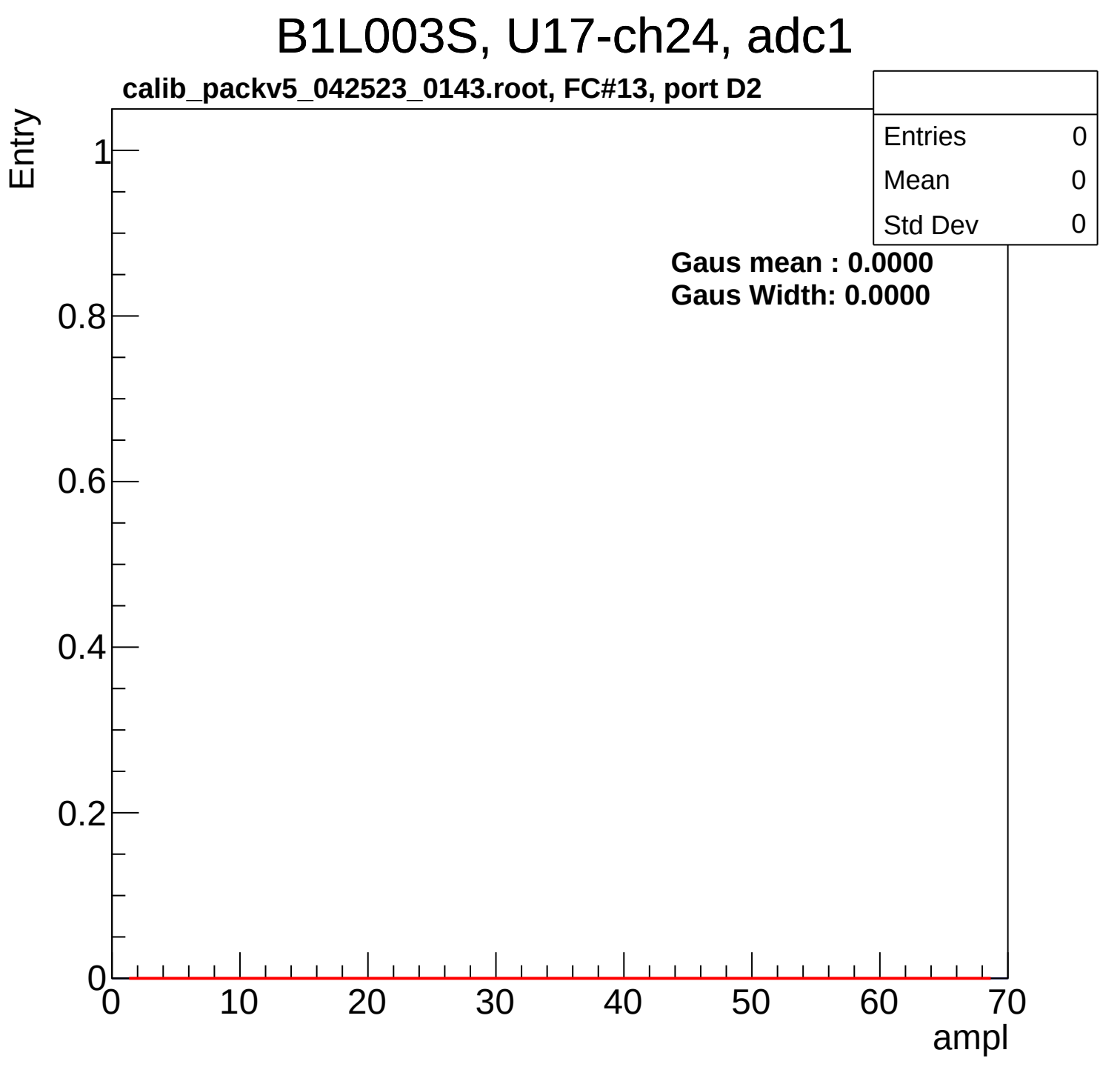
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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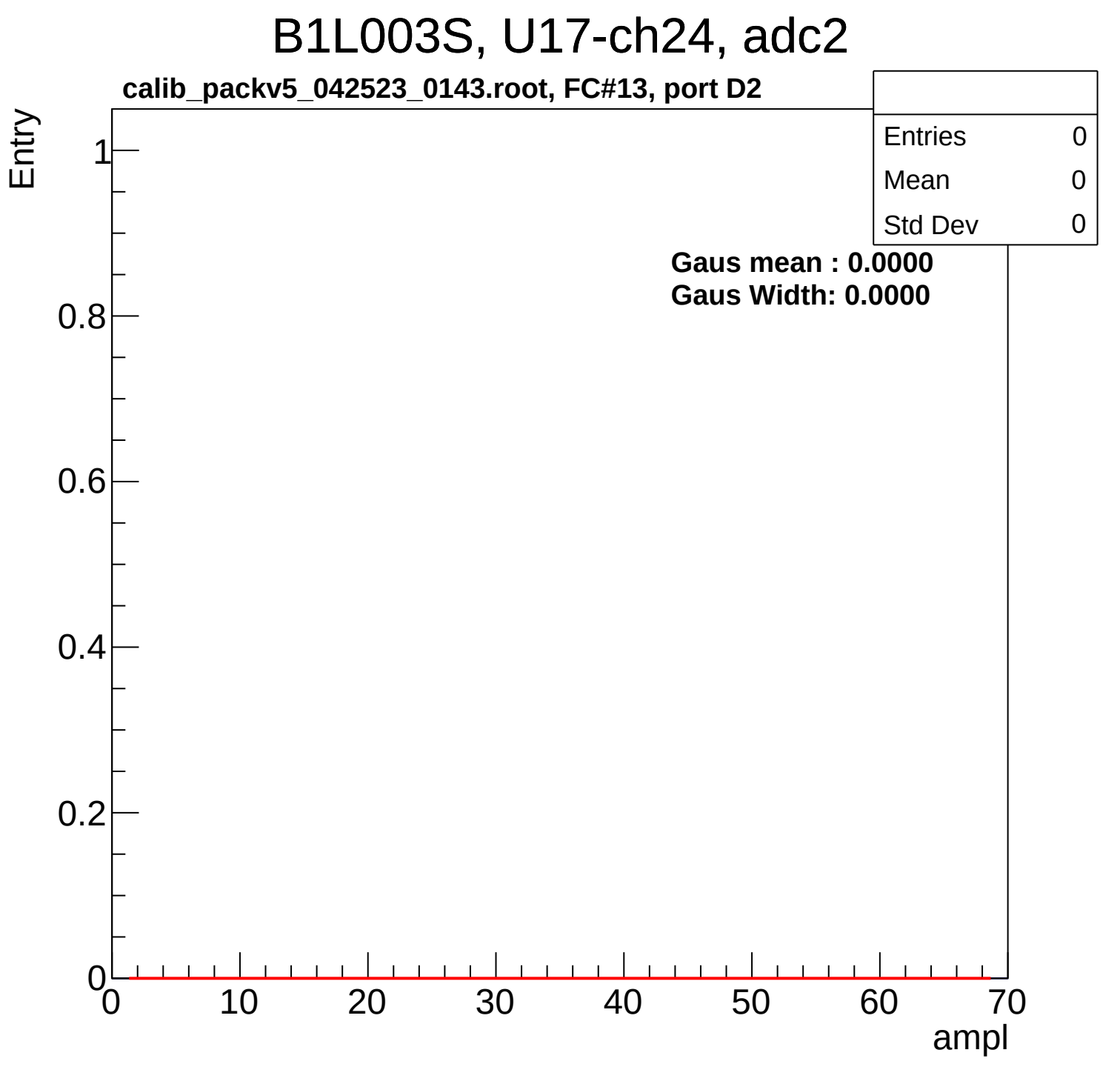
B1L003S, U17-ch24, adc2
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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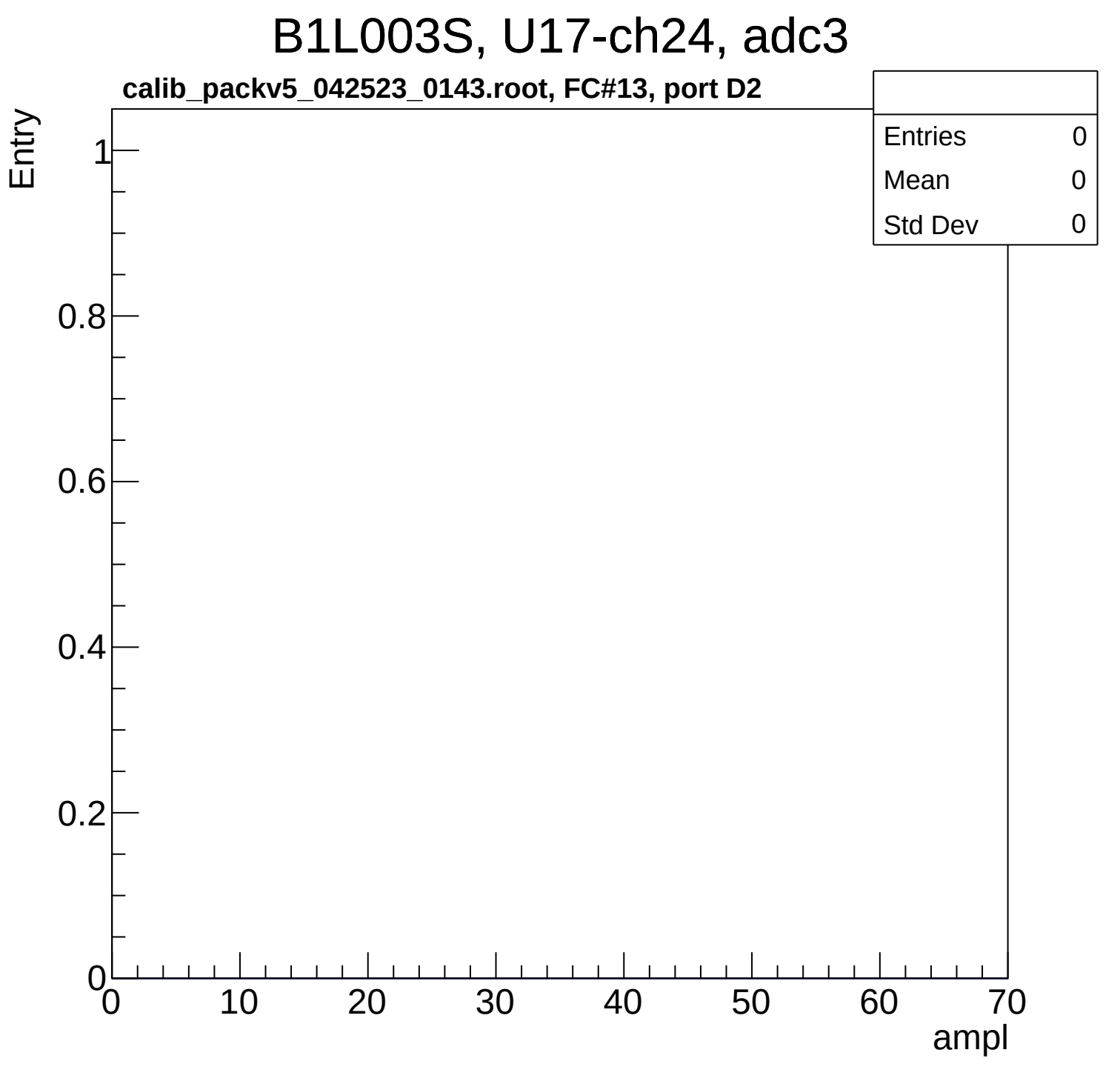
B1L003S, U17-ch24, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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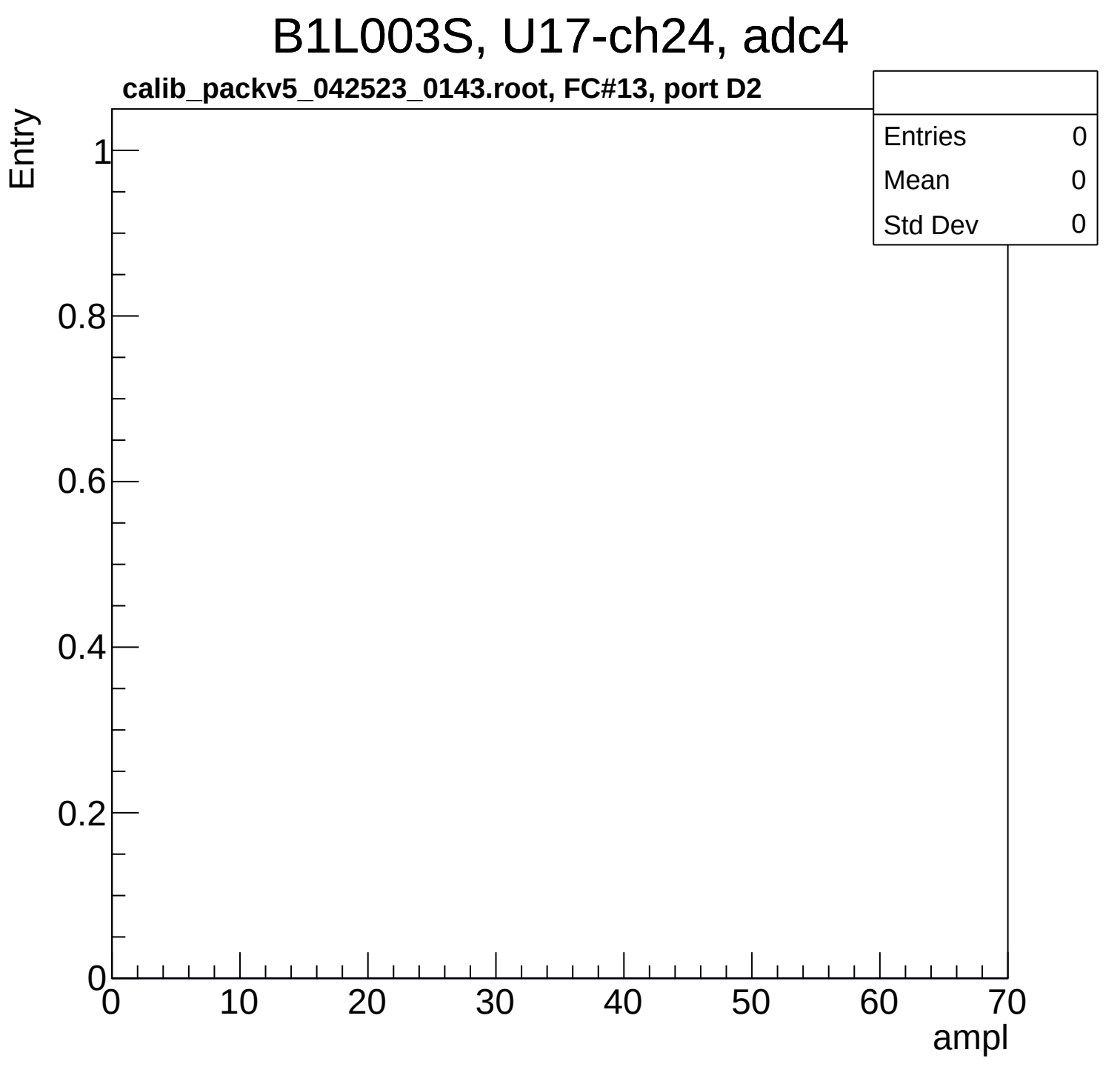
ampl


B1L003S, U17-ch24, adc4
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Mean 0
Std Dev 0
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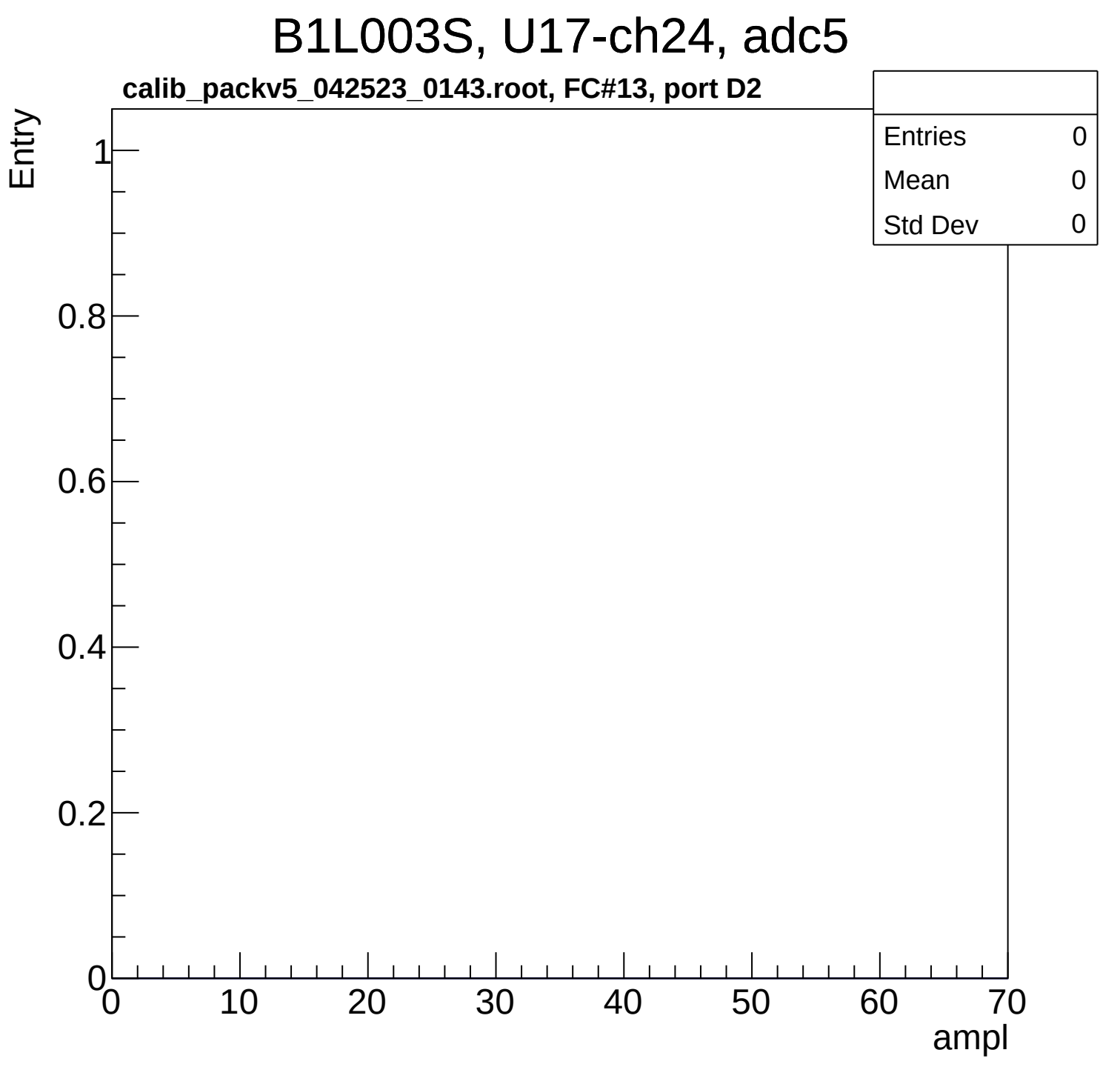
ampl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Mean 0
Std Dev 0
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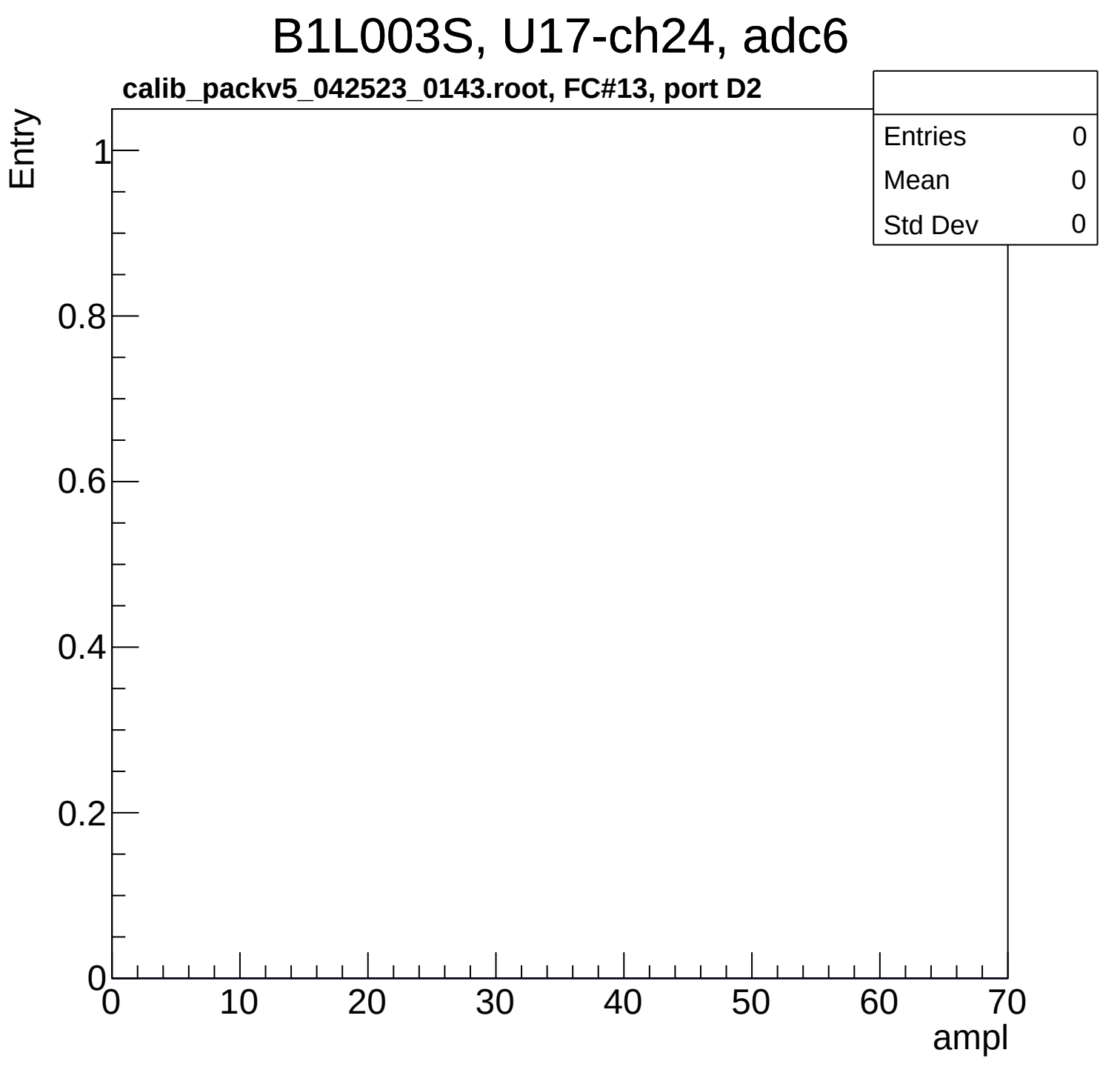
B1L003S, U17-ch24, adc6
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Mean 0
Std Dev 0
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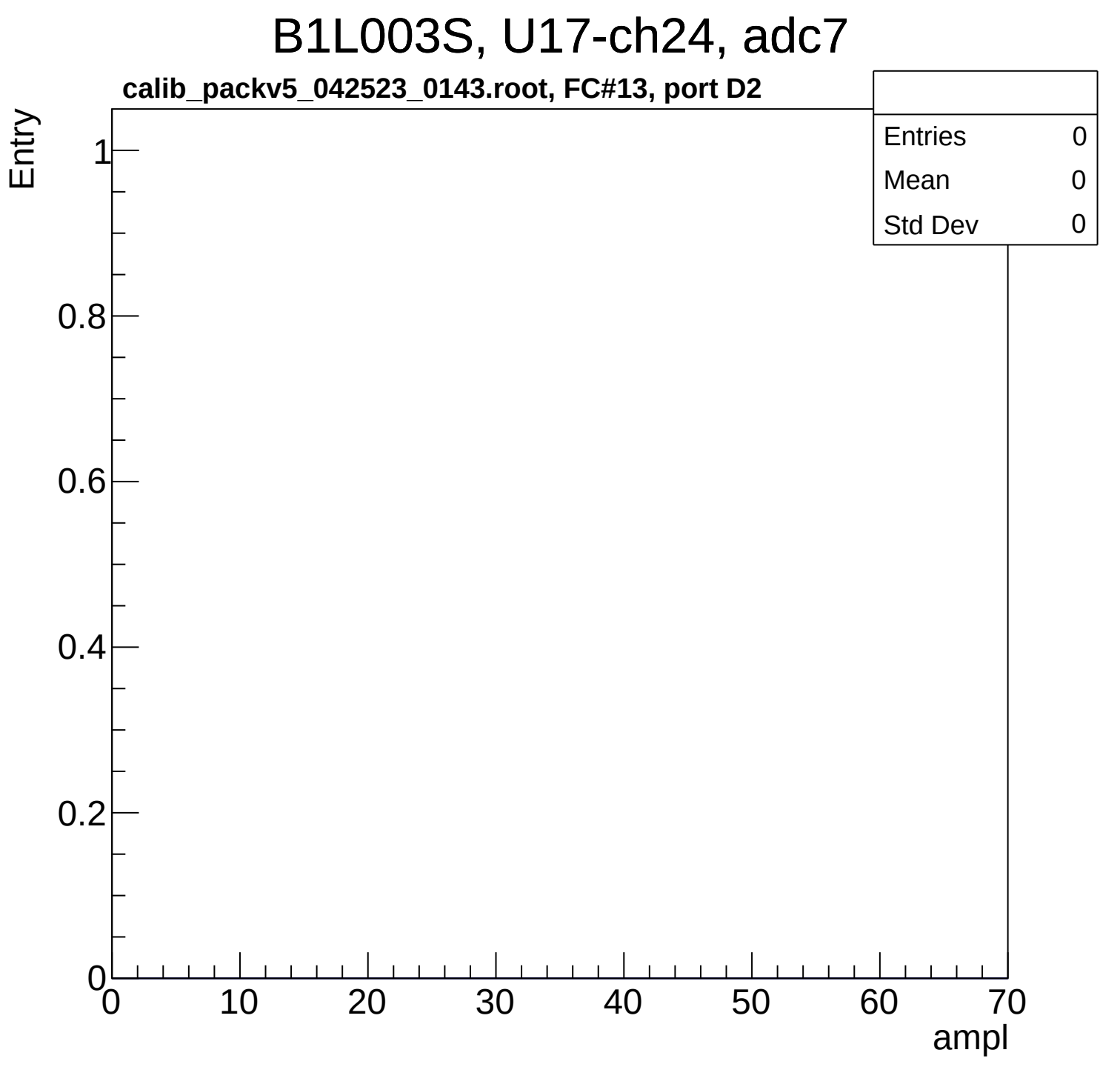
ampl


B1L003S, U17-ch24, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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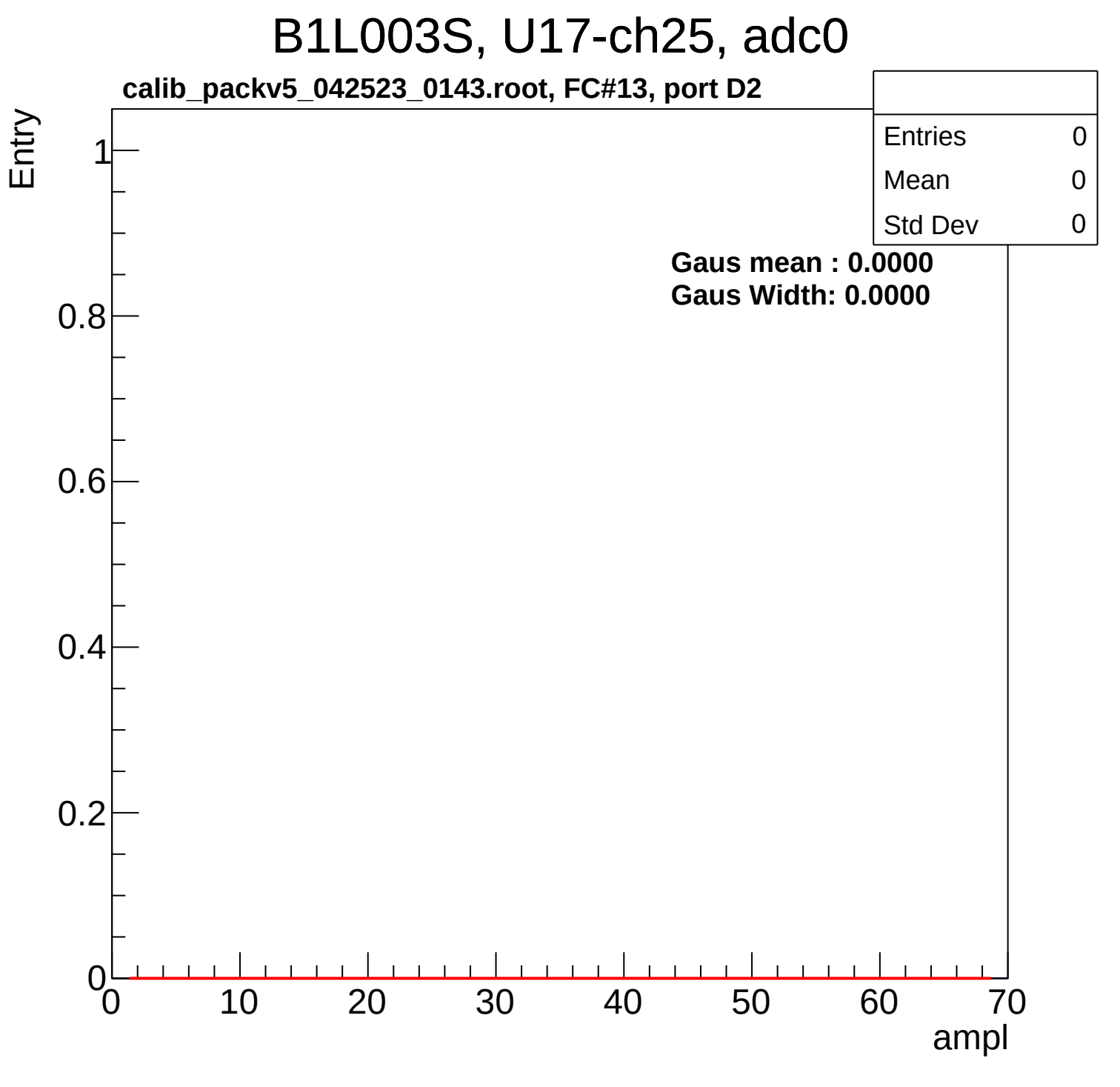
B1L003S, U17-ch25, adc0
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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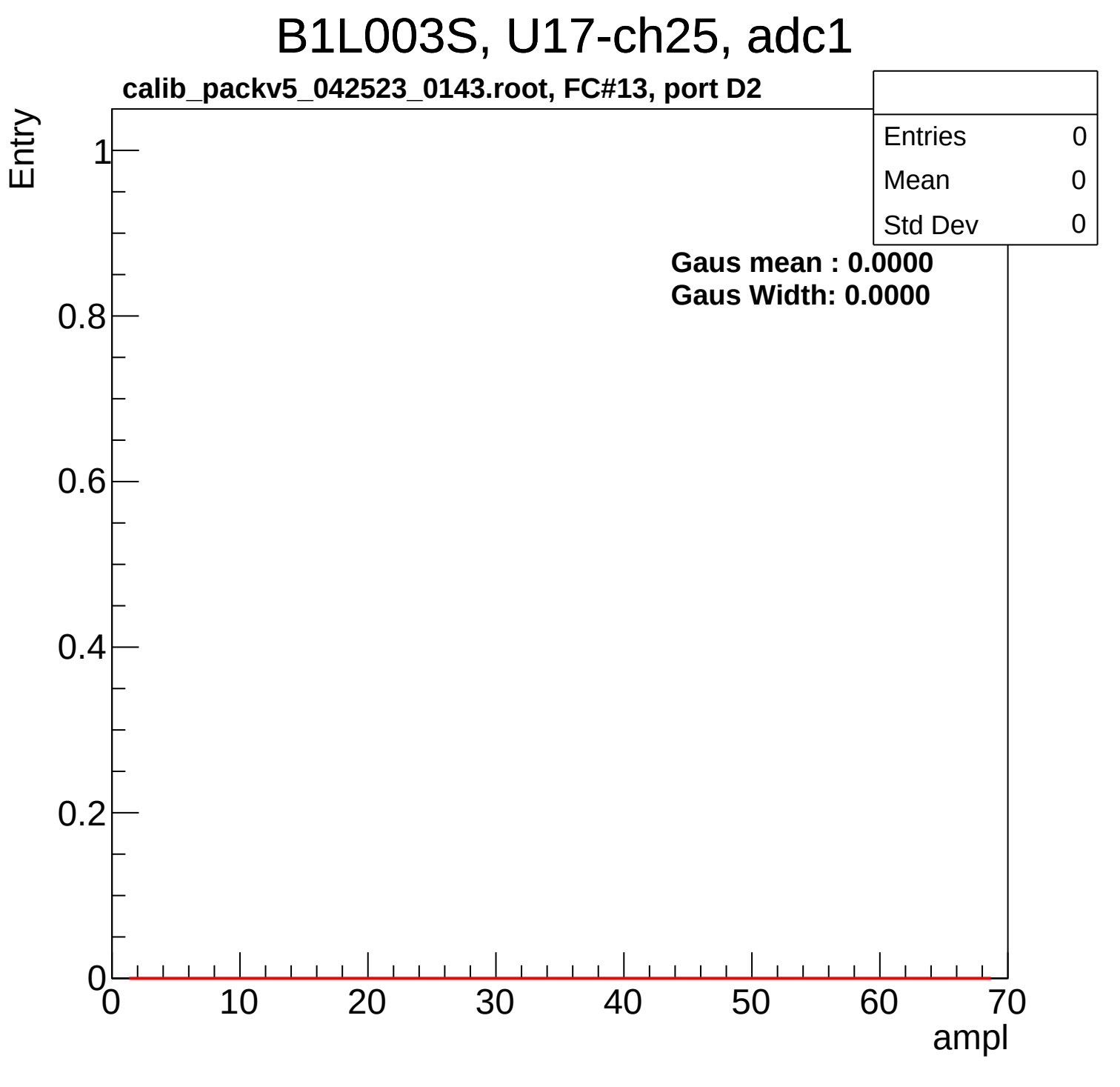
B1L003S, U17-ch25, adc1
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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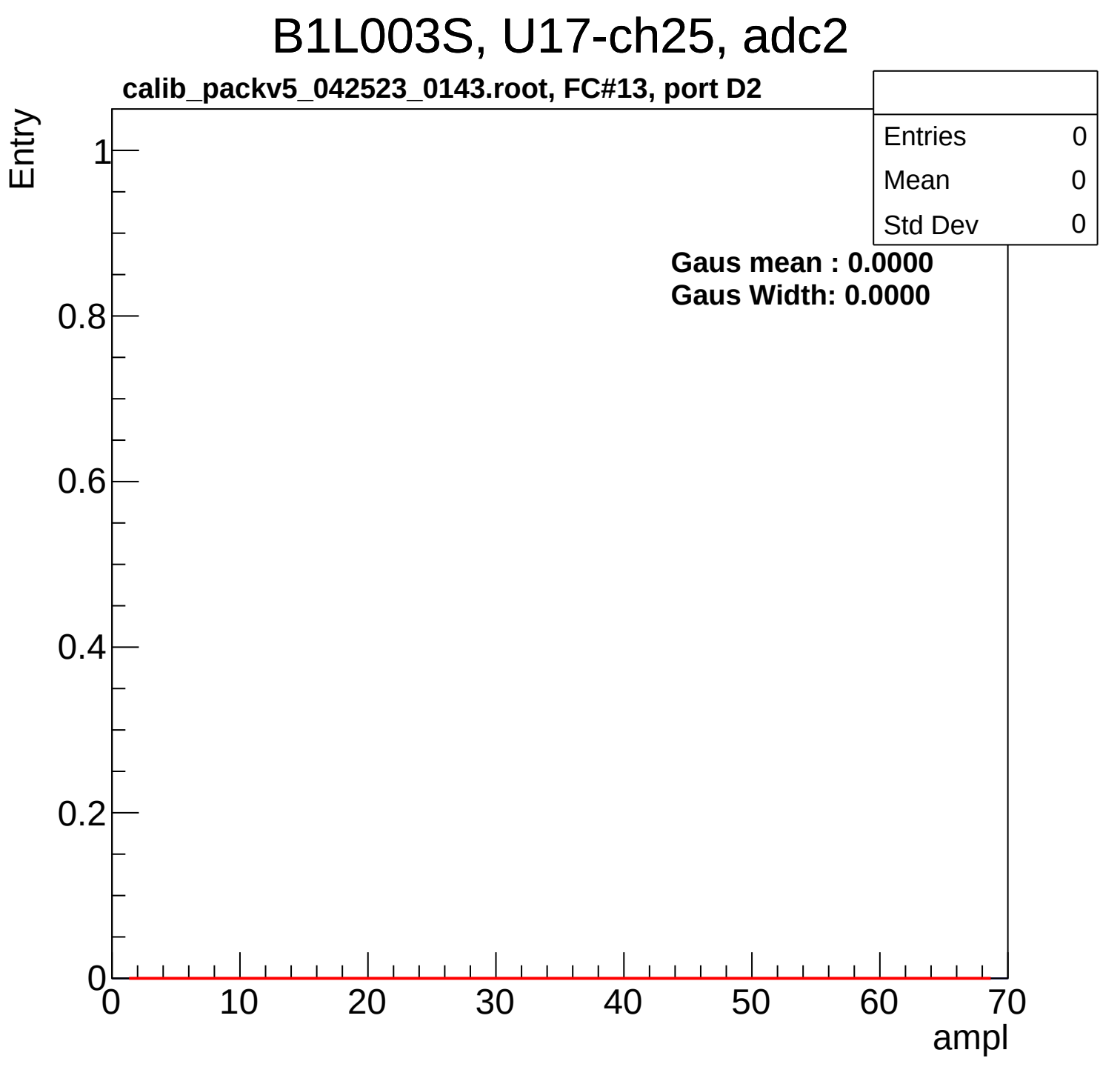
ampl


B1L003S, U17-ch25, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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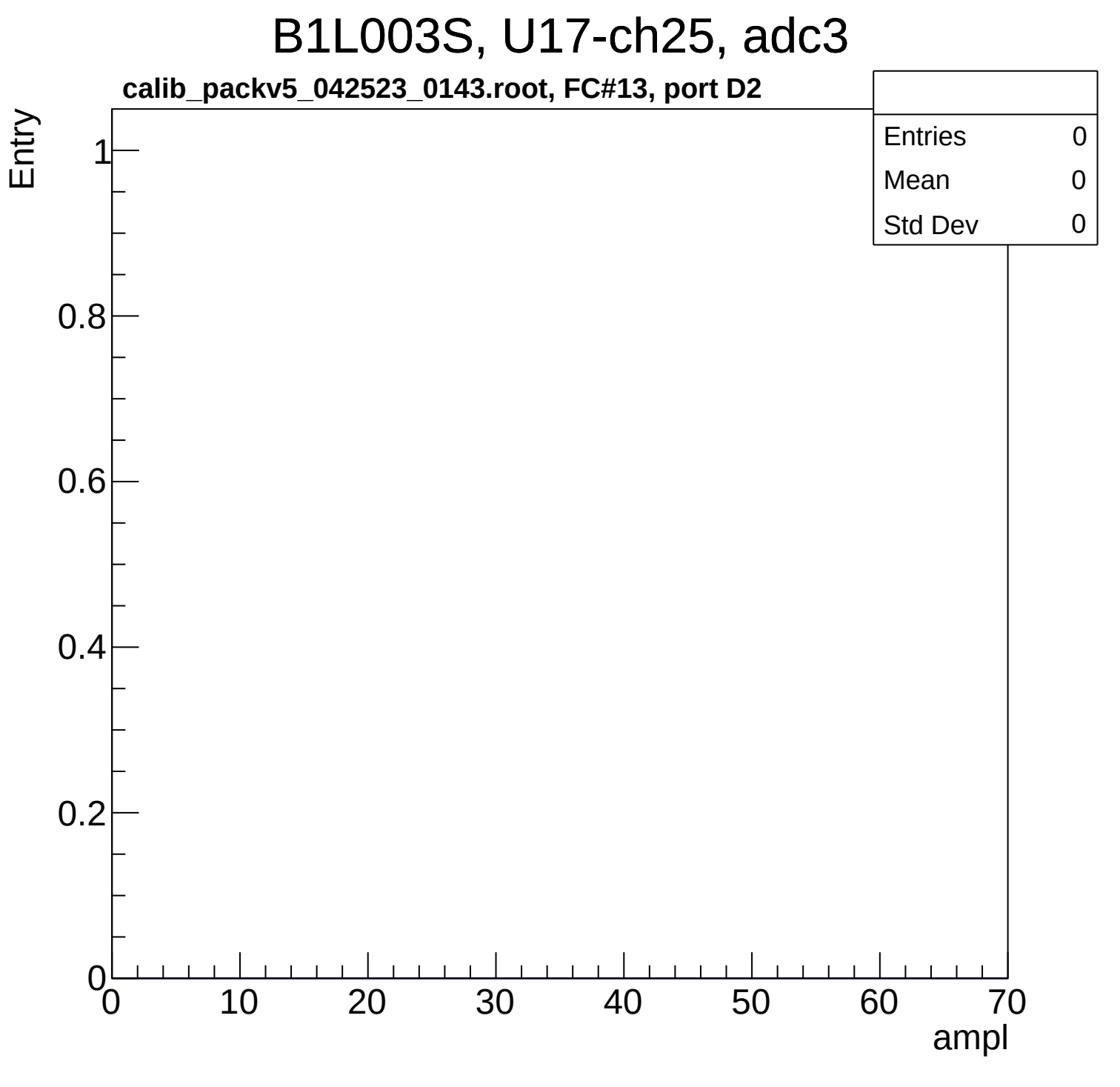
ampl


B1L003S, U17-ch25, adc3
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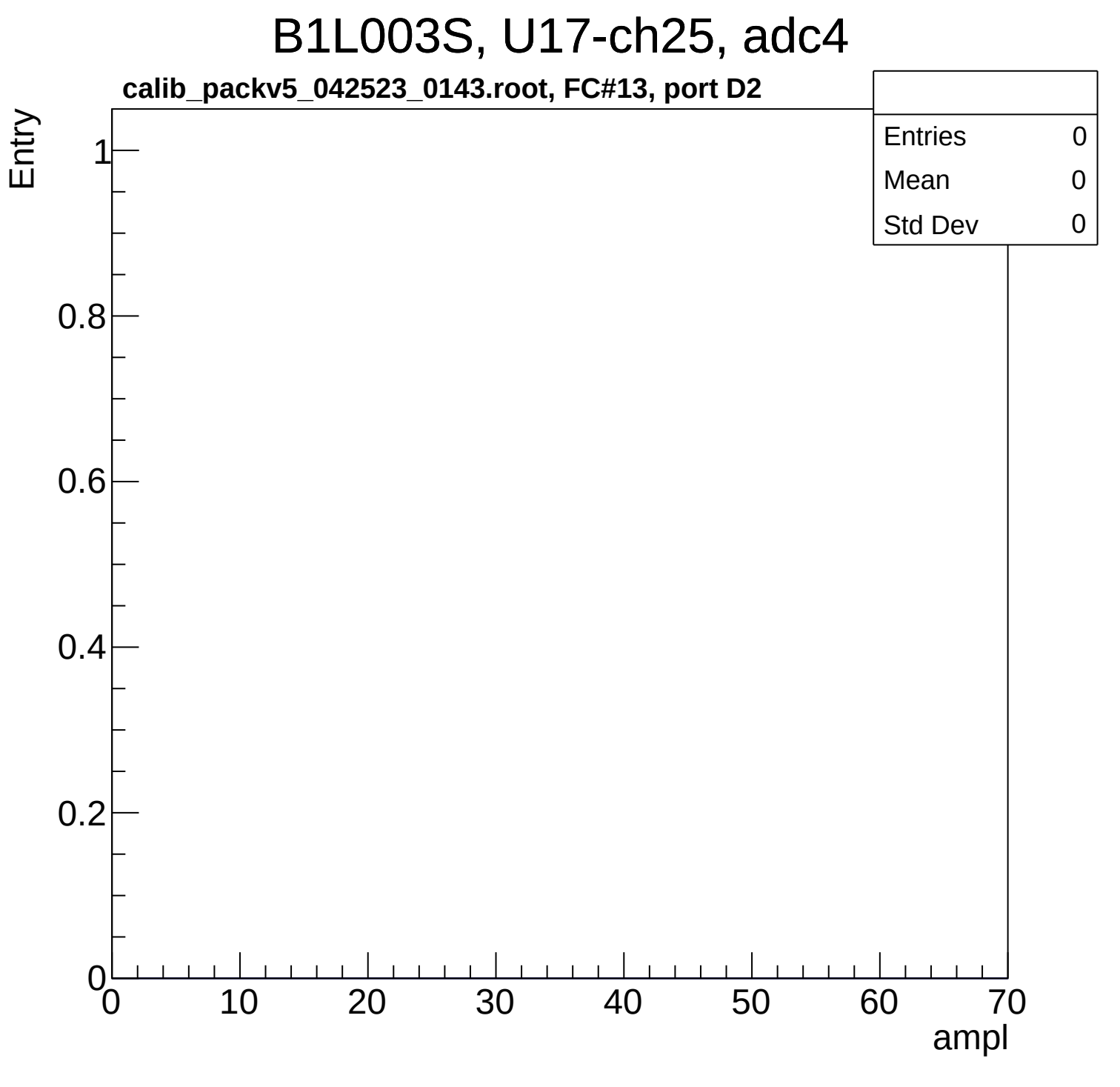
ampl


B1L003S, U17-ch25, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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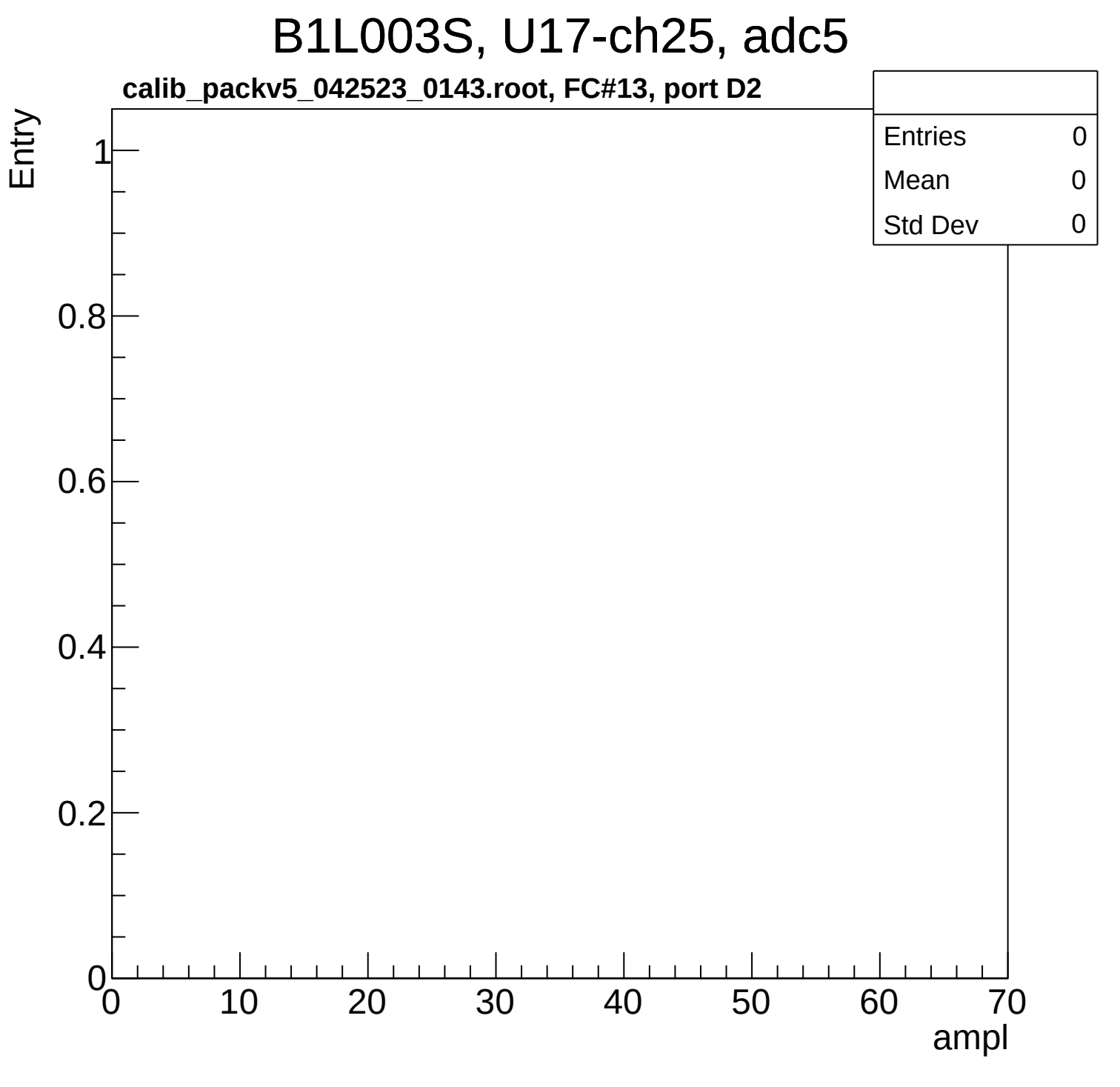
ampl


B1L003S, U17-ch25, adc5
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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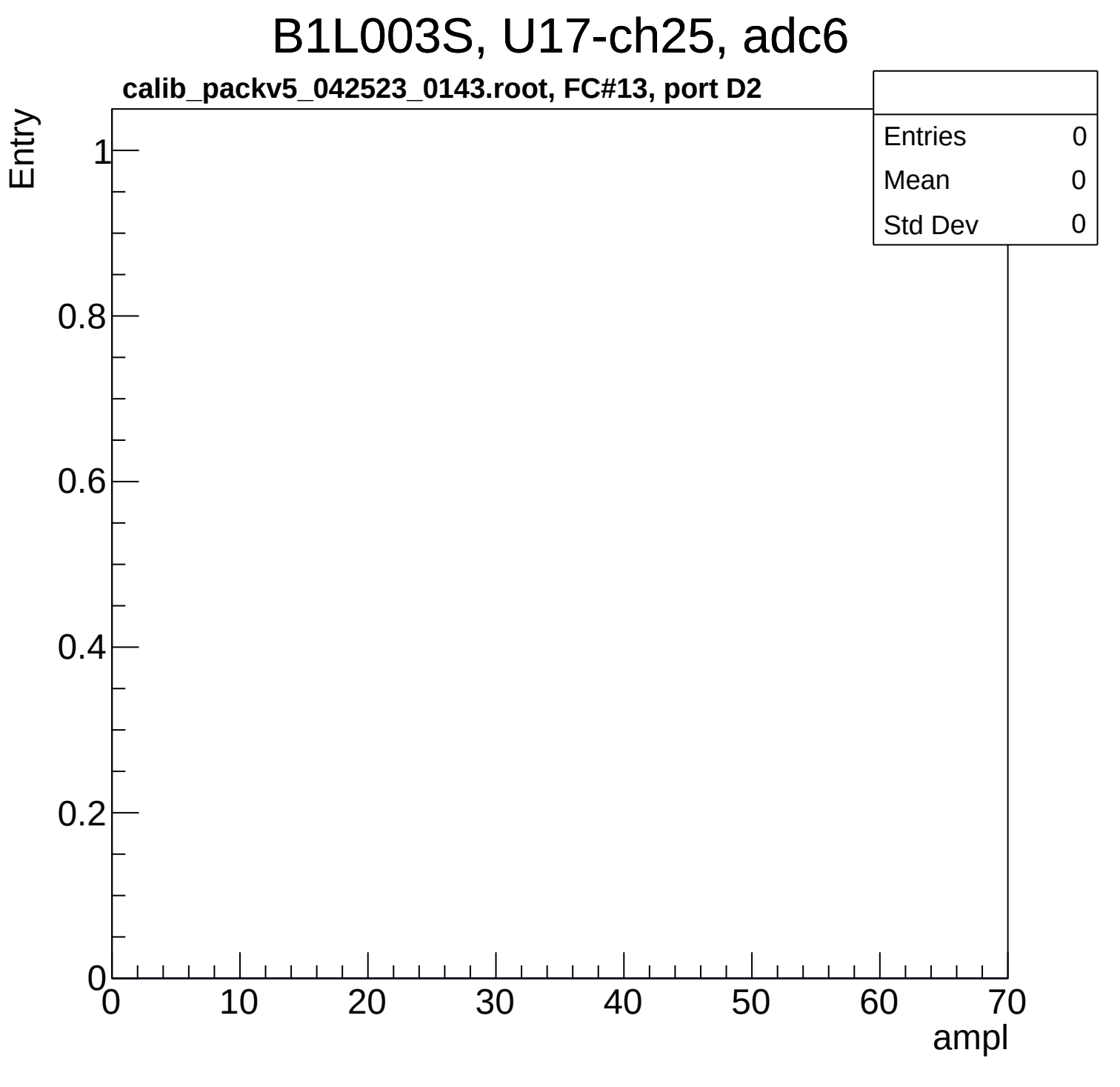
B1L003S, U17-ch25, adc6
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Std Dev 0
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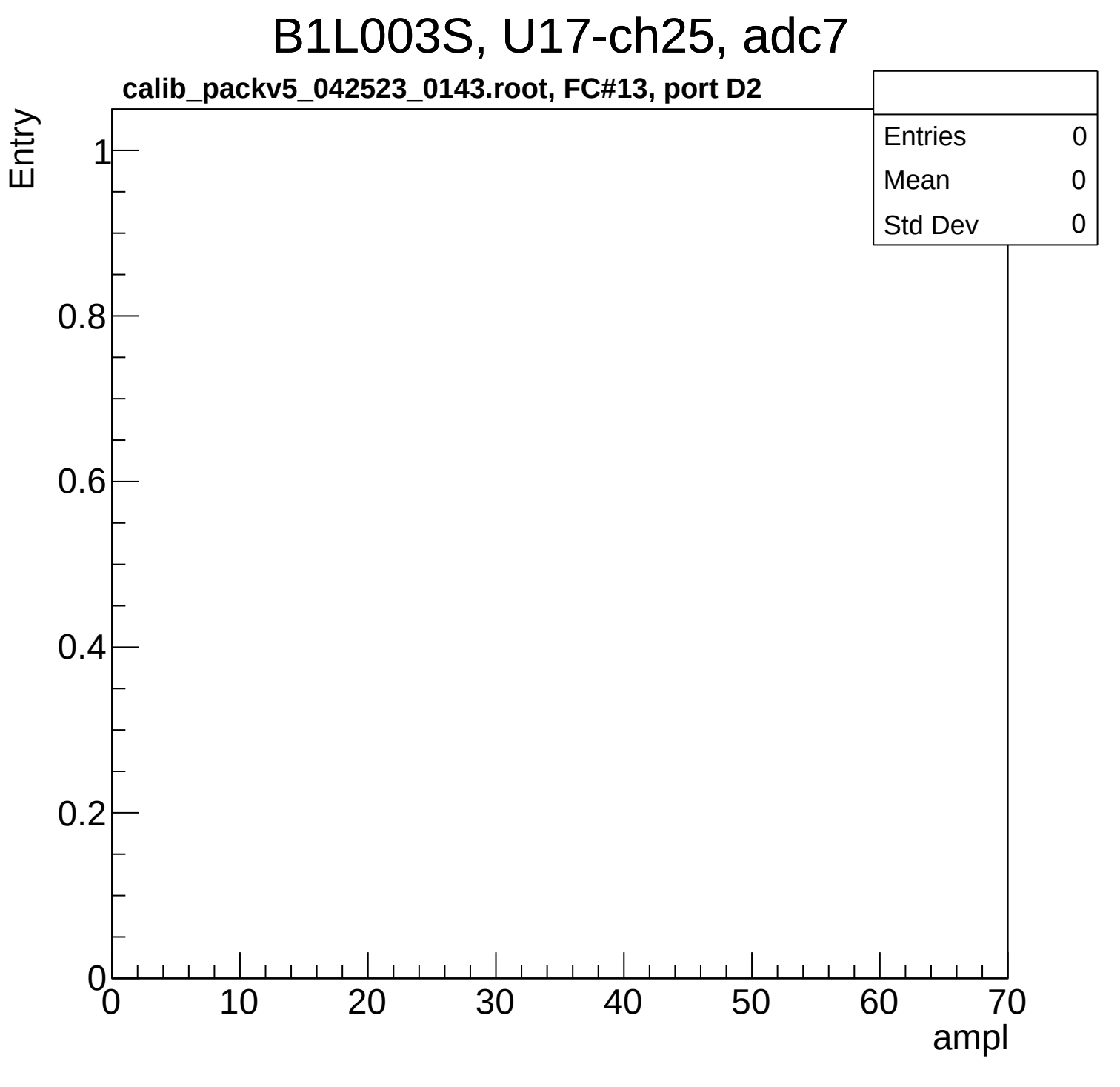
B1L003S, U17-ch25, adc7
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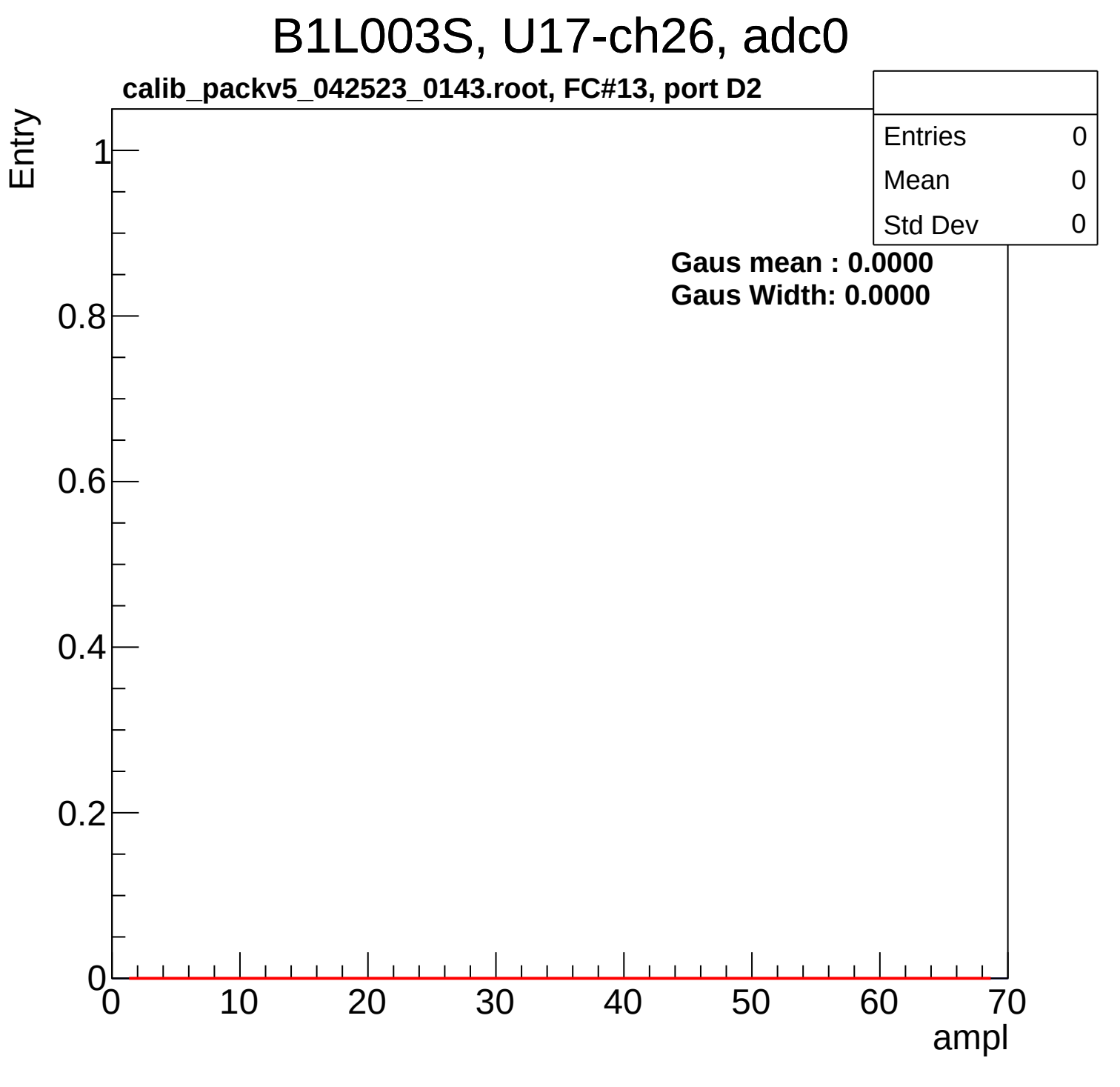
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Std Dev 0
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Gaus Width: 0.0000
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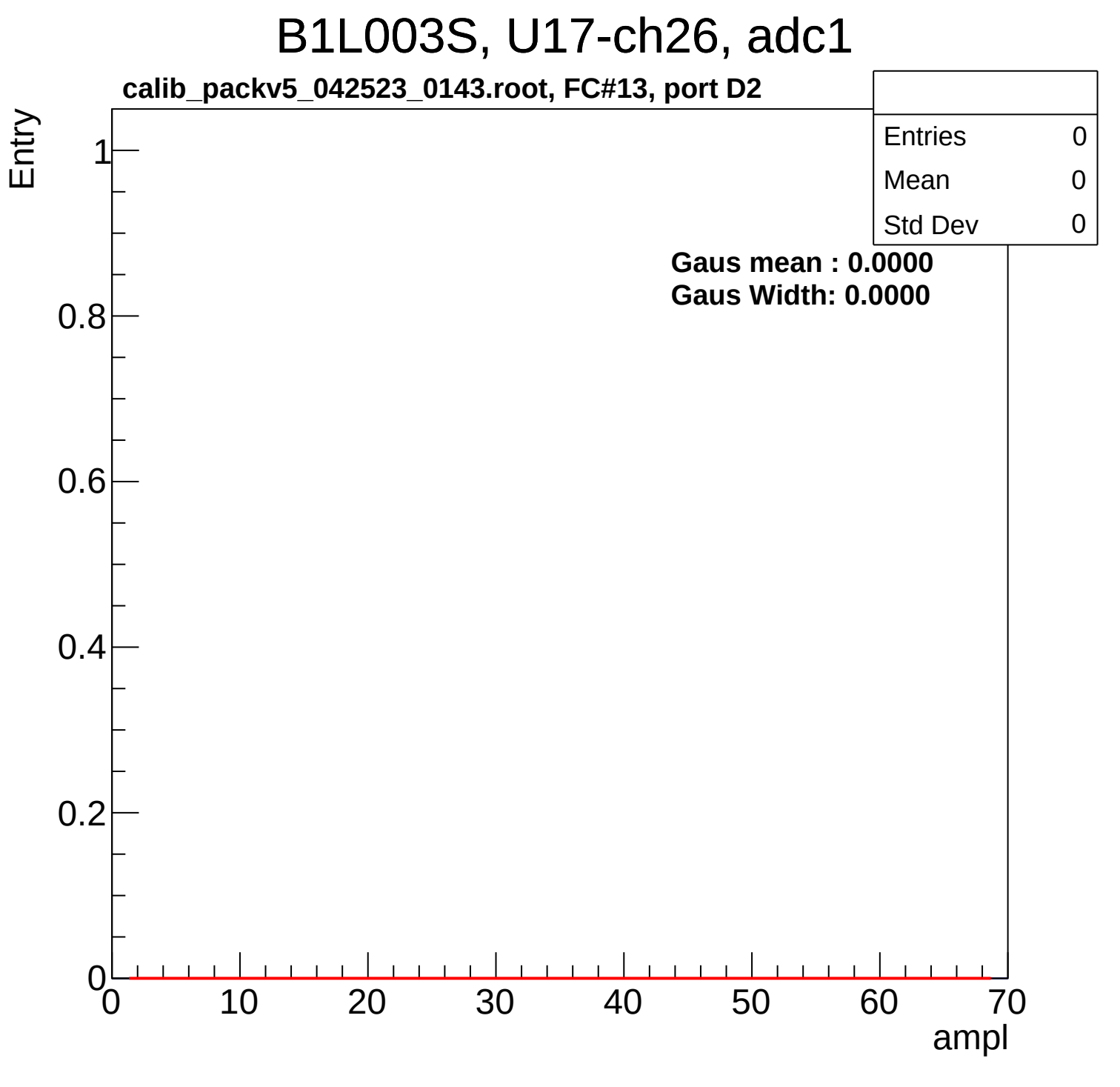
B1L003S, U17-ch26, adc1
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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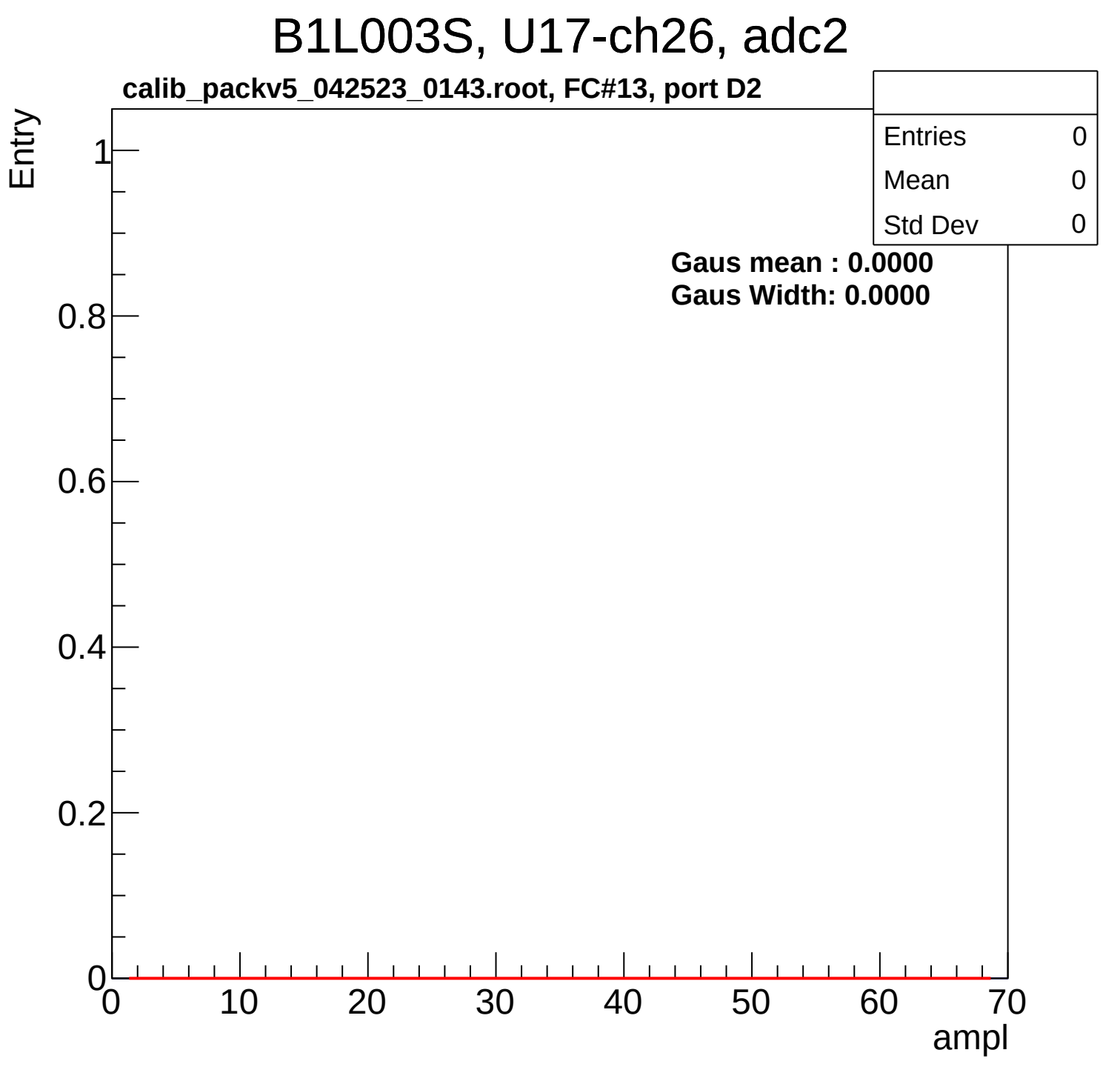
B1L003S, U17-ch26, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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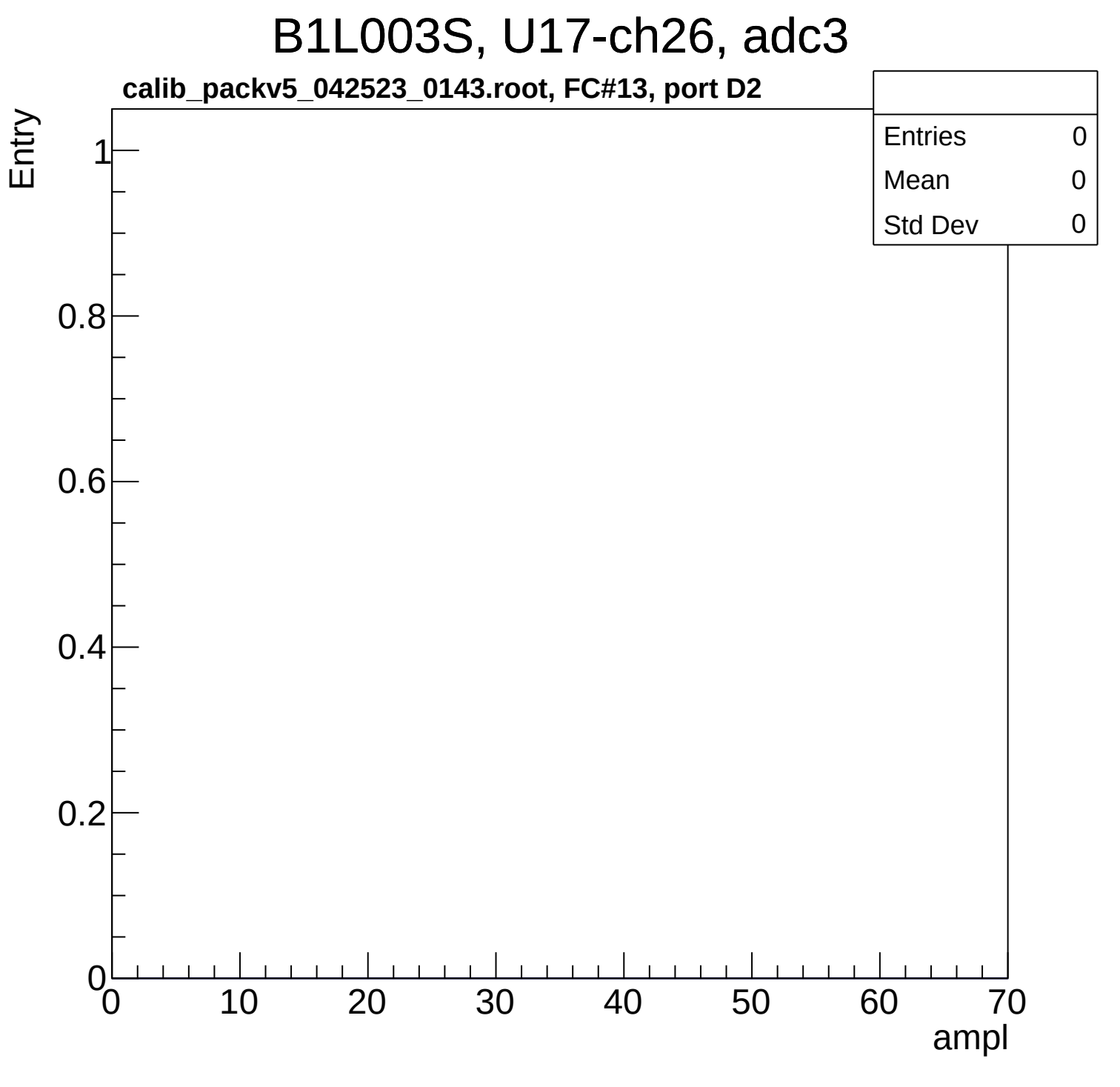
B1L003S, U17-ch26, adc3
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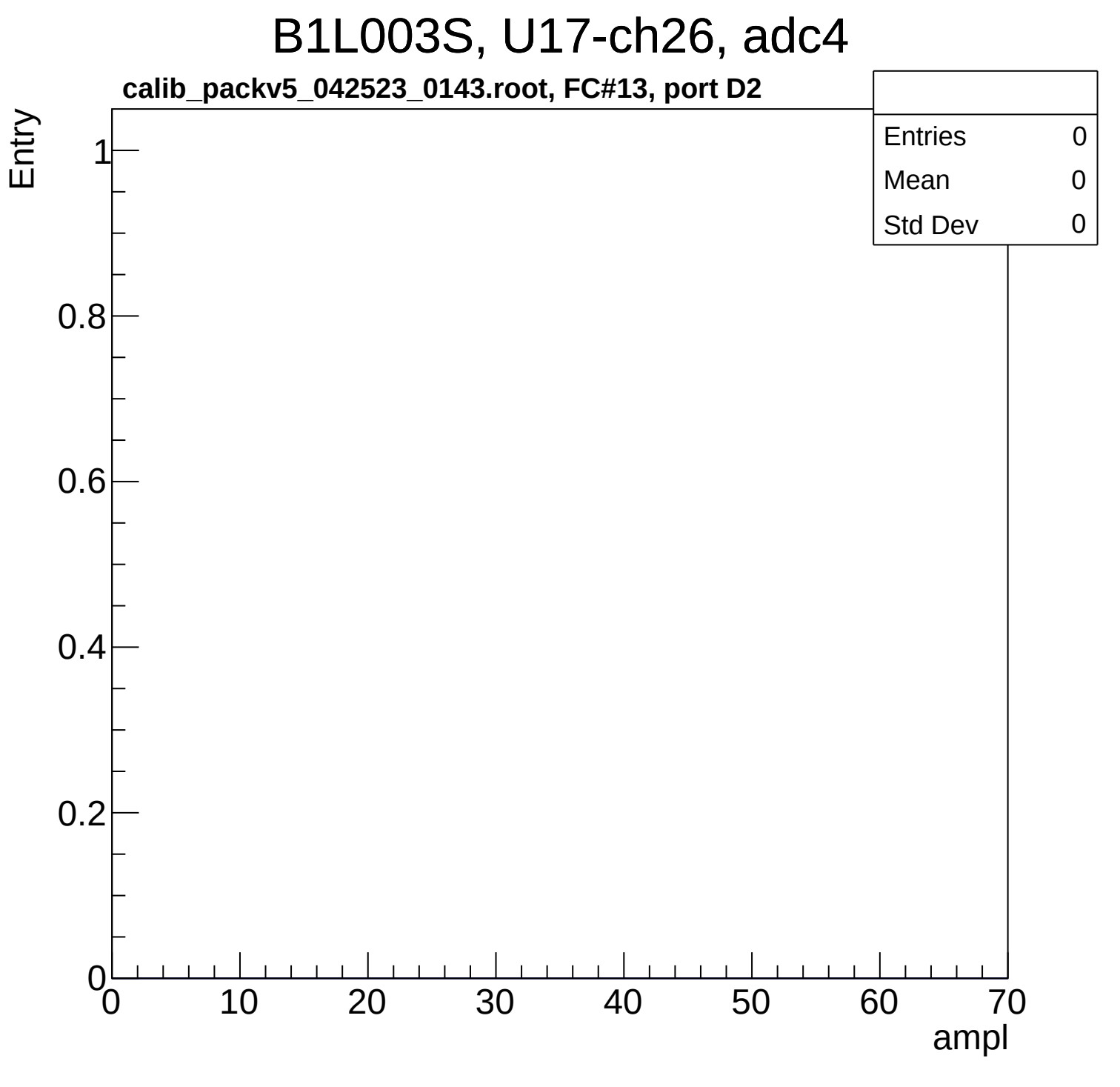
B1L003S, U17-ch26, adc4
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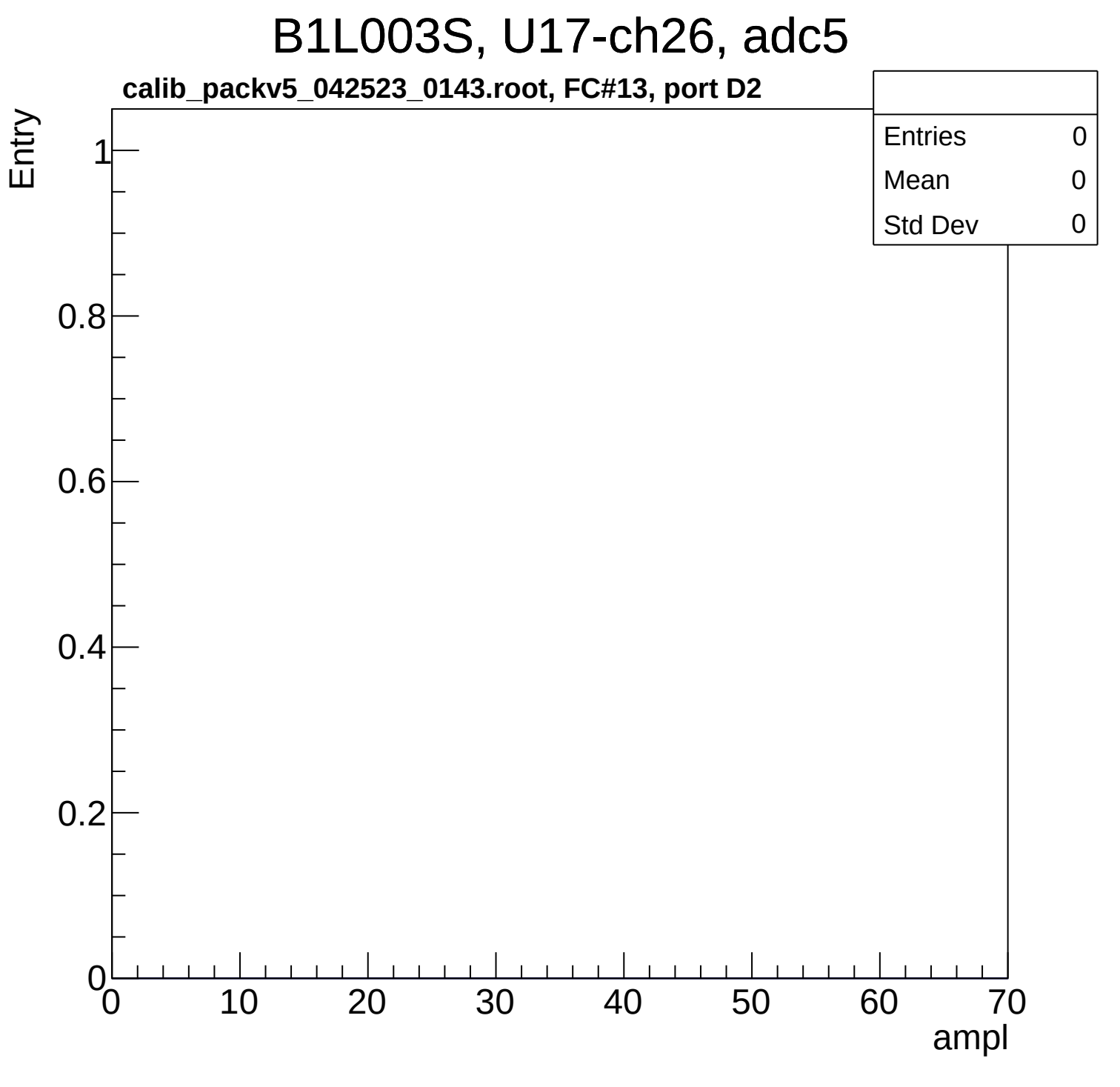
B1L003S, U17-ch26, adc5
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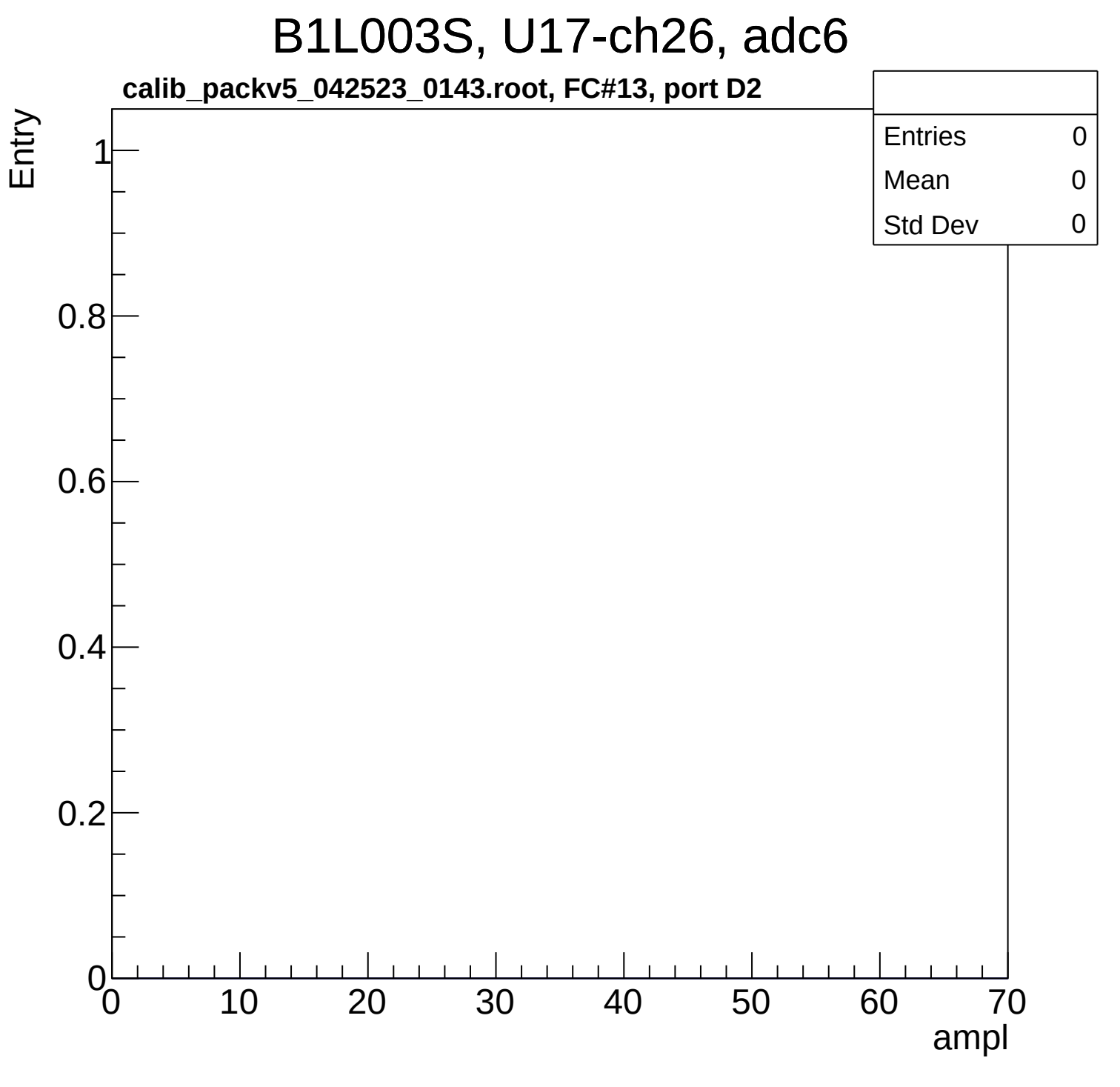
B1L003S, U17-ch26, adc6
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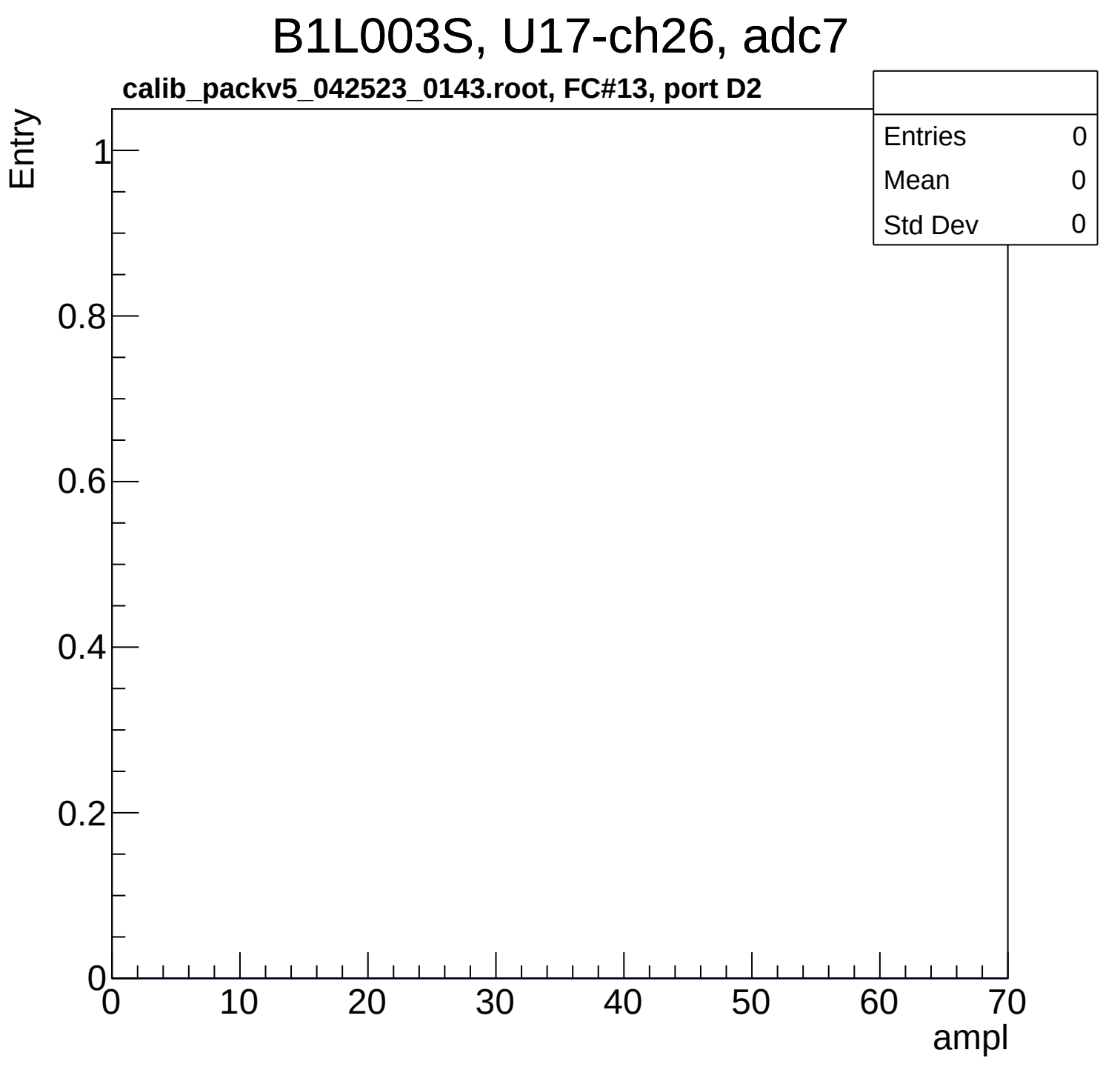
B1L003S, U17-ch26, adc7
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Mean 0
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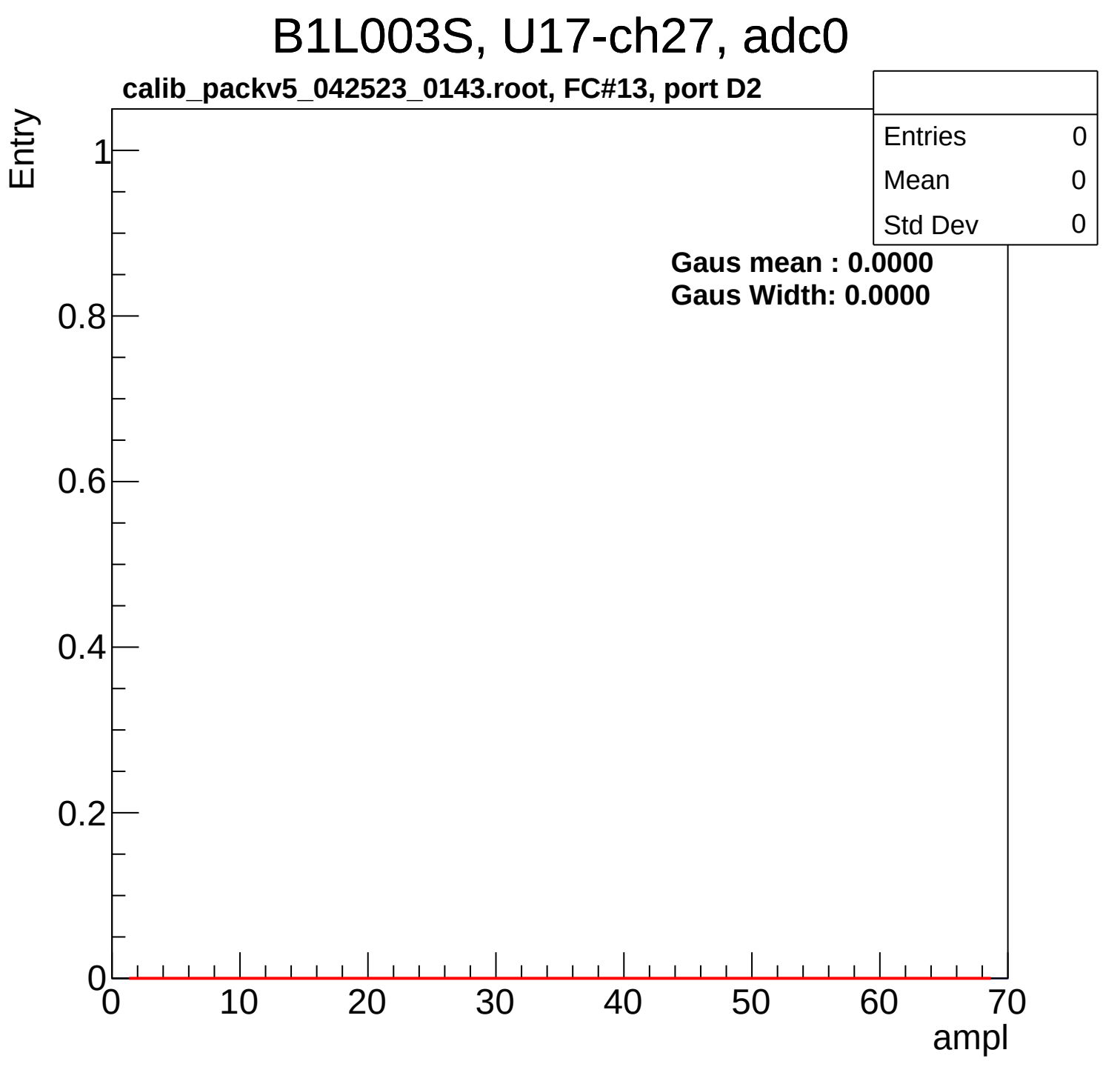
B1L003S, U17-ch27, adc0
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Gaus Width: 0.0000
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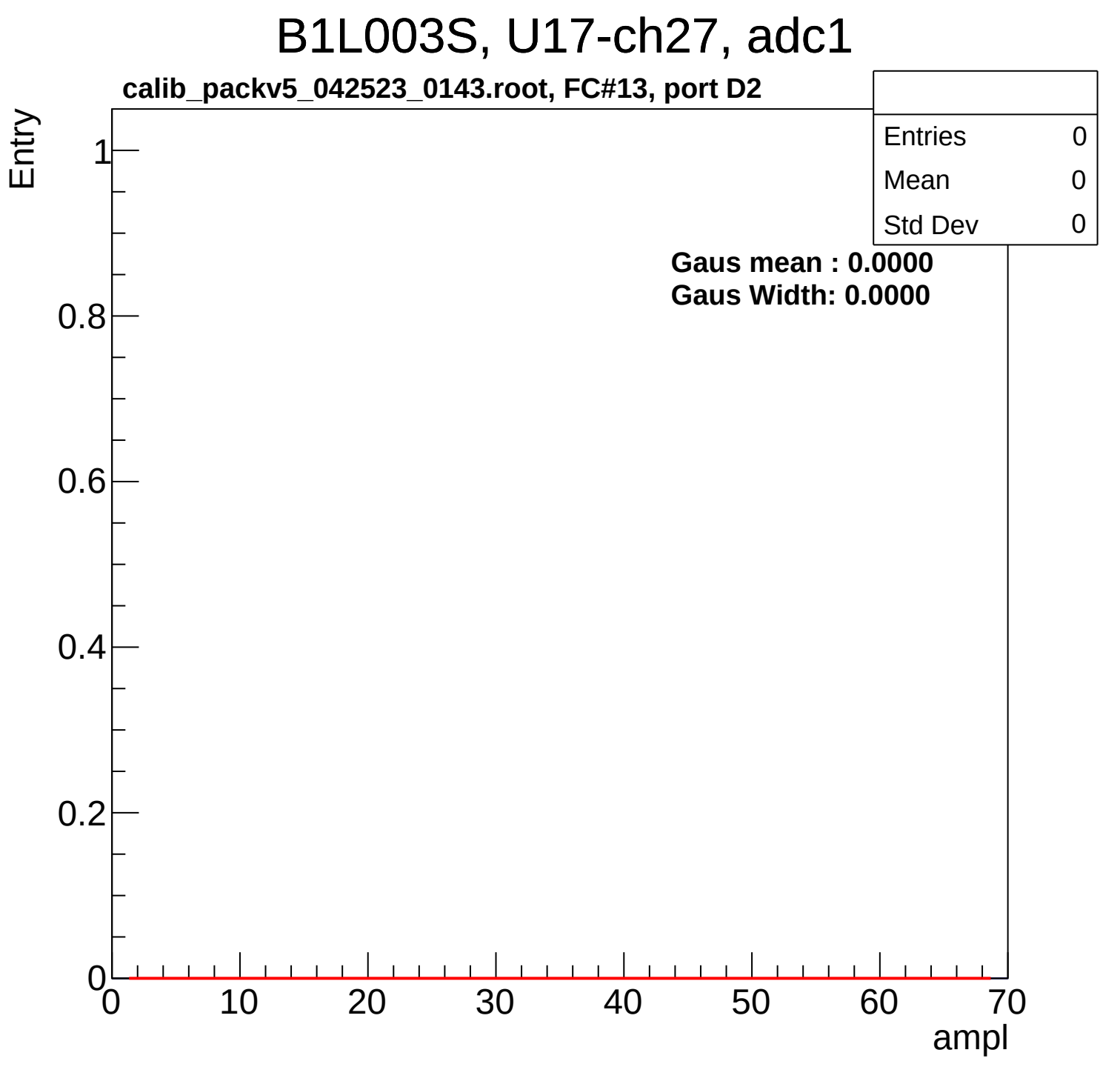
B1L003S, U17-ch27, adc1
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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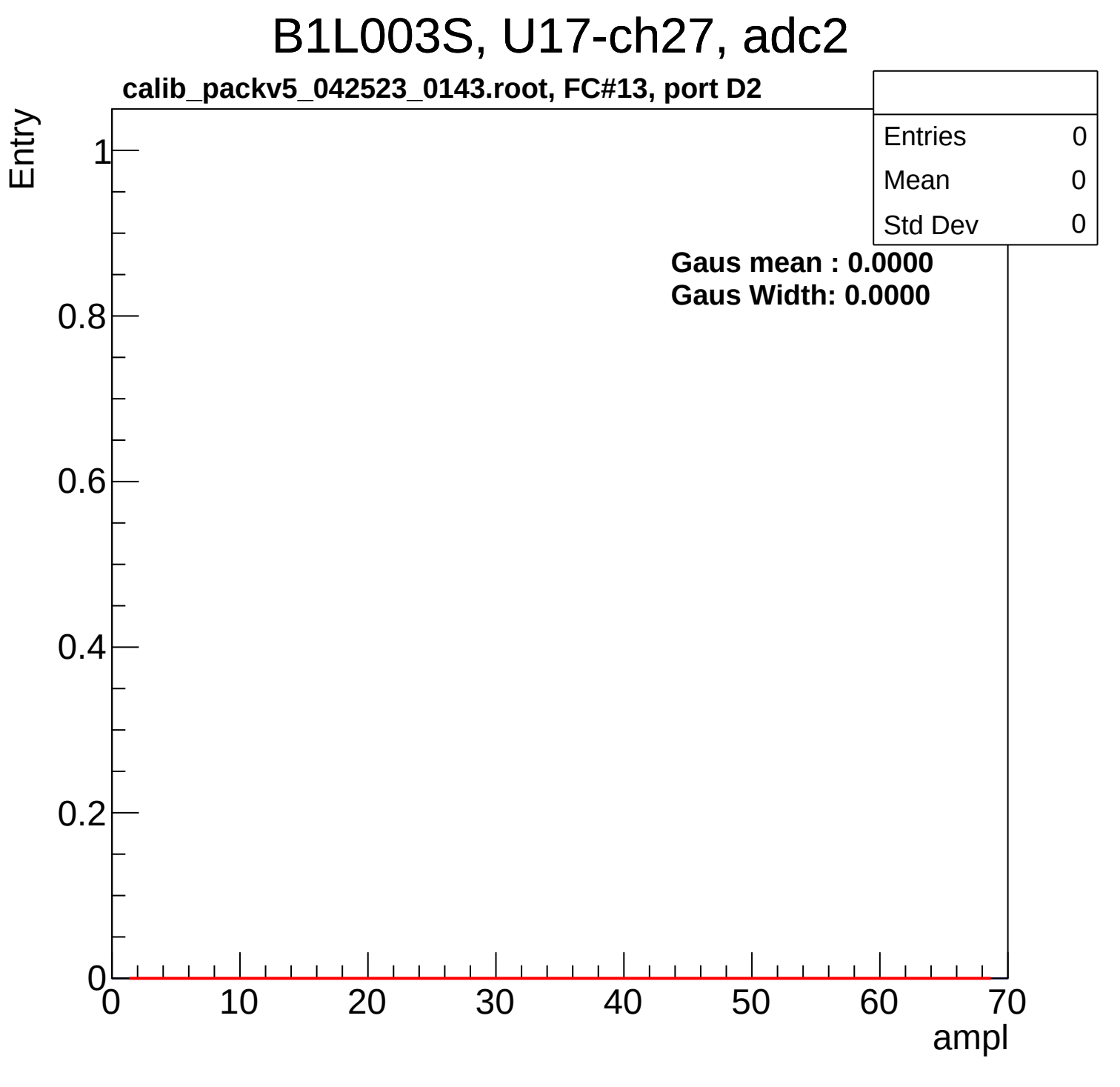
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Std Dev 0
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Gaus Width: 0.0000
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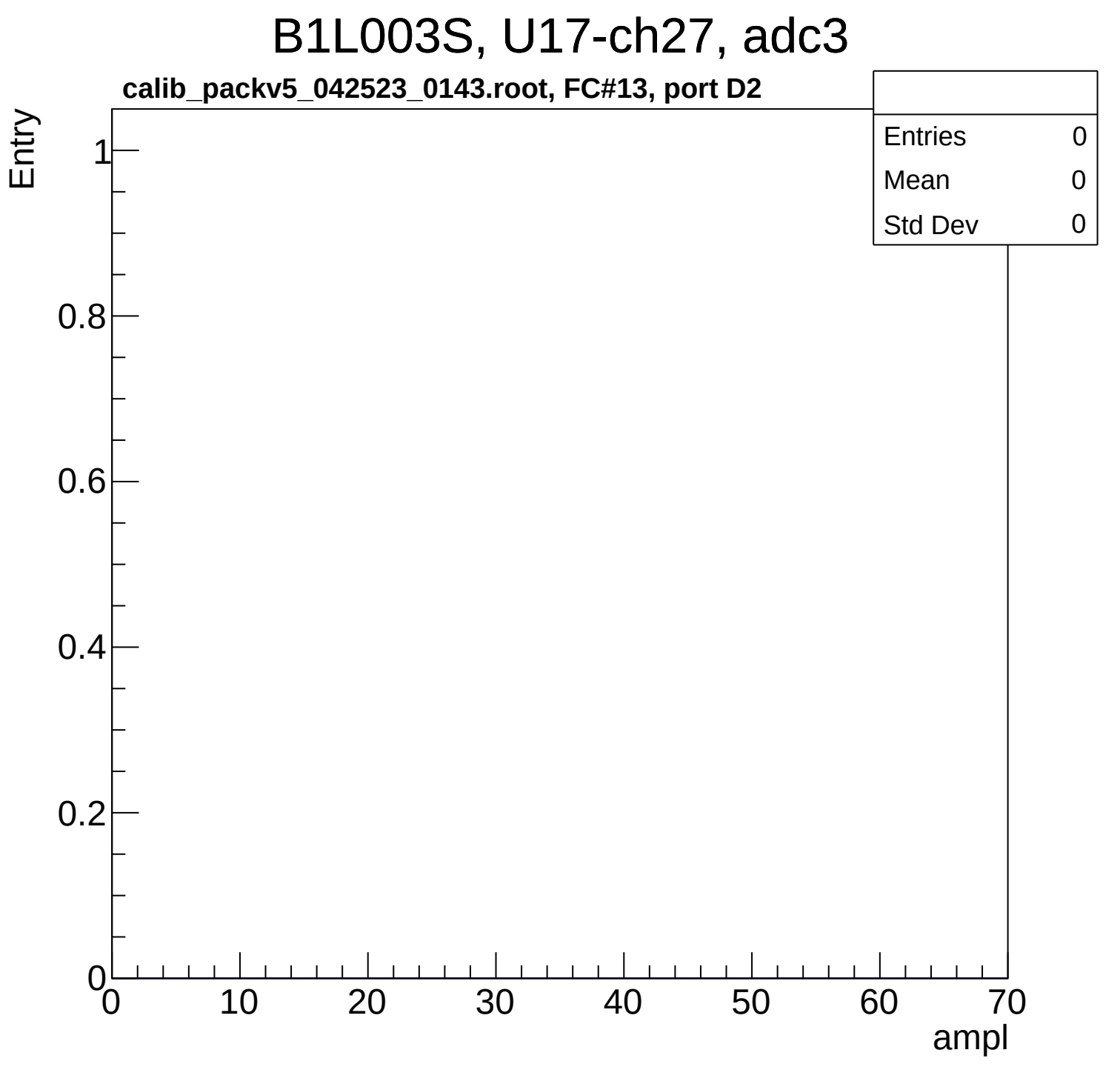
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Std Dev 0
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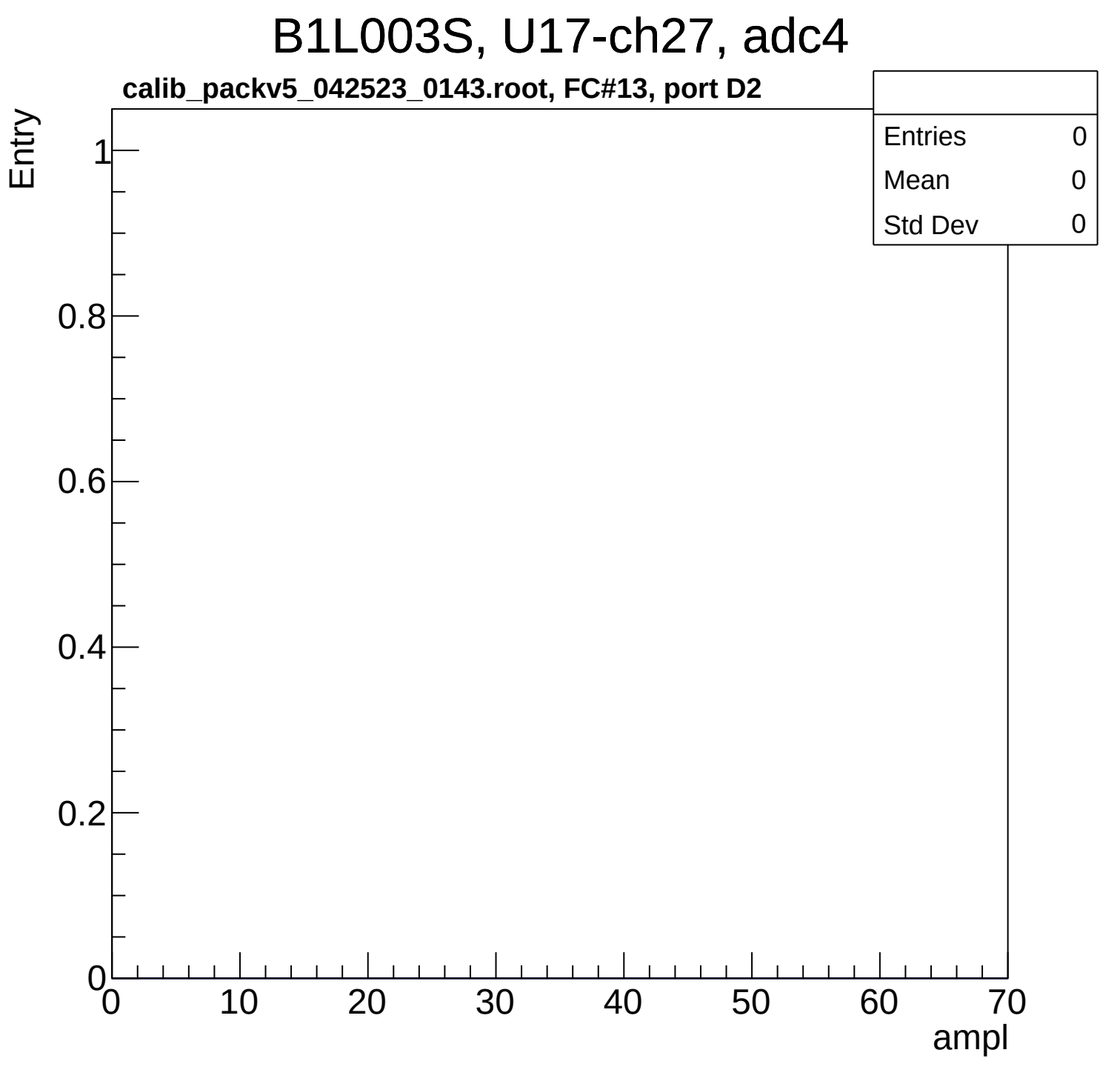
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Std Dev 0
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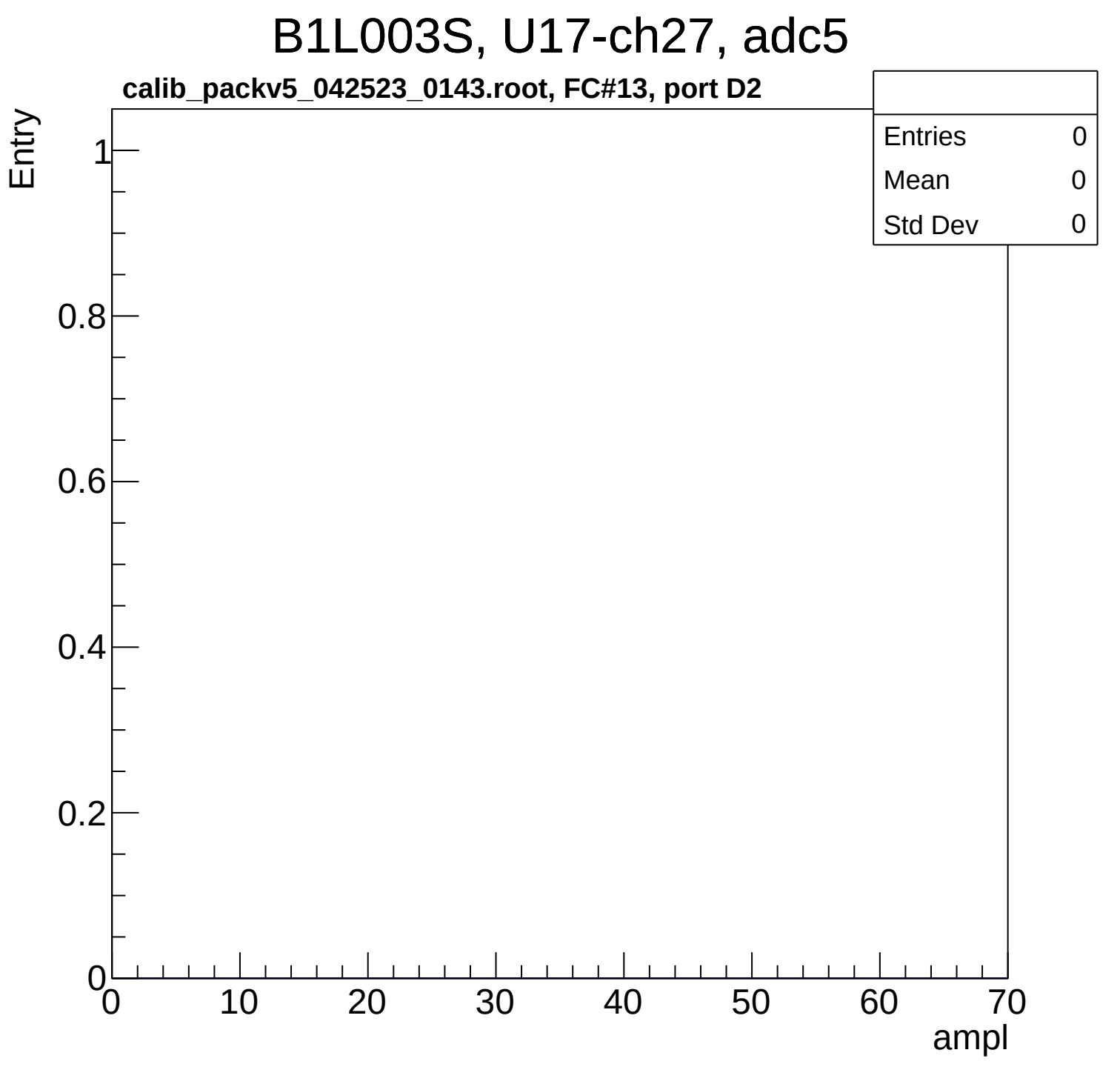
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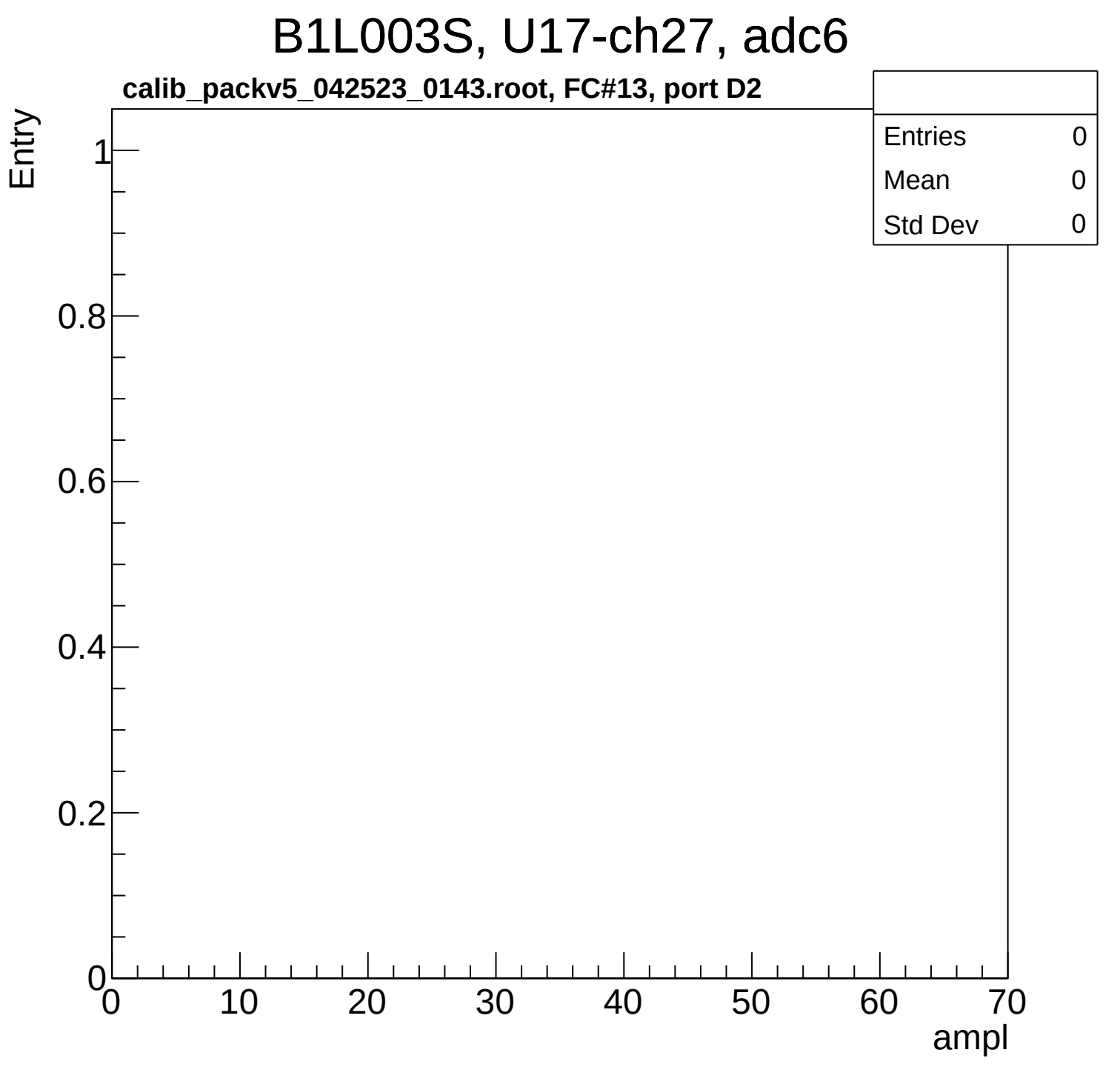
B1L003S, U17-ch27, adc6
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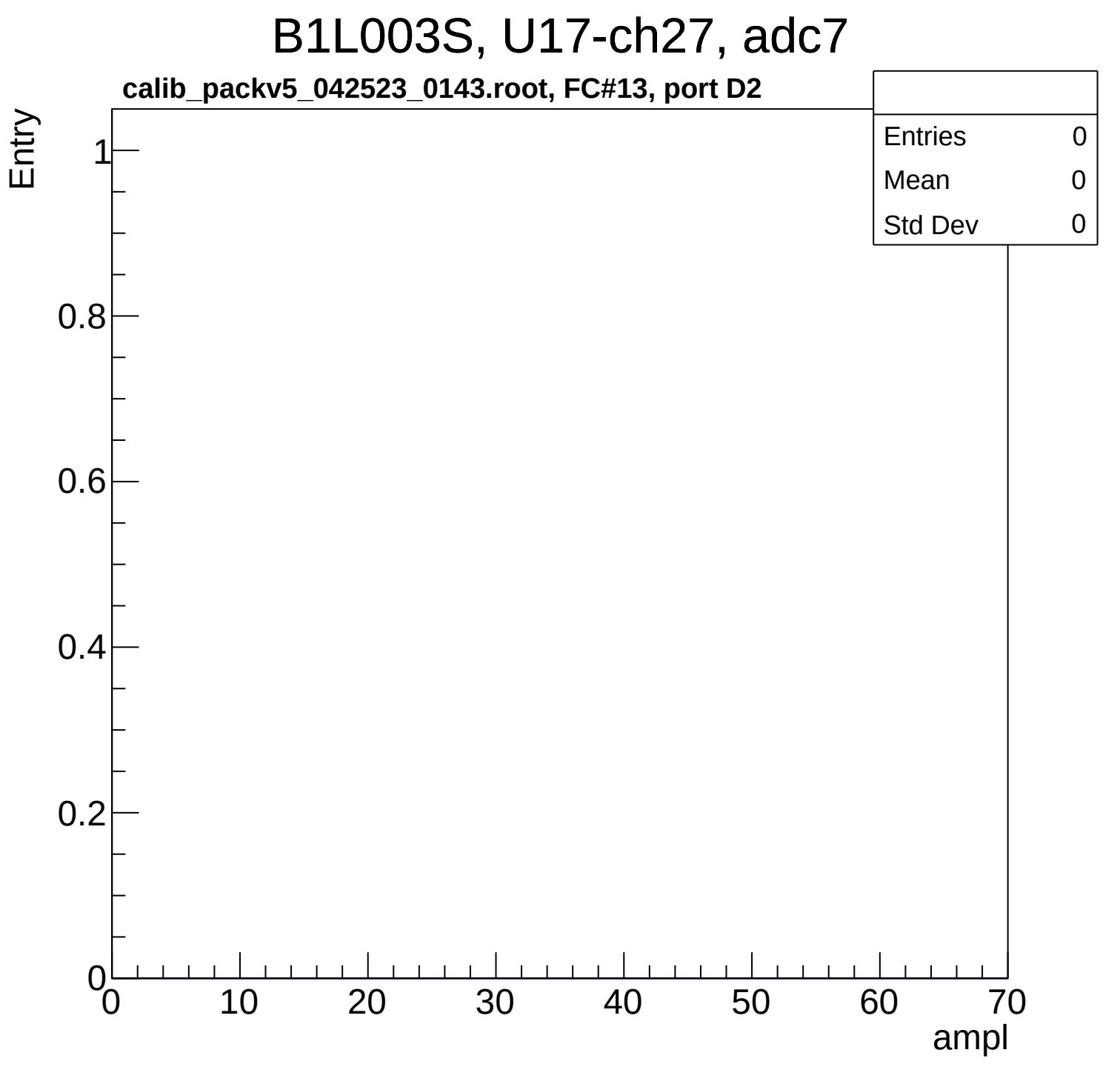
B1L003S, U17-ch27, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
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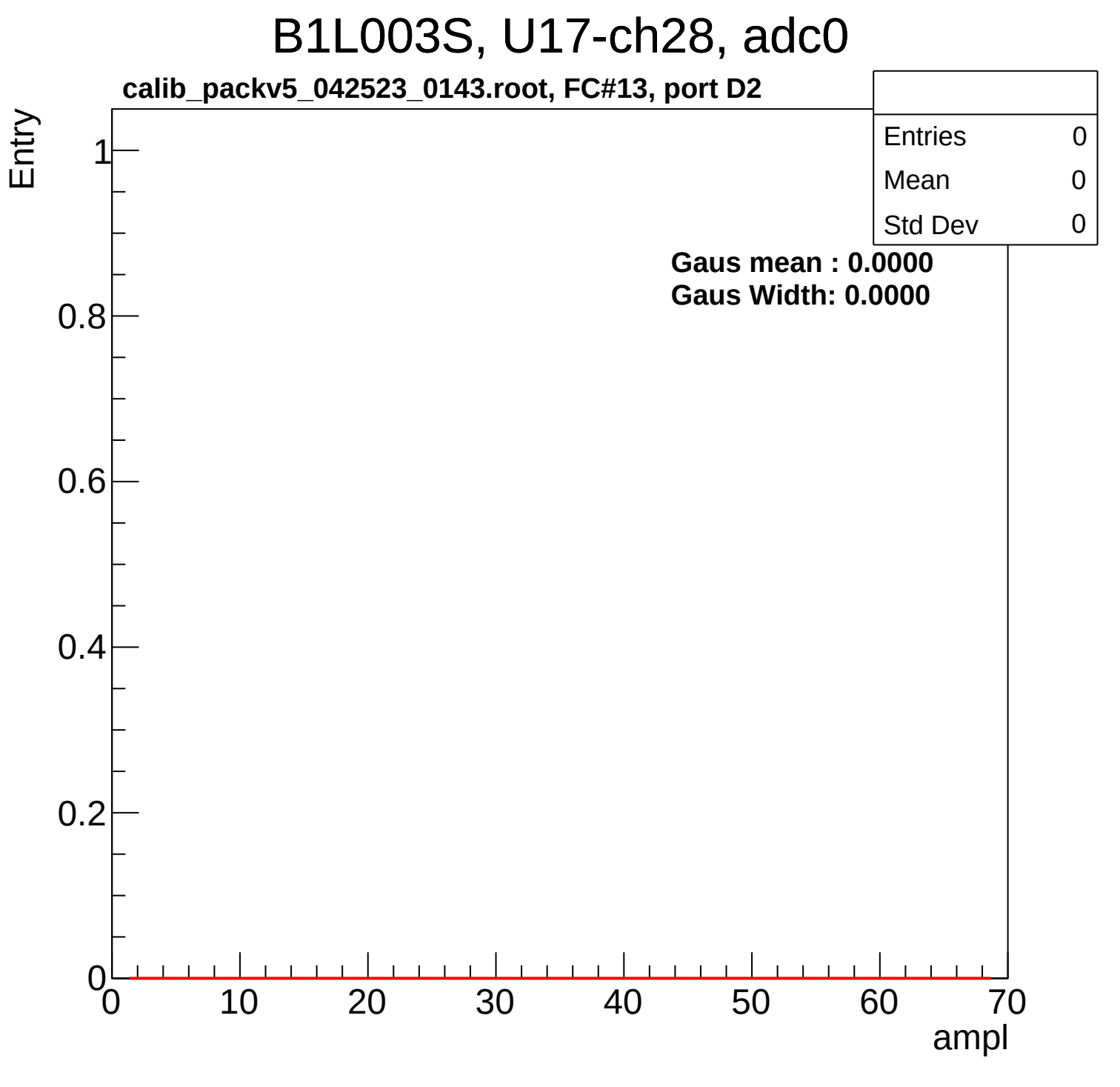
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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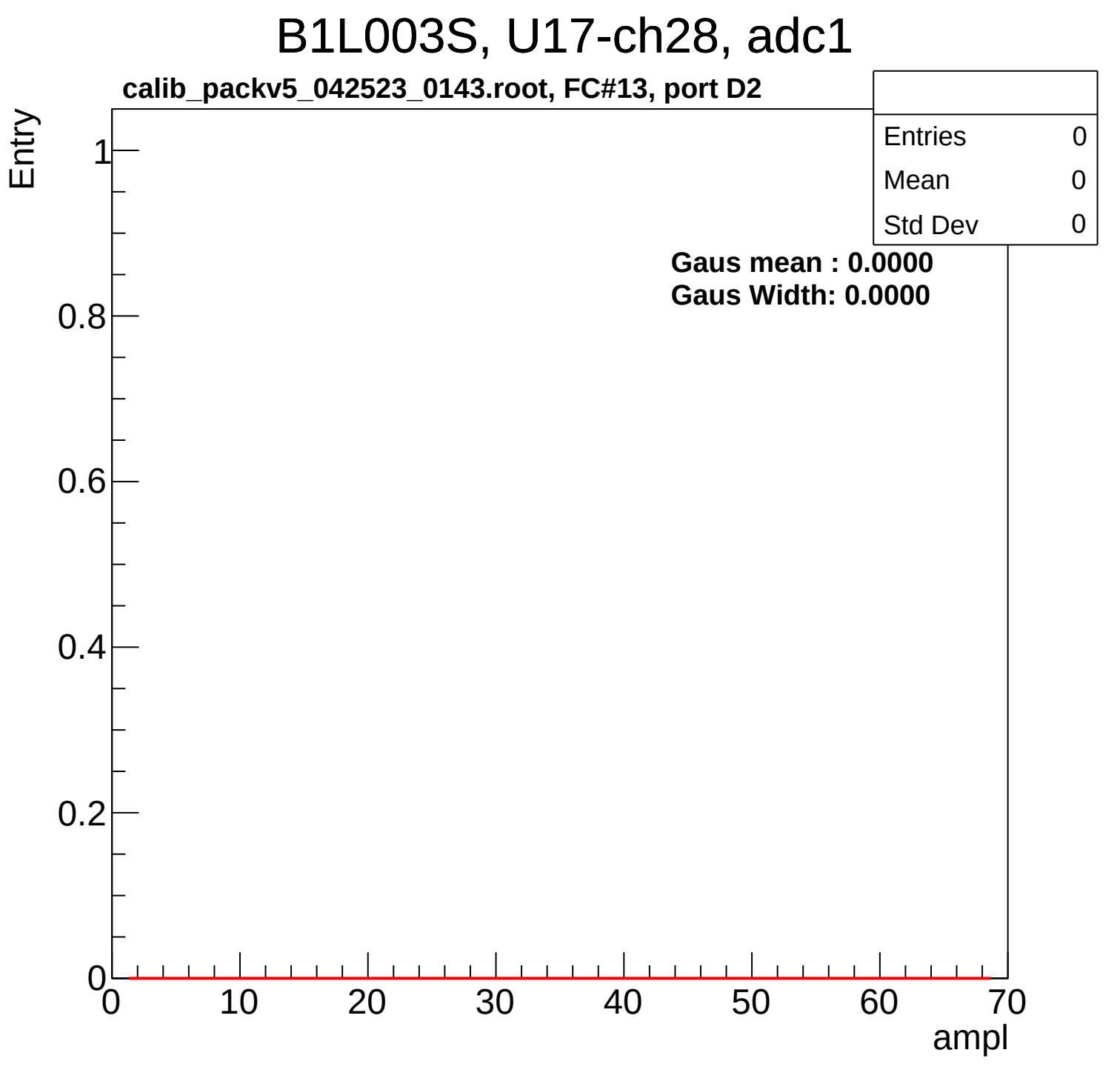
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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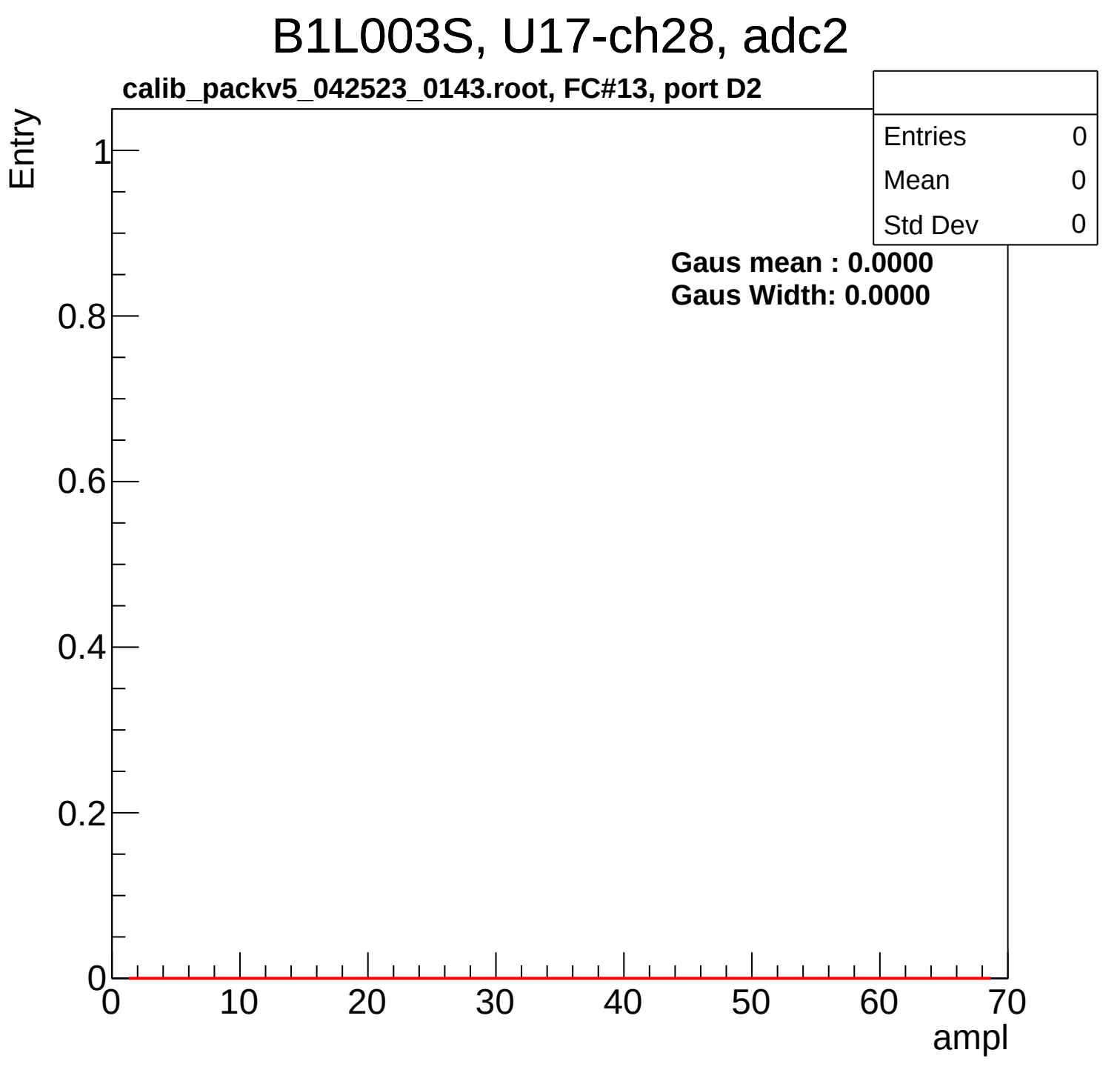
B1L003S, U17-ch28, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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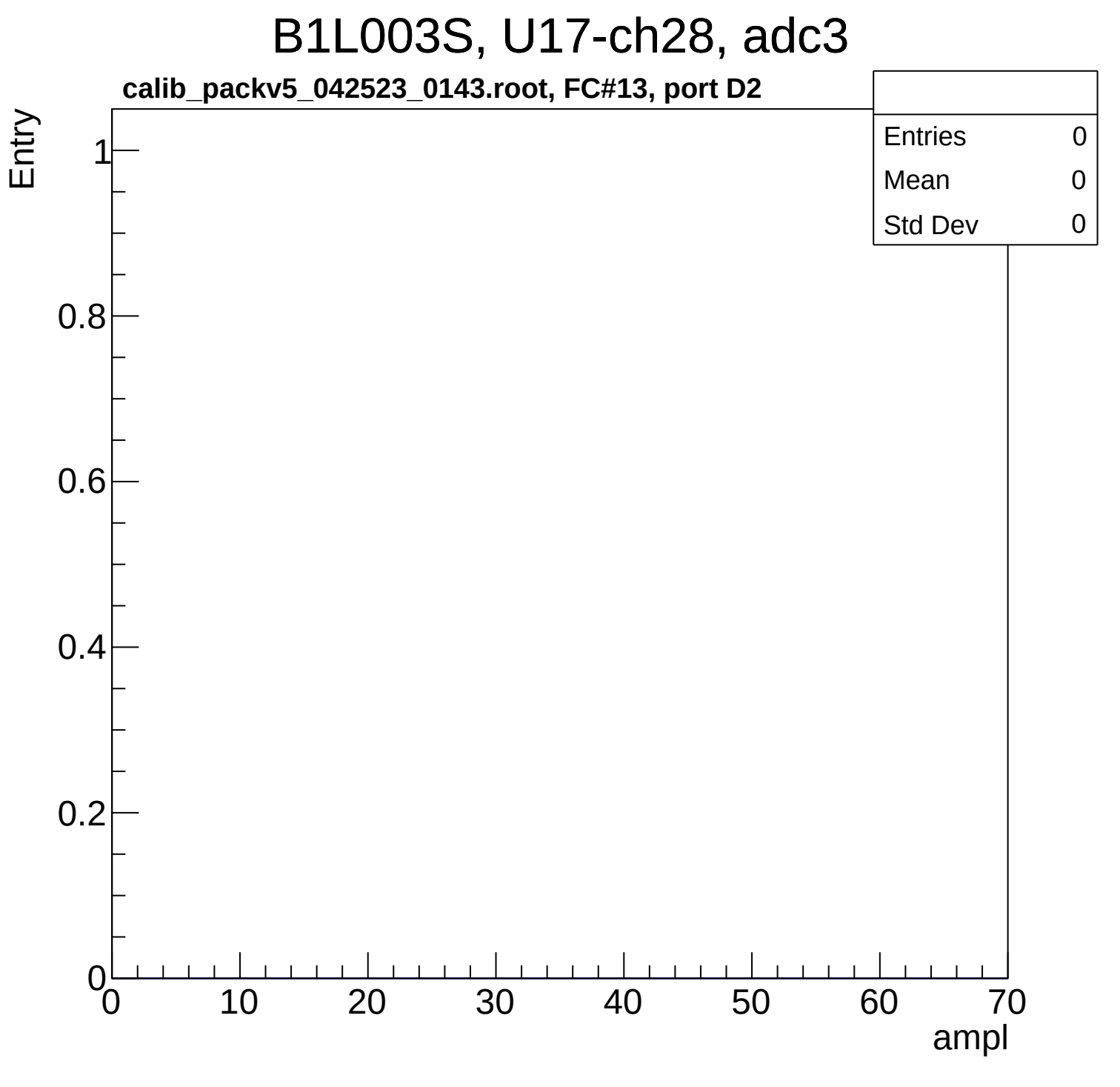
ampl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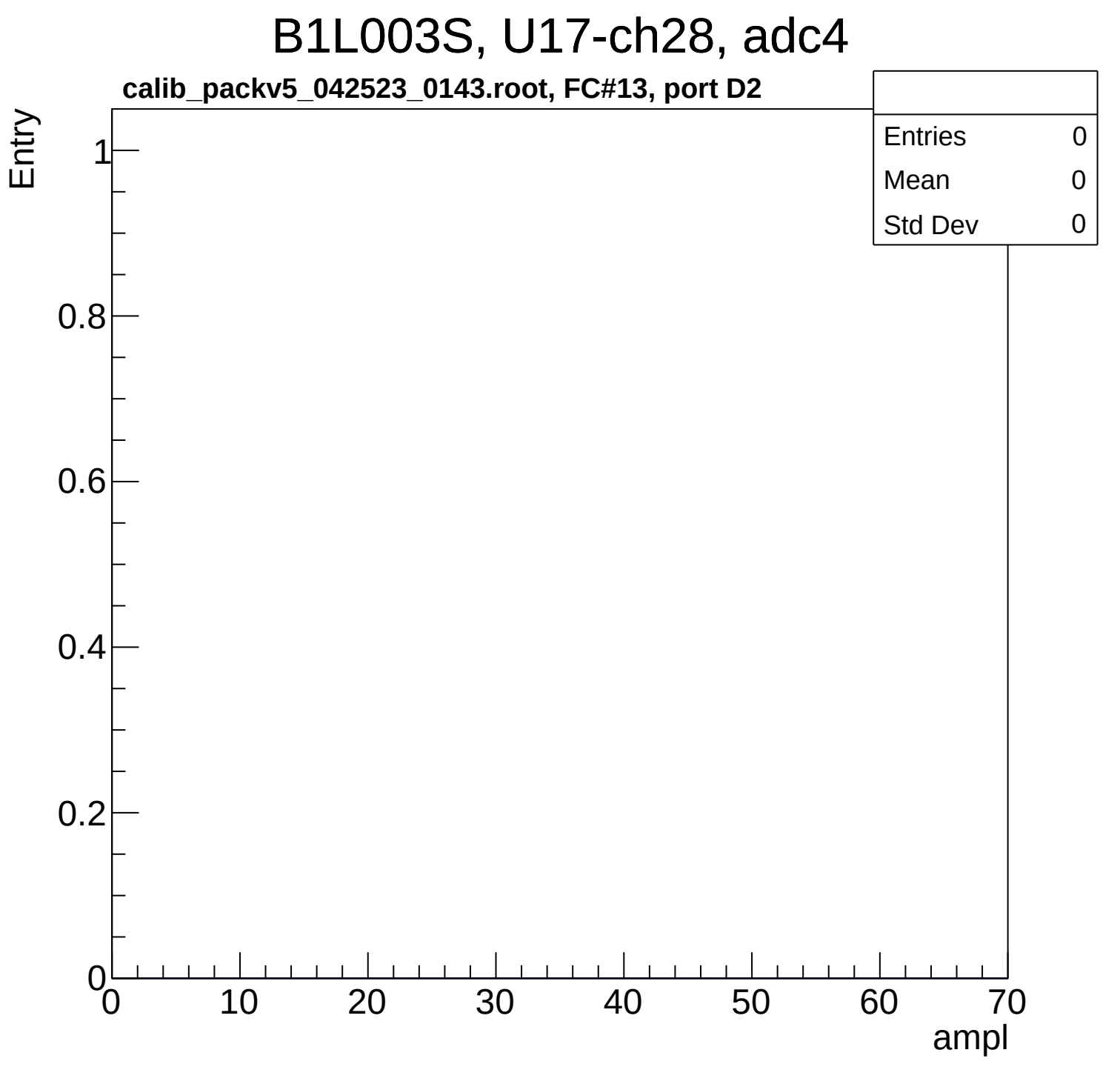
ampl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calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
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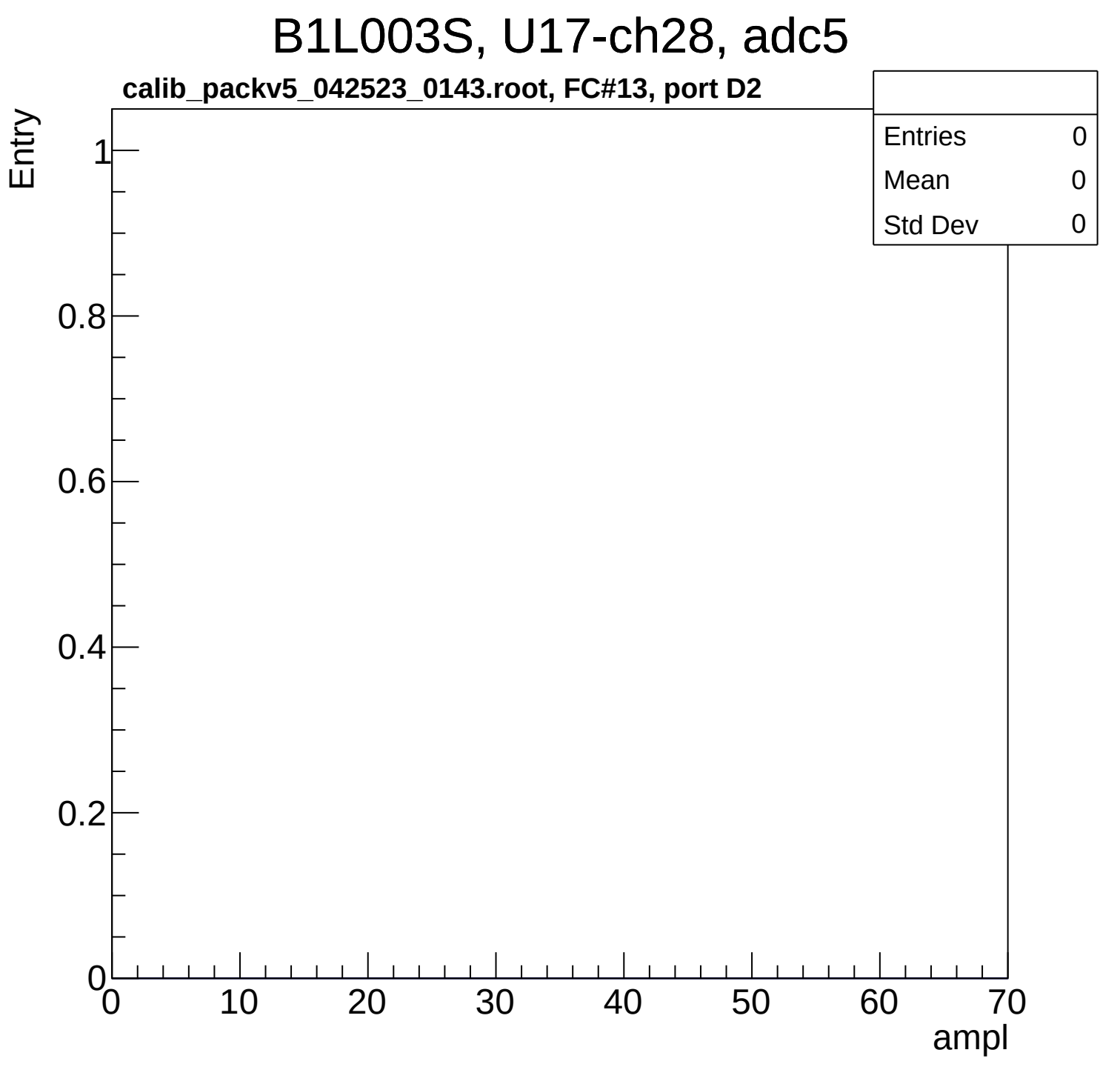
ampl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Entries 0
Mean 0
Std Dev 0
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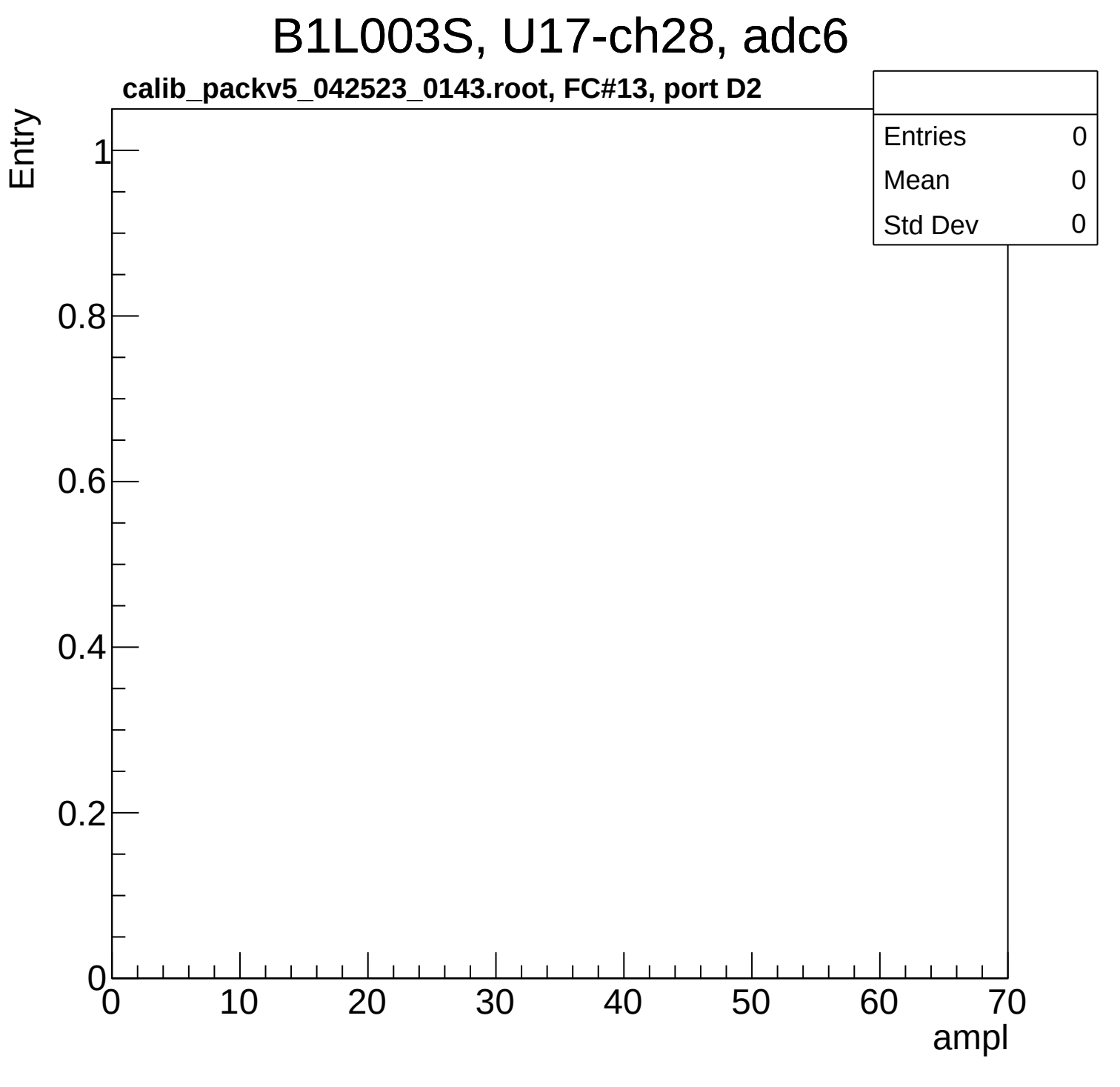
B1L003S, U17-ch28, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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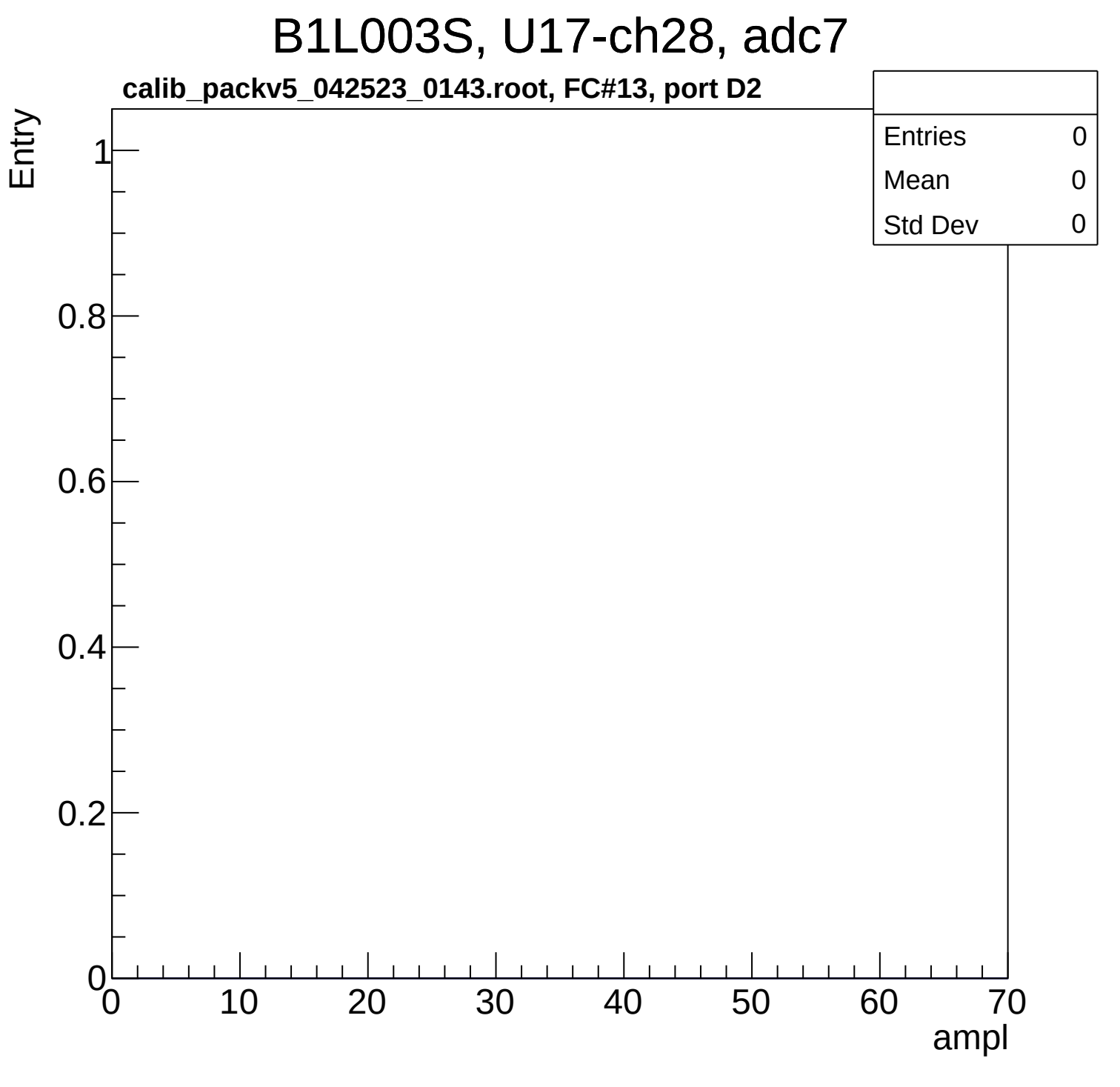
B1L003S, U17-ch28, adc7
calib_packv5_042523_0143.root, FC#13, port D2
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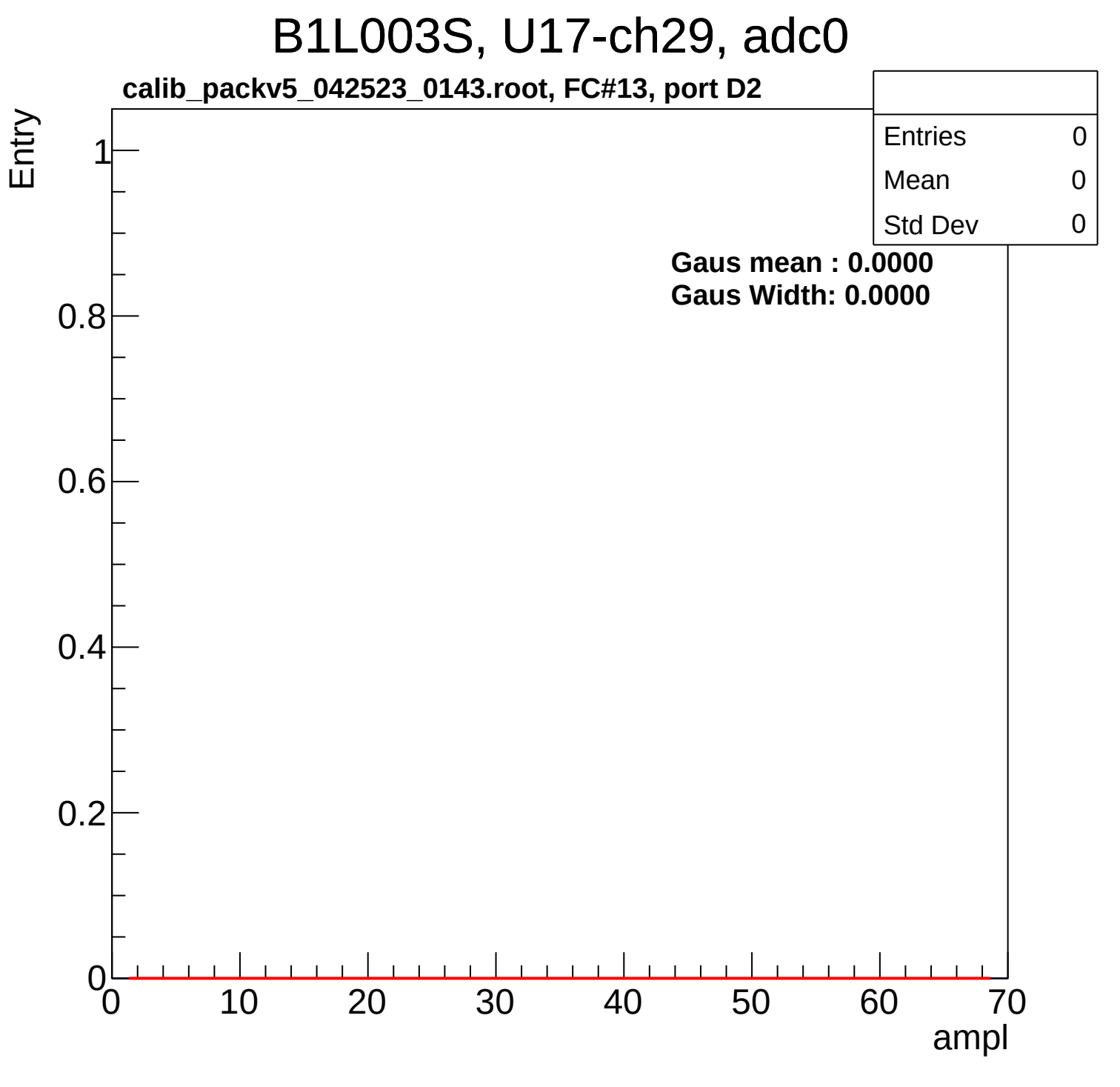
B1L003S, U17-ch29, adc0
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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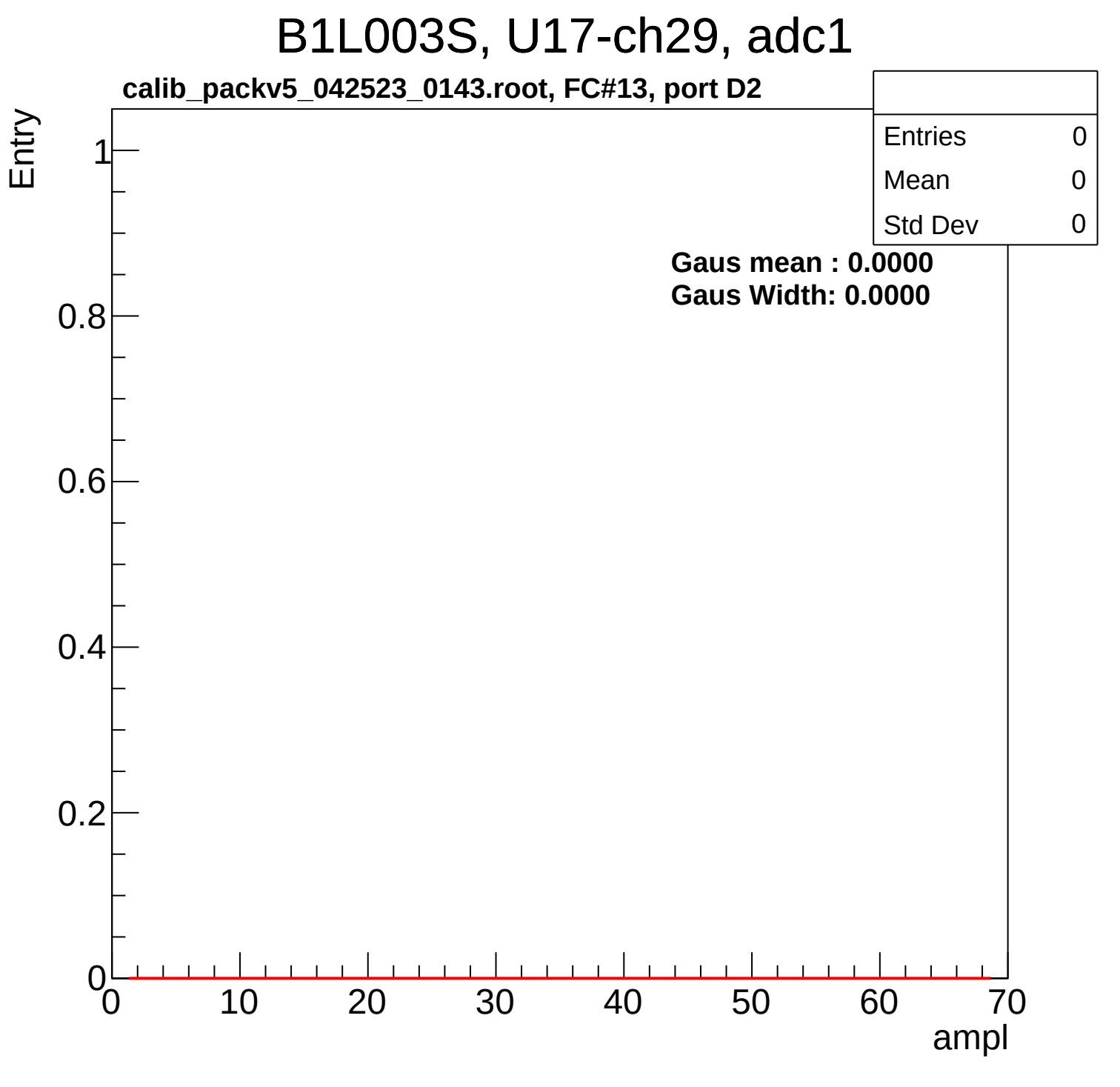
ampl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Entries 0
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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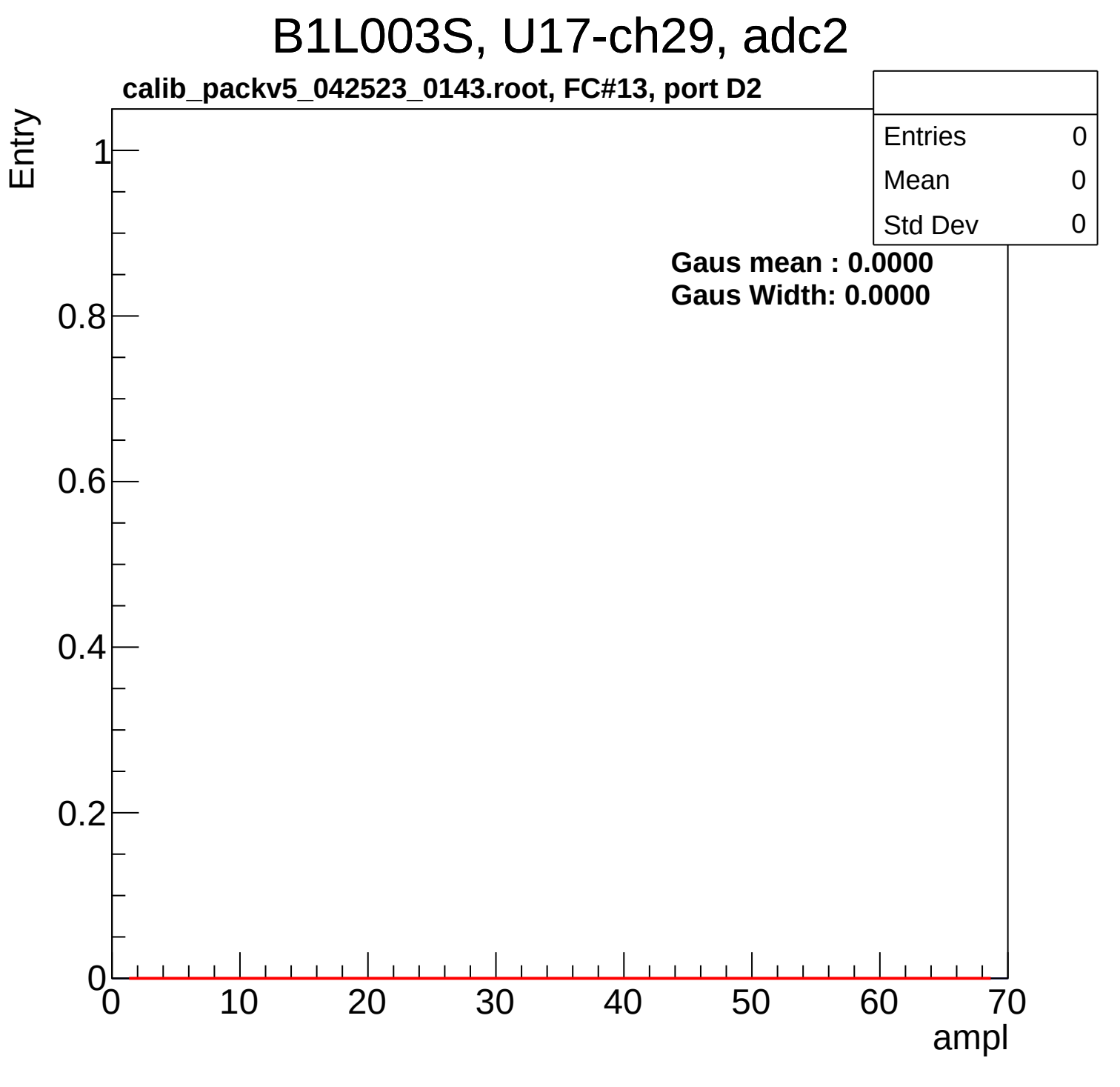
ampl


B1L003S, U17-ch29, adc2
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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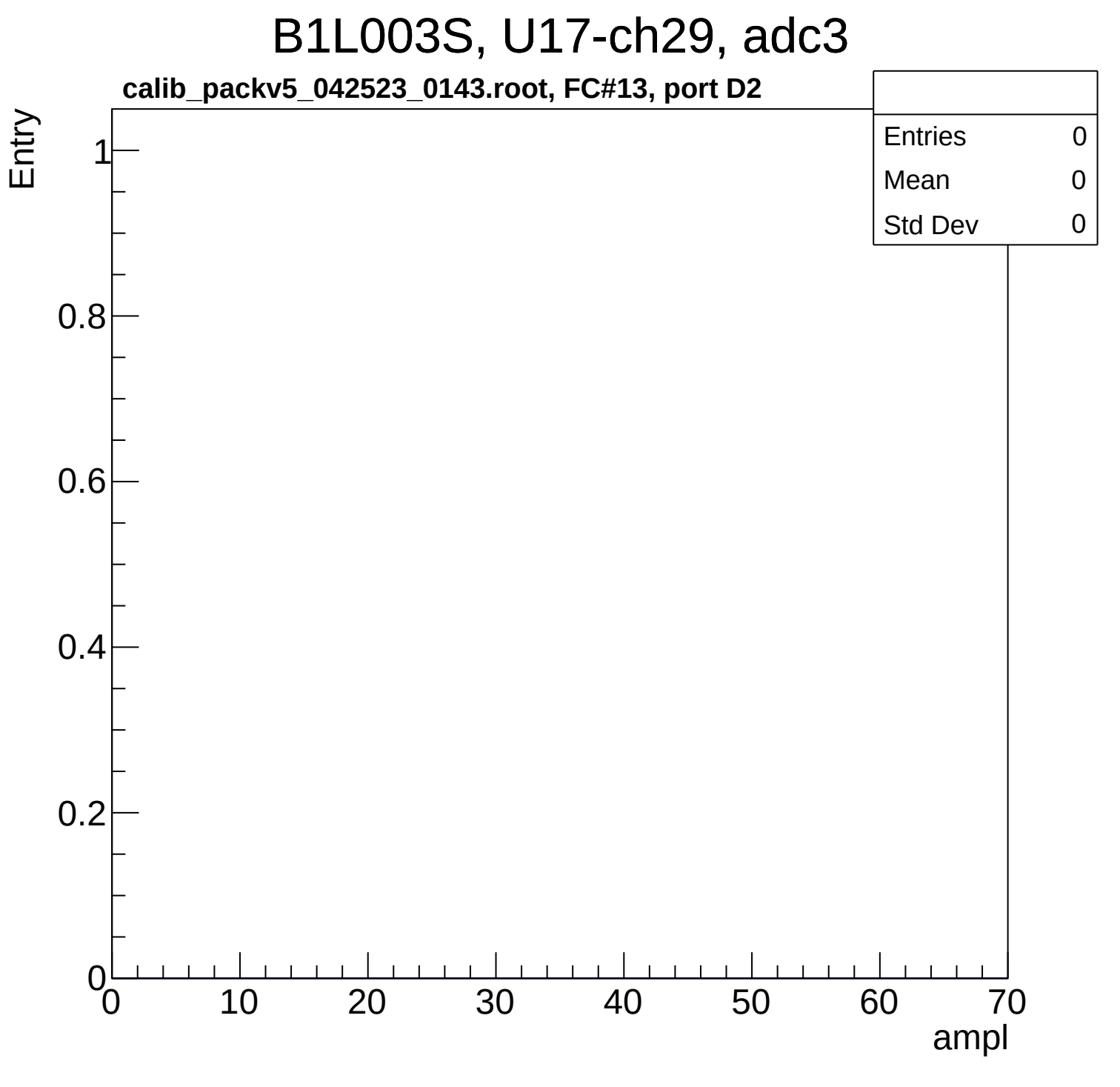
ampl


B1L003S, U17-ch29, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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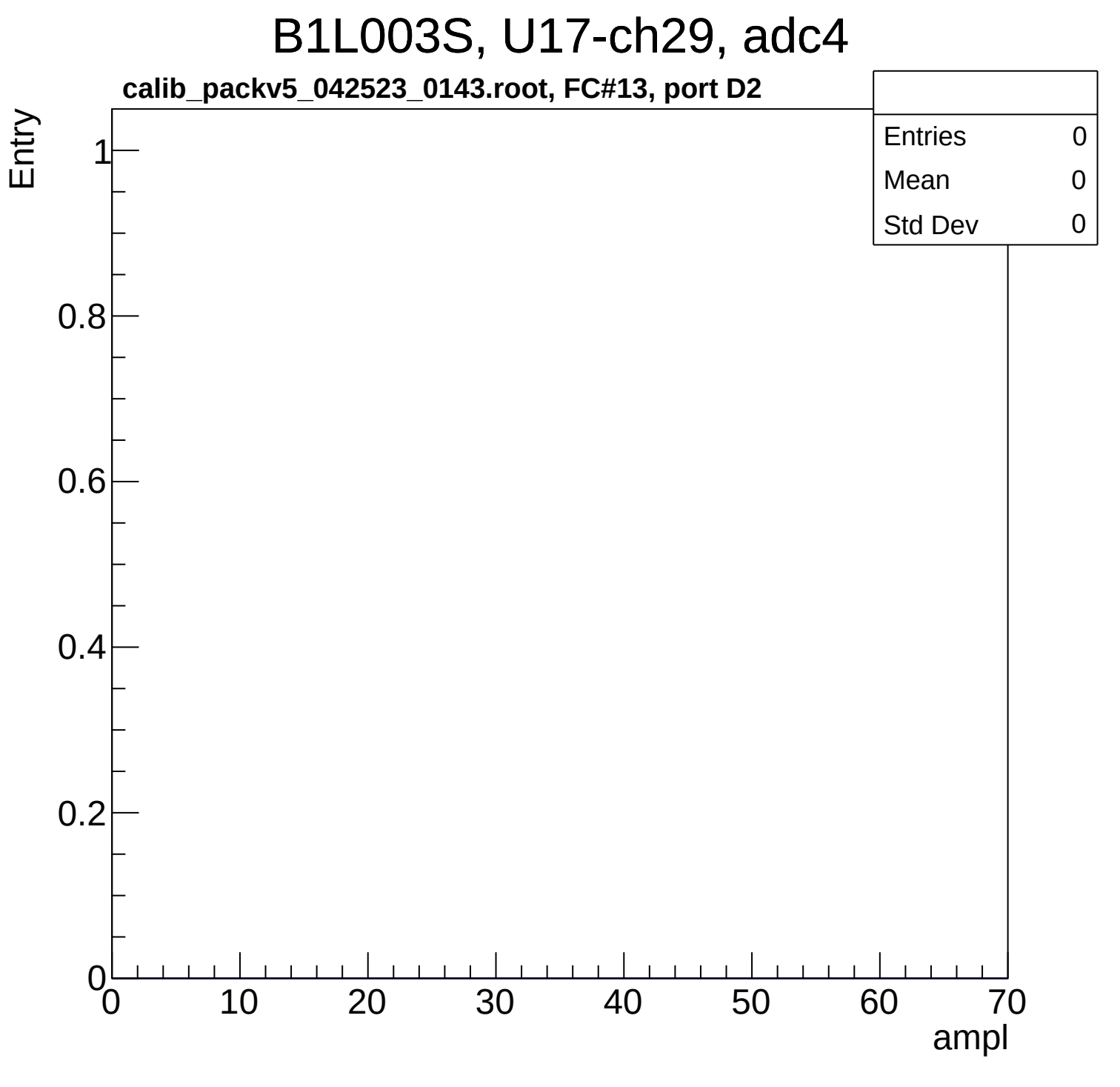
ampl


B1L003S, U17-ch29, adc4
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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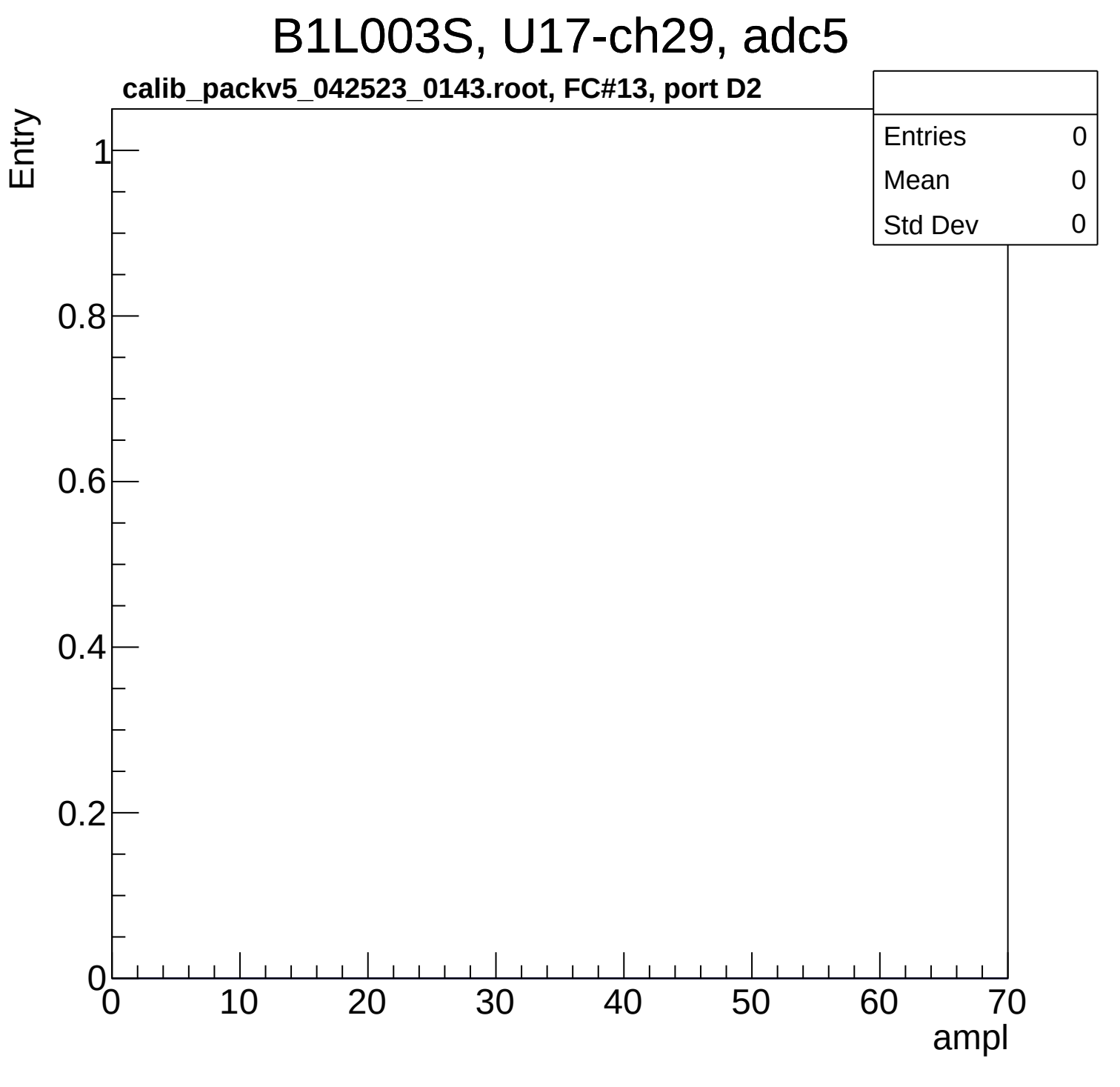
ampl


B1L003S, U17-ch29, adc5
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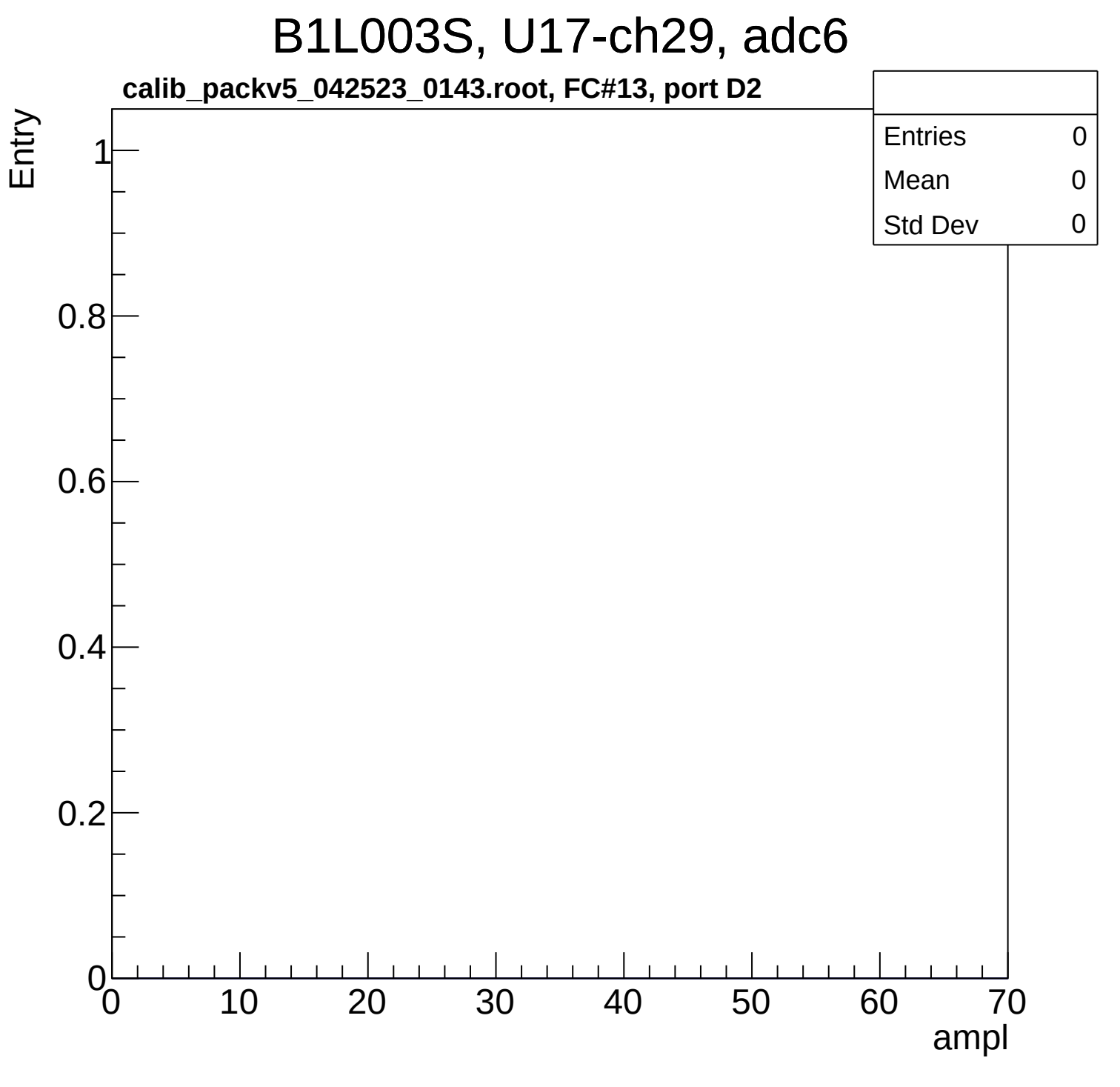
B1L003S, U17-ch29, adc6
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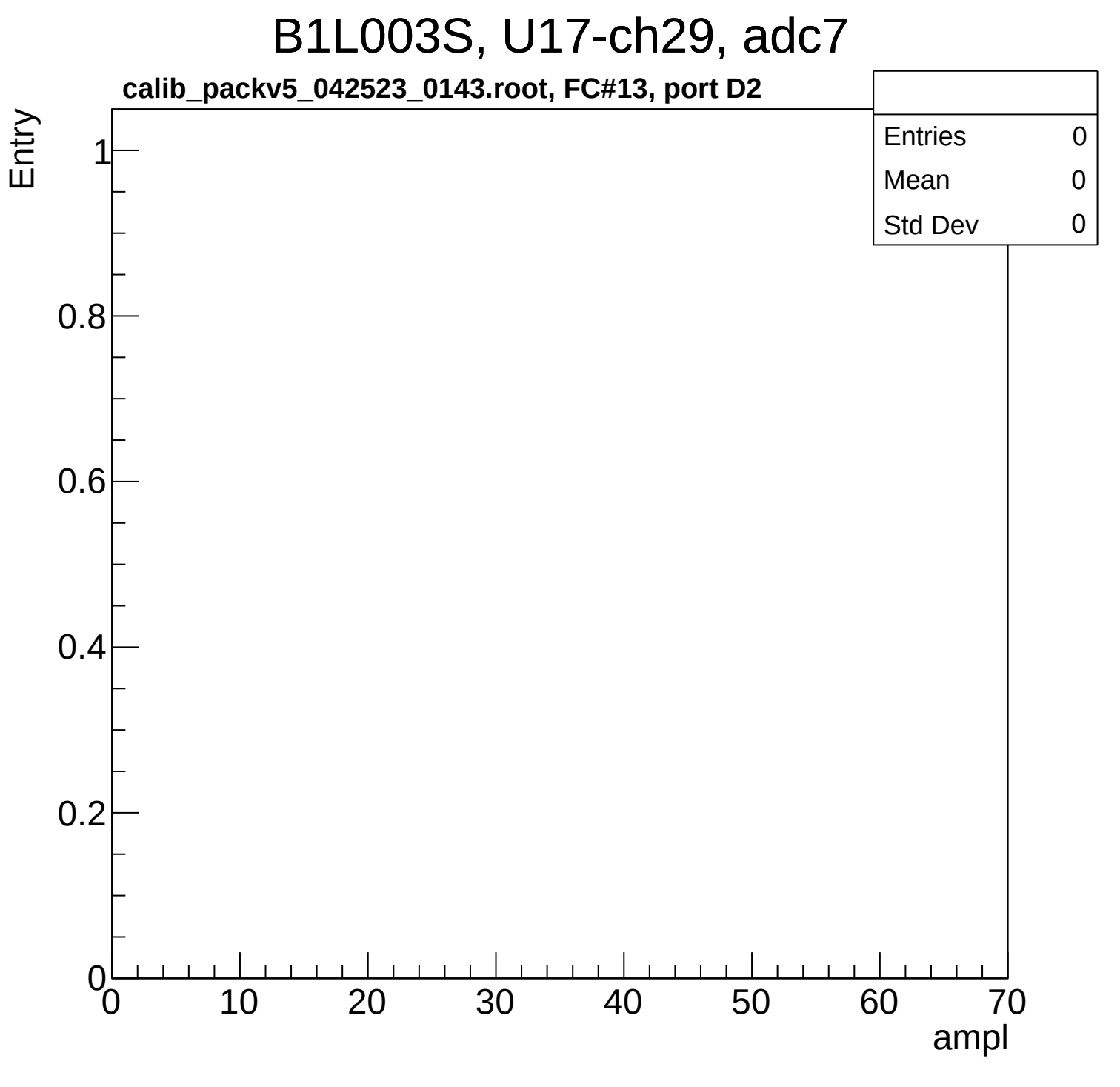
B1L003S, U17-ch29, adc7
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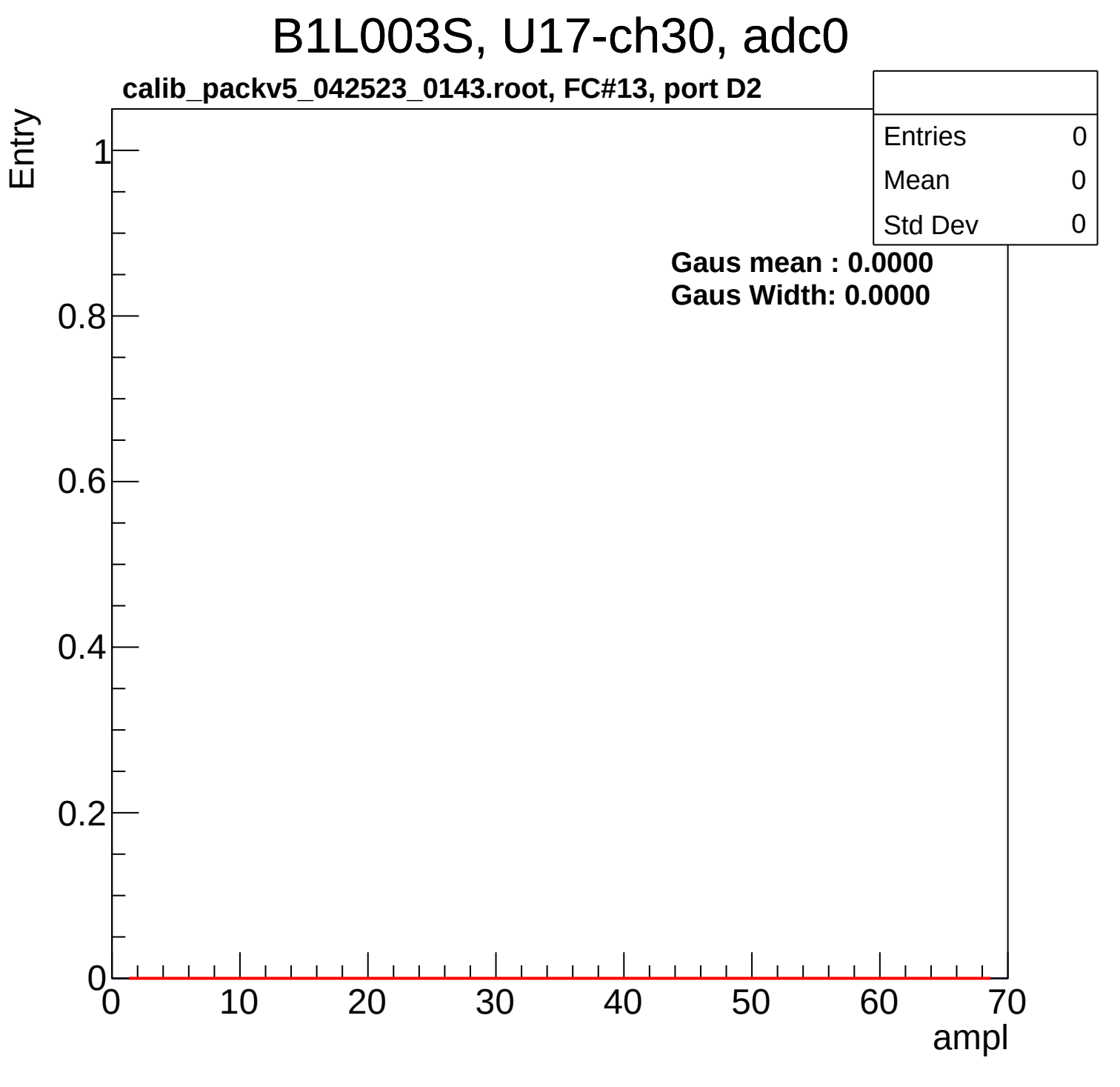
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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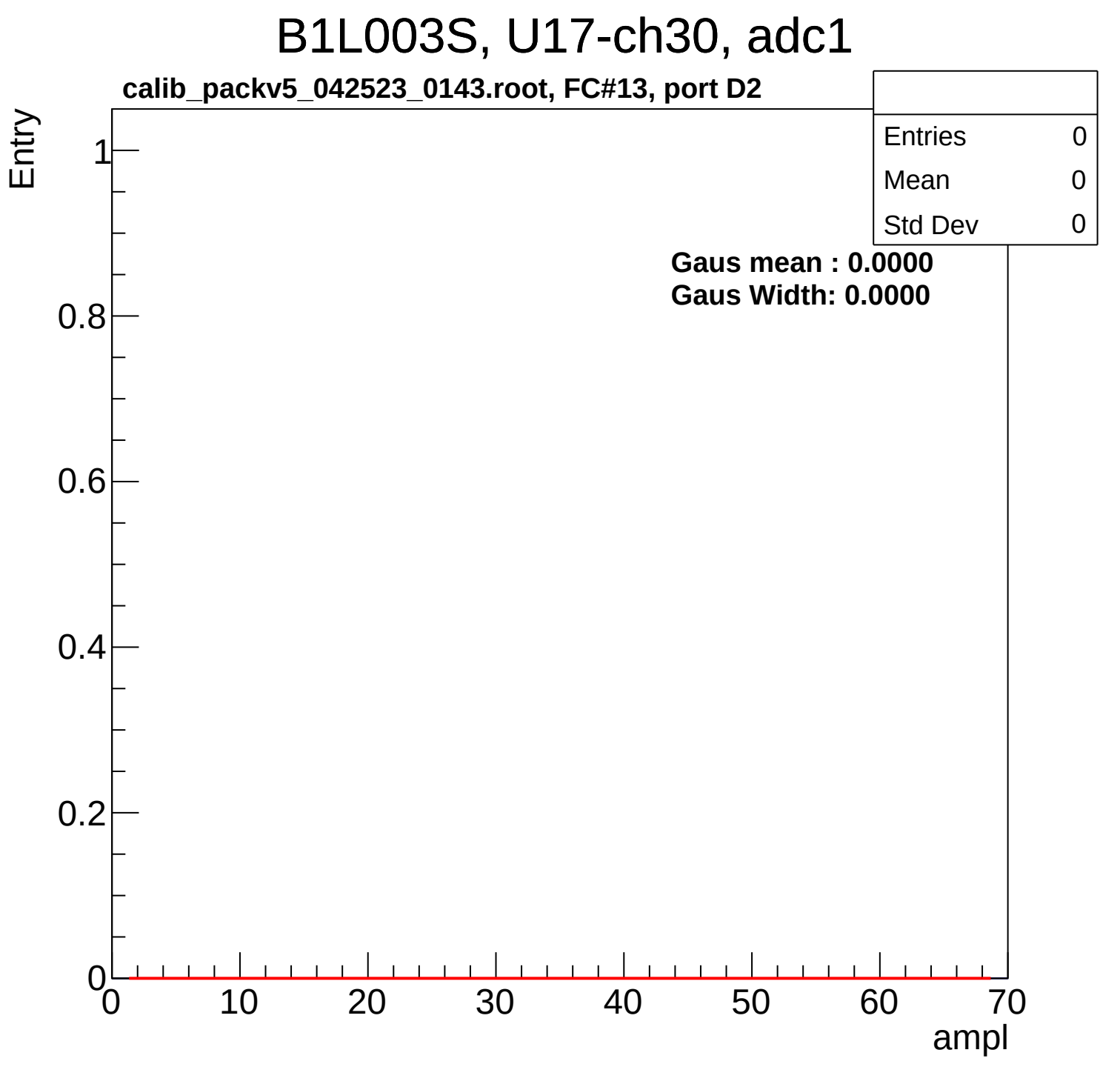
B1L003S, U17-ch30, adc1
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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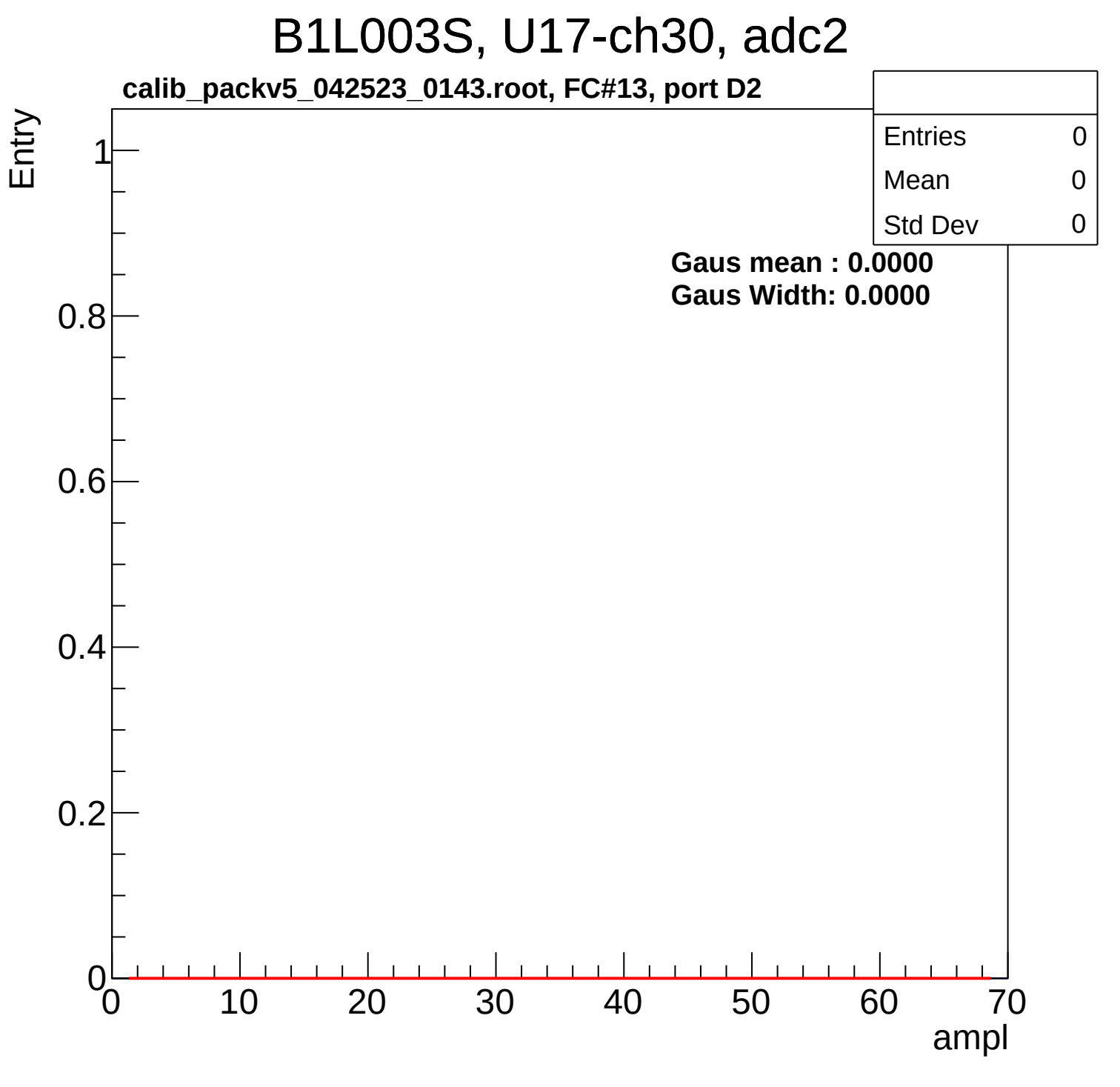
ampl


B1L003S, U17-ch30, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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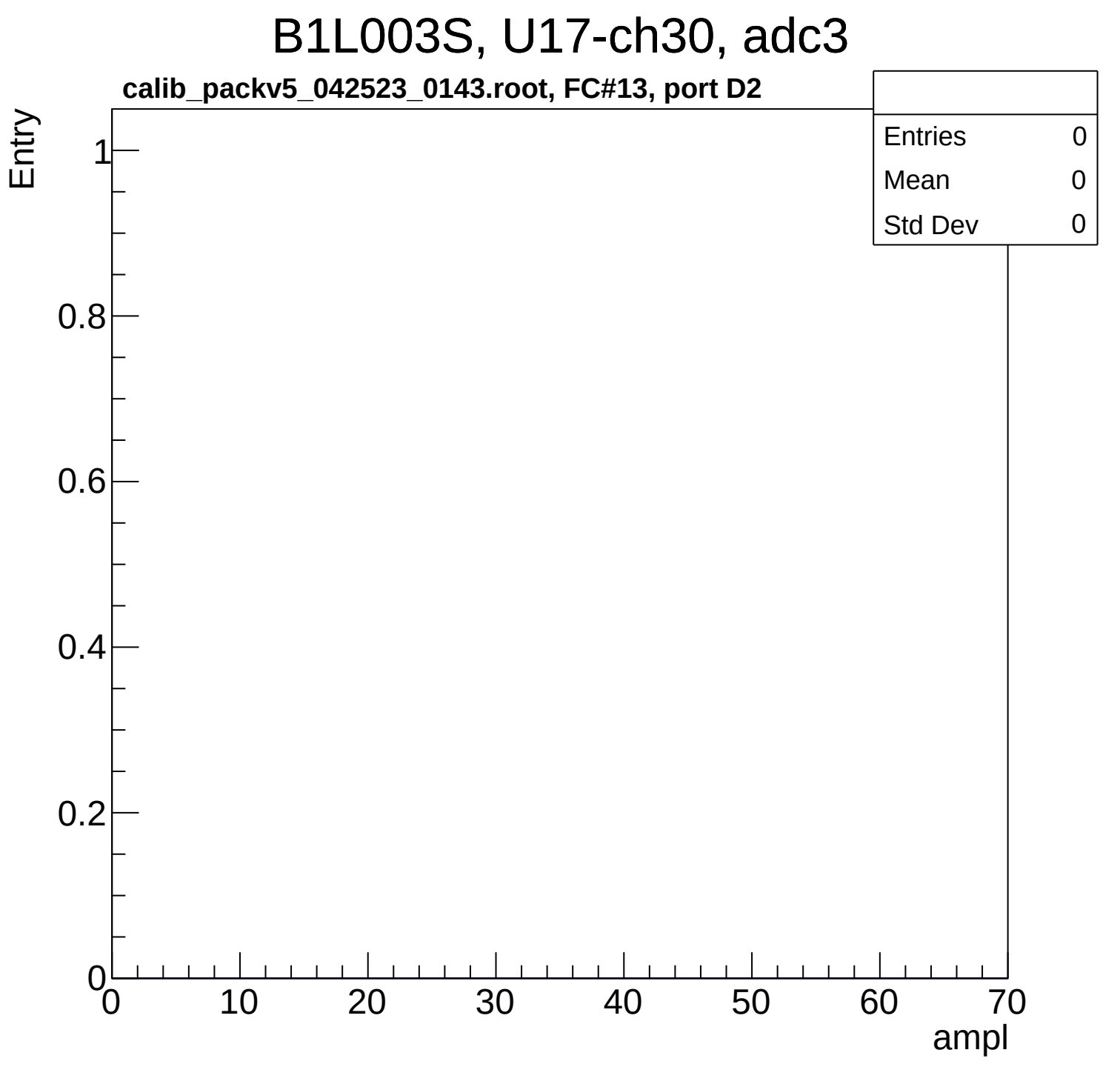
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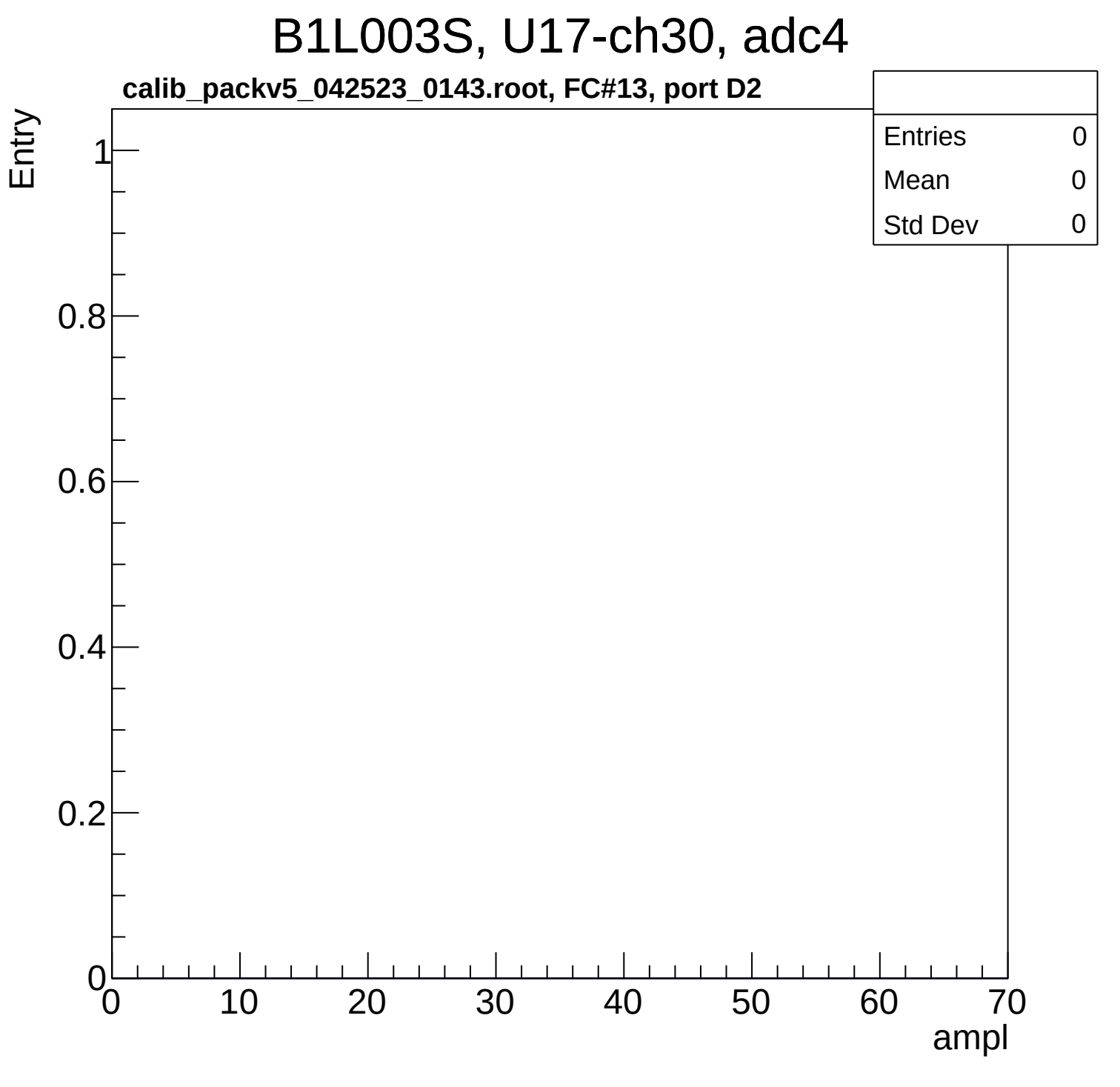
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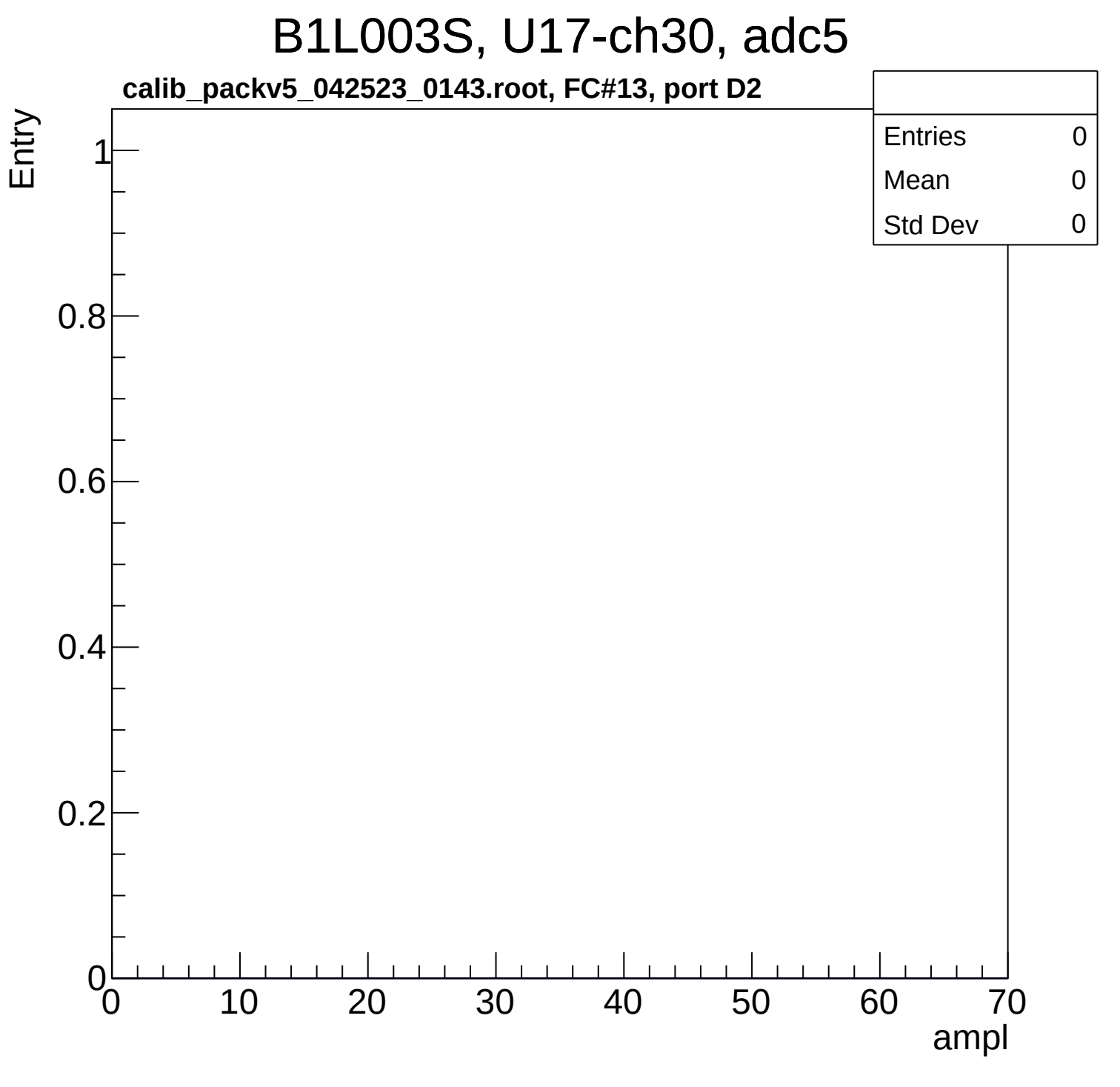
B1L003S, U17-ch30, adc5
calib_packv5_042523_0143.root, FC#13, port D2
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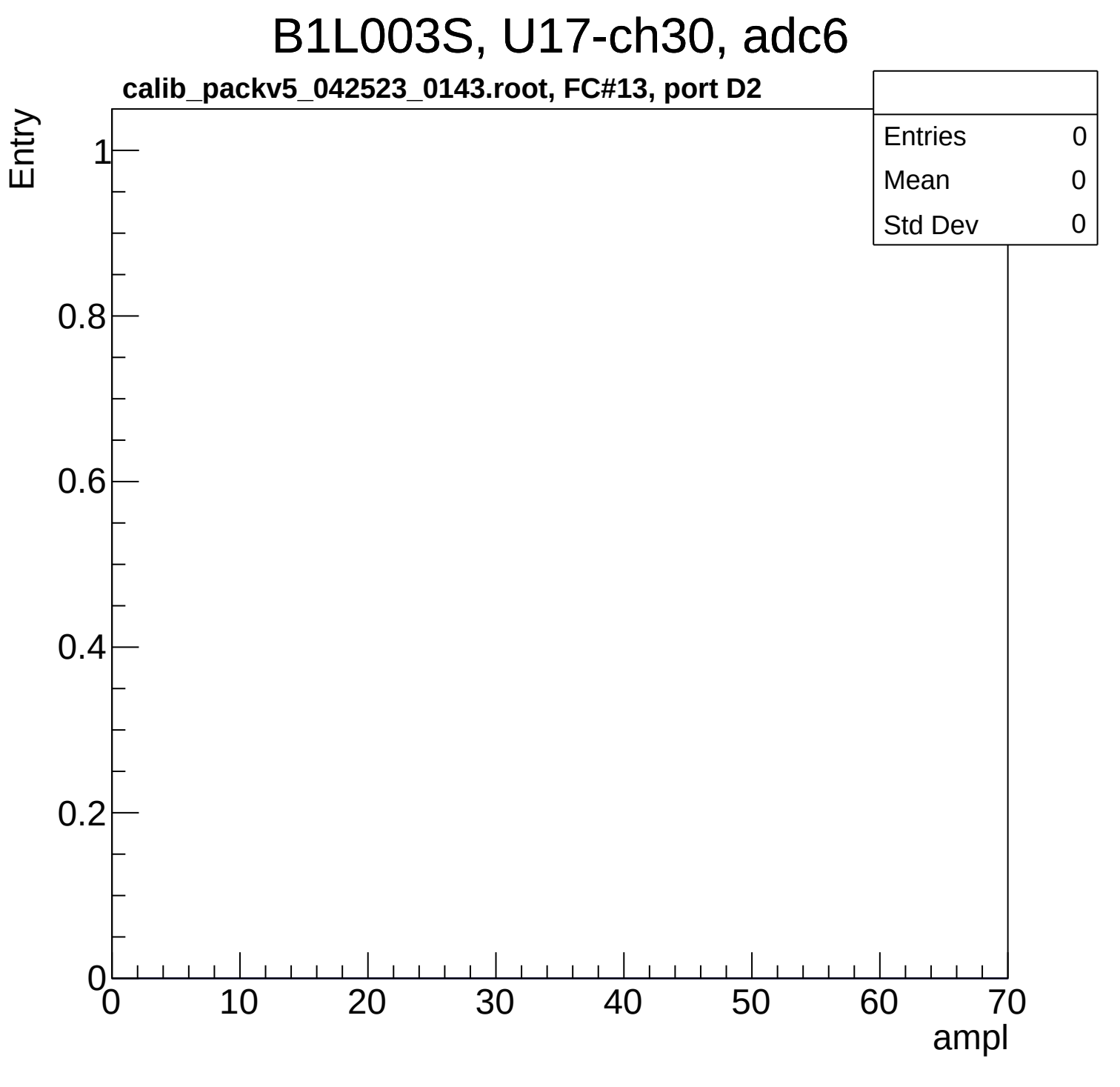
B1L003S, U17-ch30, adc6
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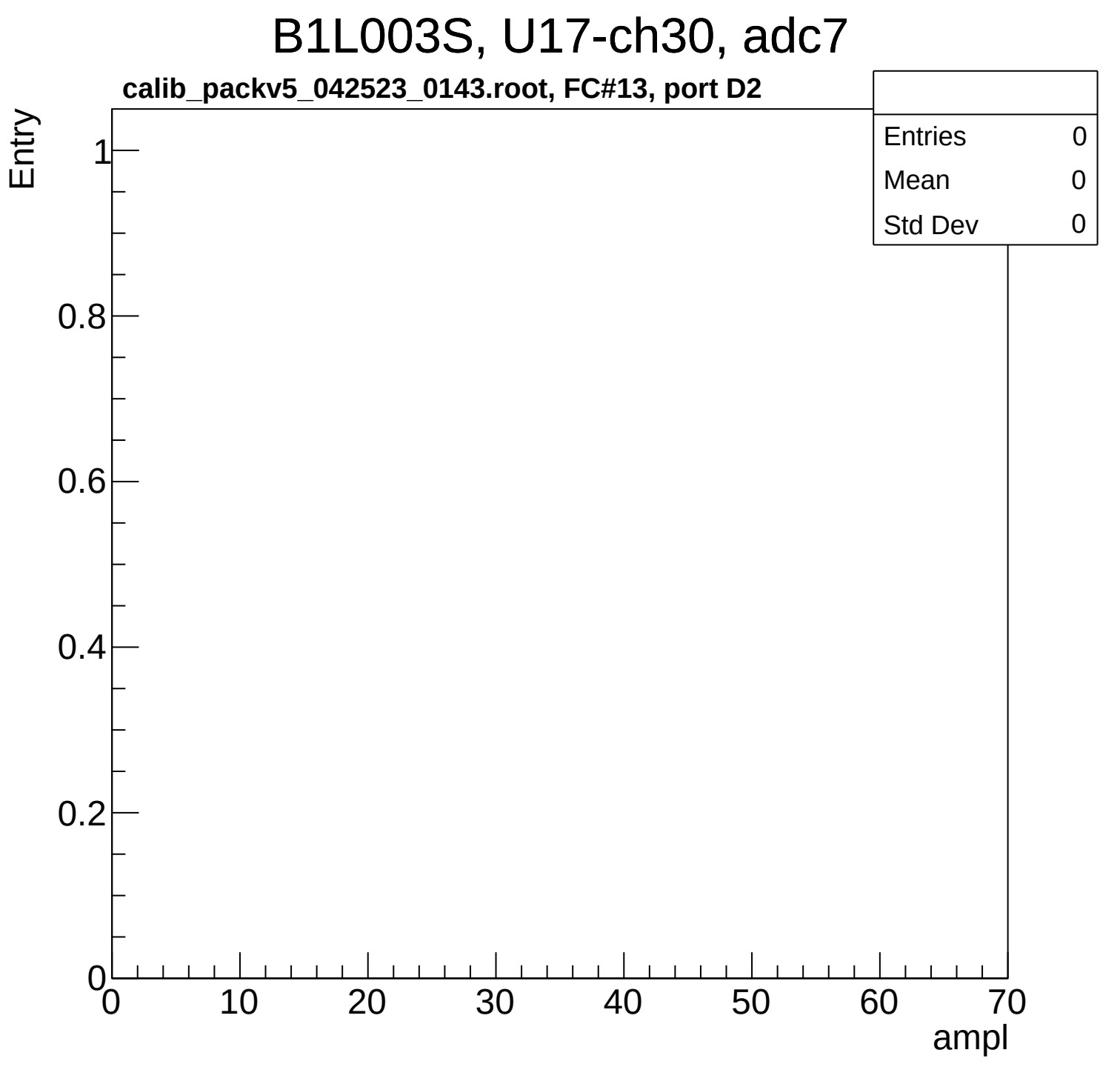
B1L003S, U17-ch30, adc7
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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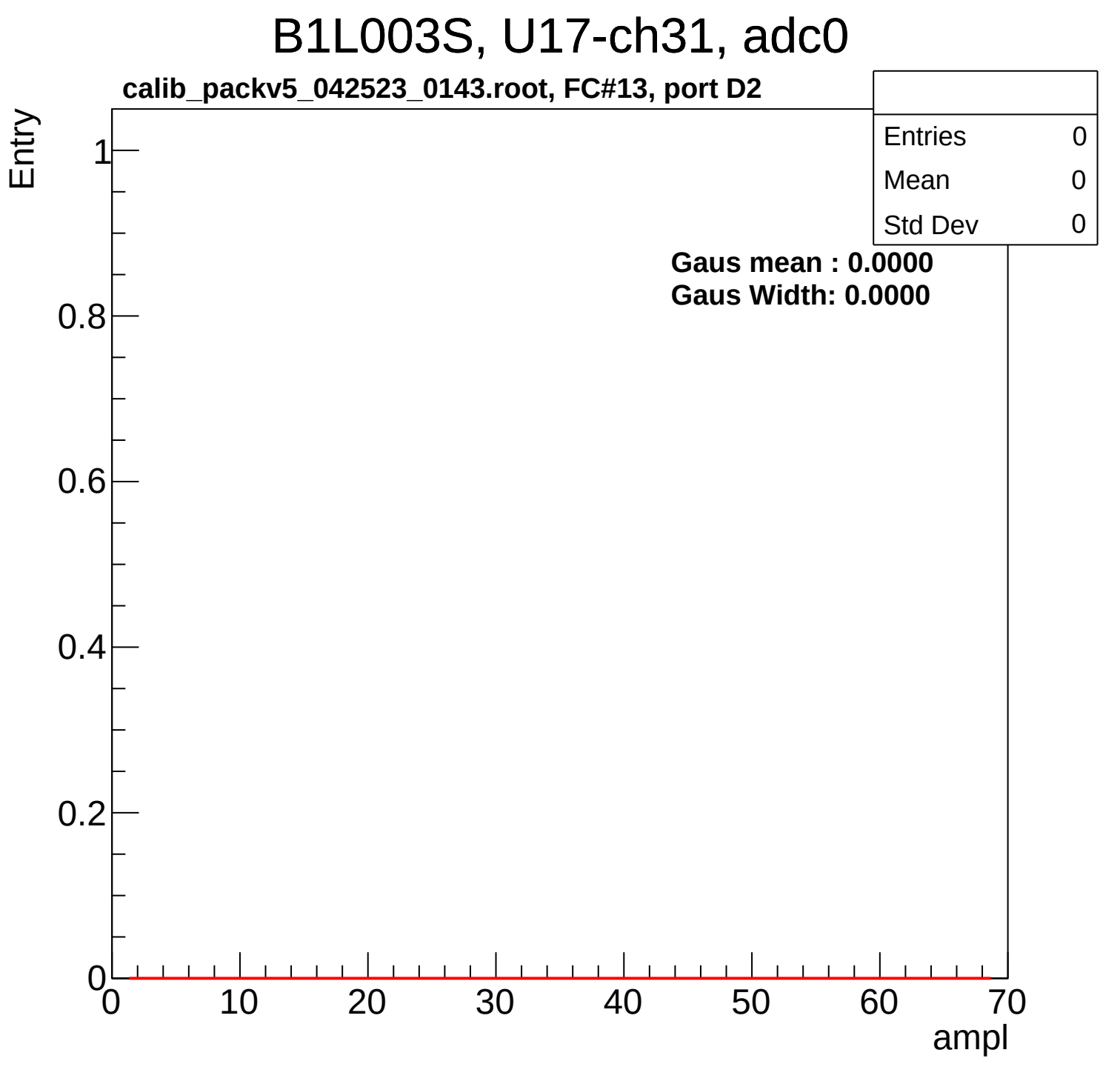
B1L003S, U17-ch31, adc0
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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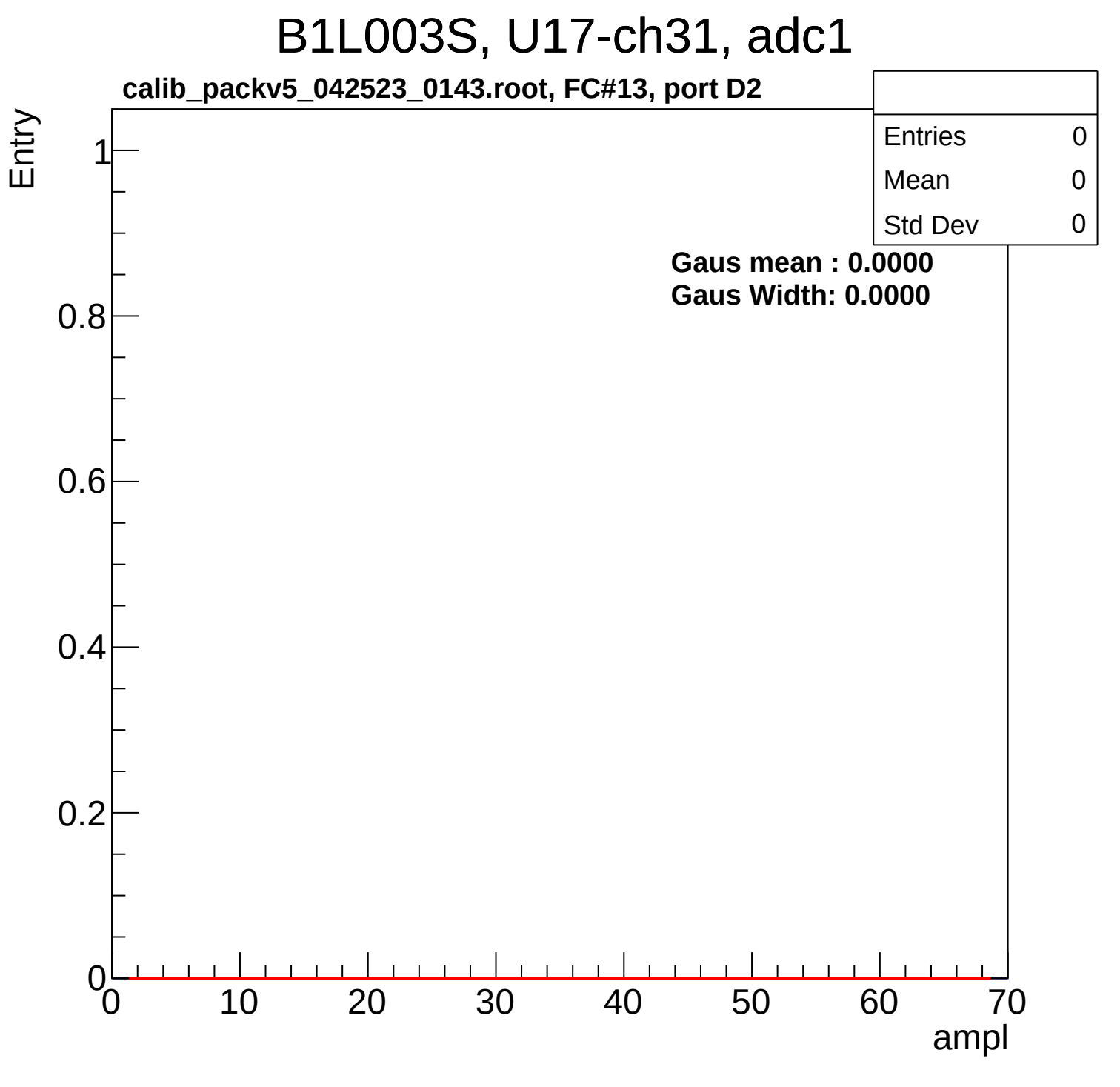
ampl


B1L003S, U17-ch31, adc1
calib_packv5_042523_0143.root, FC#13, port D2
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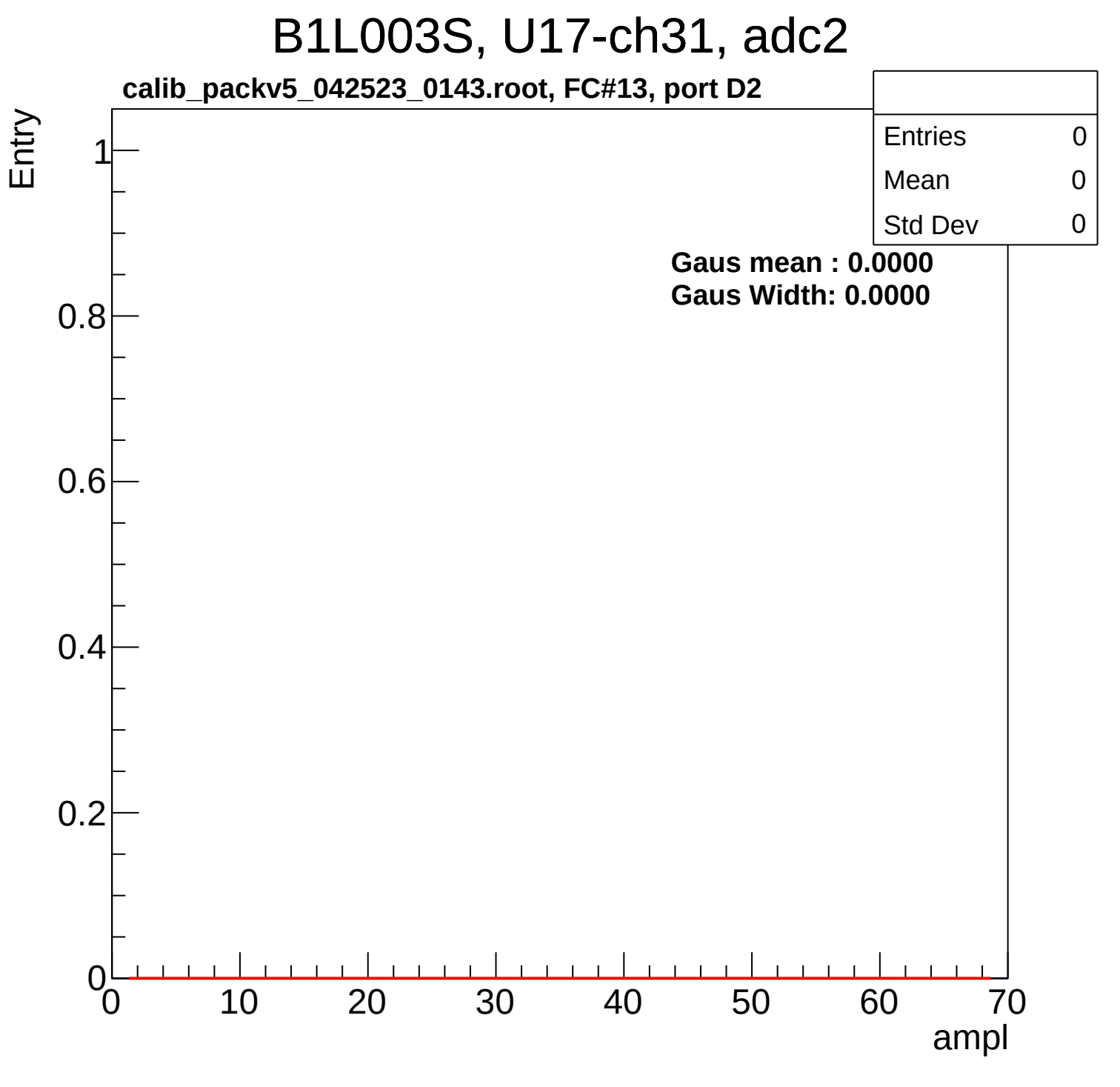
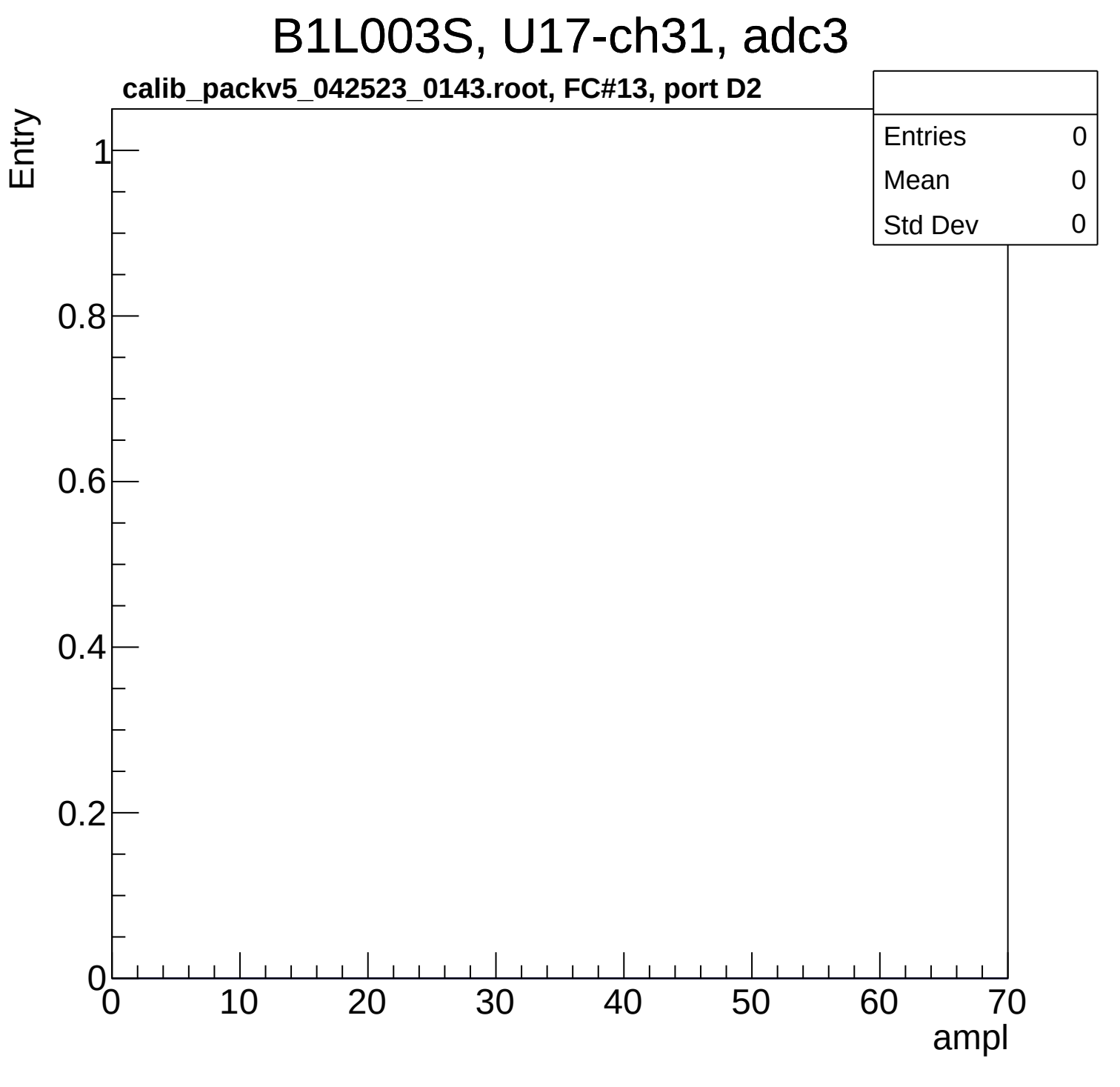
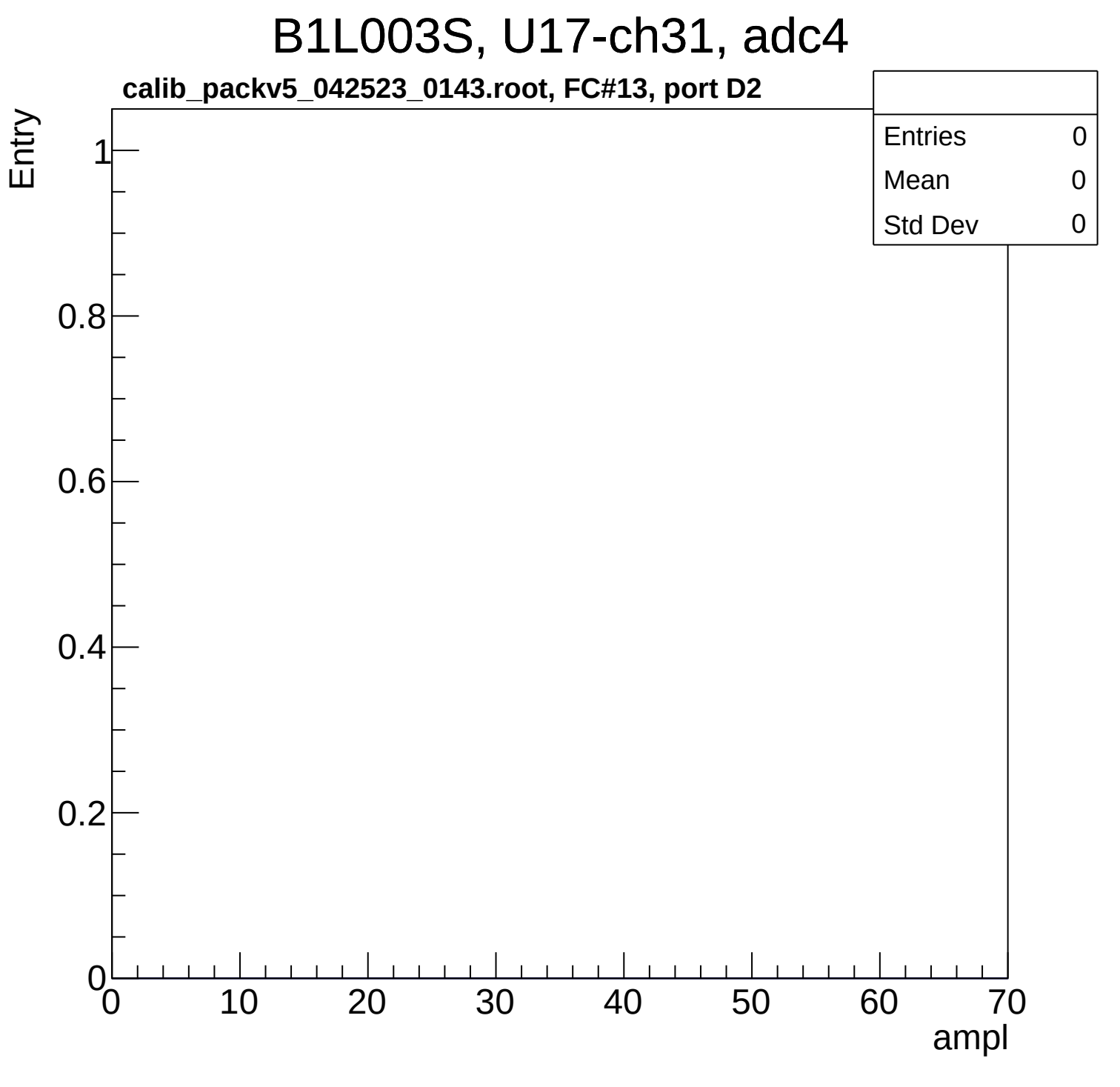
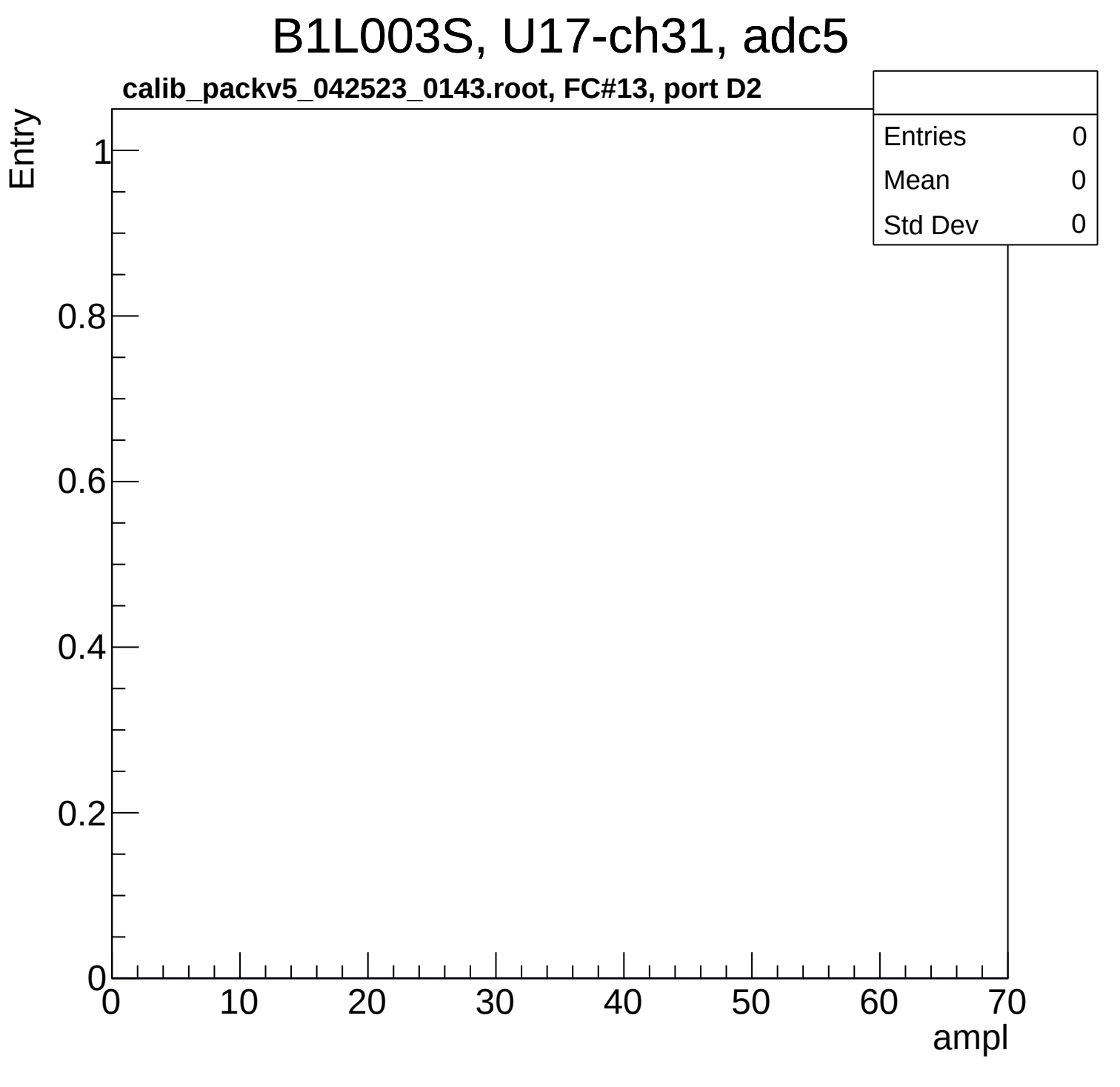
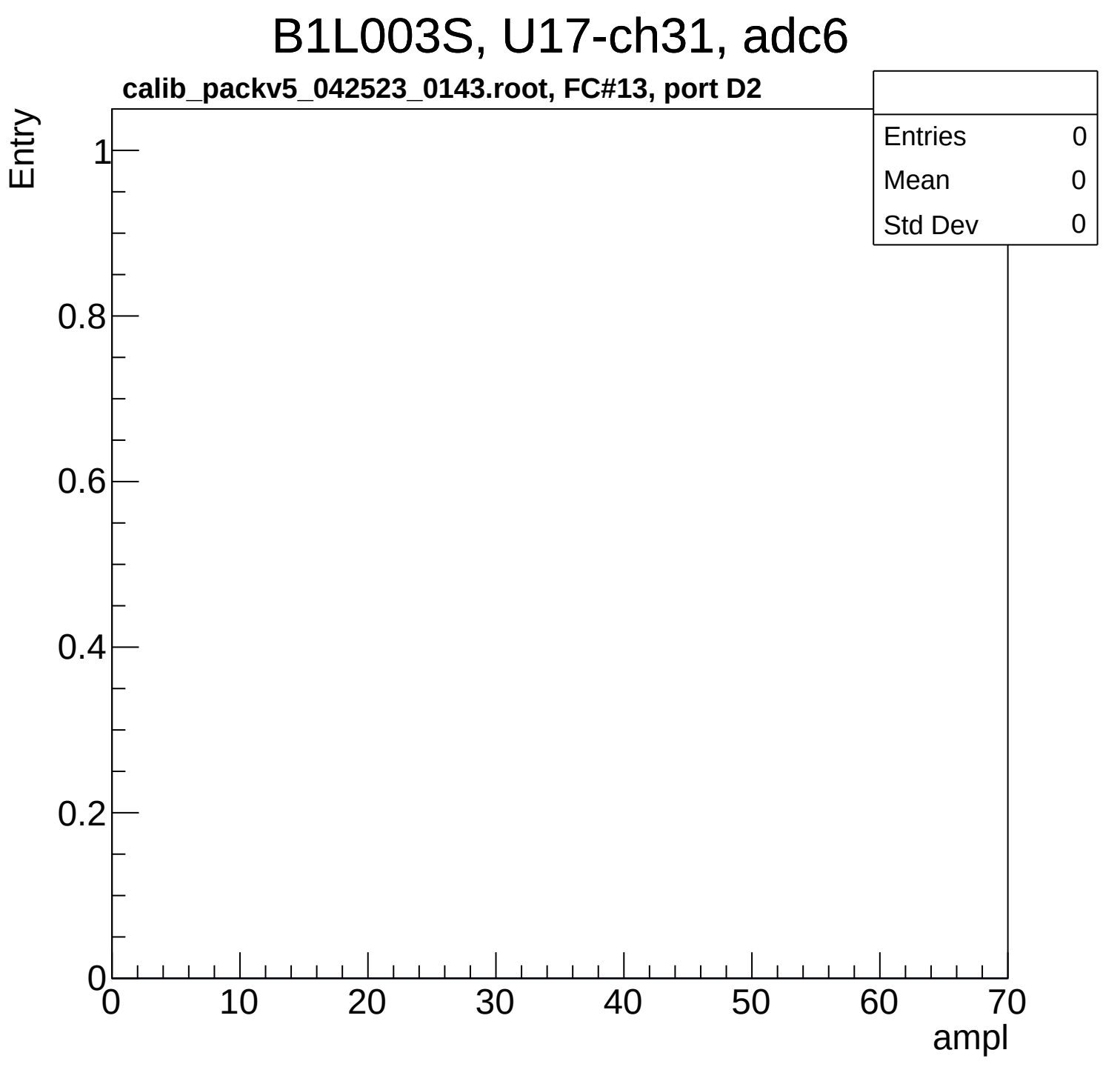
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

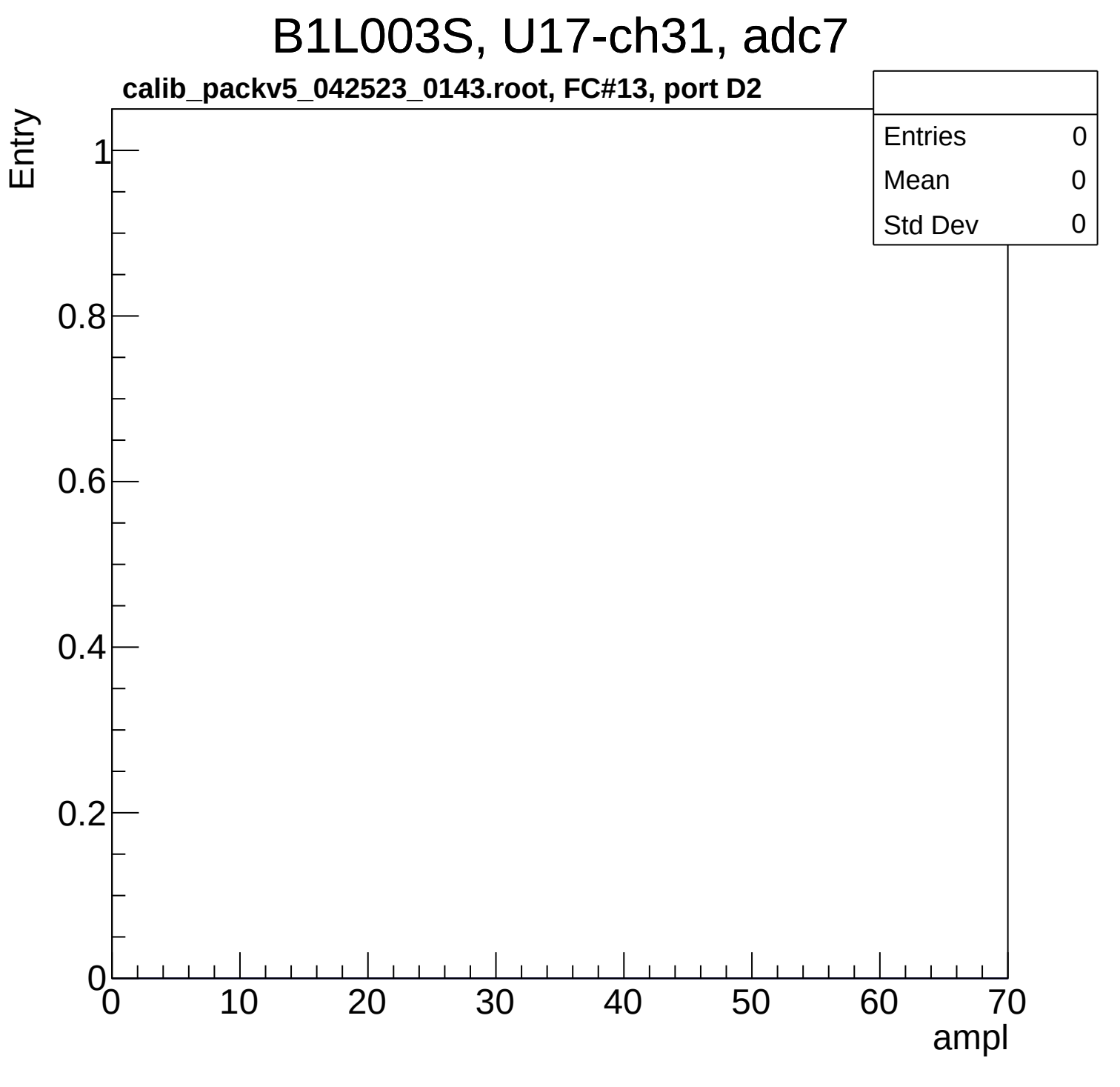

B1L003S, U17-ch31, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

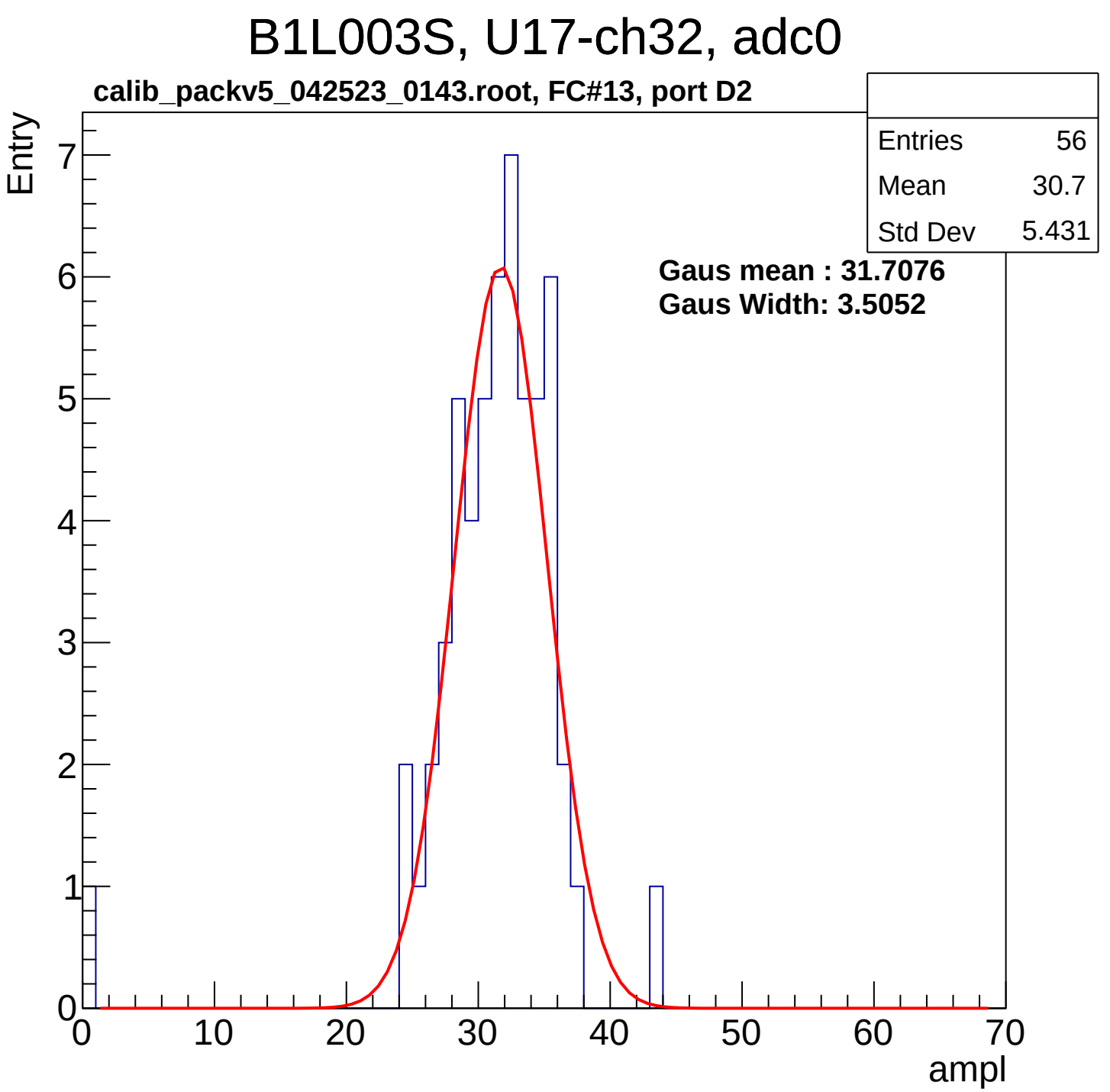

B1L003S, U17-ch31, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

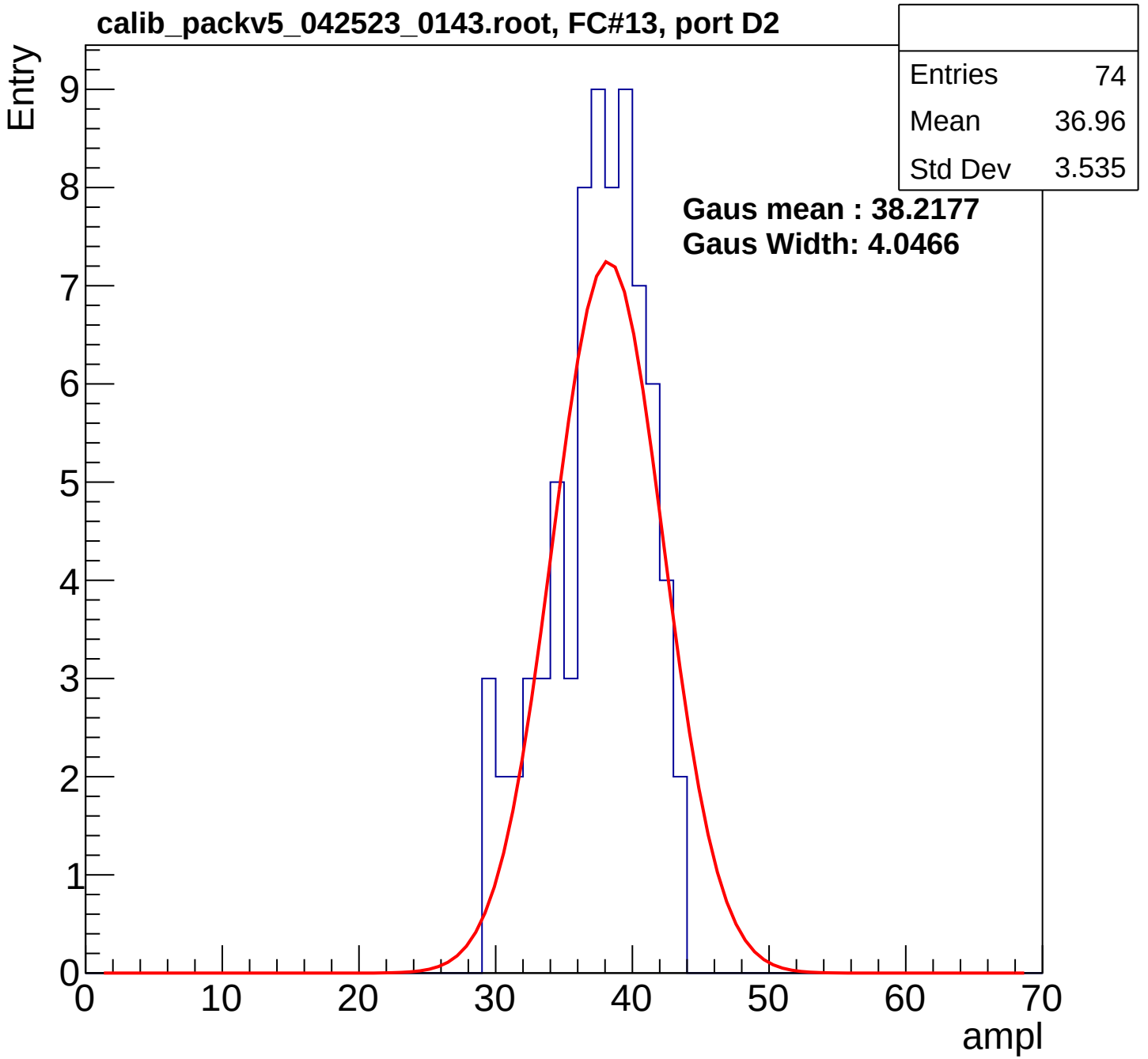

B1L003S, U17-ch31, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

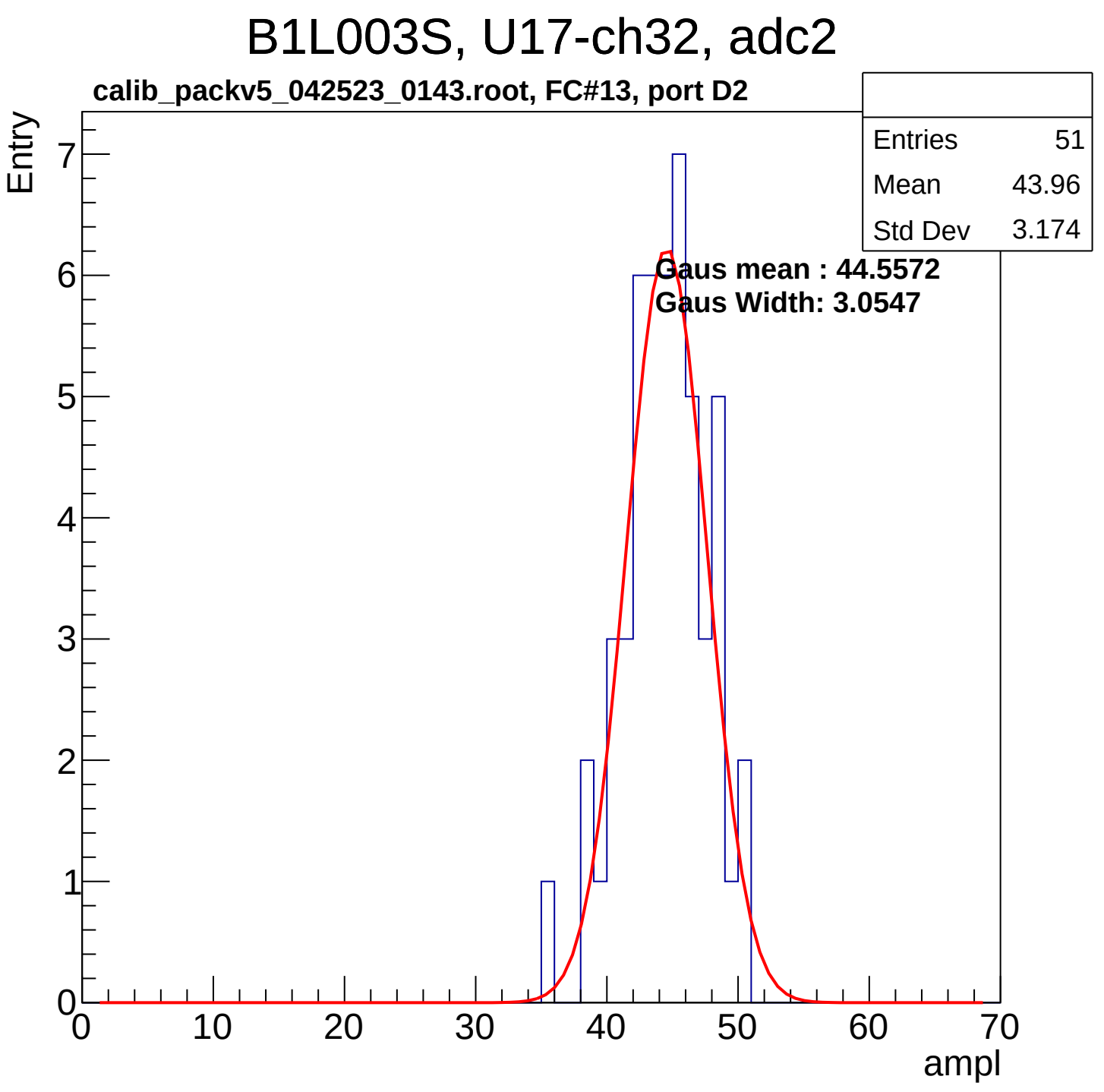

B1L003S, U17-ch31, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

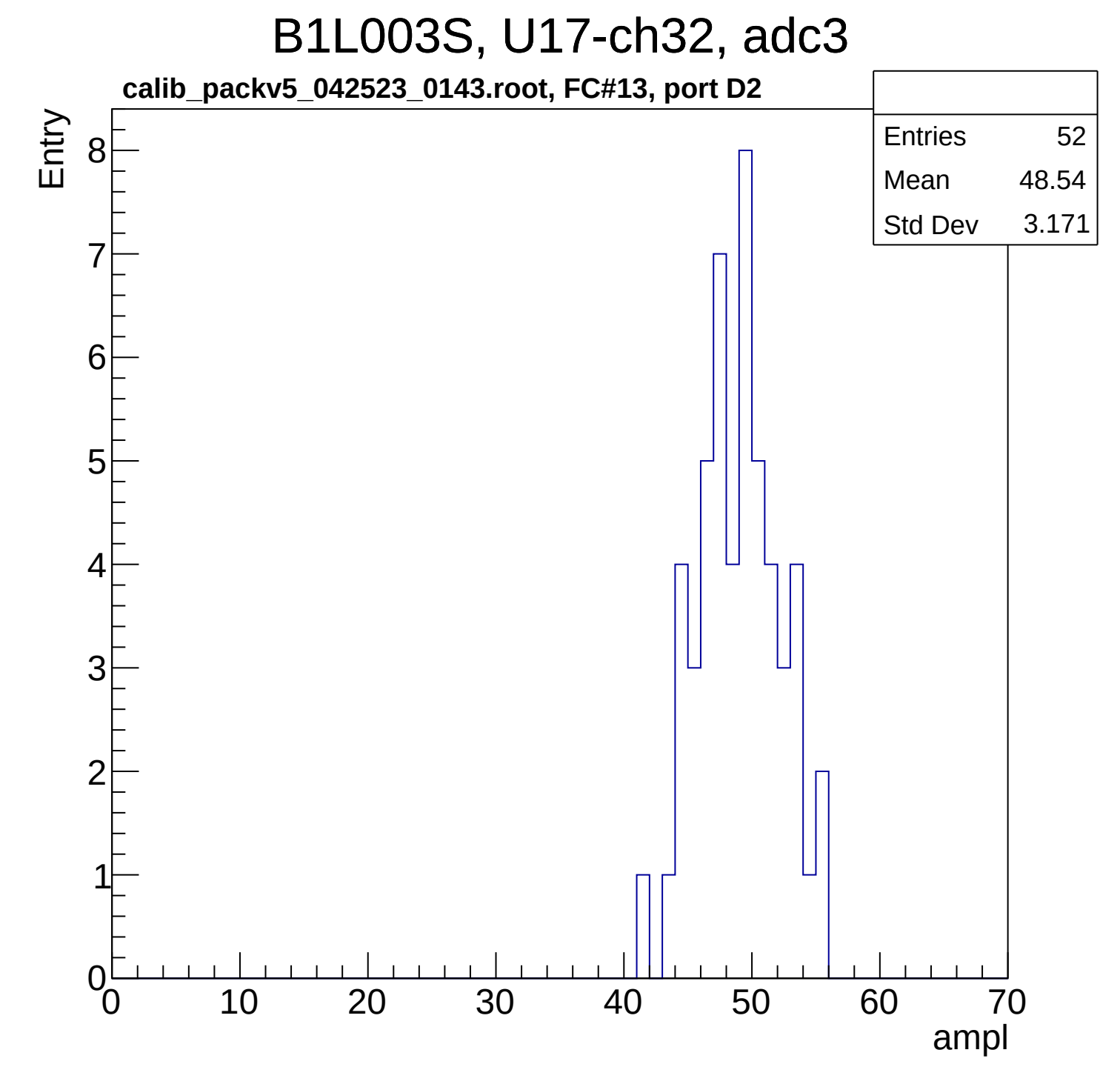

B1L003S, U17-ch31, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

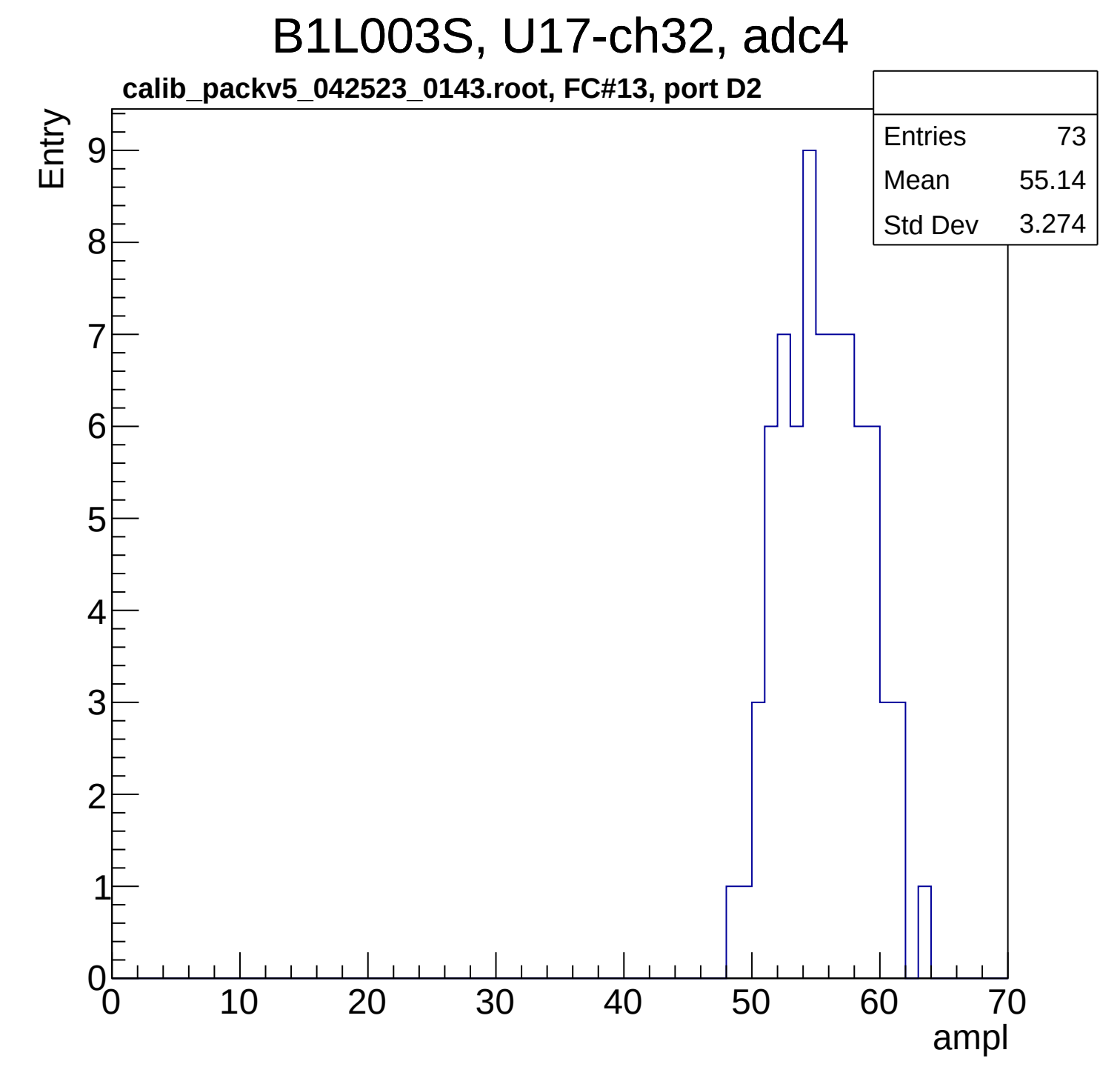

B1L003S, U17-ch31, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

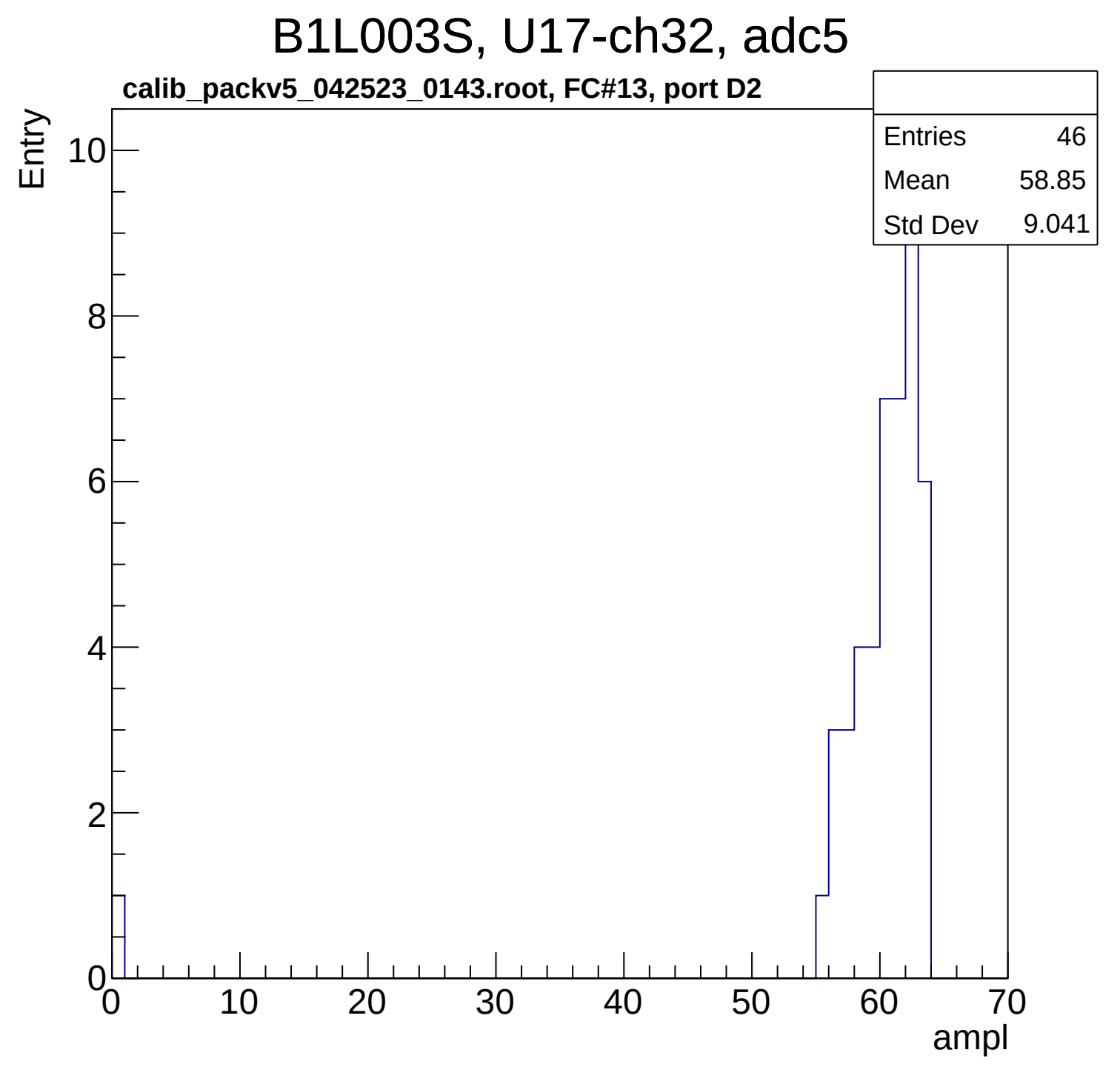

B1L003S, U17-ch32, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 30.7
Std Dev 5.431
Gaus mean : 31.7076
Gaus Width: 3.5052
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

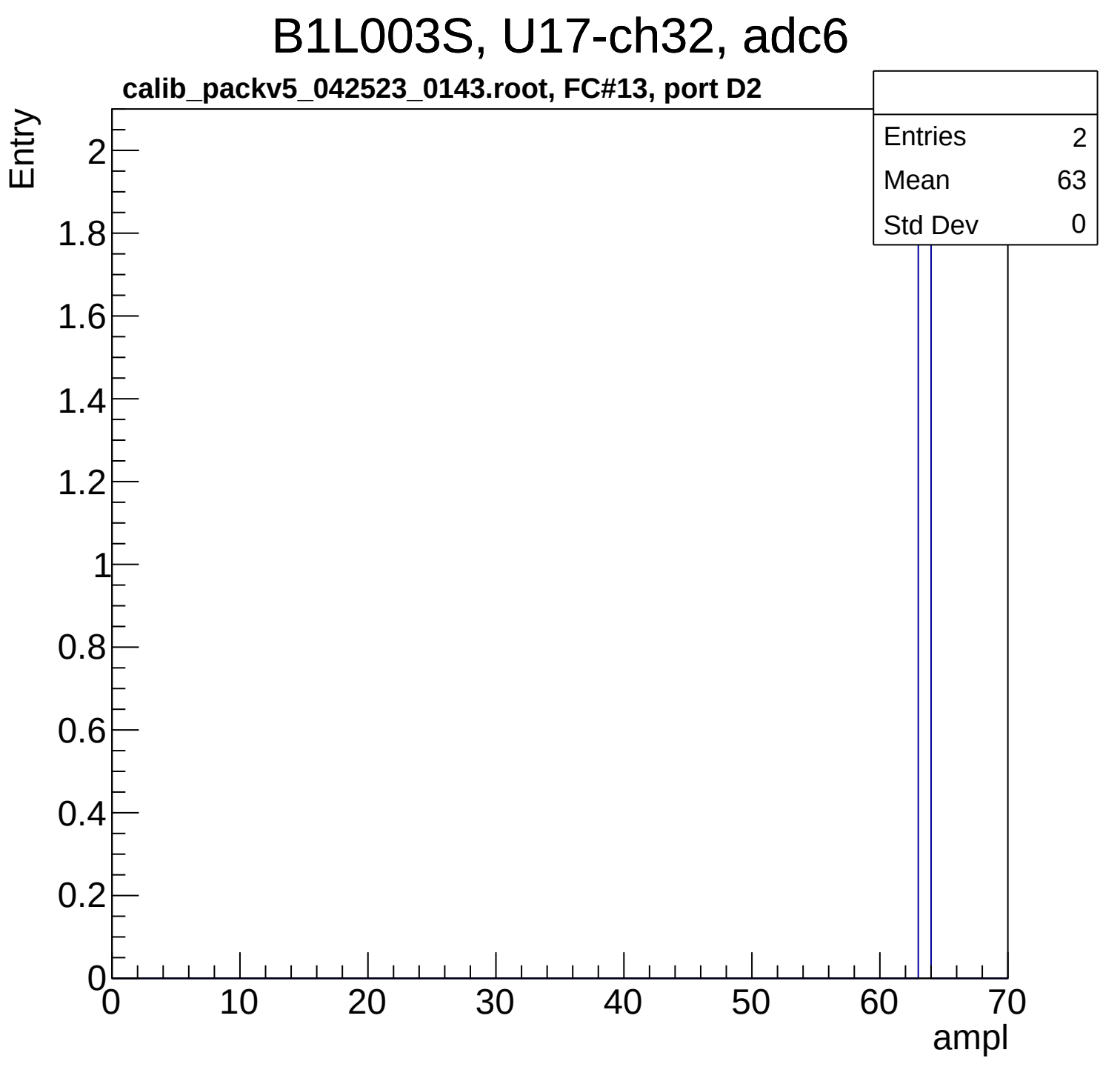

B1L003S, U17-ch32, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.96
Std Dev 3.535
Gaus mean : 38.2177
Gaus Width: 4.0466
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

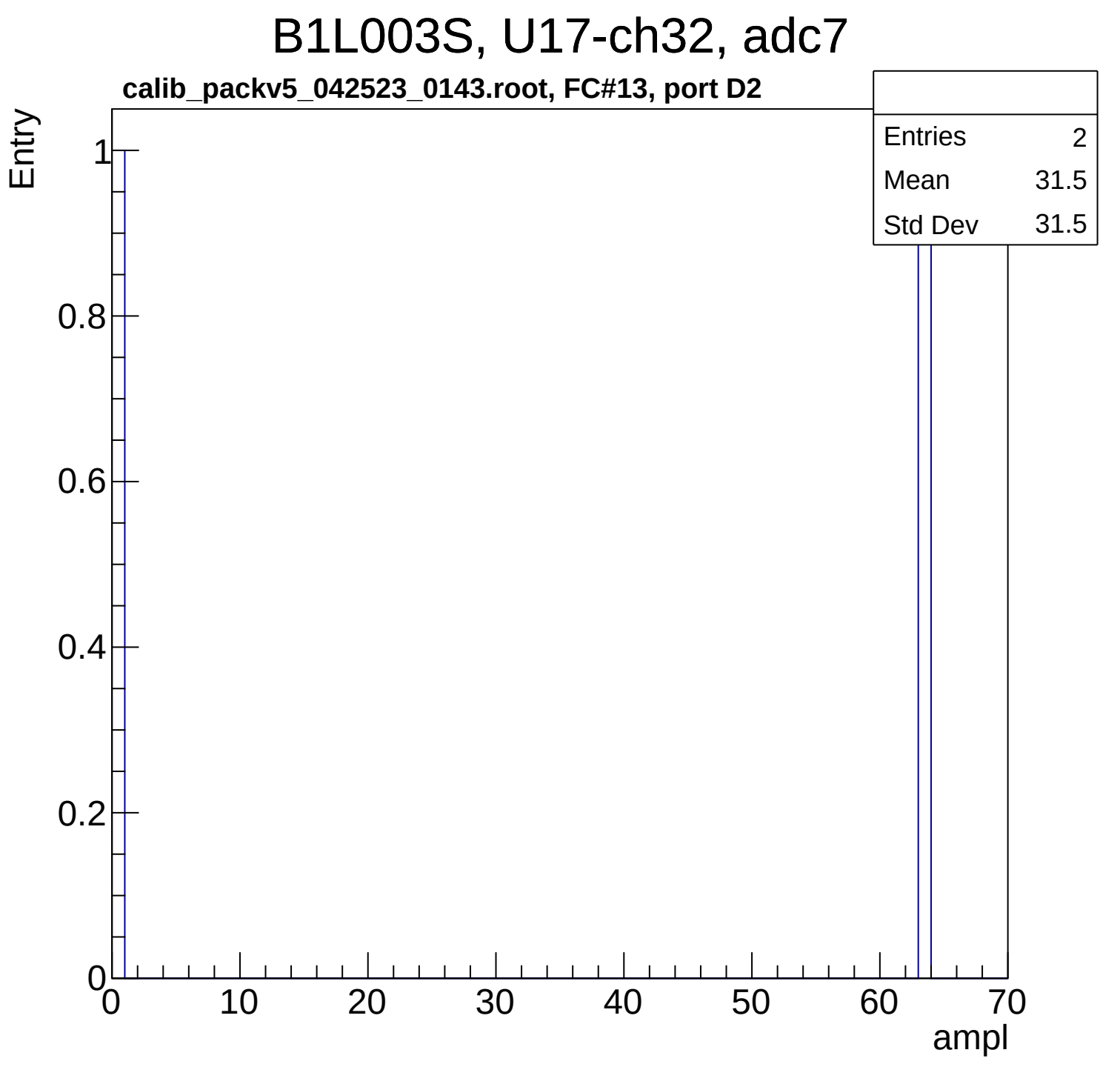

B1L003S, U17-ch32, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 43.96
Std Dev 3.174
Gaus mean : 44.5572
Gaus Width: 3.0547
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

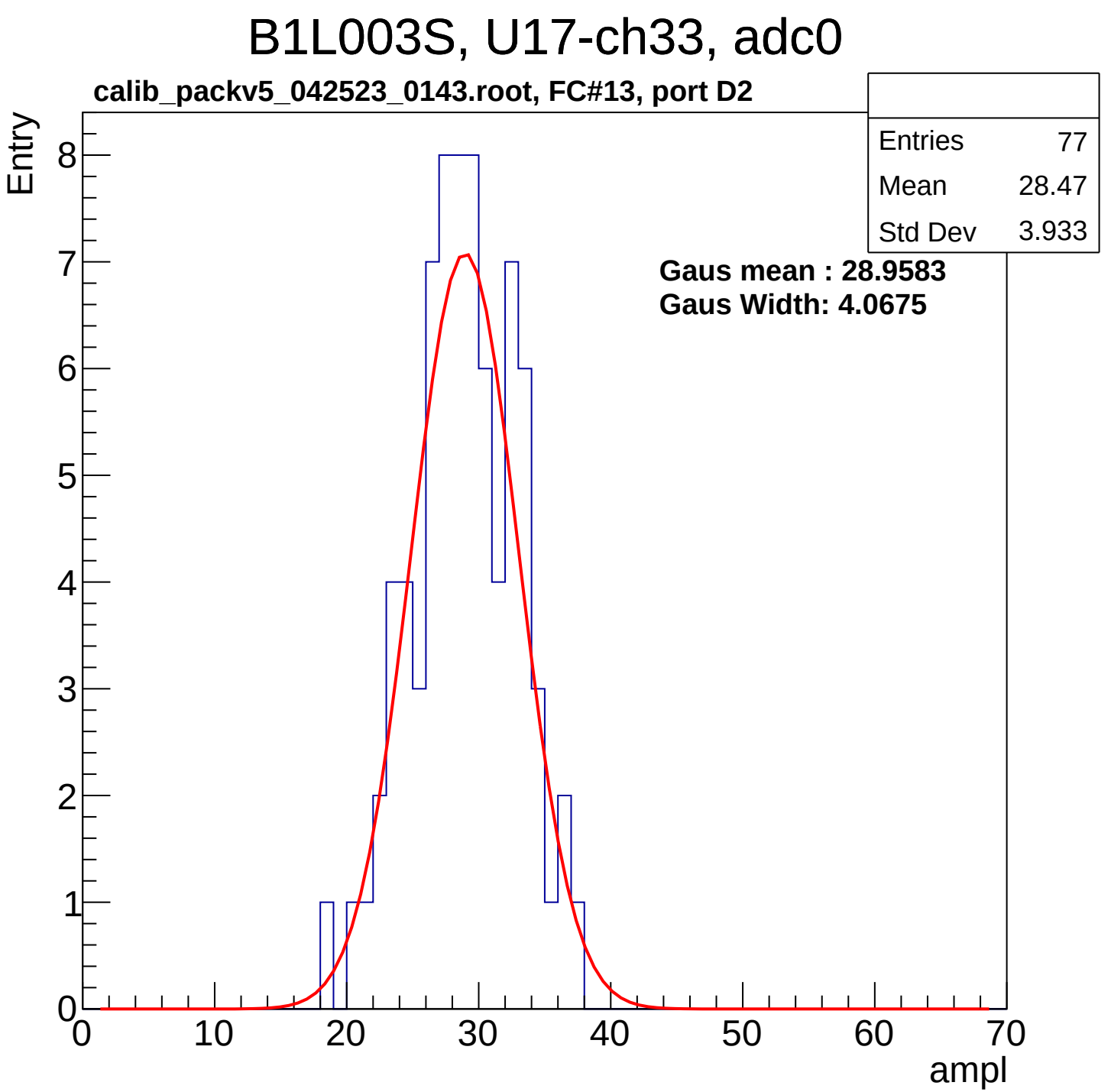

B1L003S, U17-ch32, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 48.54
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

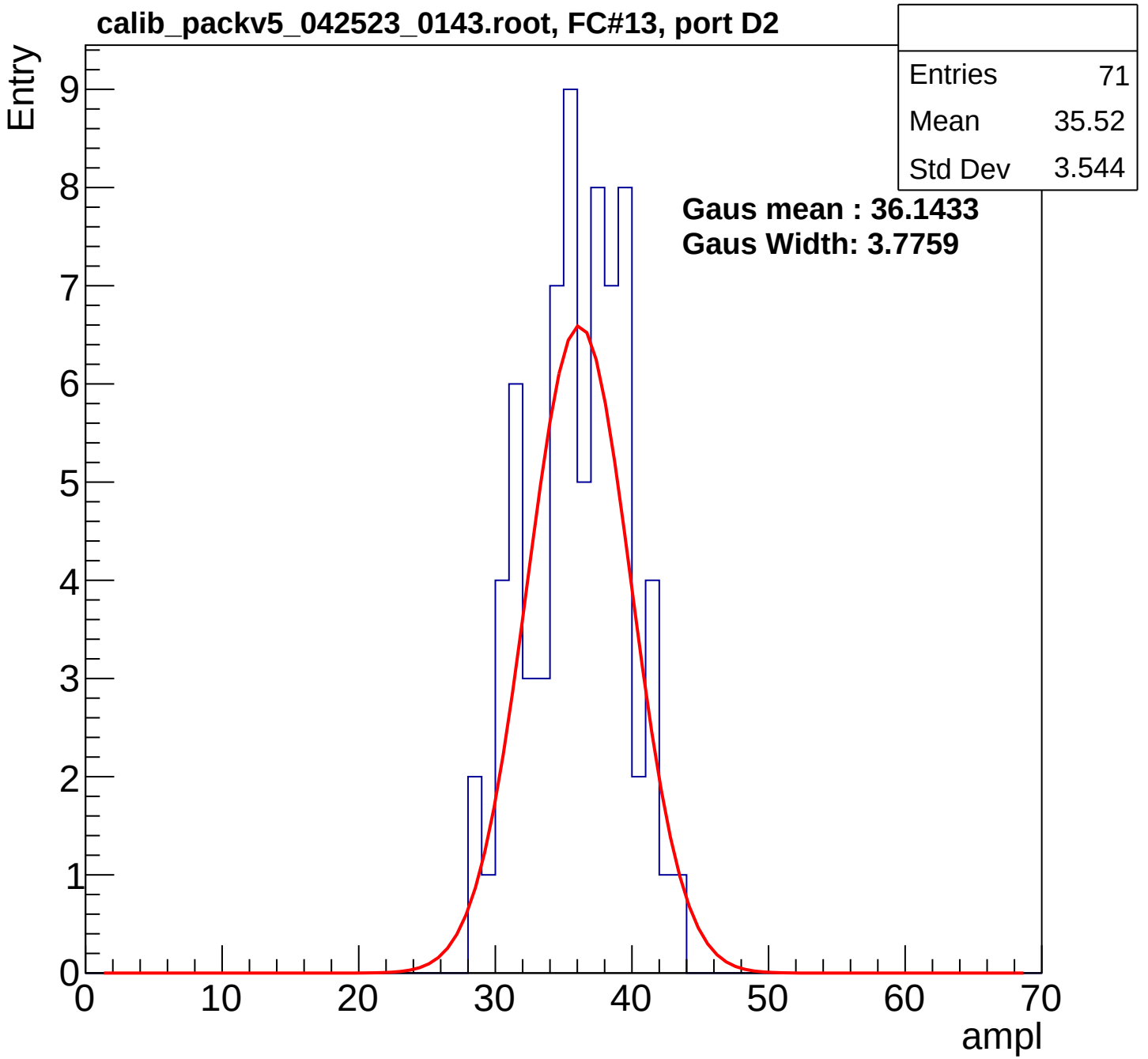

B1L003S, U17-ch32, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 55.14
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

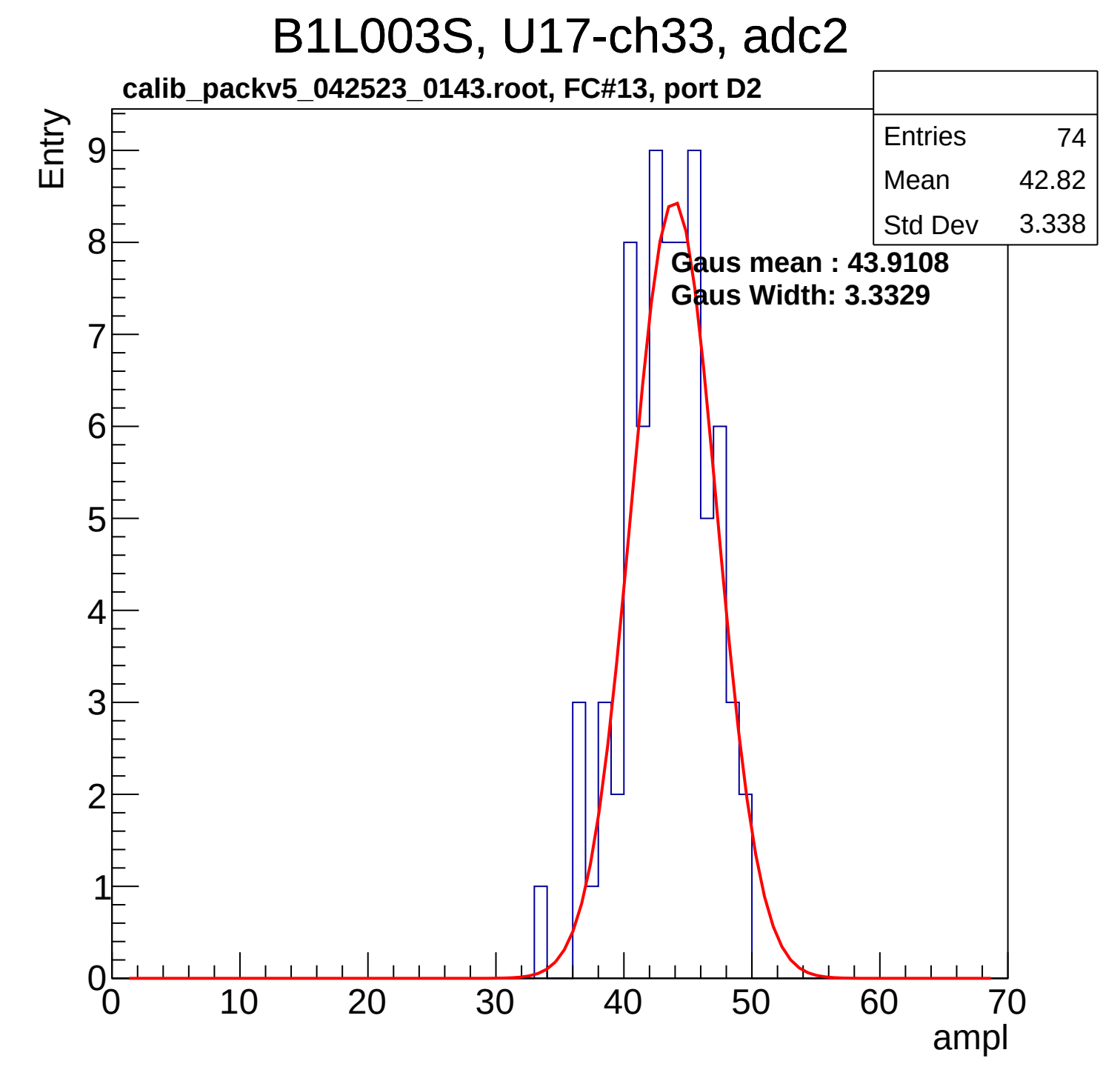

B1L003S, U17-ch32, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.85
Std Dev 9.041
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

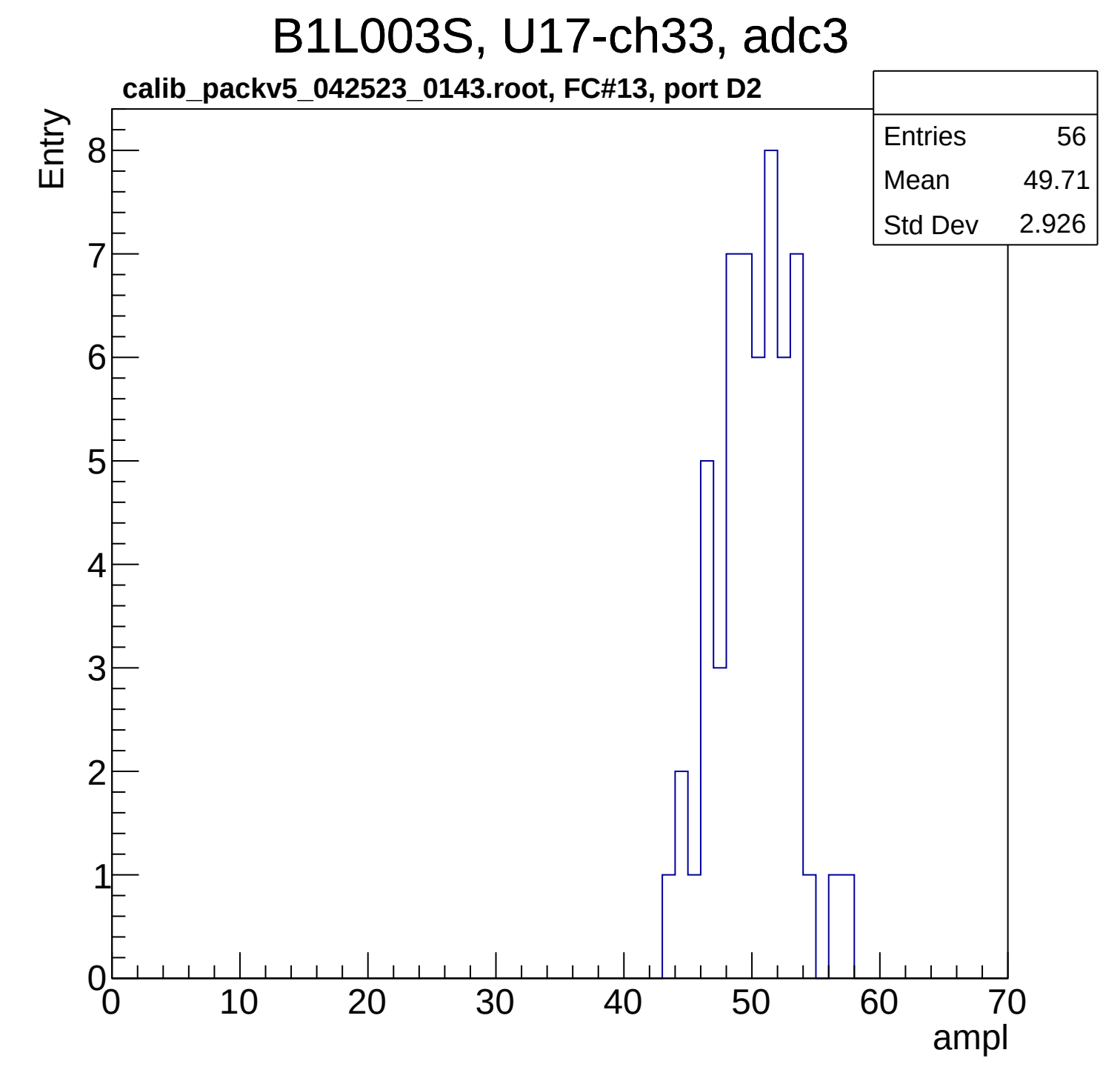

B1L003S, U17-ch32, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

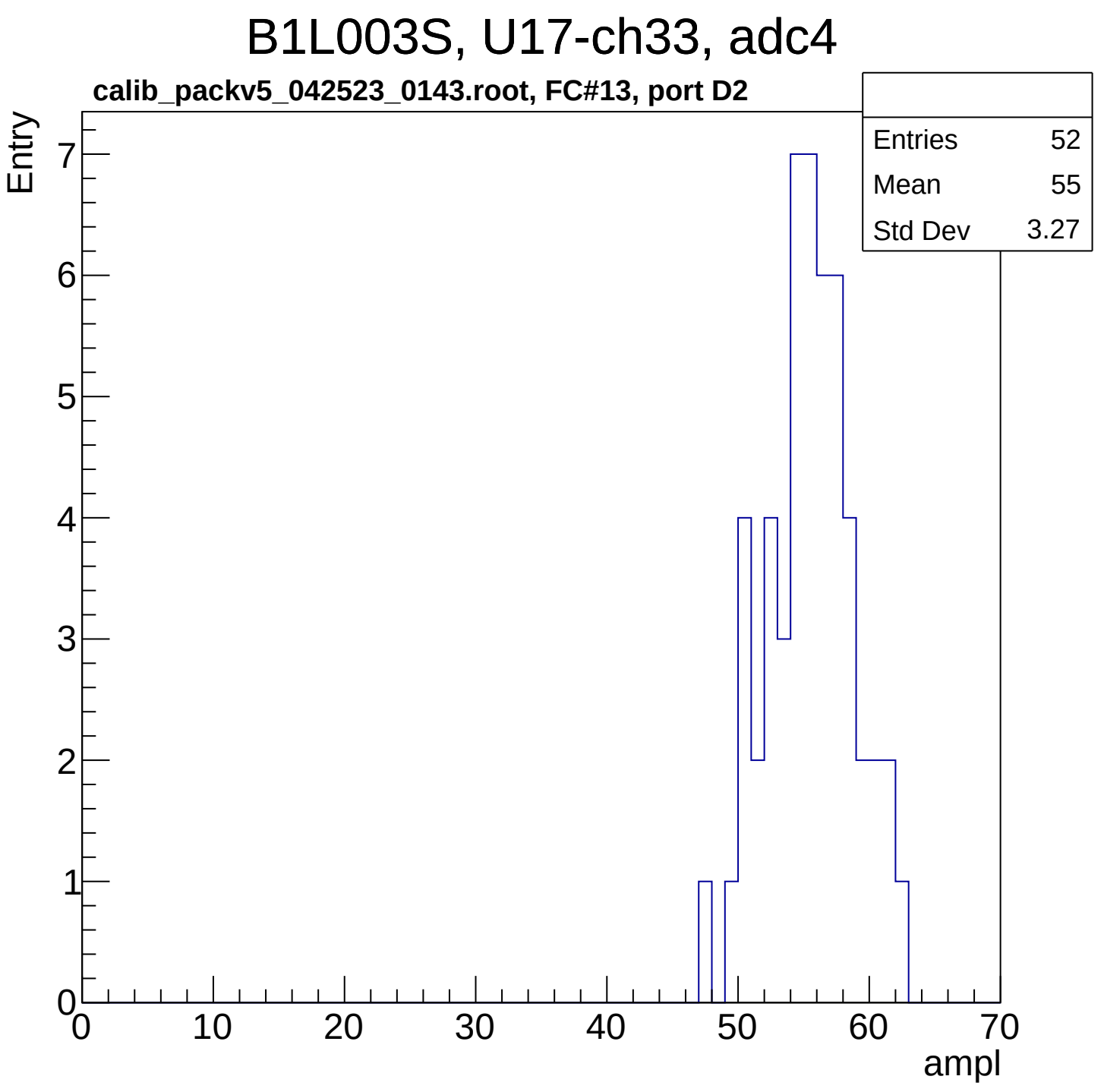

B1L003S, U17-ch32, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch33, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.47
Std Dev 3.933
Gaus mean : 28.9583
Gaus Width: 4.0675
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch33, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.52
Std Dev 3.544
Gaus mean : 36.1433
Gaus Width: 3.7759
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch33, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 42.82
Std Dev 3.338
Gaus mean : 43.9108
Gaus Width: 3.3329
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch33, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 49.71
Std Dev 2.926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch33, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55
Std Dev 3.27
Entry
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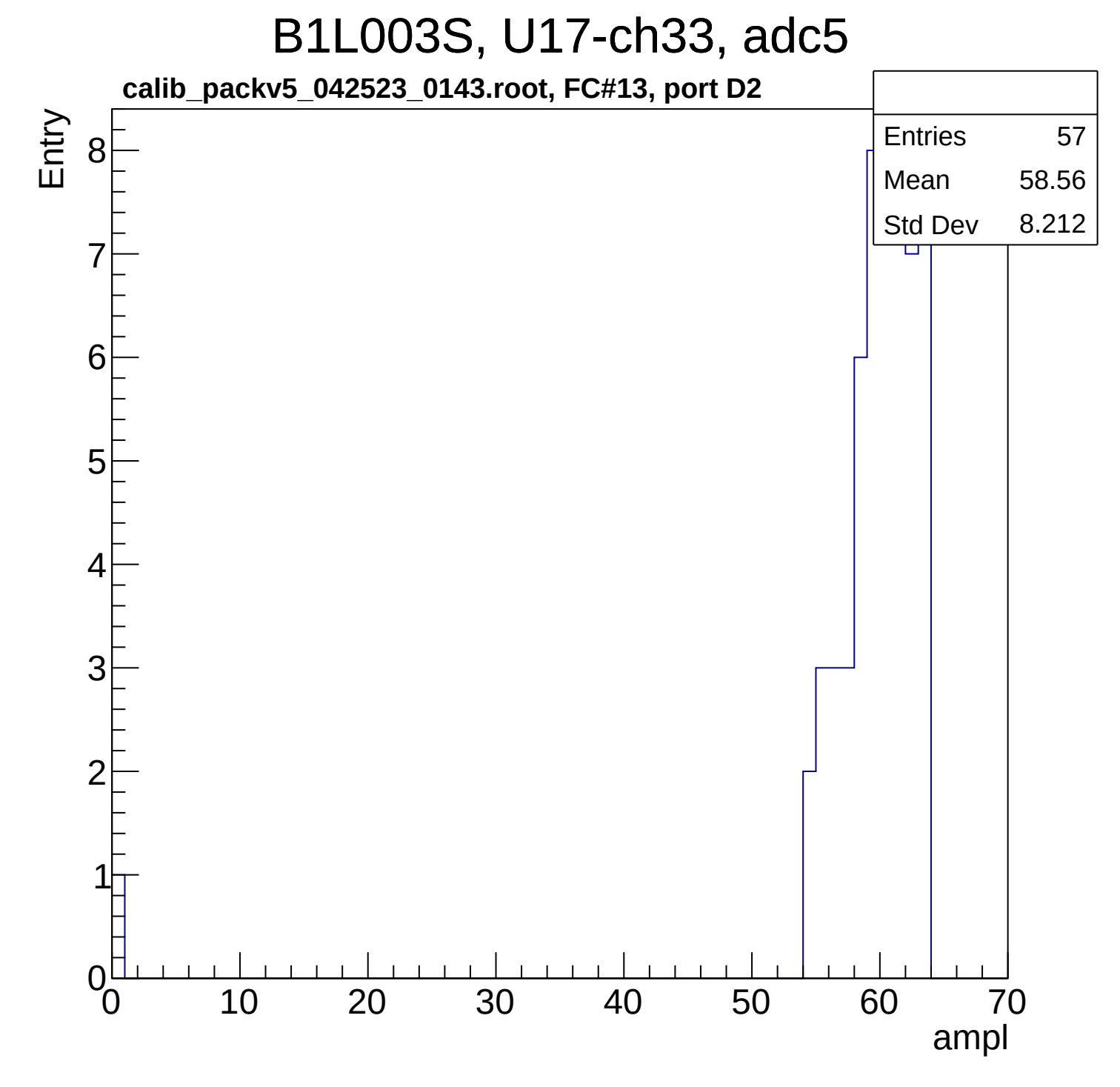
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch33, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.56
Std Dev 8.212
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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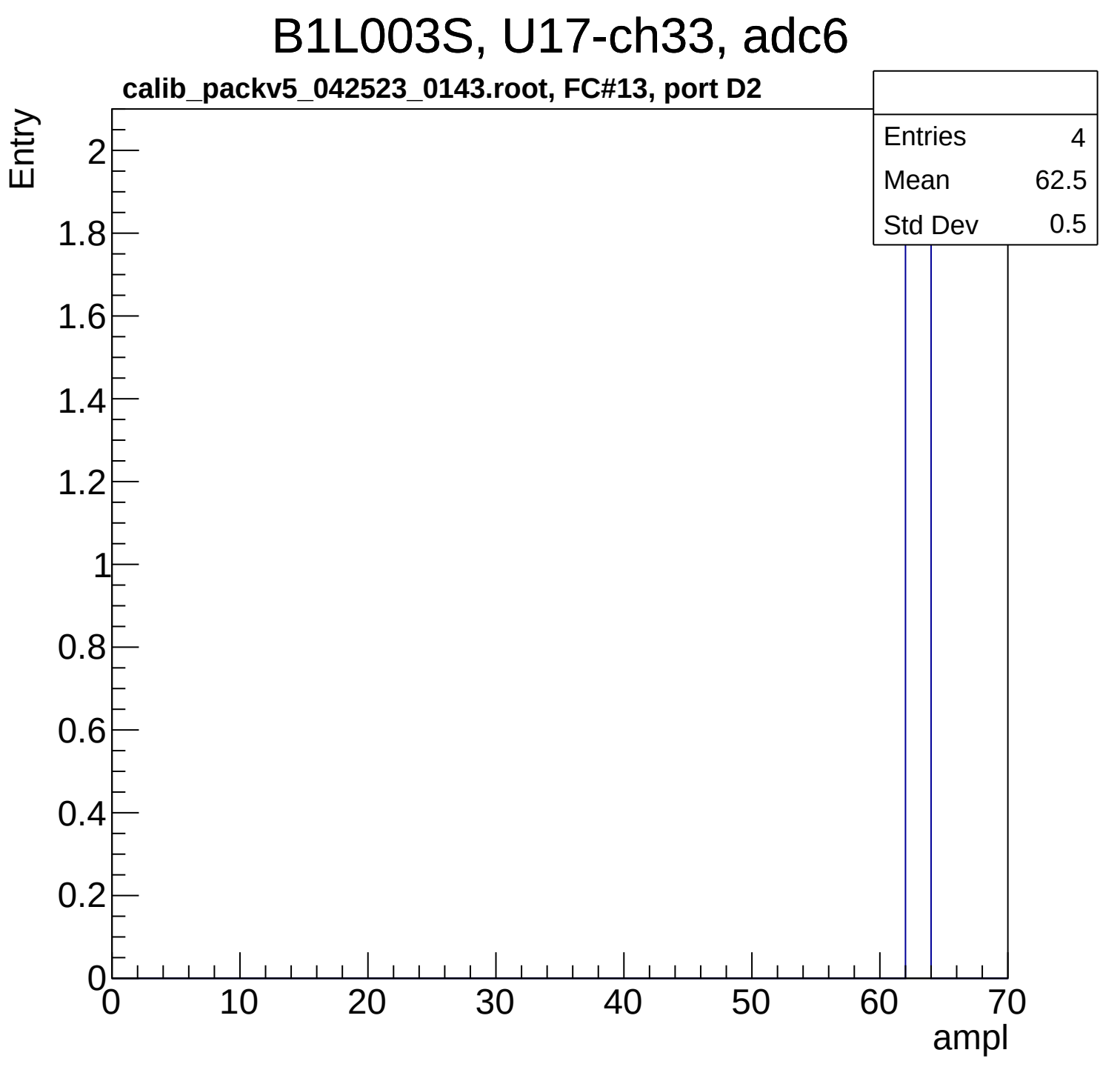
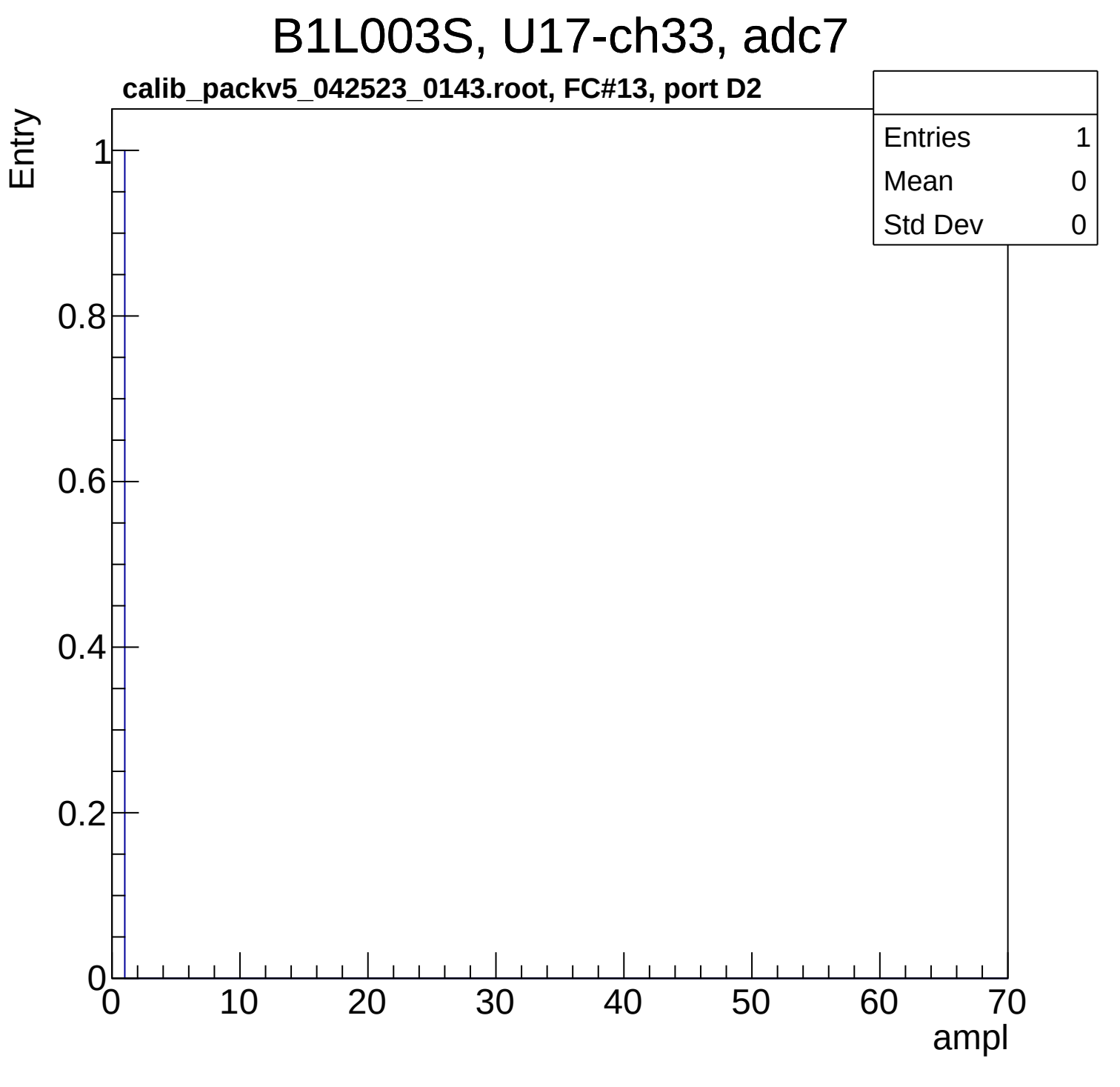
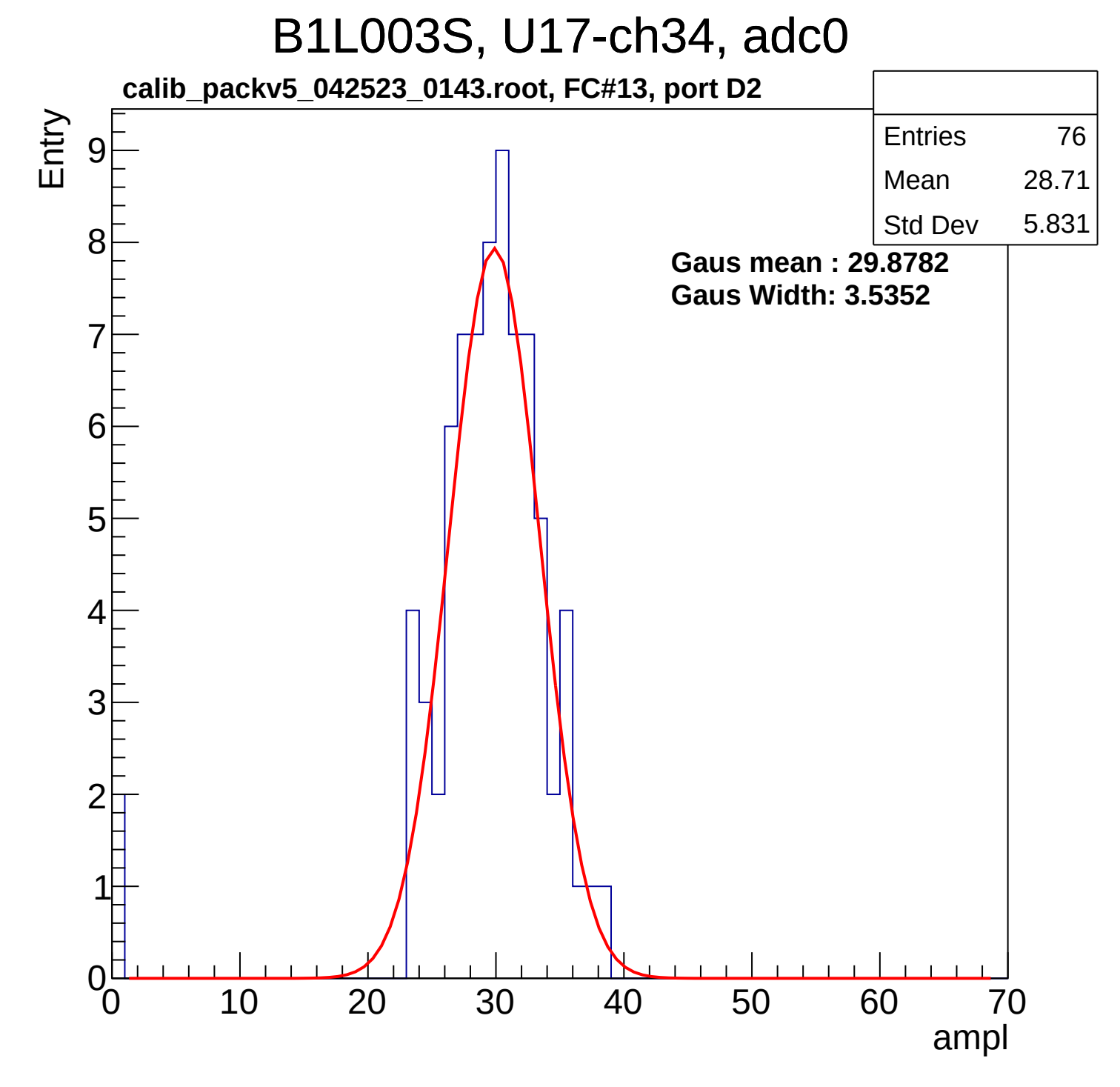
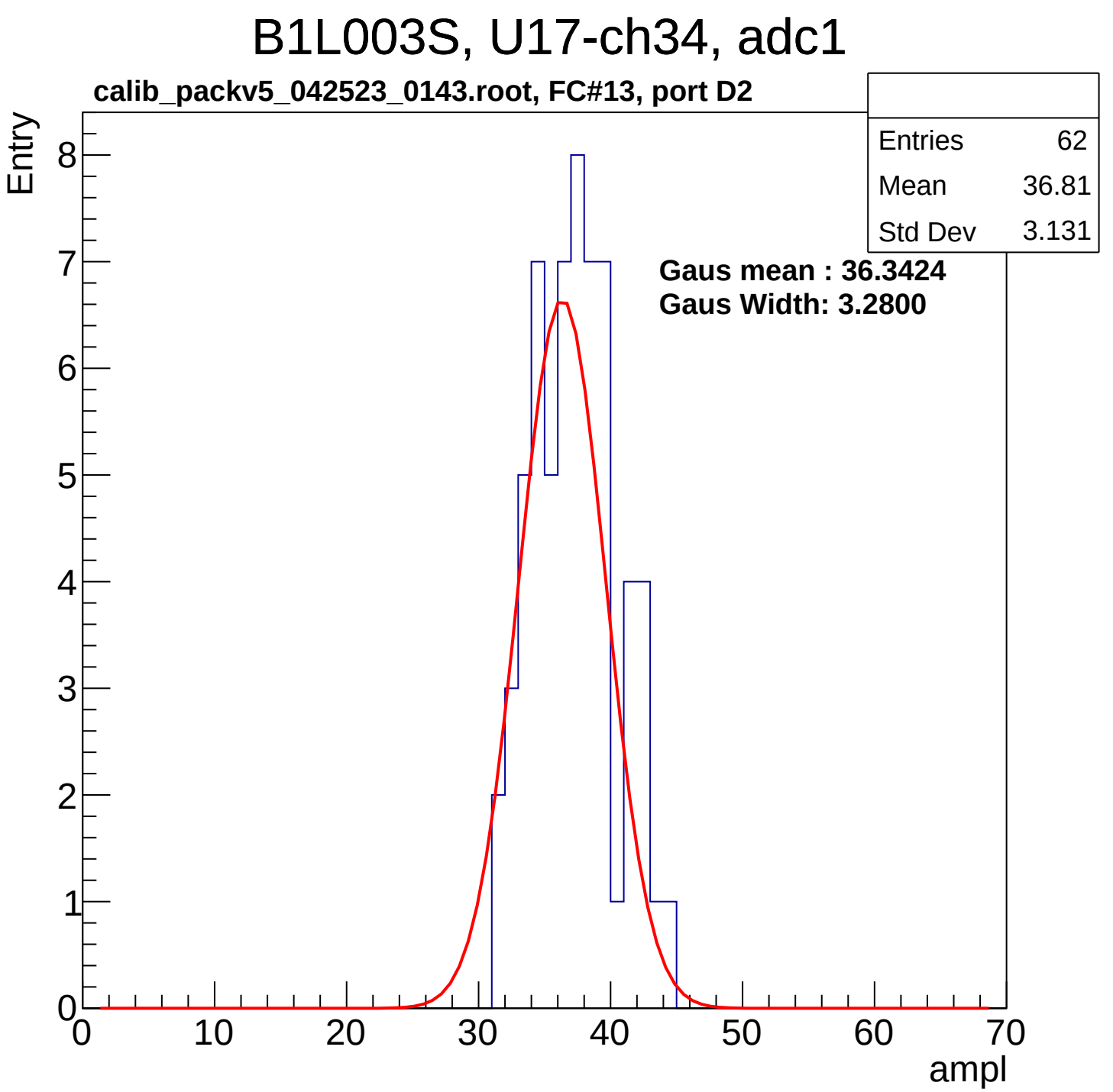
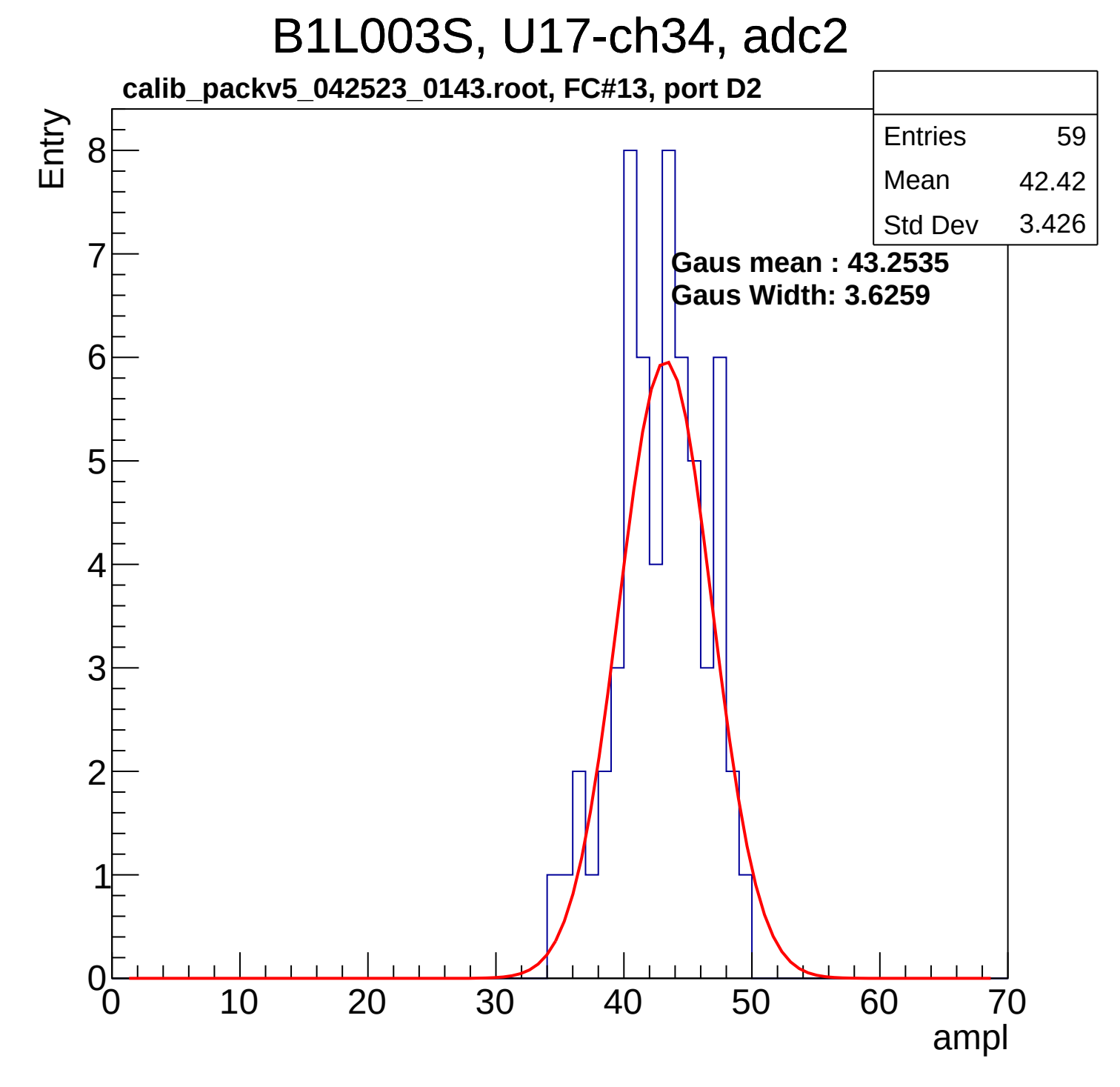
40
50
60
70


B1L003S, U17-ch33, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U17-ch33, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch34, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 28.71
Std Dev 5.831
Gaus mean : 29.8782
Gaus Width: 3.5352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch34, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.81
Std Dev 3.131
Gaus mean : 36.3424
Gaus Width: 3.2800
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch34, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.42
Std Dev 3.426
Gaus mean : 43.2535
Gaus Width: 3.6259
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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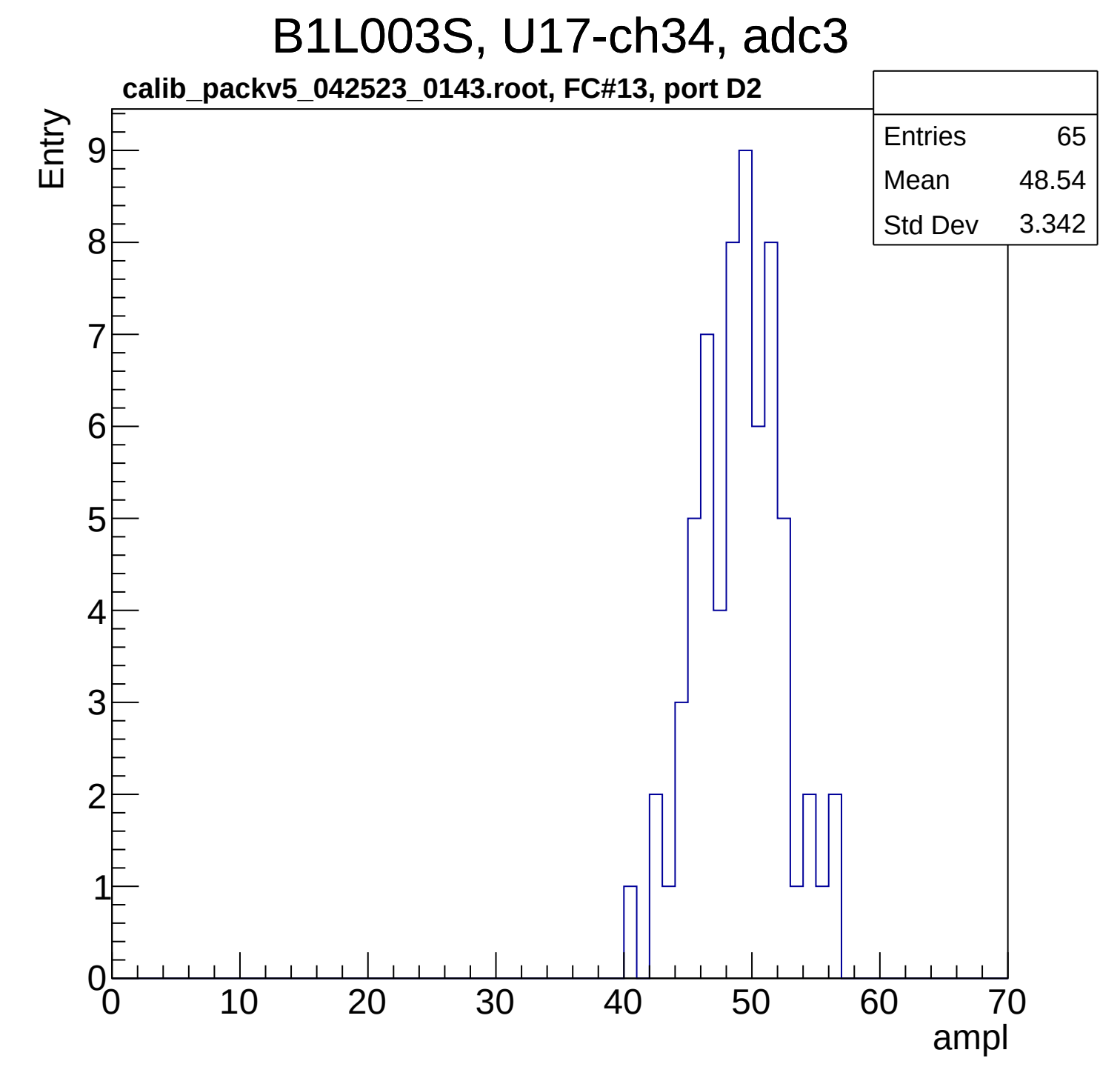
40
50
60
70


B1L003S, U17-ch34, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 48.54
Std Dev 3.342
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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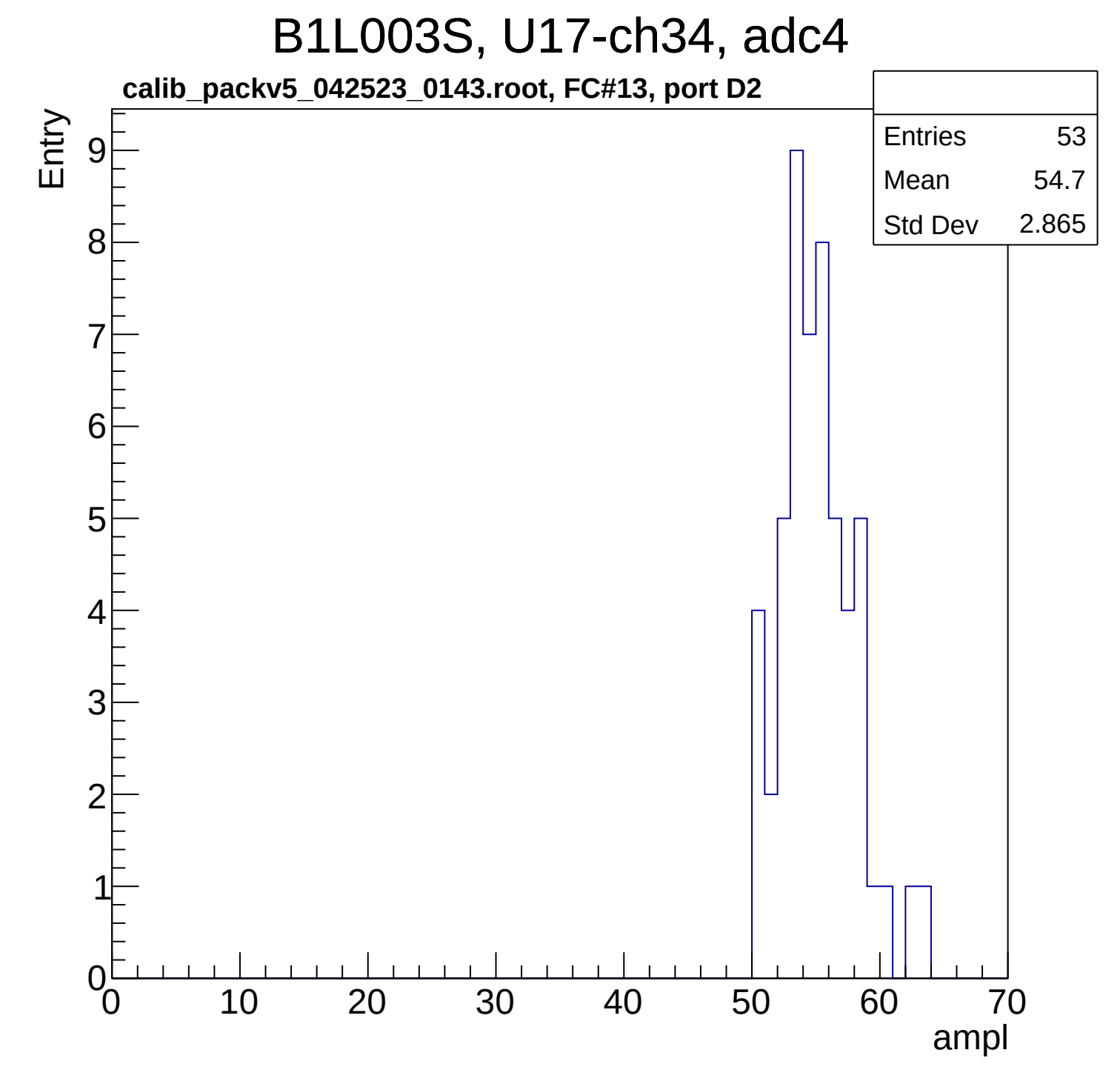
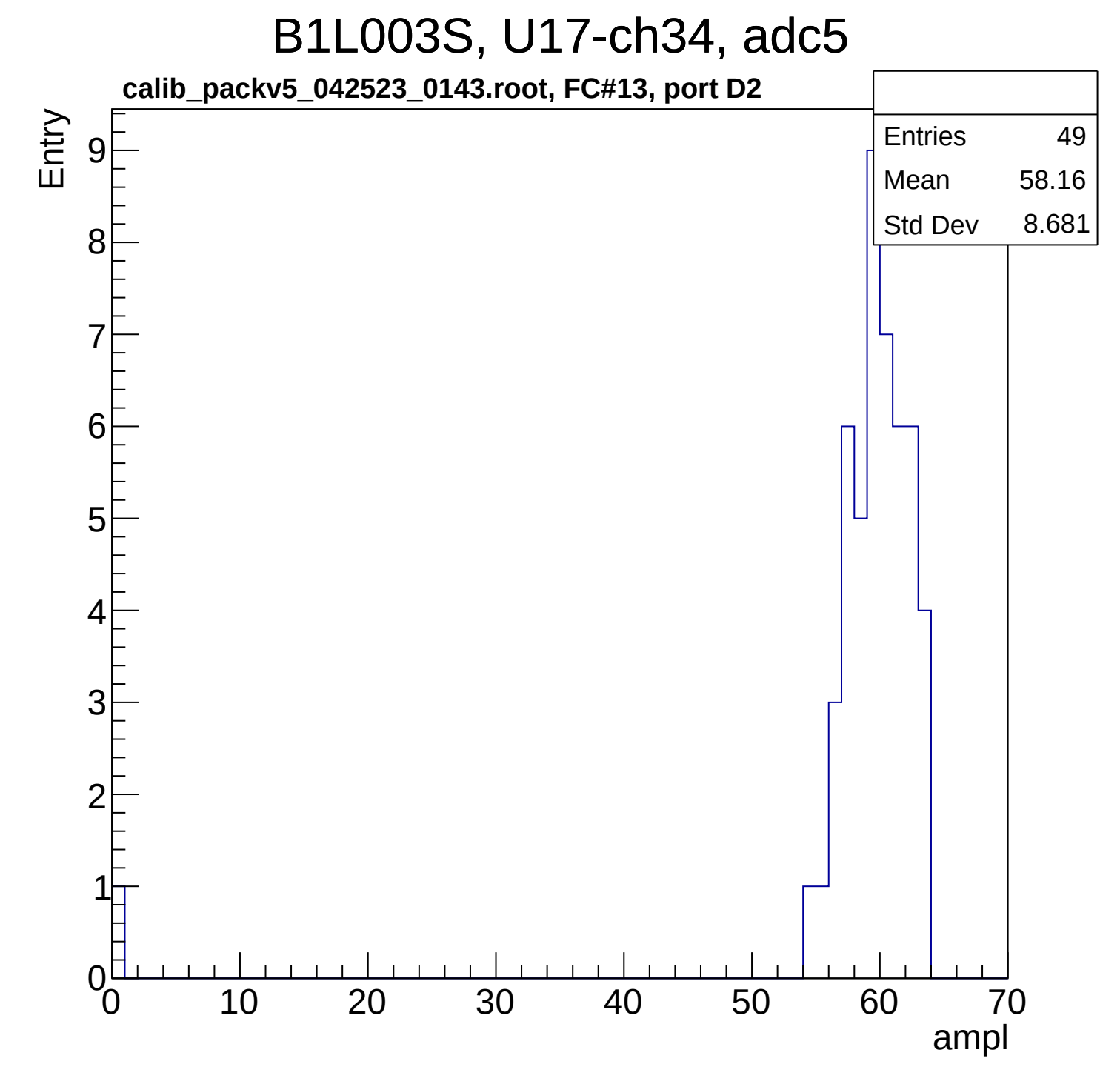

B1L003S, U17-ch34, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.7
Std Dev 2.865
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch34, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.16
Std Dev 8.681
Entry
ampl
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2
3
4
5
6
7
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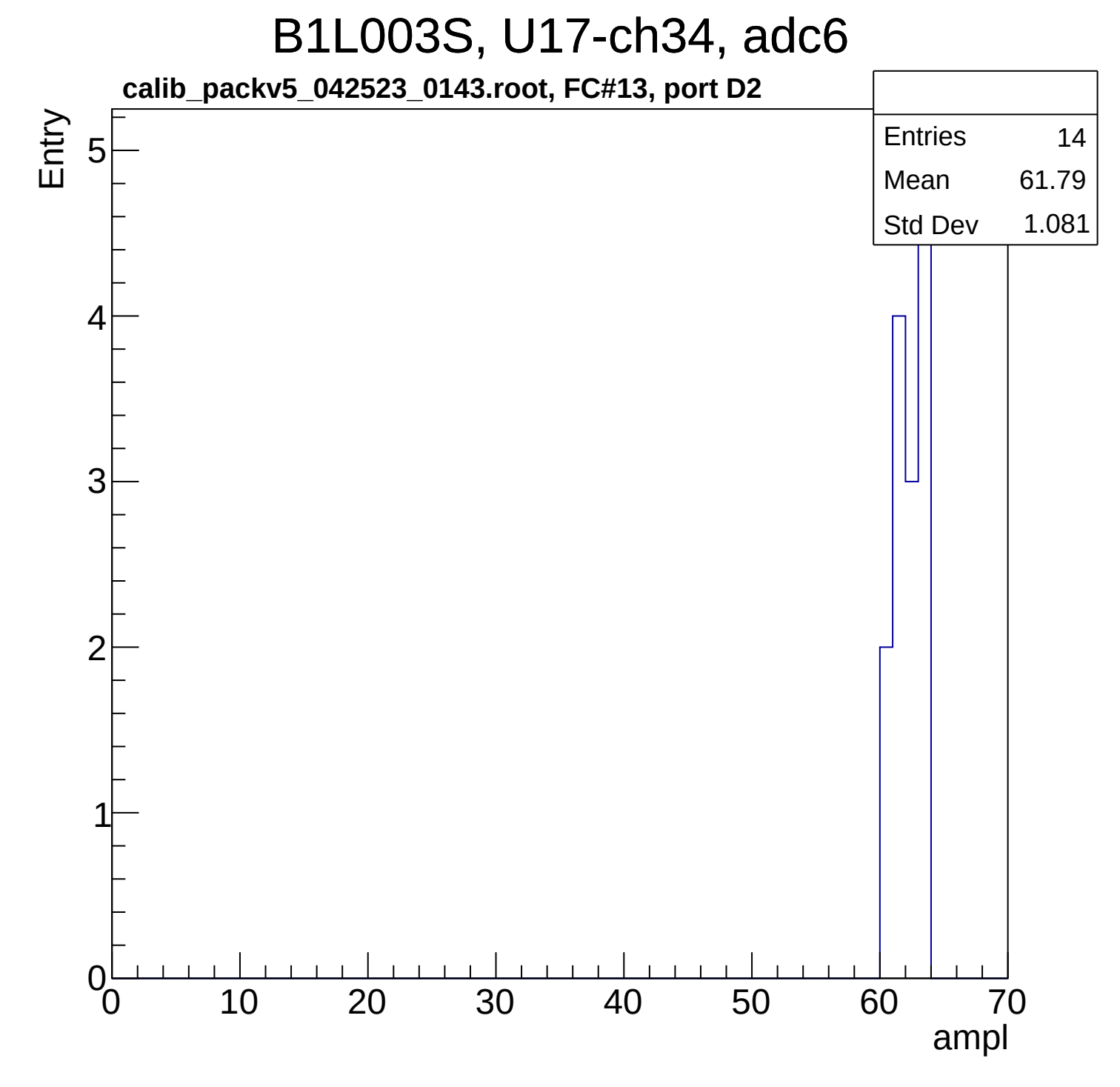
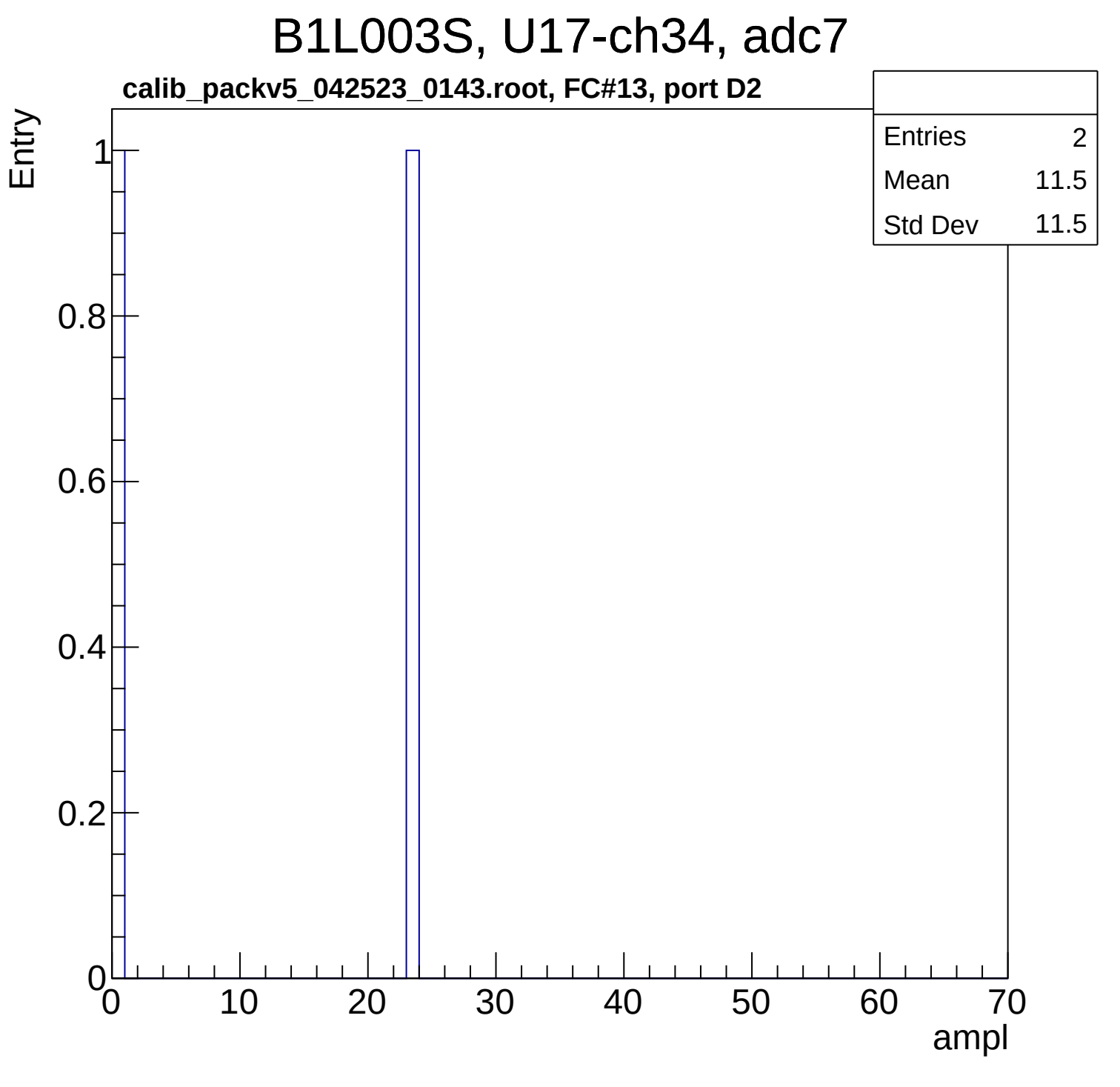
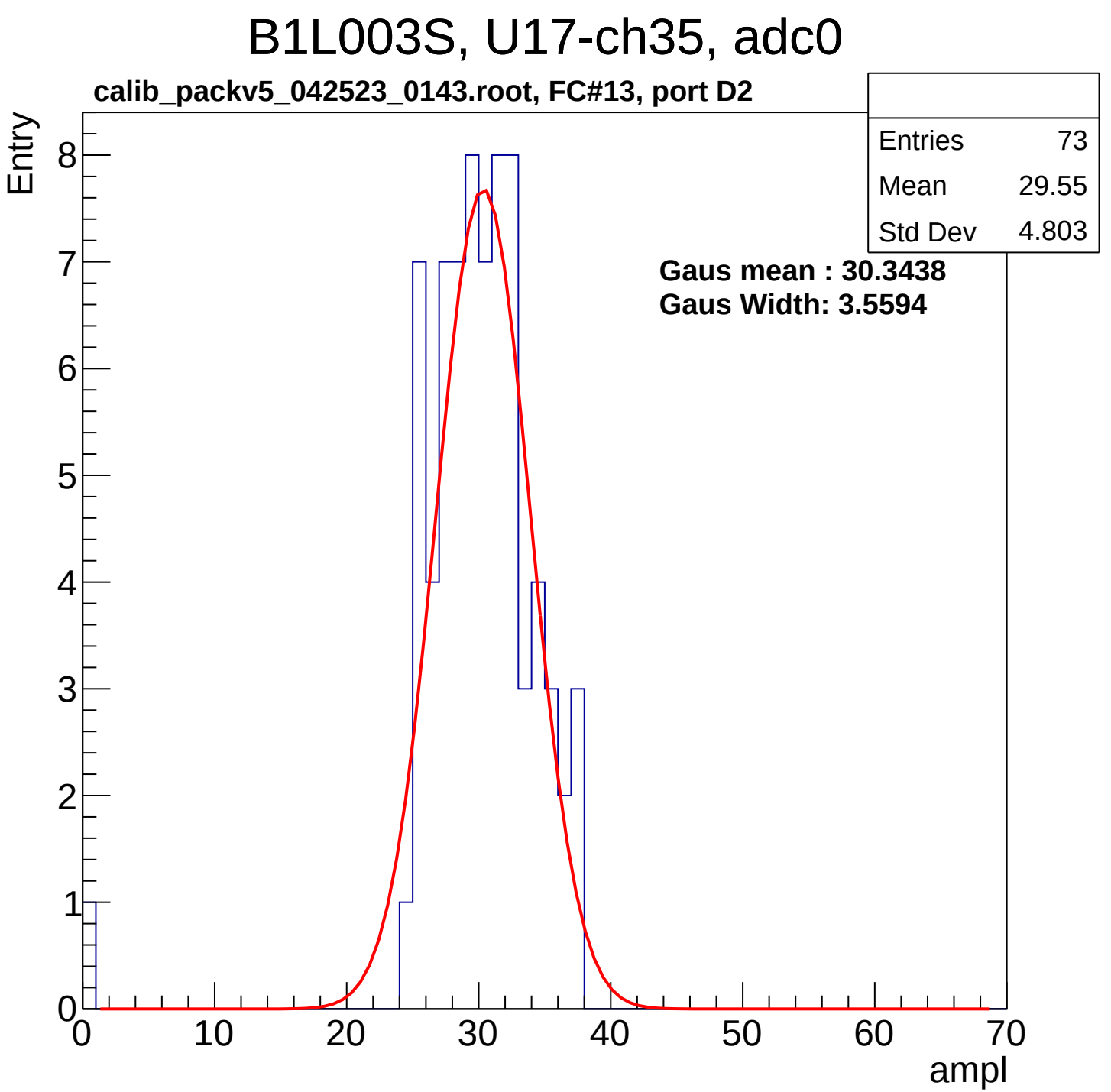
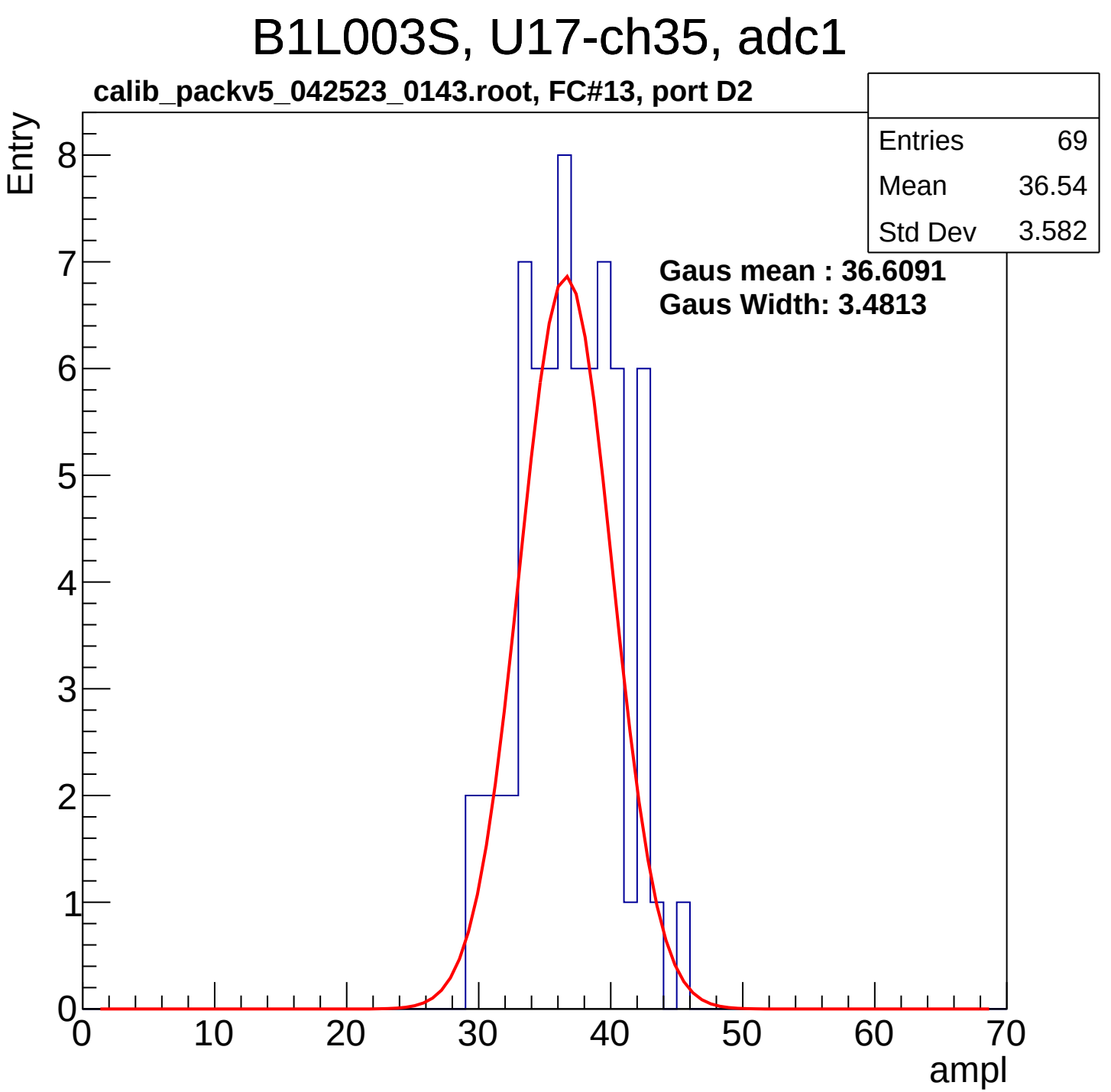

B1L003S, U17-ch34, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.79
Std Dev 1.081
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch34, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch35, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 29.55
Std Dev 4.803
Gaus mean : 30.3438
Gaus Width: 3.5594
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch35, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36.54
Std Dev 3.582
Gaus mean : 36.6091
Gaus Width: 3.4813
Entry
ampl
0
1
2
3
4
5
6
7
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0
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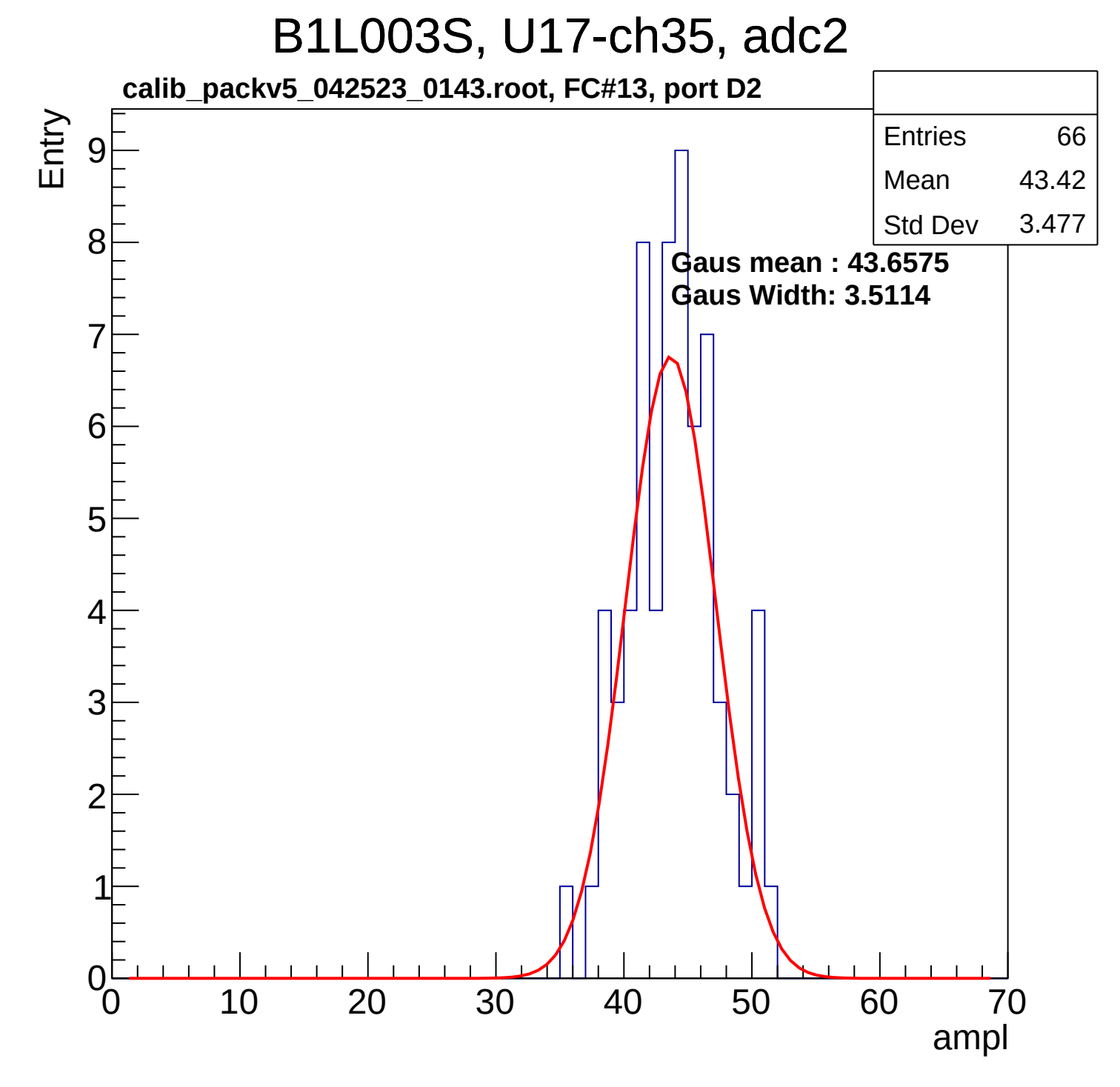
B1L003S, U17-ch35, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.42
Std Dev 3.477
Gaus mean : 43.6575
Gaus Width: 3.5114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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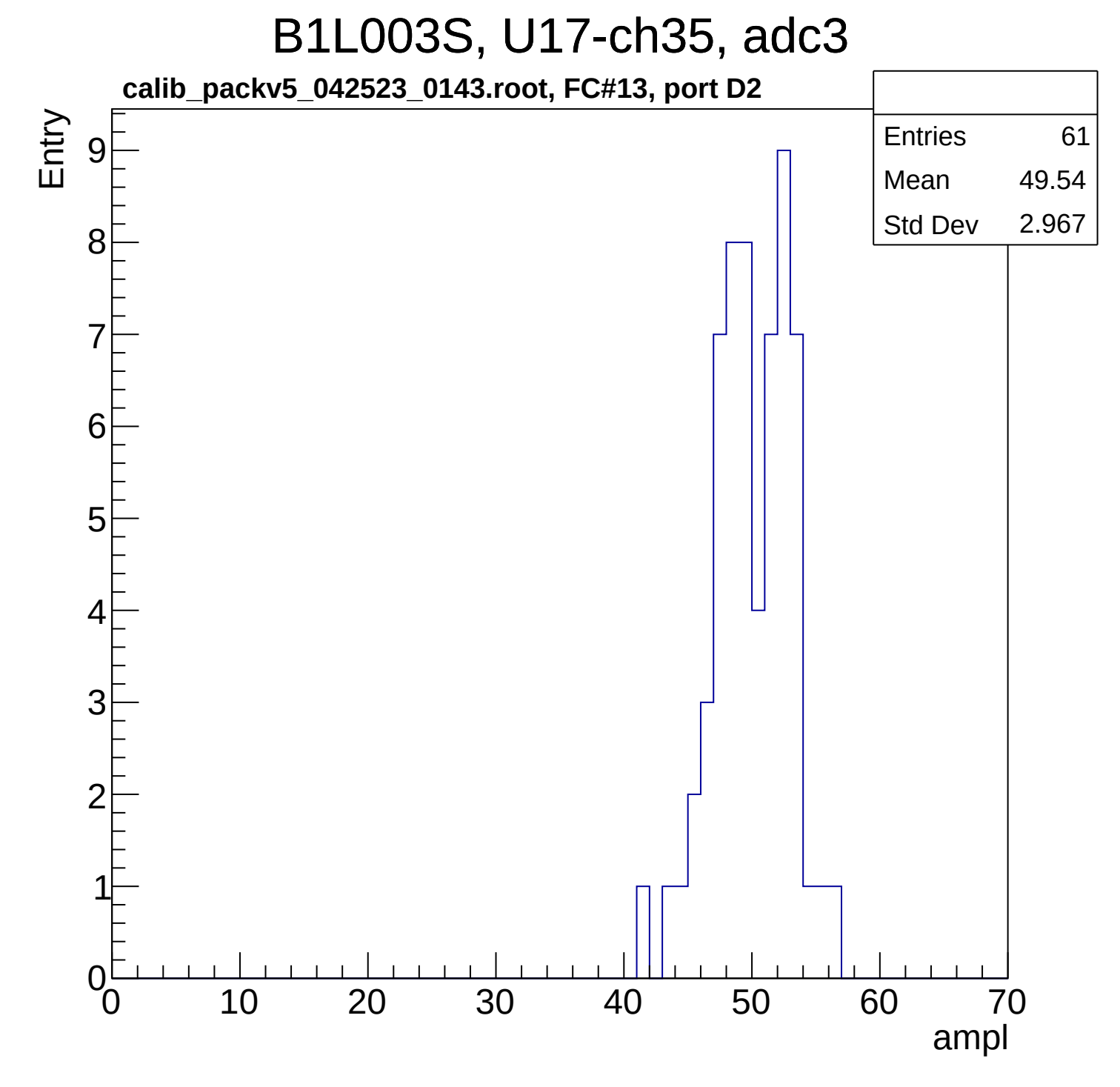
B1L003S, U17-ch35, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.54
Std Dev 2.967
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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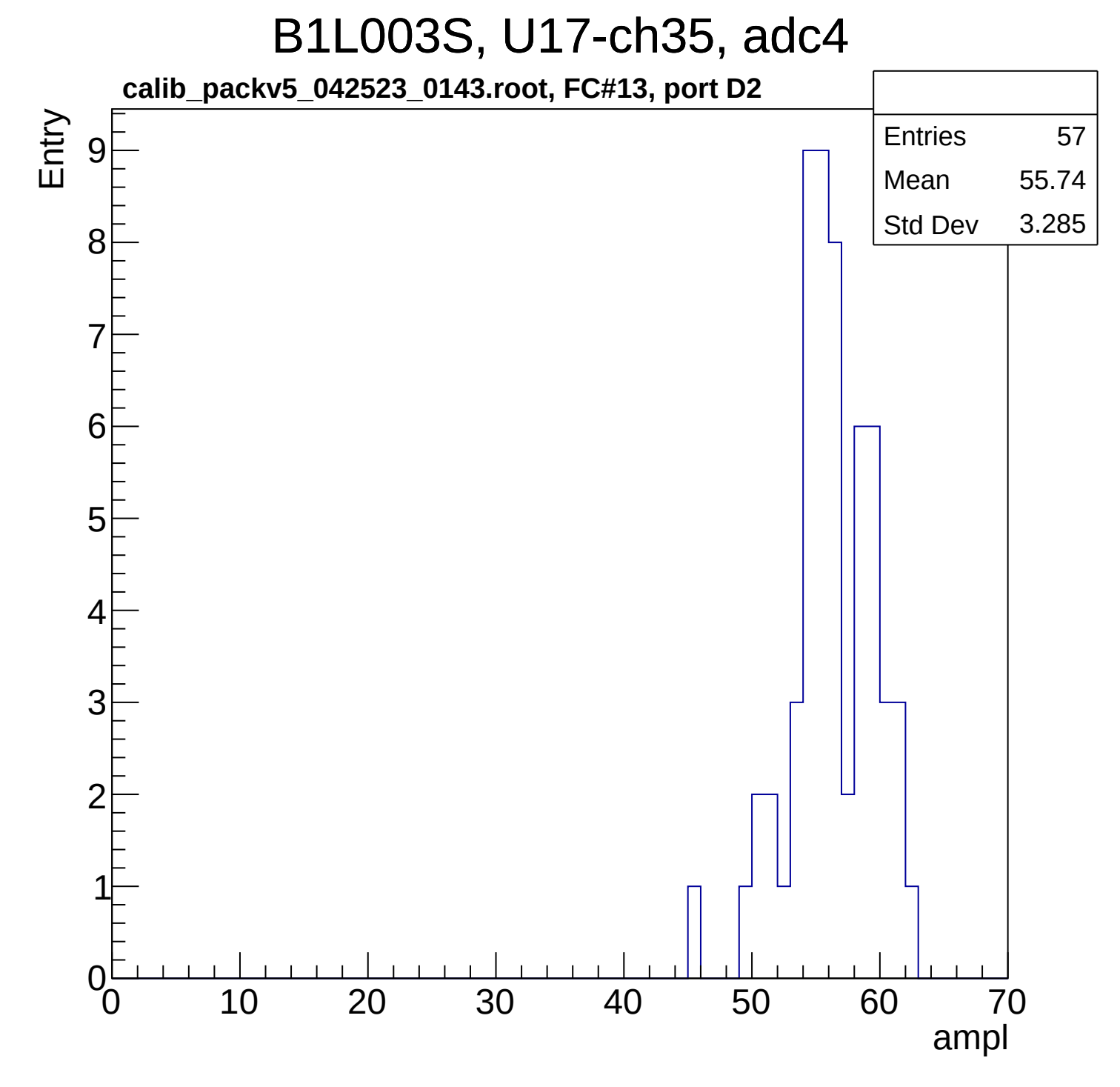
B1L003S, U17-ch35, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.74
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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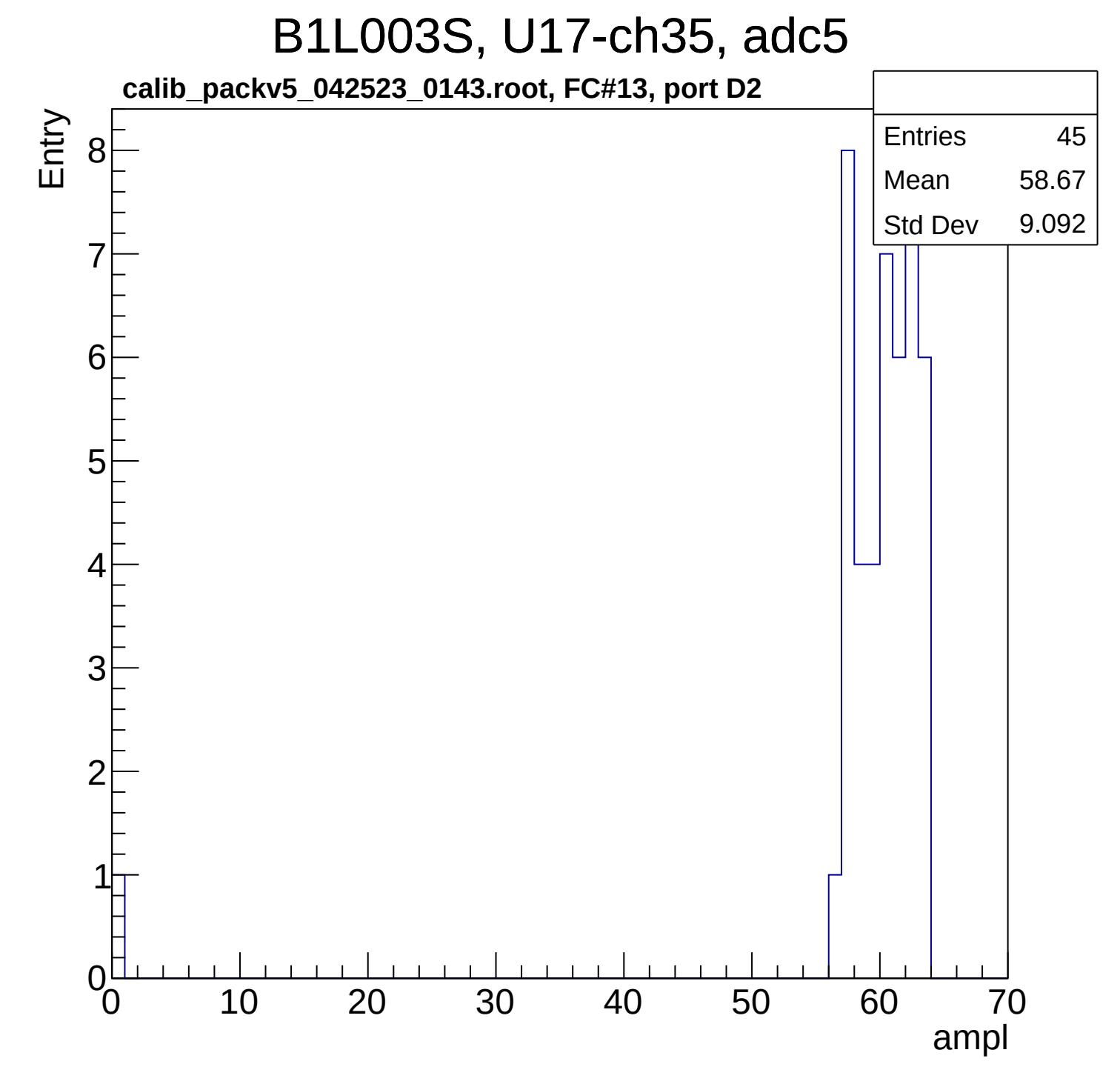
B1L003S, U17-ch35, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.67
Std Dev 9.092
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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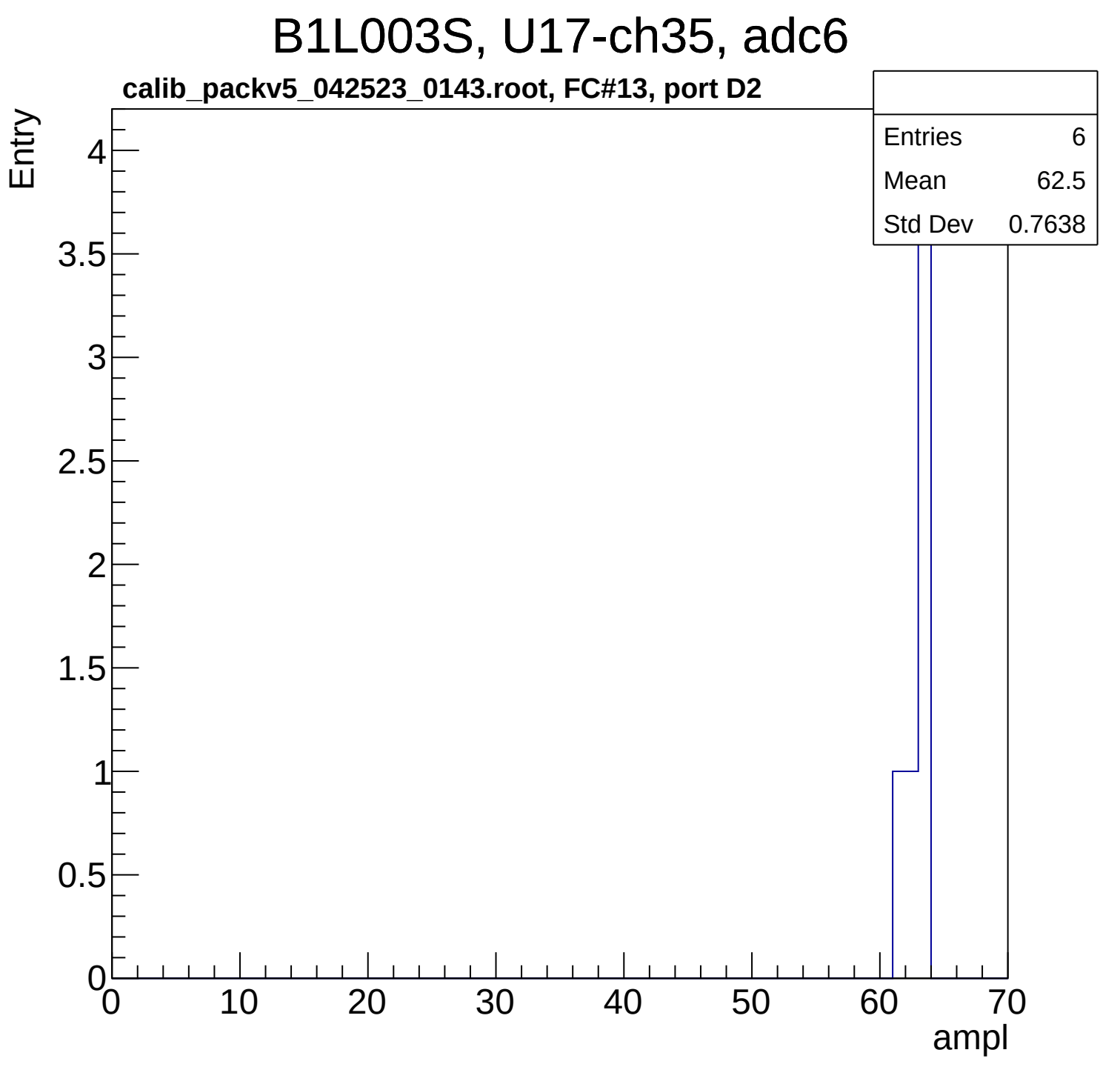
B1L003S, U17-ch35, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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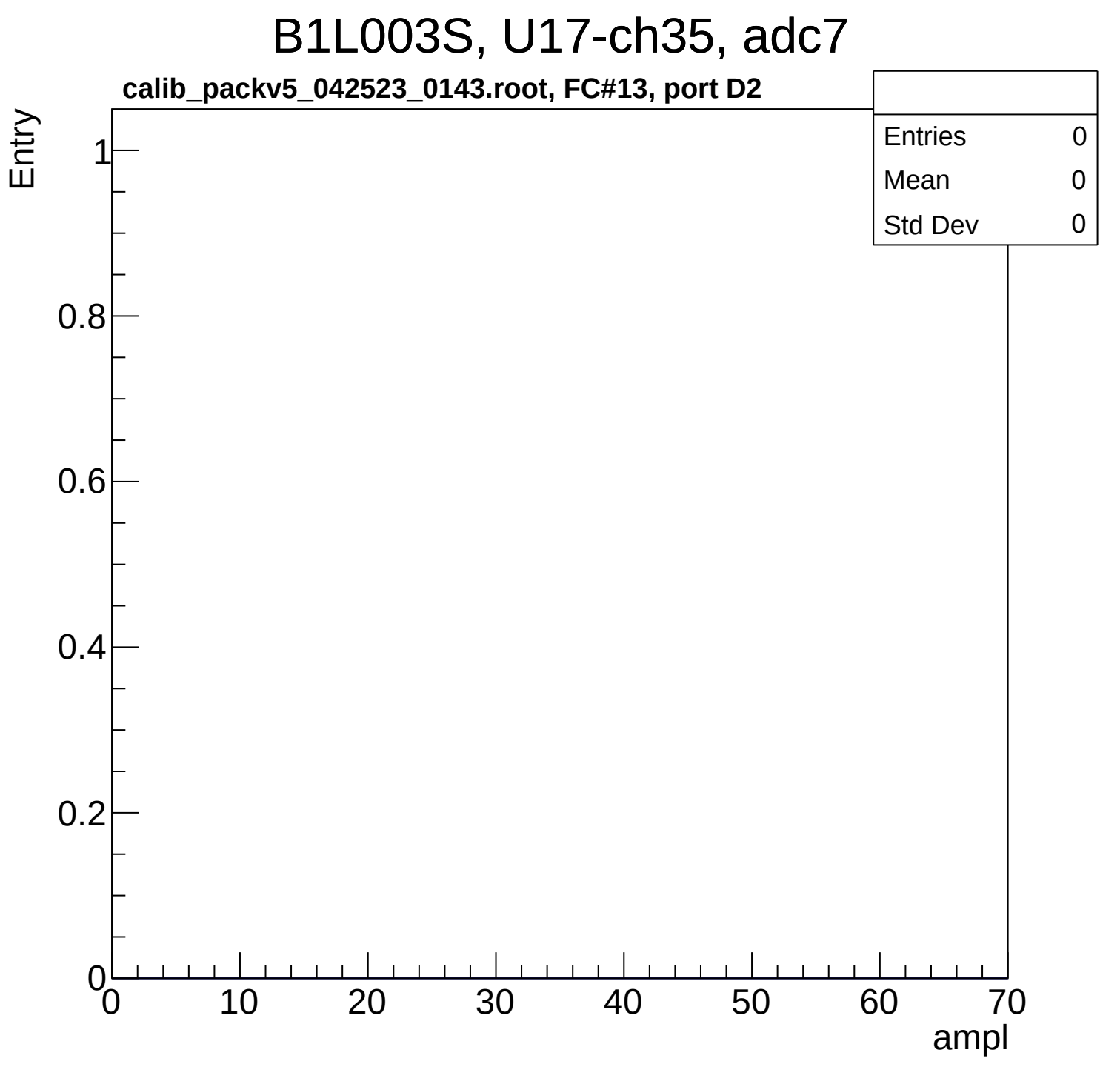
B1L003S, U17-ch35, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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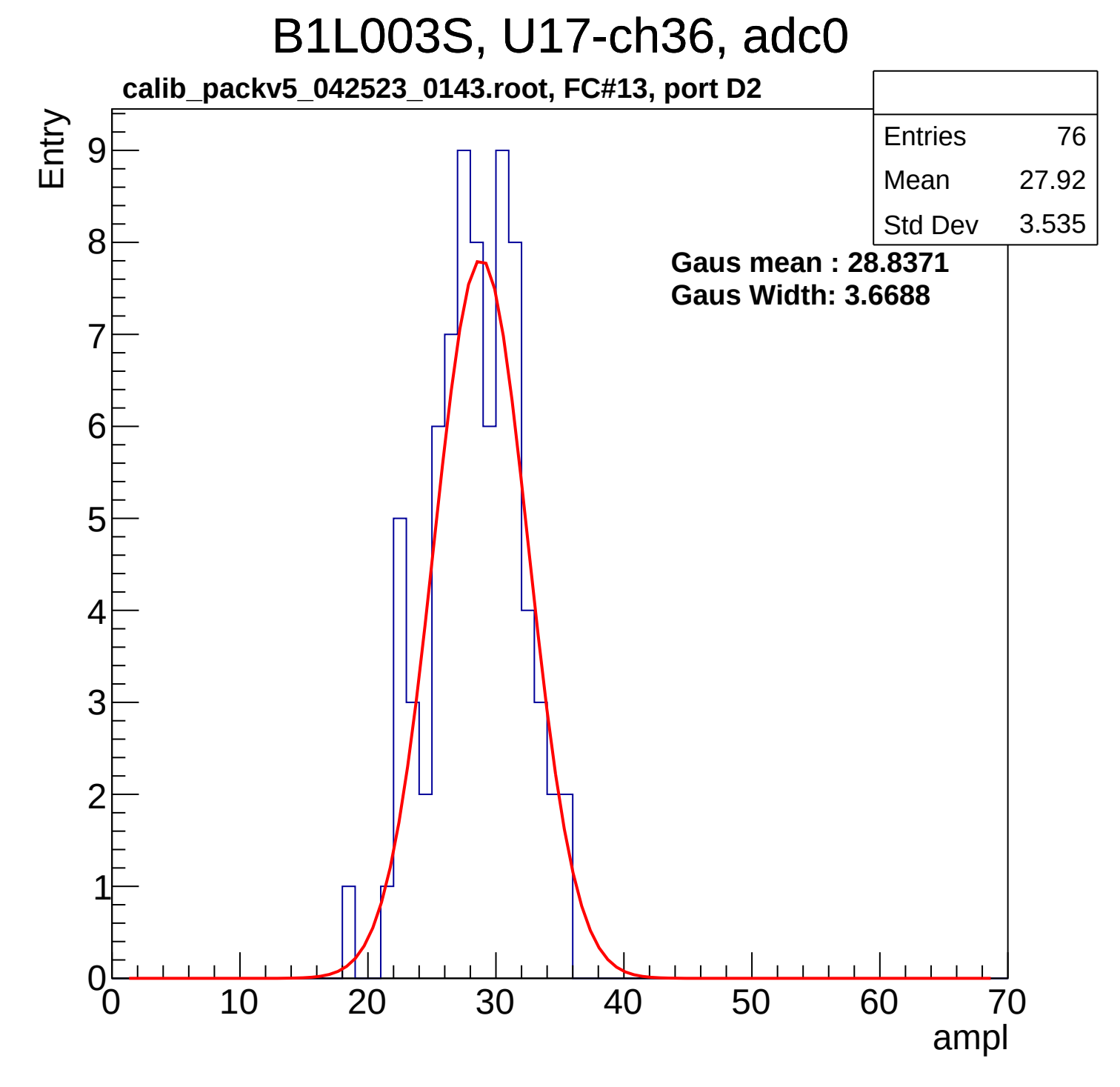
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch36, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 27.92
Std Dev 3.535
Gaus mean : 28.8371
Gaus Width: 3.6688
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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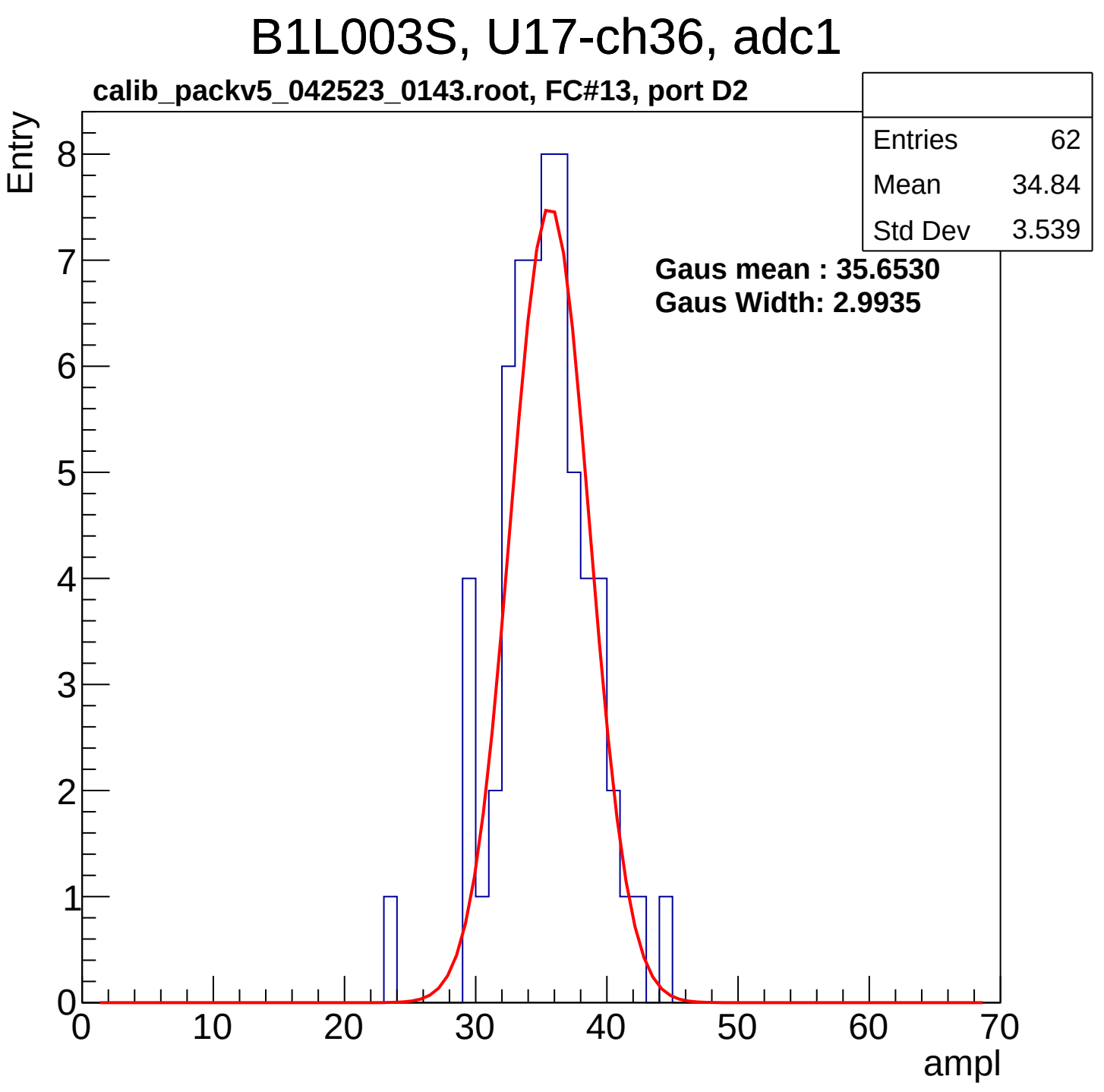
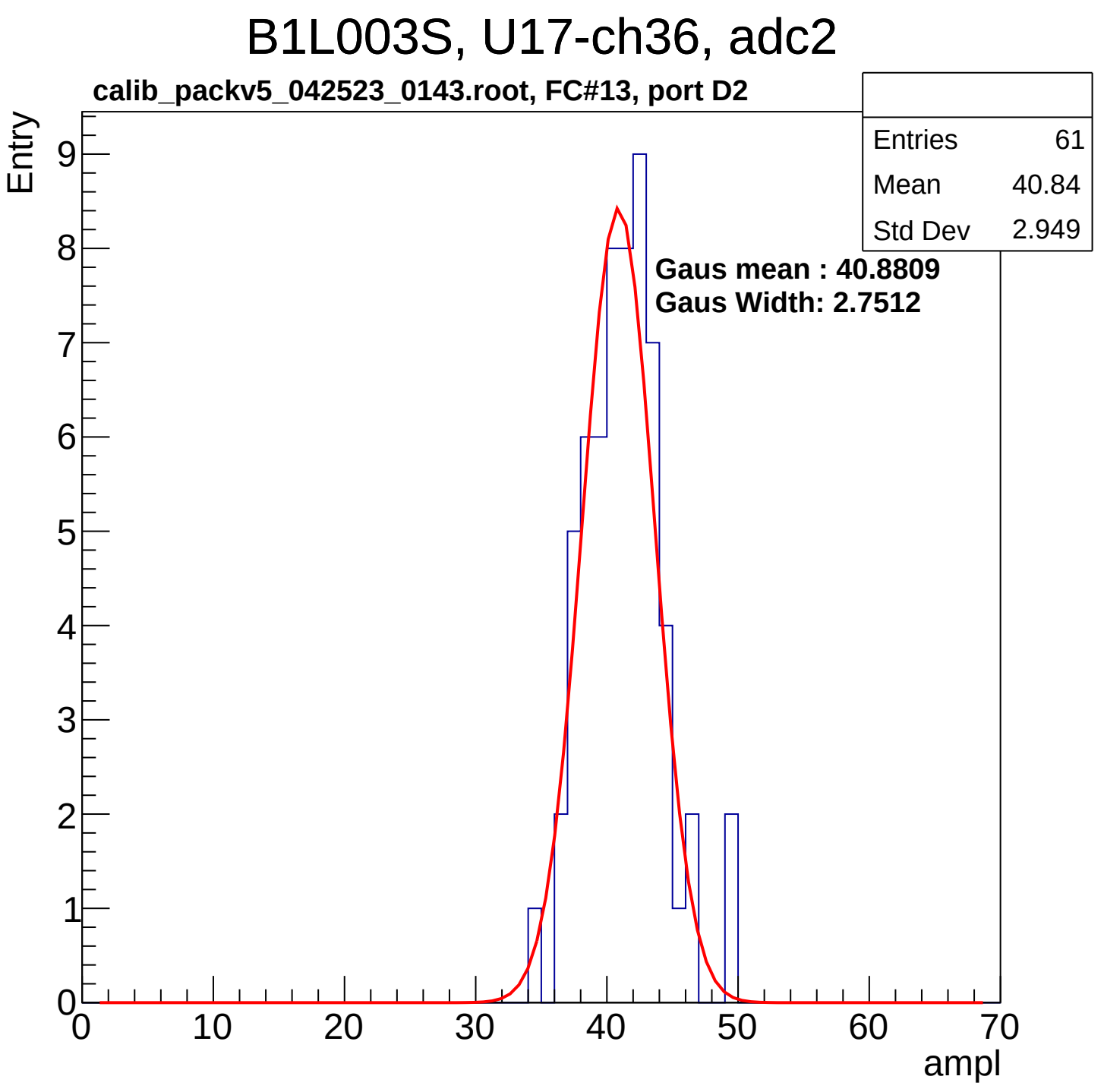

B1L003S, U17-ch36, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 34.84
Std Dev 3.539
Gaus mean : 35.6530
Gaus Width: 2.9935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch36, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 40.84
Std Dev 2.949
Gaus mean : 40.8809
Gaus Width: 2.7512
Entry
ampl
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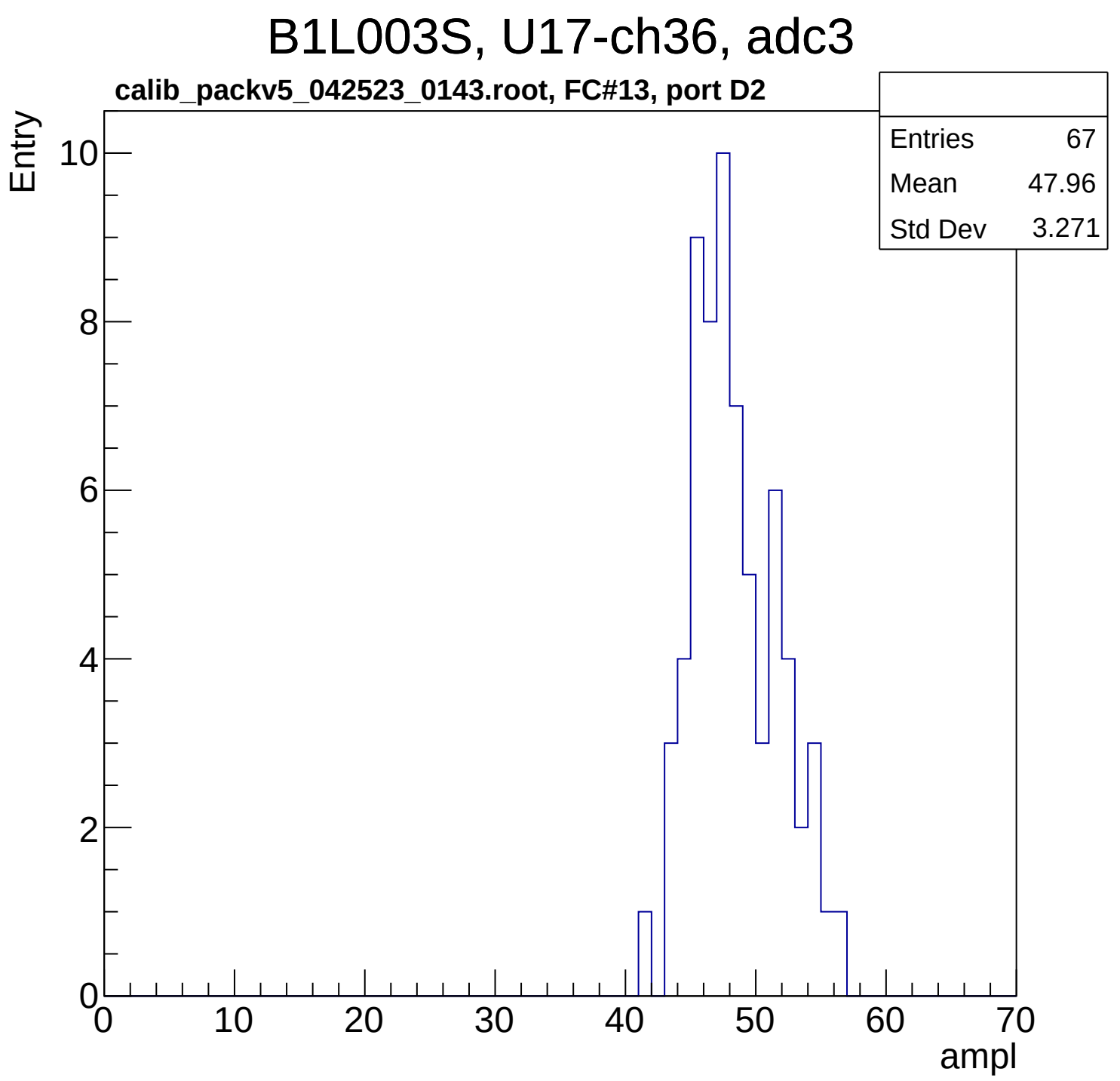
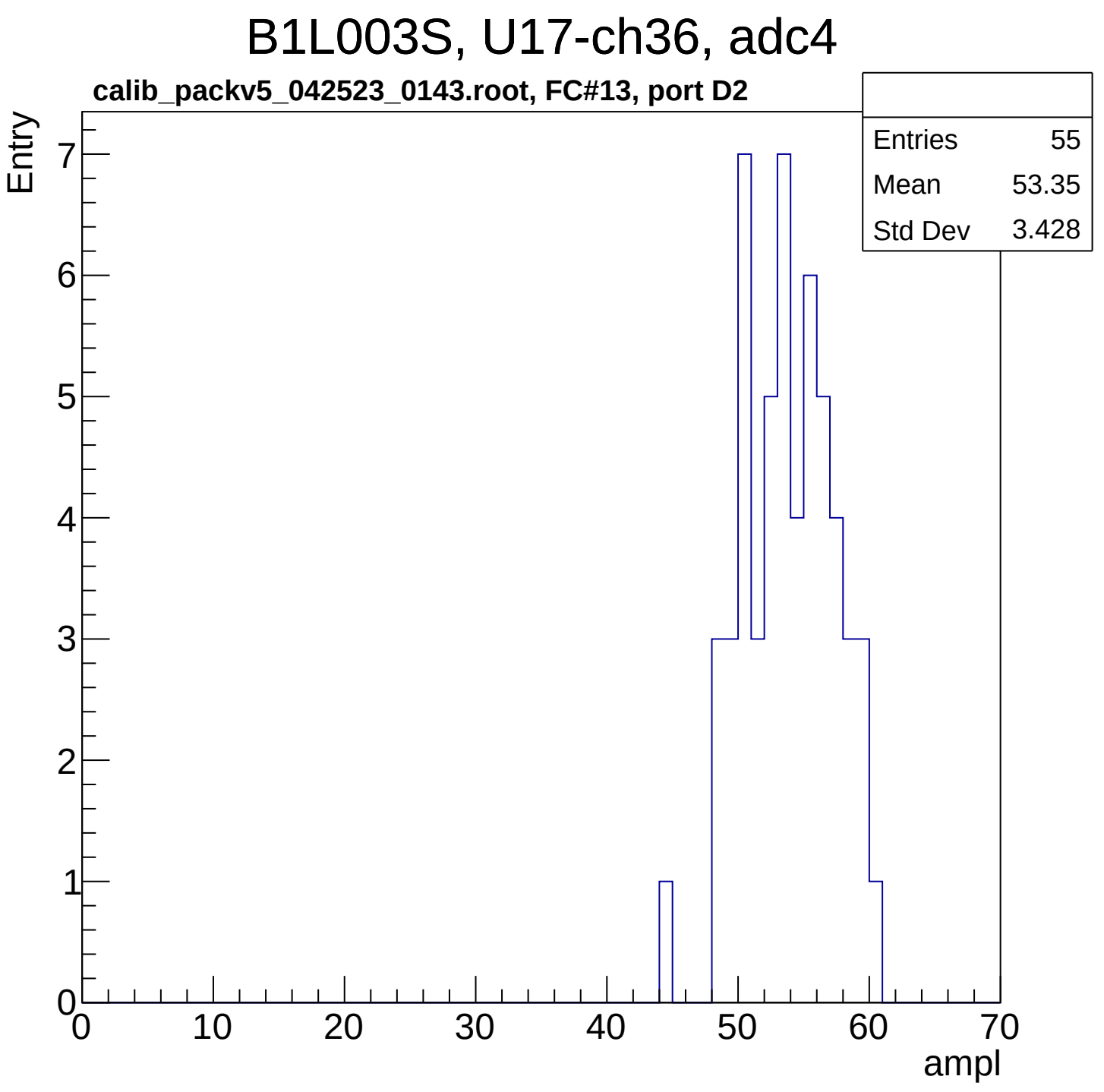
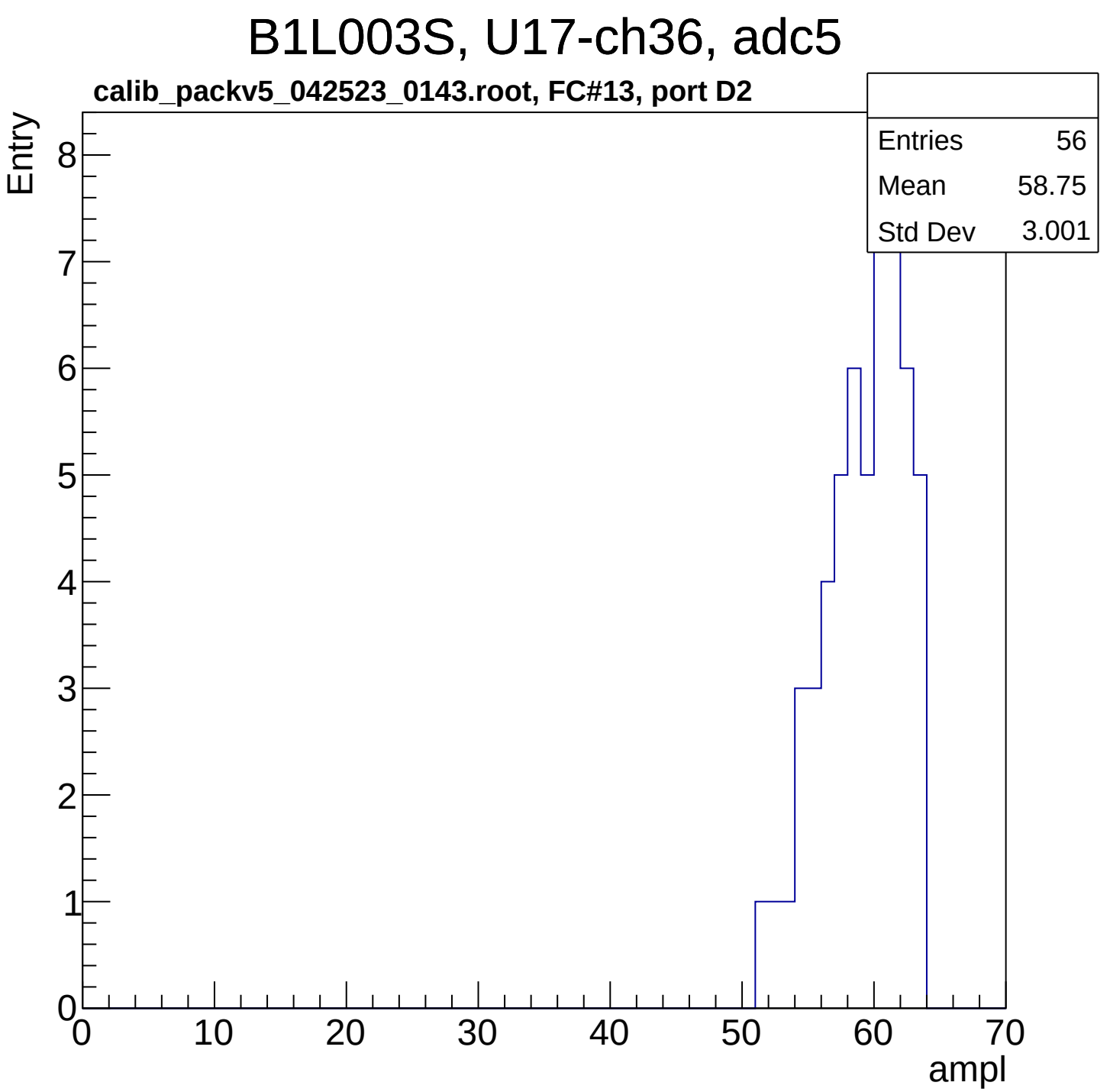
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch36, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 47.96
Std Dev 3.271
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch36, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.35
Std Dev 3.428
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch36, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.75
Std Dev 3.001
Entry
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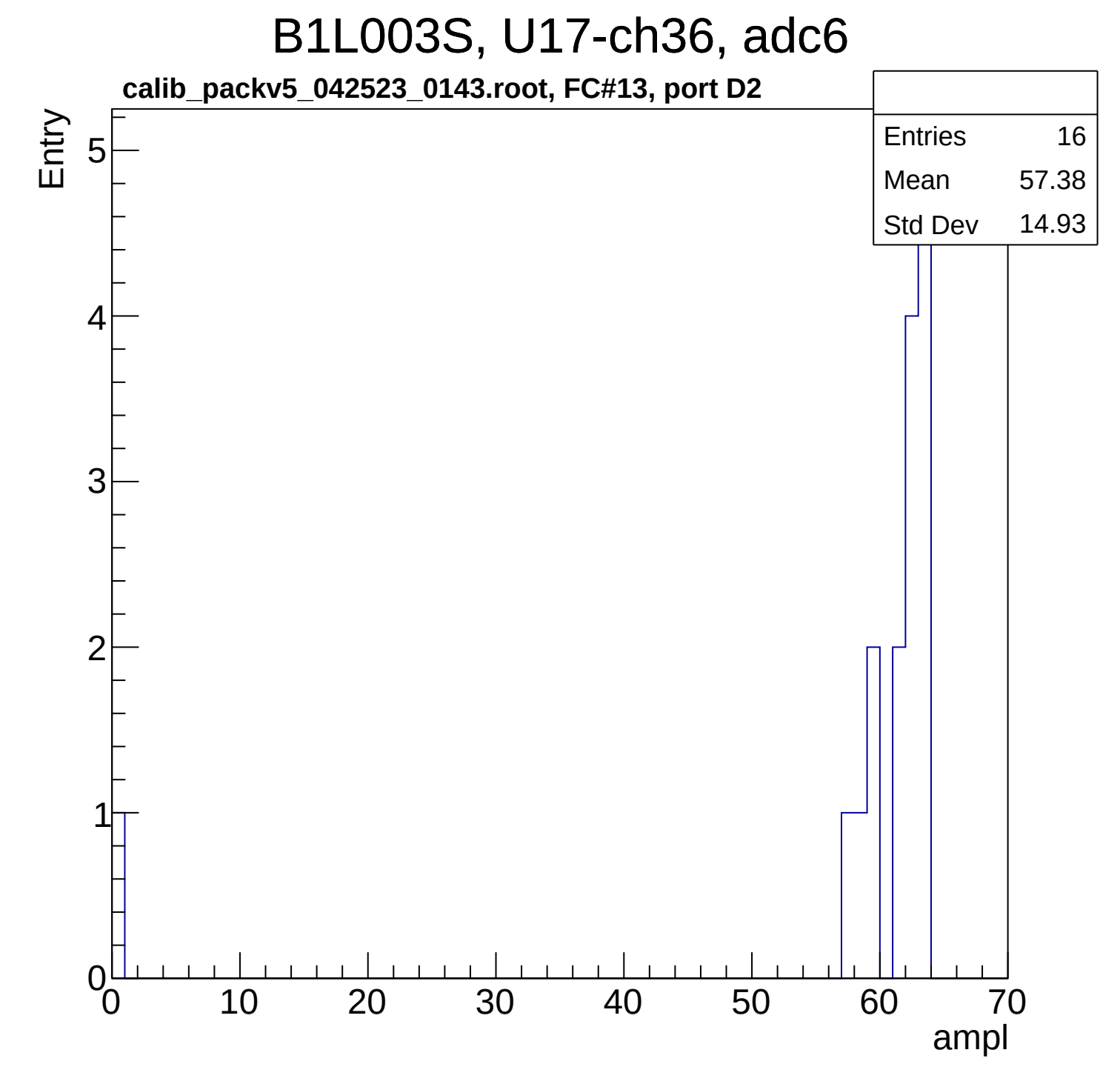
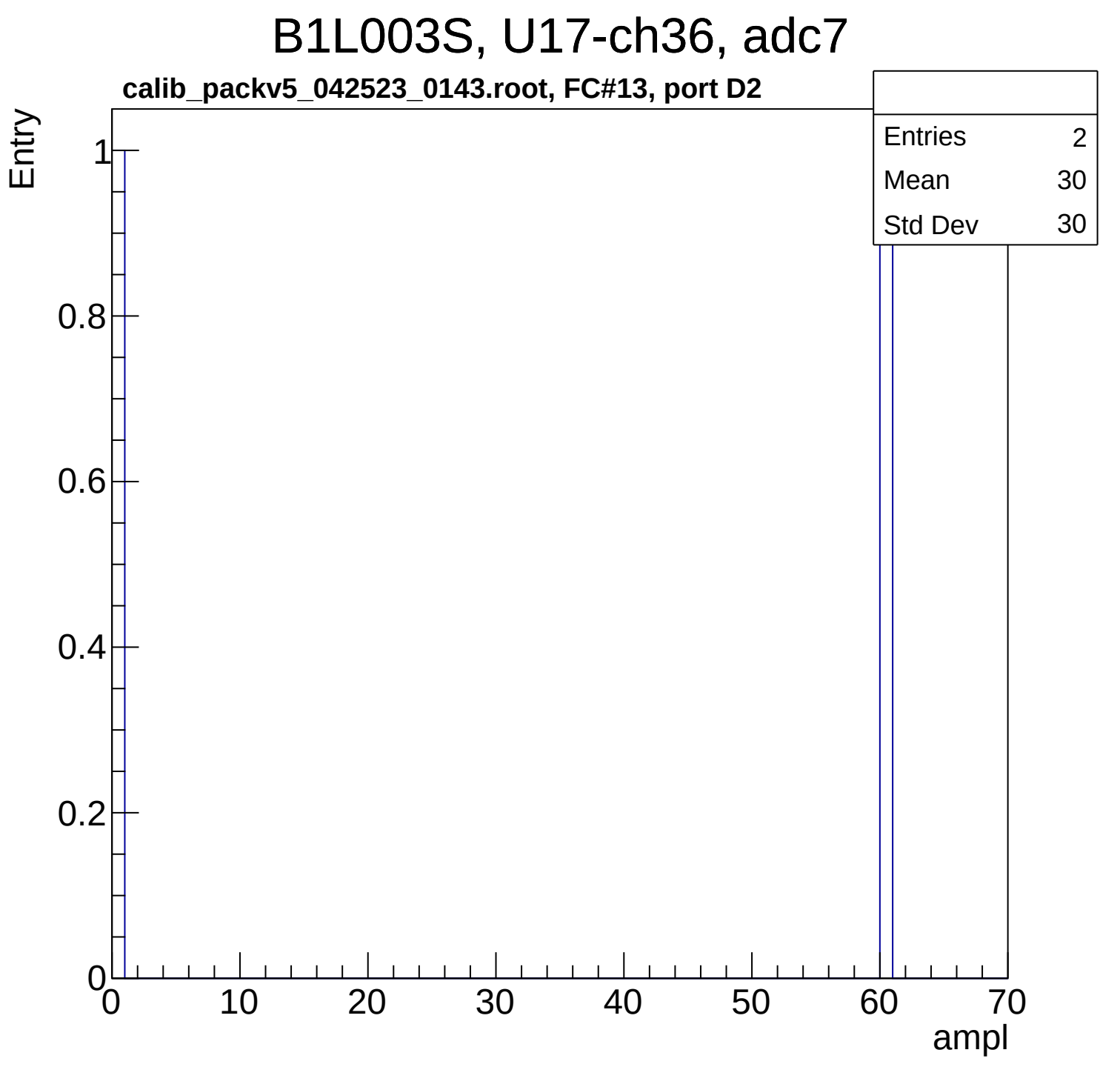
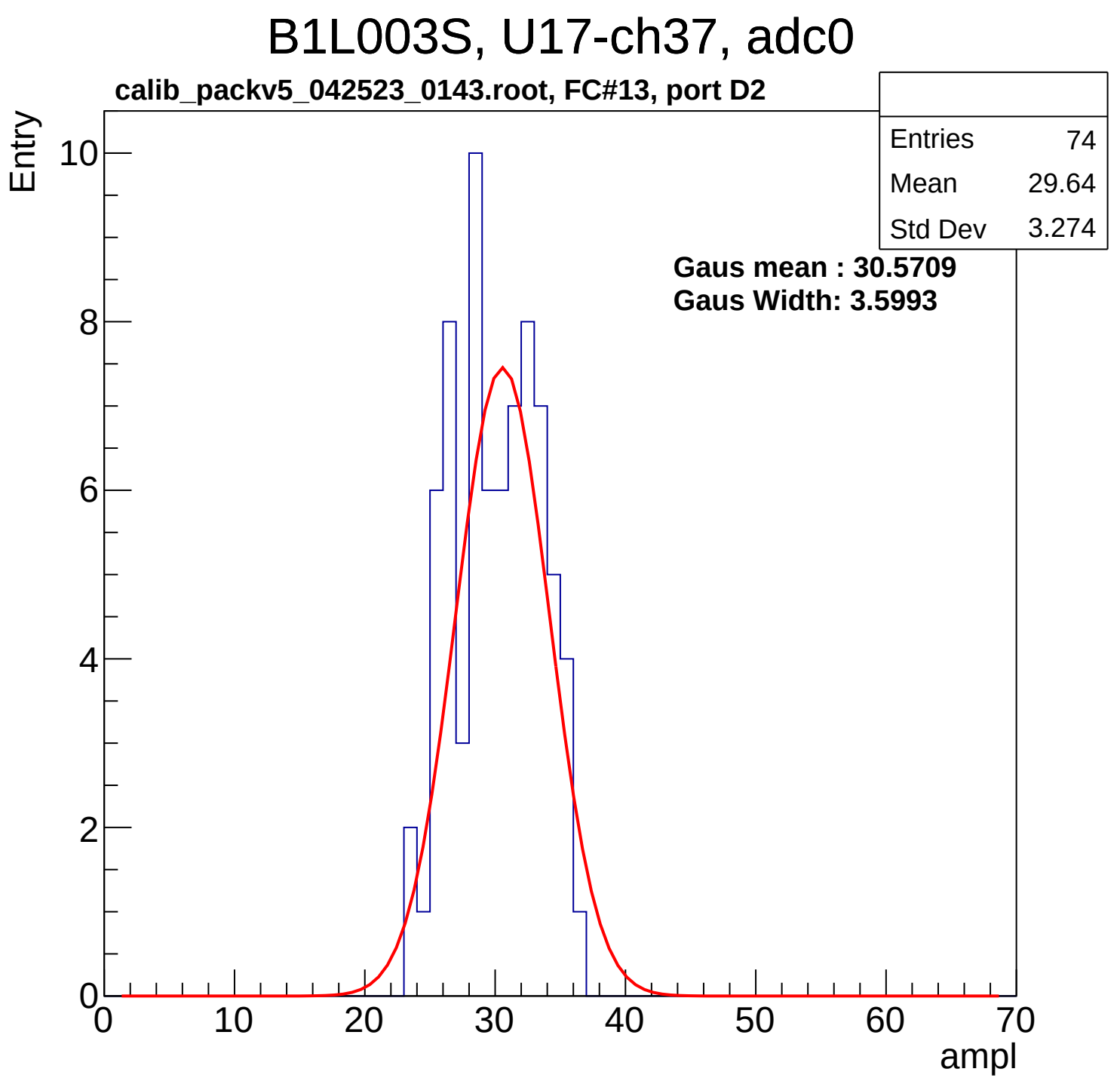
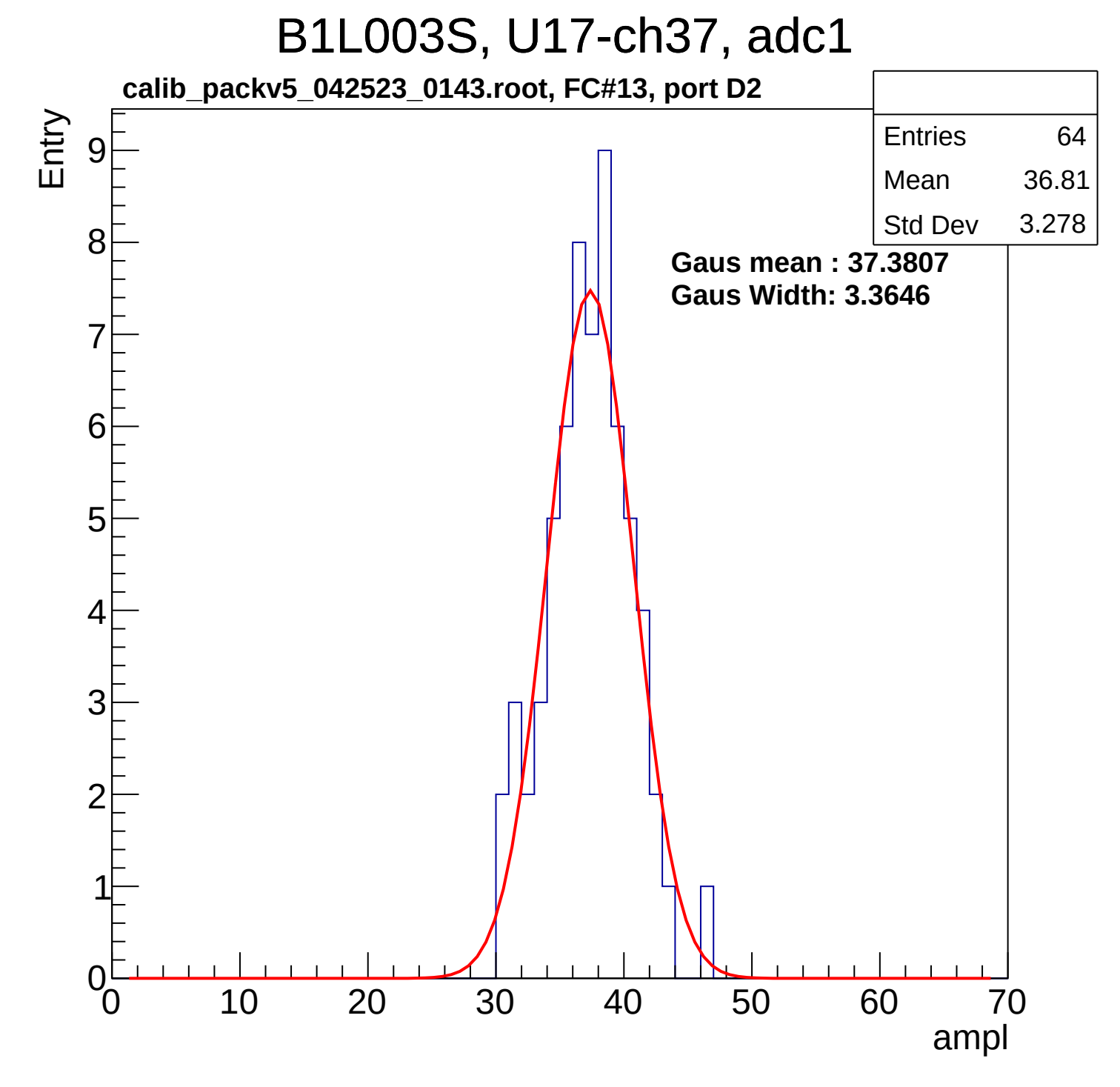
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch36, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 57.38
Std Dev 14.93
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch36, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 30
Std Dev 30
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch37, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.64
Std Dev 3.274
Gaus mean : 30.5709
Gaus Width: 3.5993
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch37, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.81
Std Dev 3.278
Gaus mean : 37.3807
Gaus Width: 3.3646
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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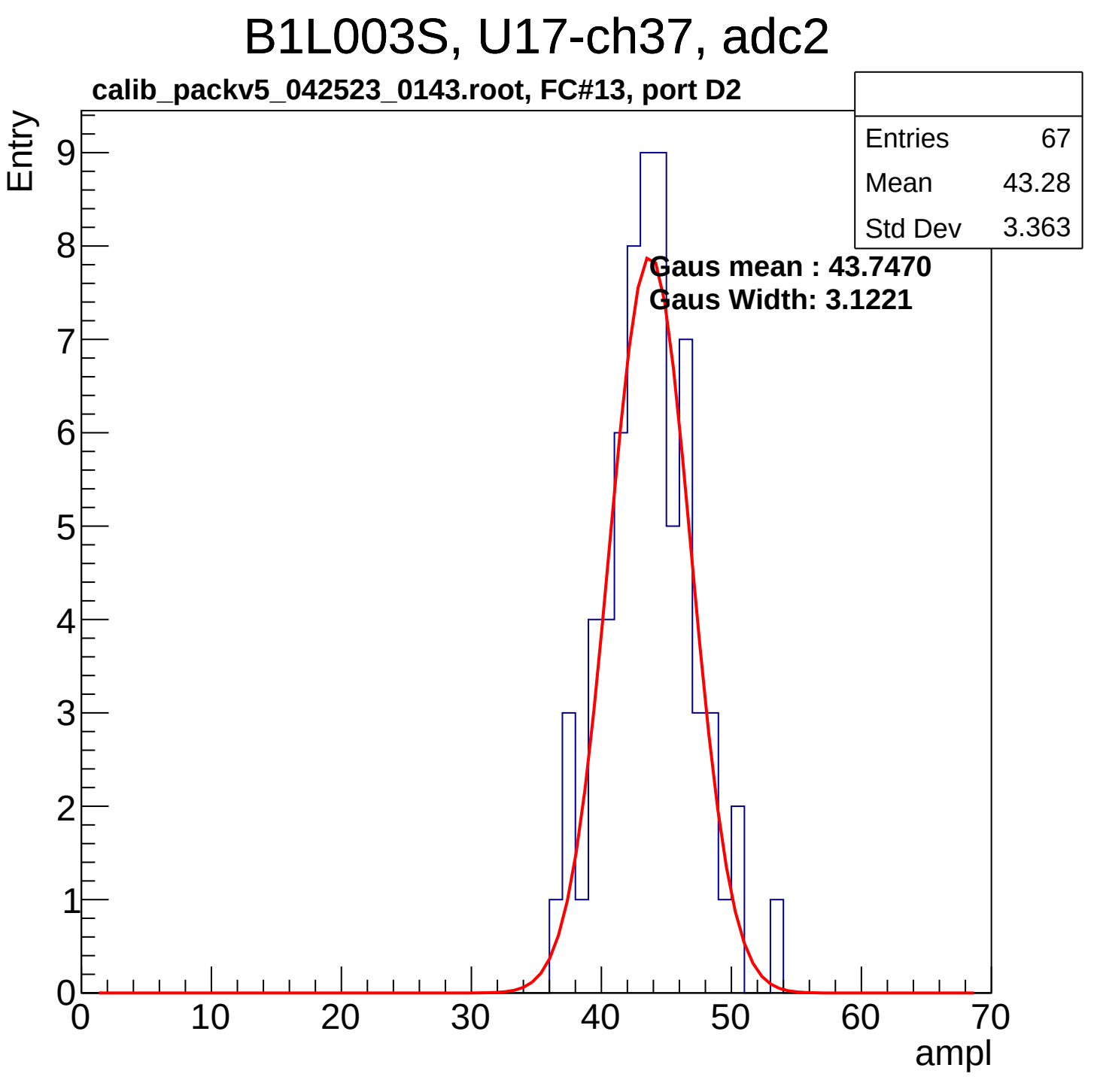
40
50
60
70


B1L003S, U17-ch37, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.28
Std Dev 3.363
Gaus mean : 43.7470
Gaus Width: 3.1221
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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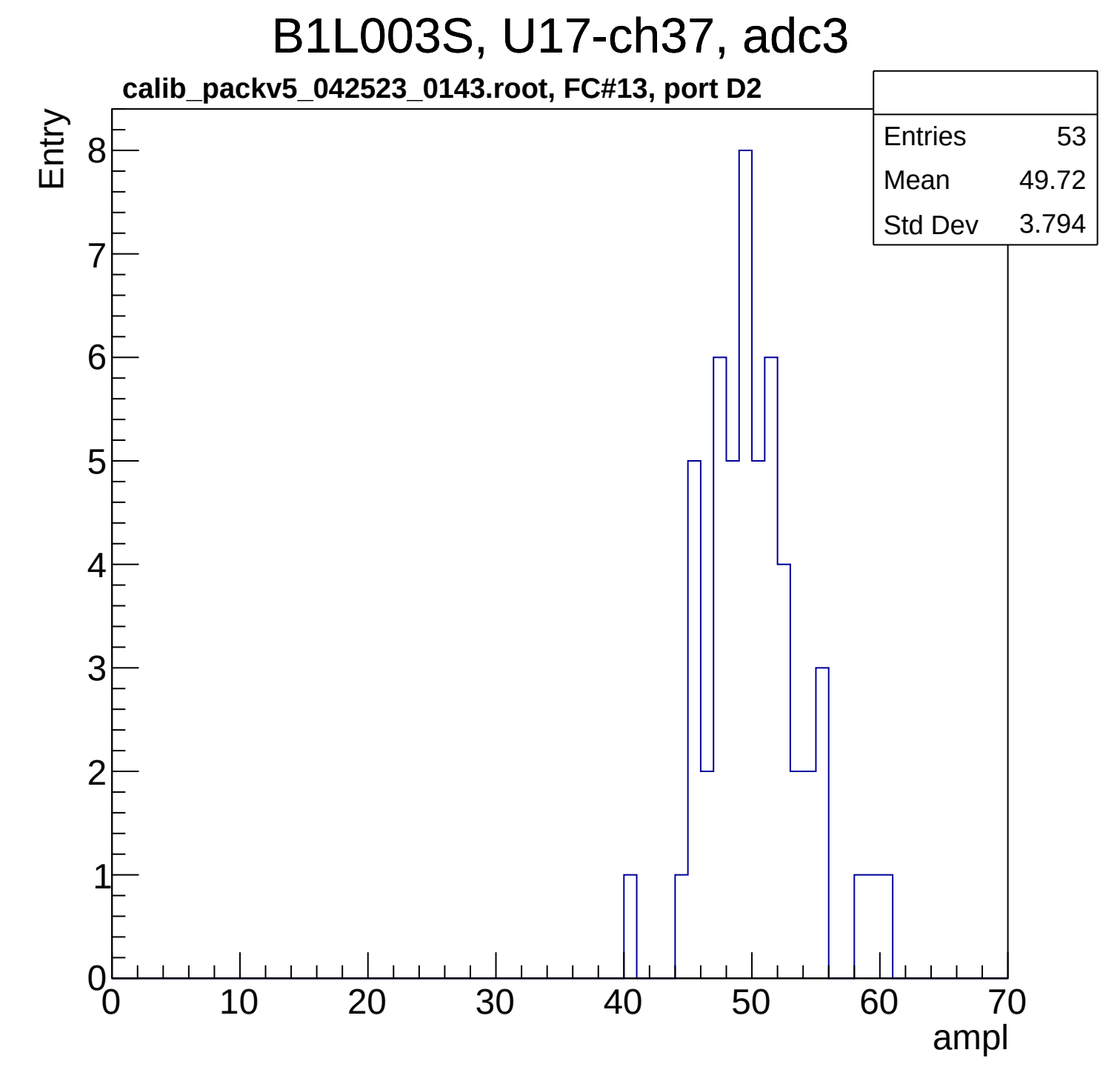
B1L003S, U17-ch37, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 49.72
Std Dev 3.794
Entry
ampl
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4
5
6
7
8
0
10
20
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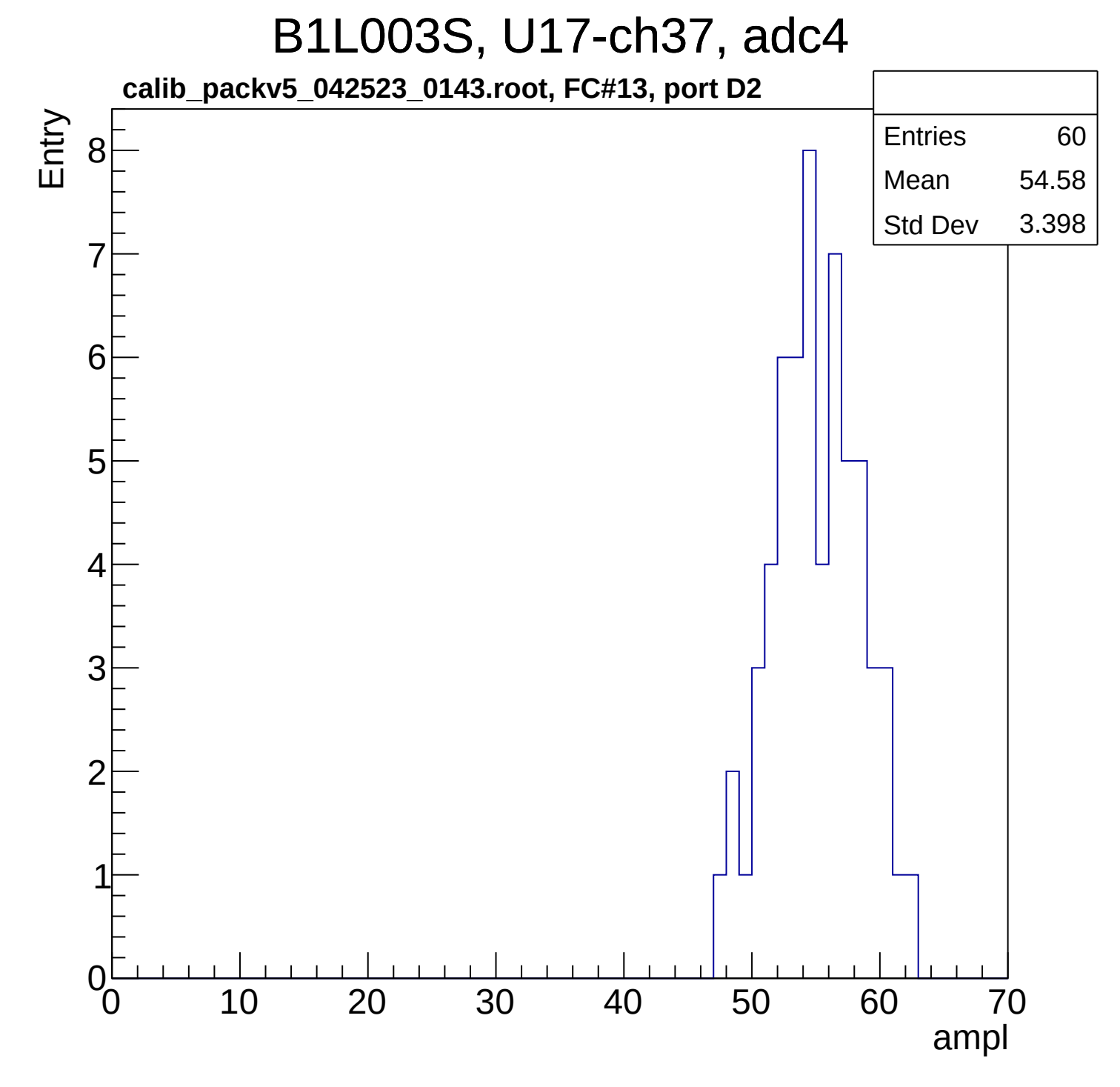
40
50
60
70


B1L003S, U17-ch37, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.58
Std Dev 3.398
Entry
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4
5
6
7
8
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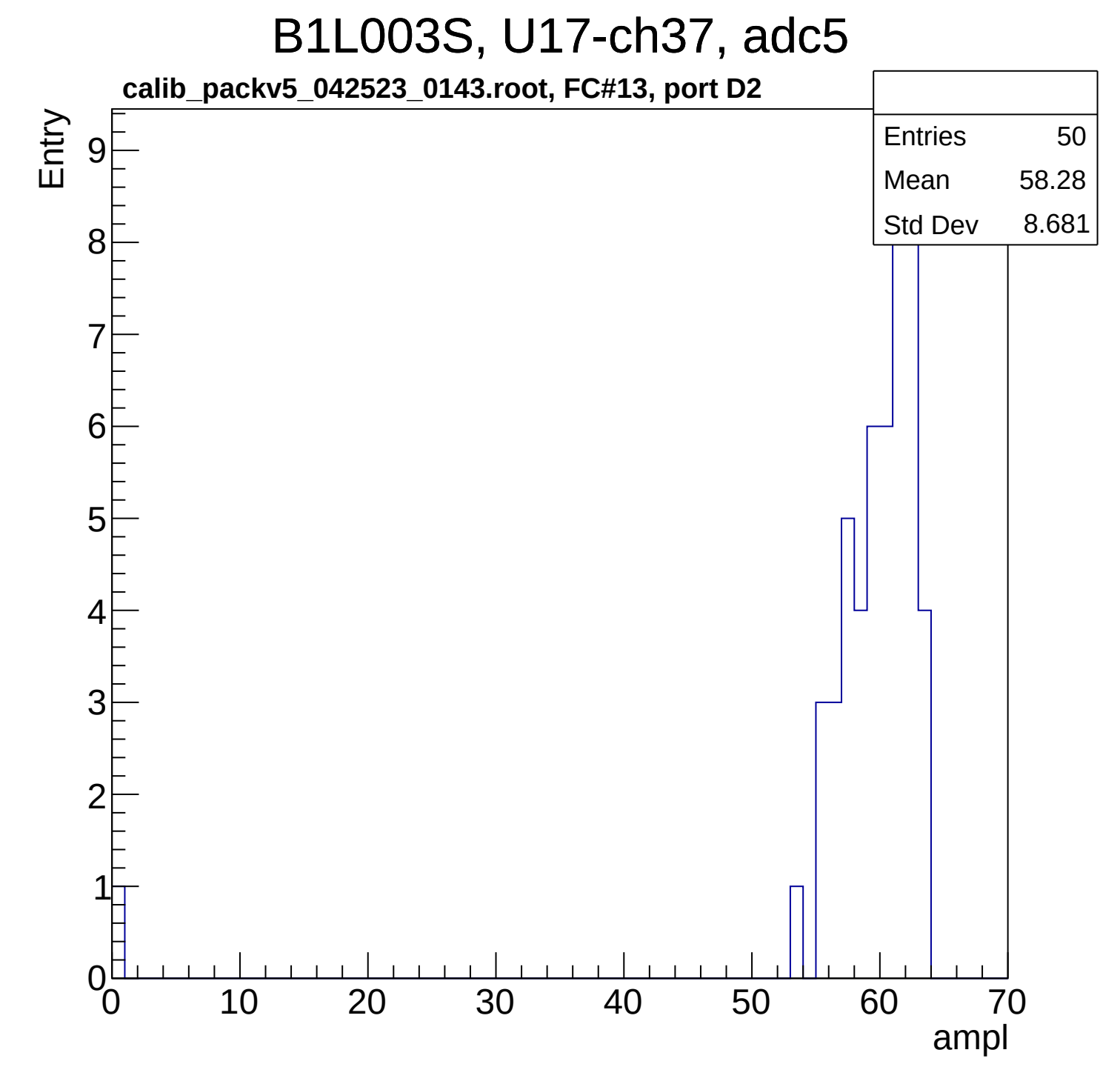
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch37, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.28
Std Dev 8.681
Entry
ampl
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4
5
6
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0
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60
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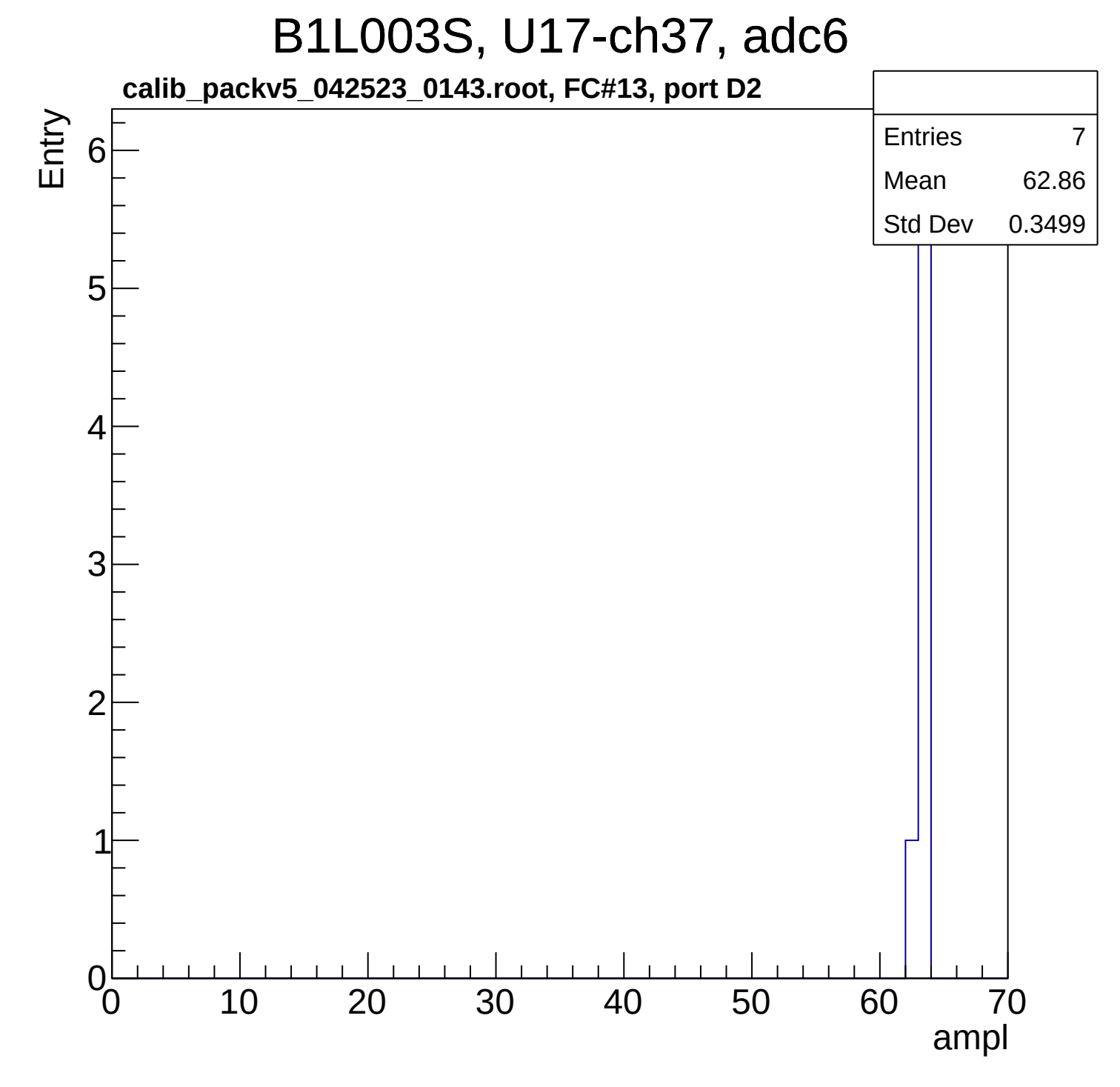
B1L003S, U17-ch37, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.86
Std Dev 0.3499
Entry
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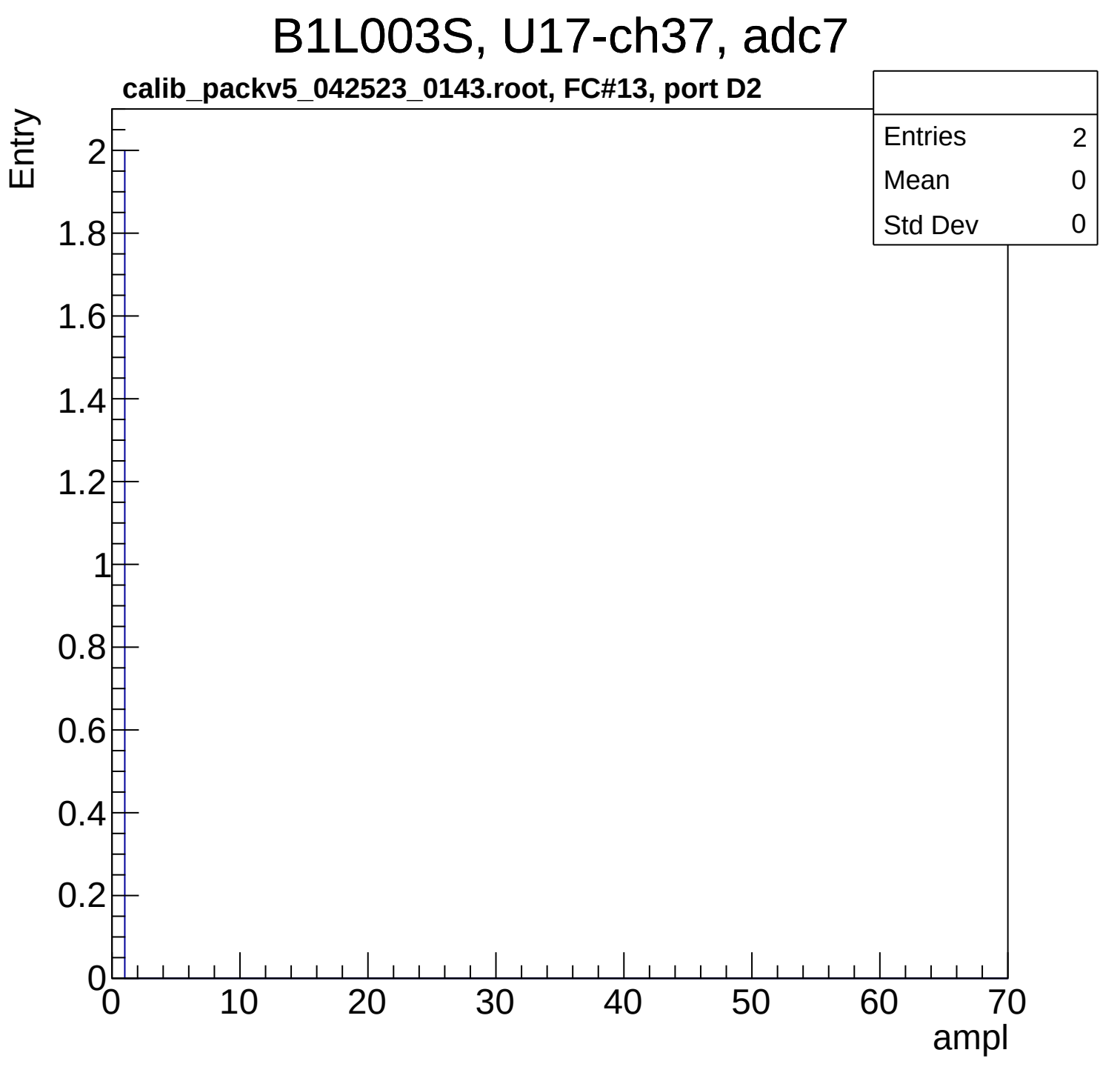
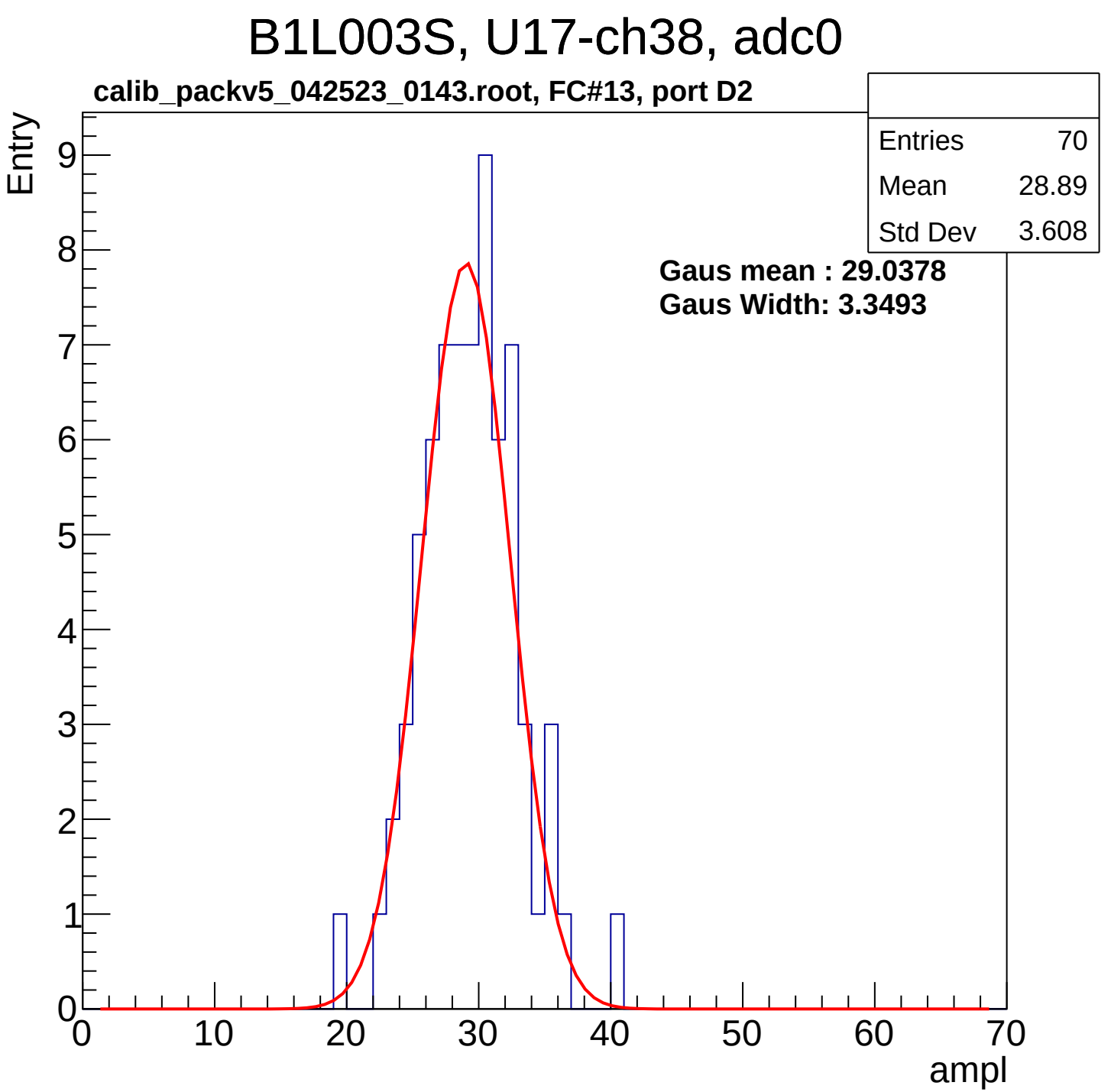
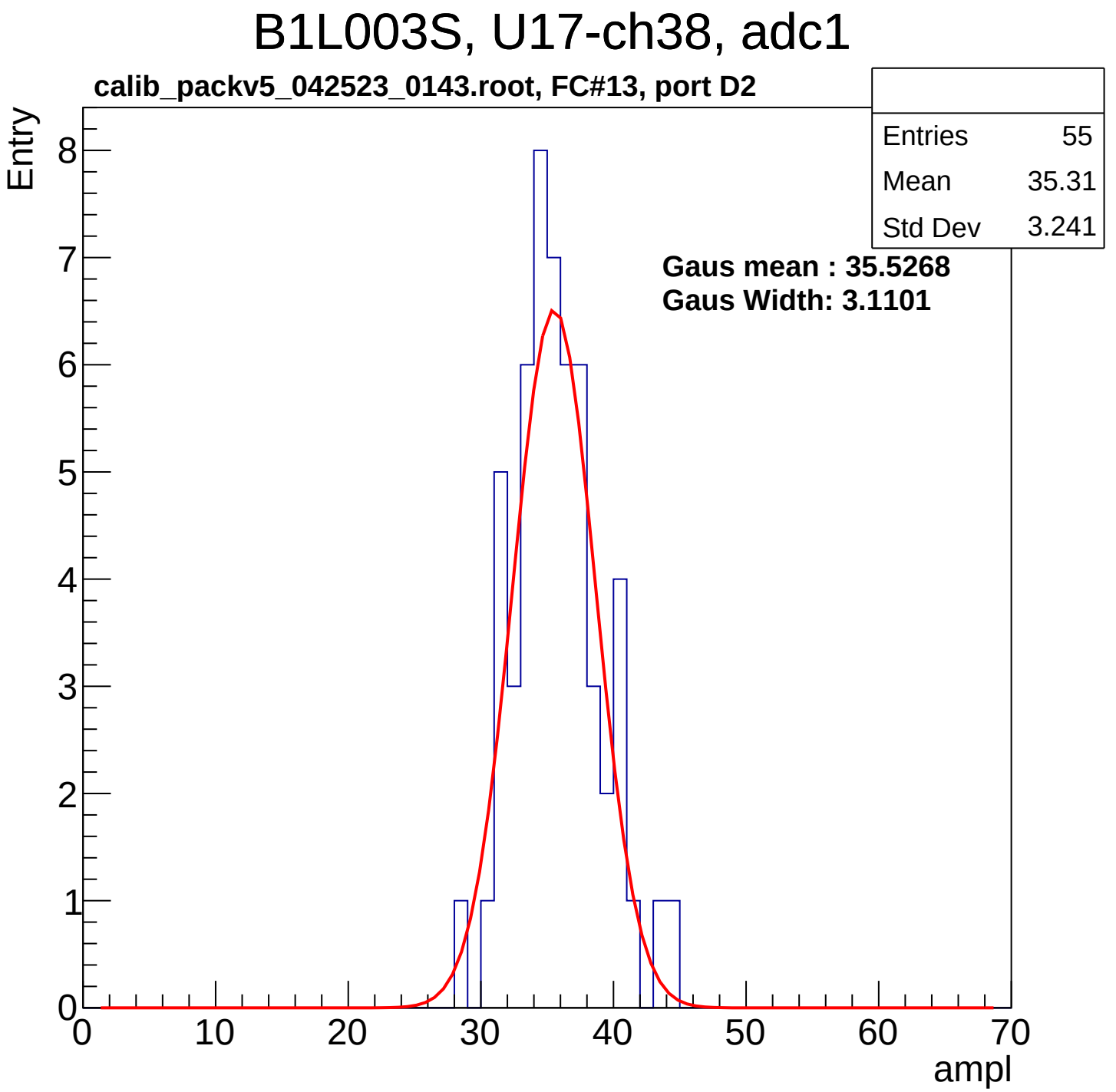
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch37, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch38, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 28.89
Std Dev 3.608
Gaus mean : 29.0378
Gaus Width: 3.3493
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch38, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 35.31
Std Dev 3.241
Gaus mean : 35.5268
Gaus Width: 3.1101
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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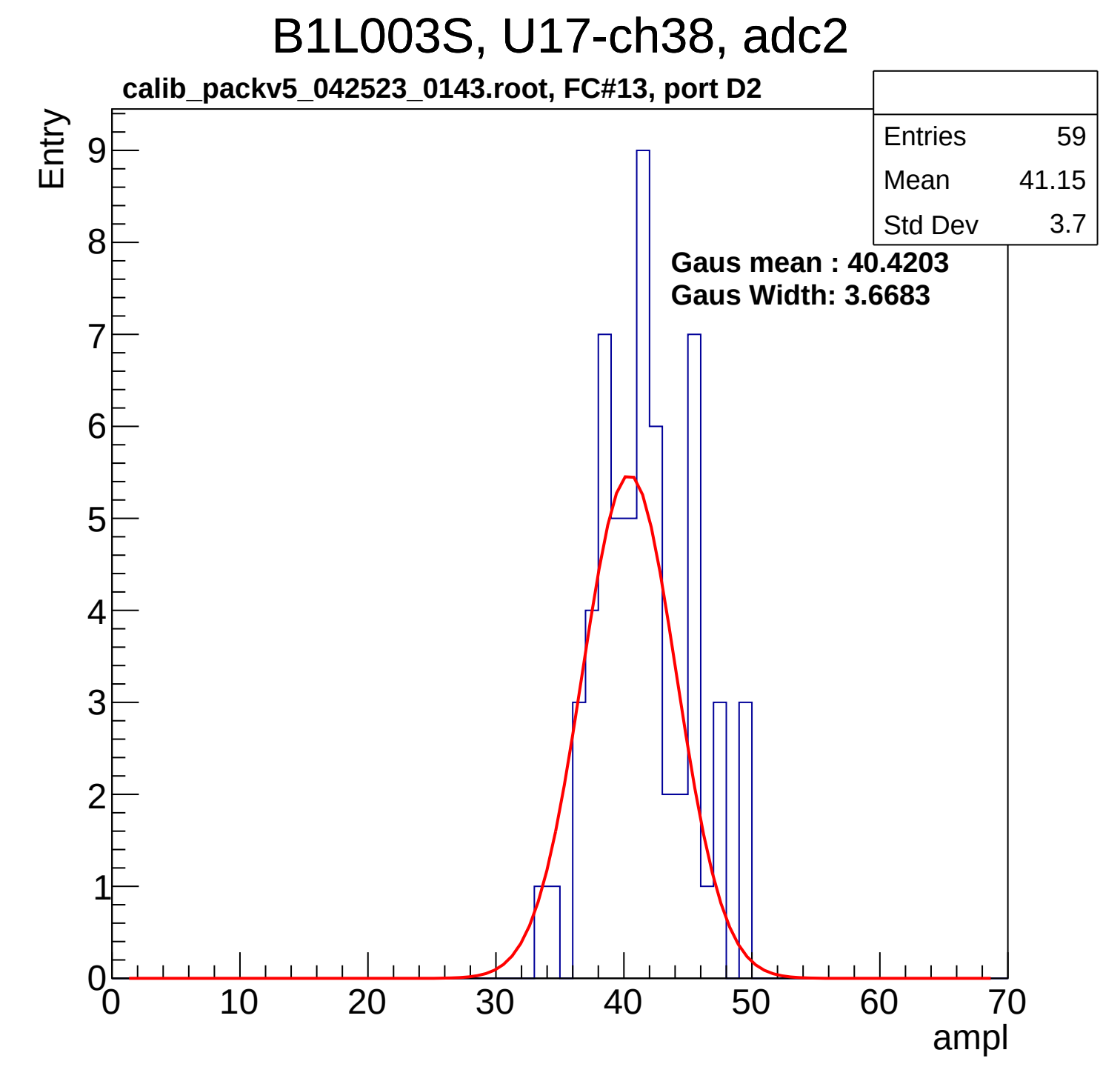
B1L003S, U17-ch38, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 41.15
Std Dev 3.7
Gaus mean : 40.4203
Gaus Width: 3.6683
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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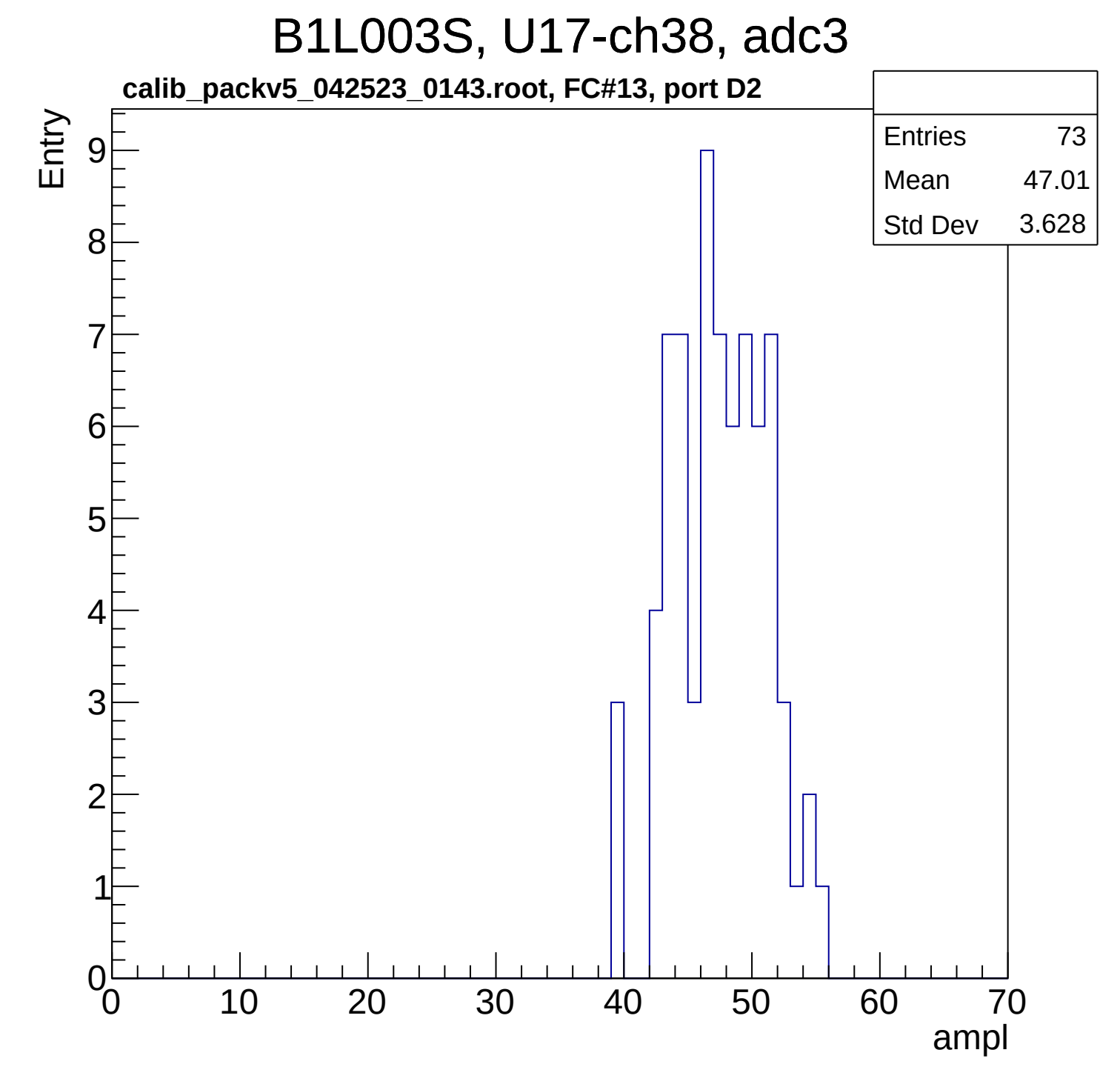
B1L003S, U17-ch38, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 47.01
Std Dev 3.628
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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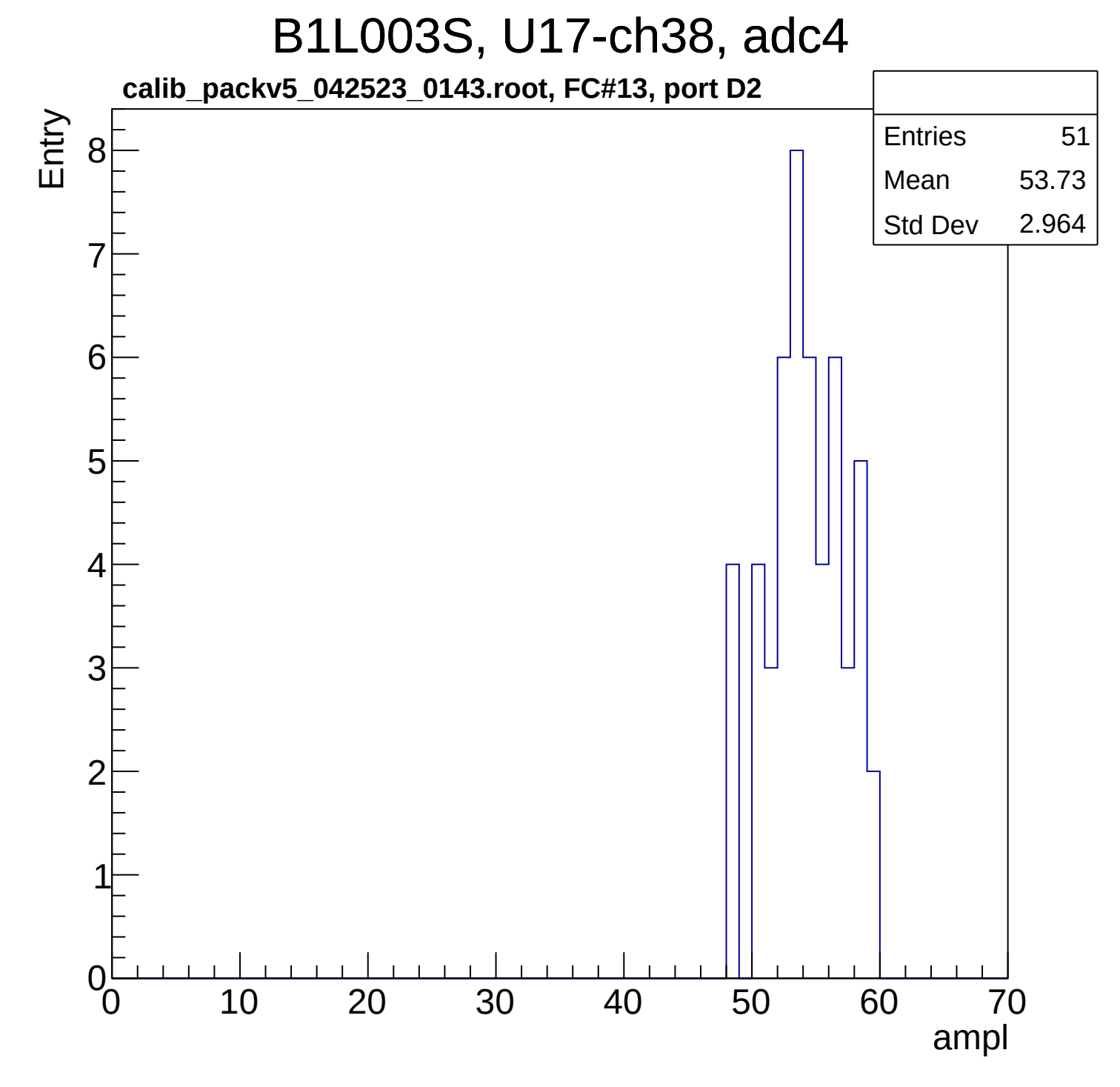
B1L003S, U17-ch38, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 53.73
Std Dev 2.964
Entry
ampl
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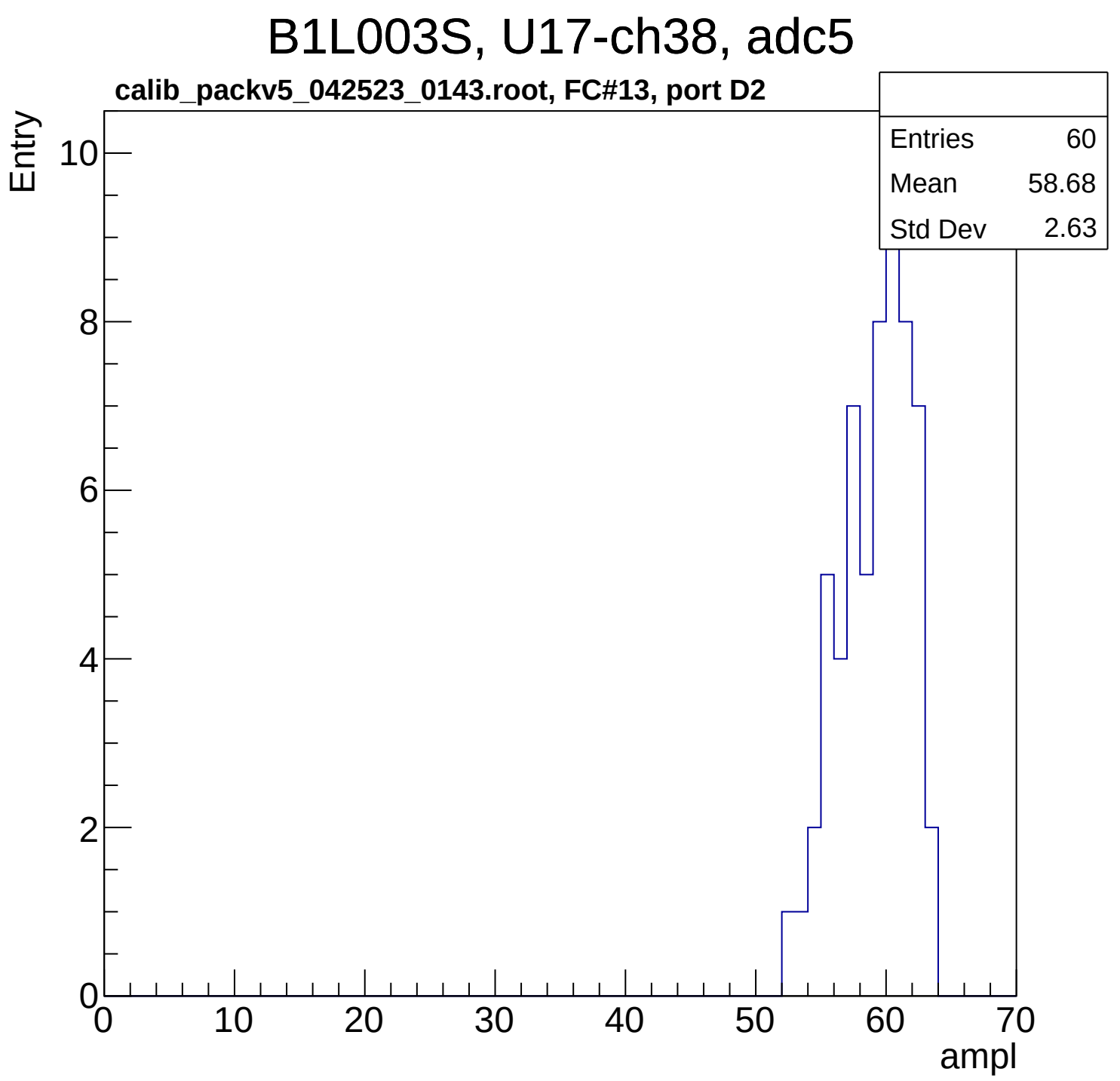
B1L003S, U17-ch38, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.68
Std Dev 2.63
Entry
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6
8
10
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10
20
30
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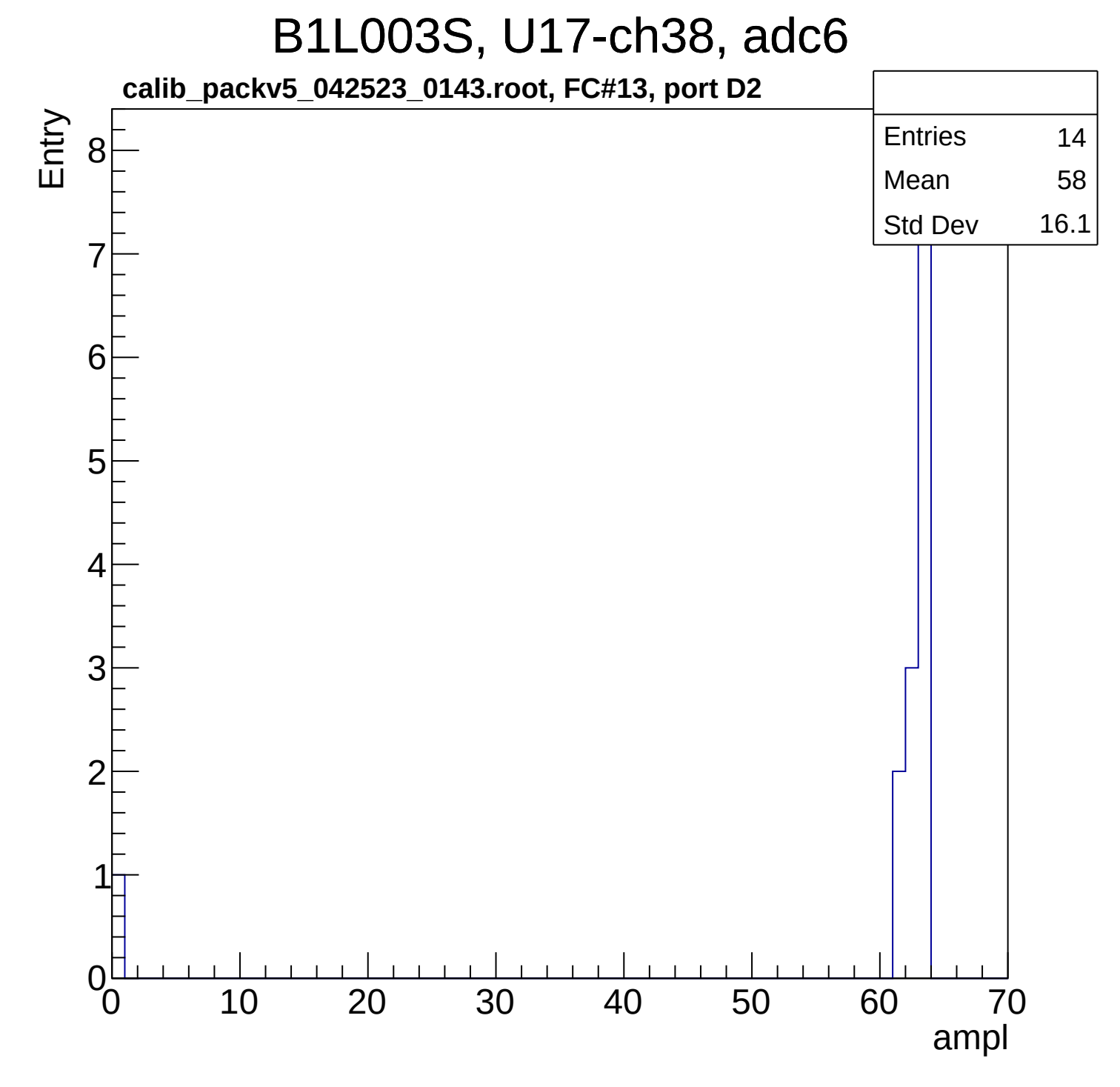
50
60
70
ampl


B1L003S, U17-ch38, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 58
Std Dev 16.1
Entry
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2
3
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7
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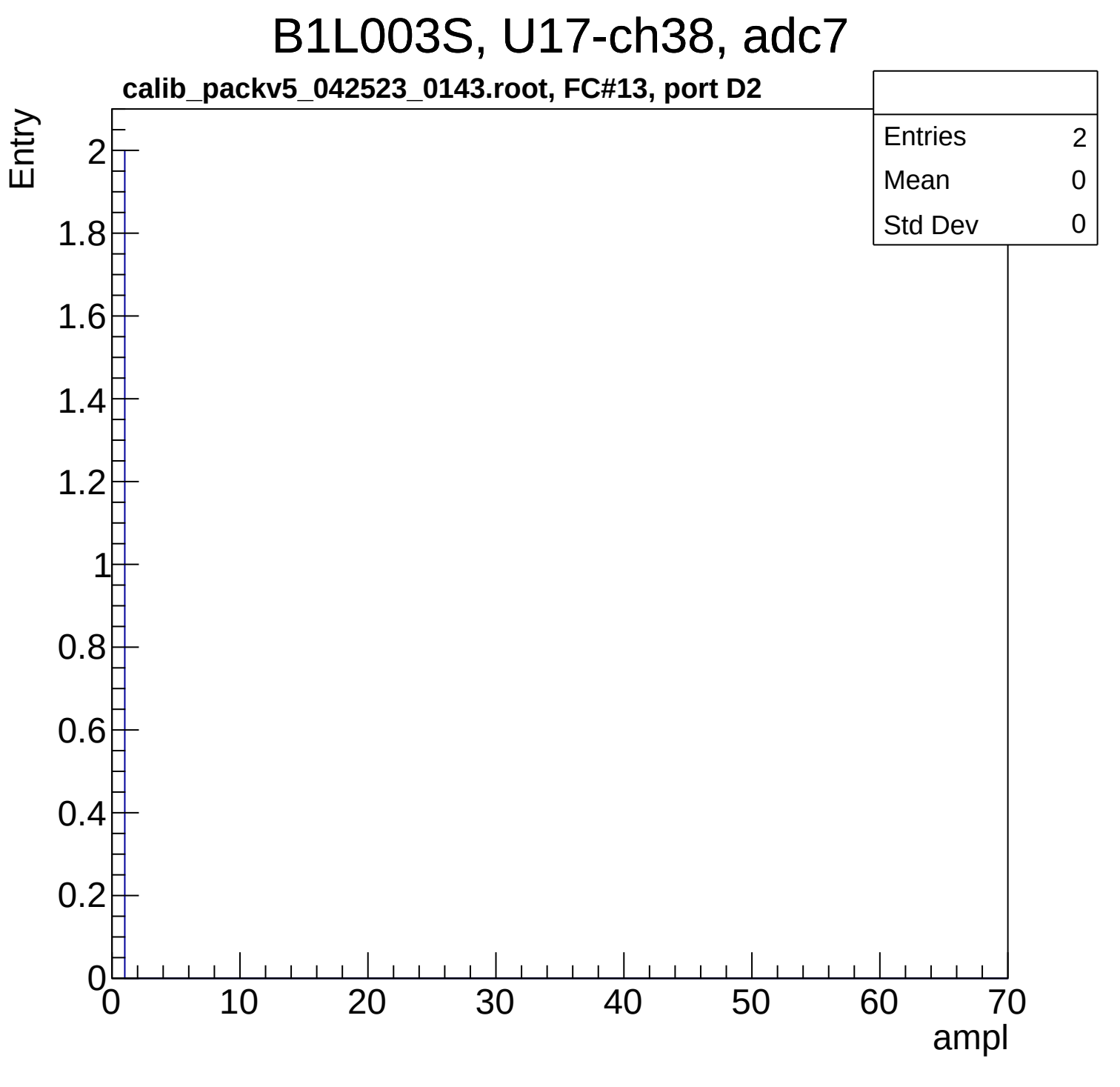
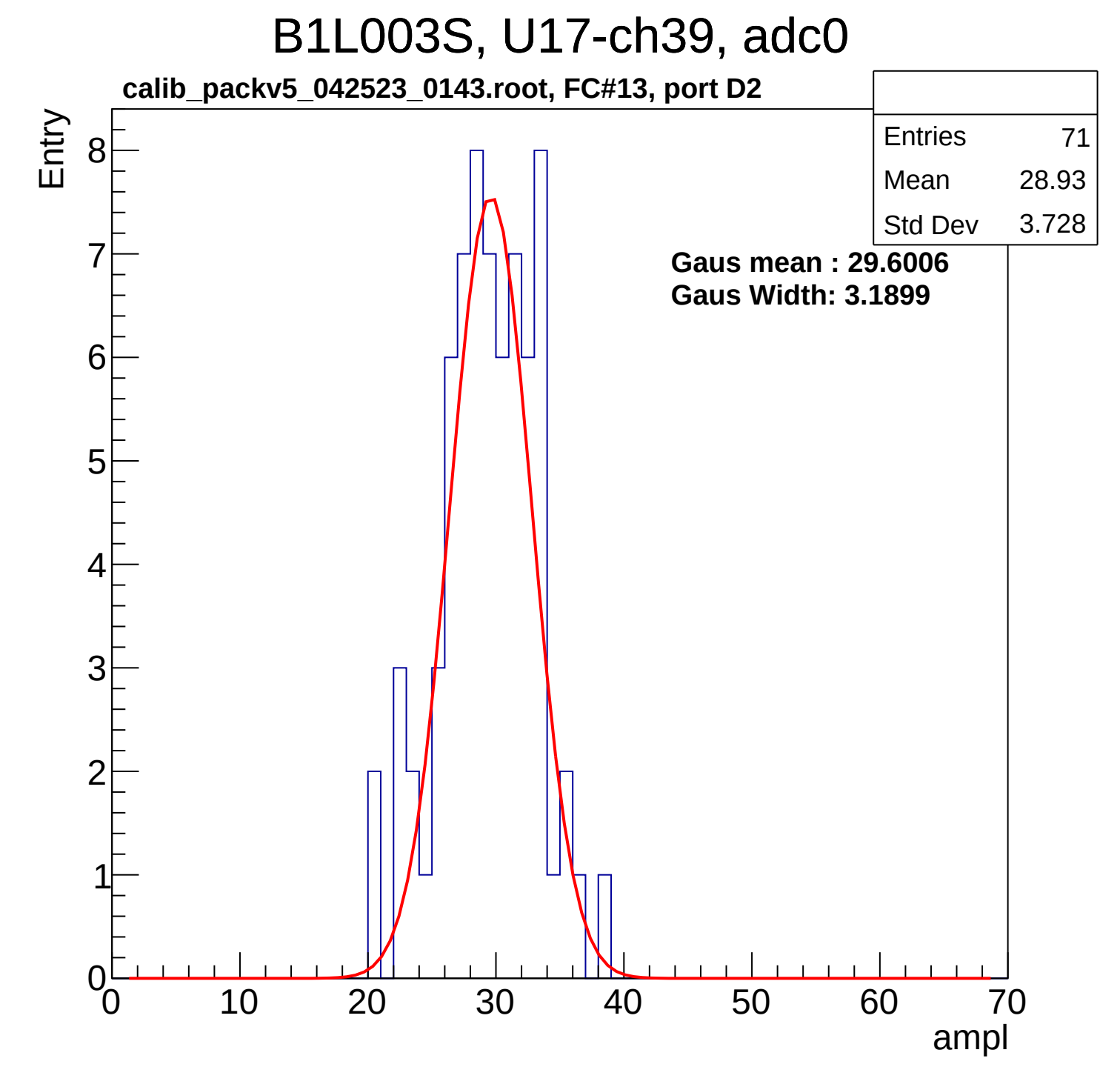
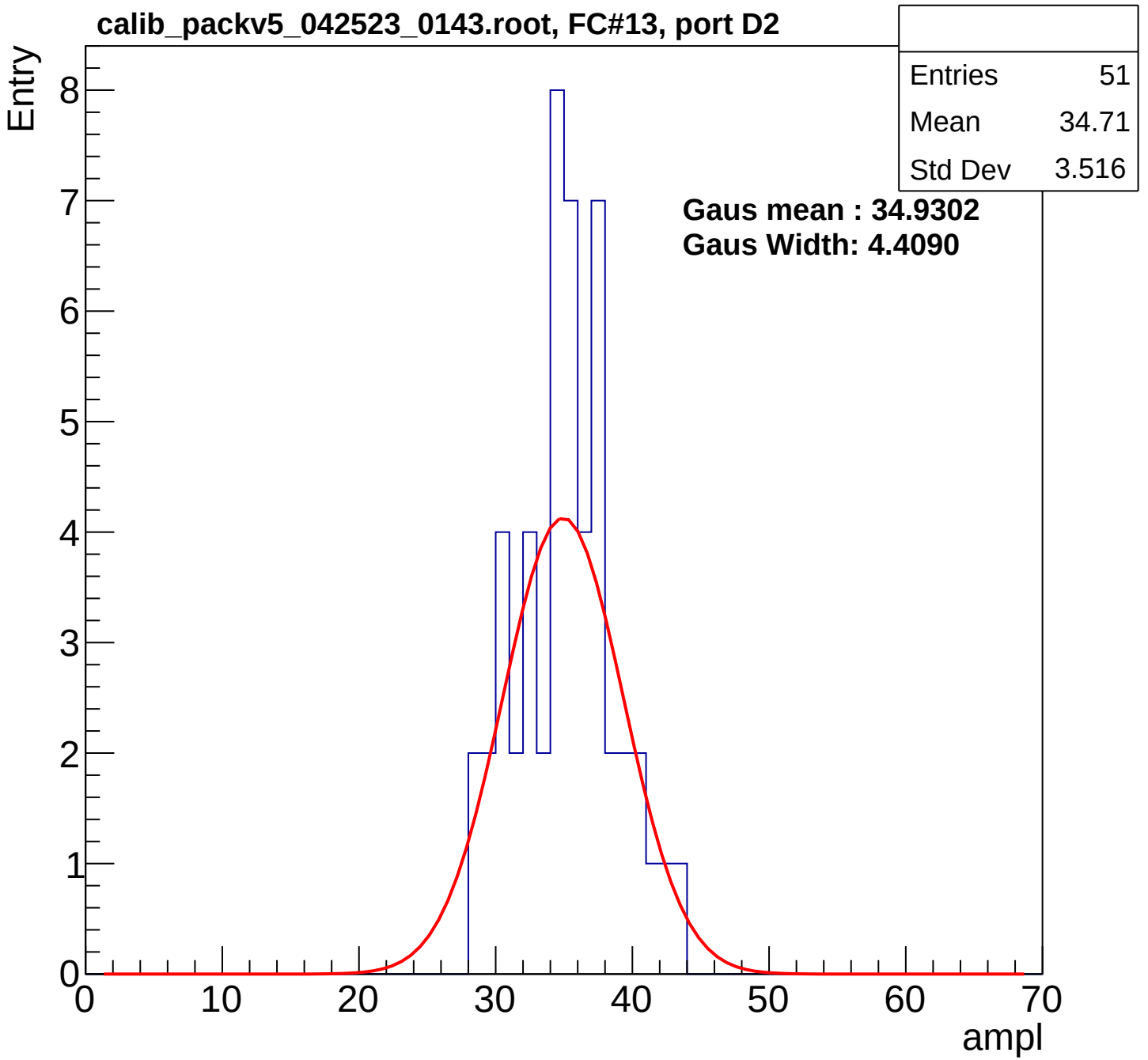
50
60
70
ampl


B1L003S, U17-ch38, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch39, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 28.93
Std Dev 3.728
Gaus mean : 29.6006
Gaus Width: 3.1899
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch39, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 34.71
Std Dev 3.516
Gaus mean : 34.9302
Gaus Width: 4.4090
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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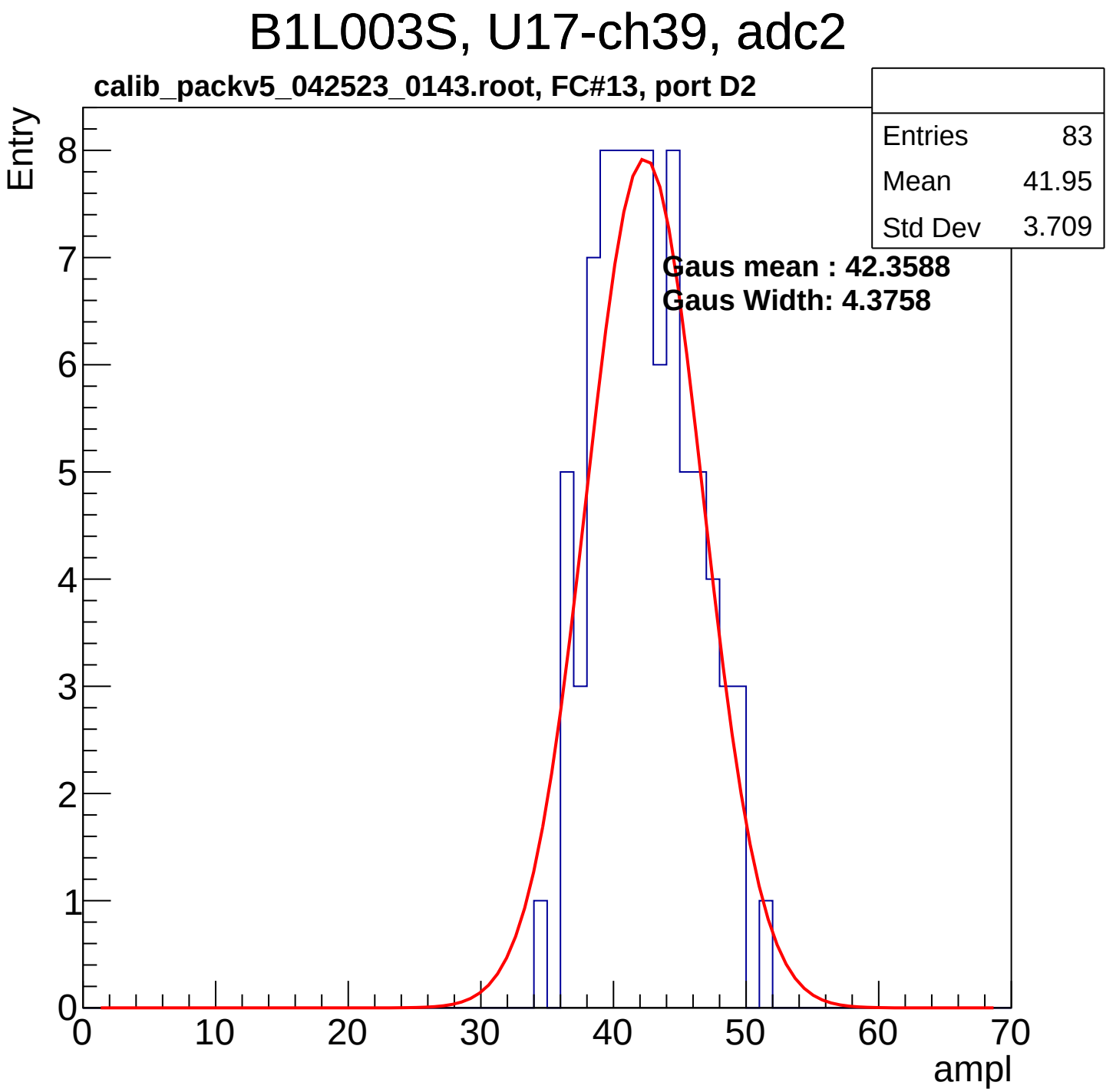
B1L003S, U17-ch39, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 41.95
Std Dev 3.709
Gaus mean : 42.3588
Gaus Width: 4.3758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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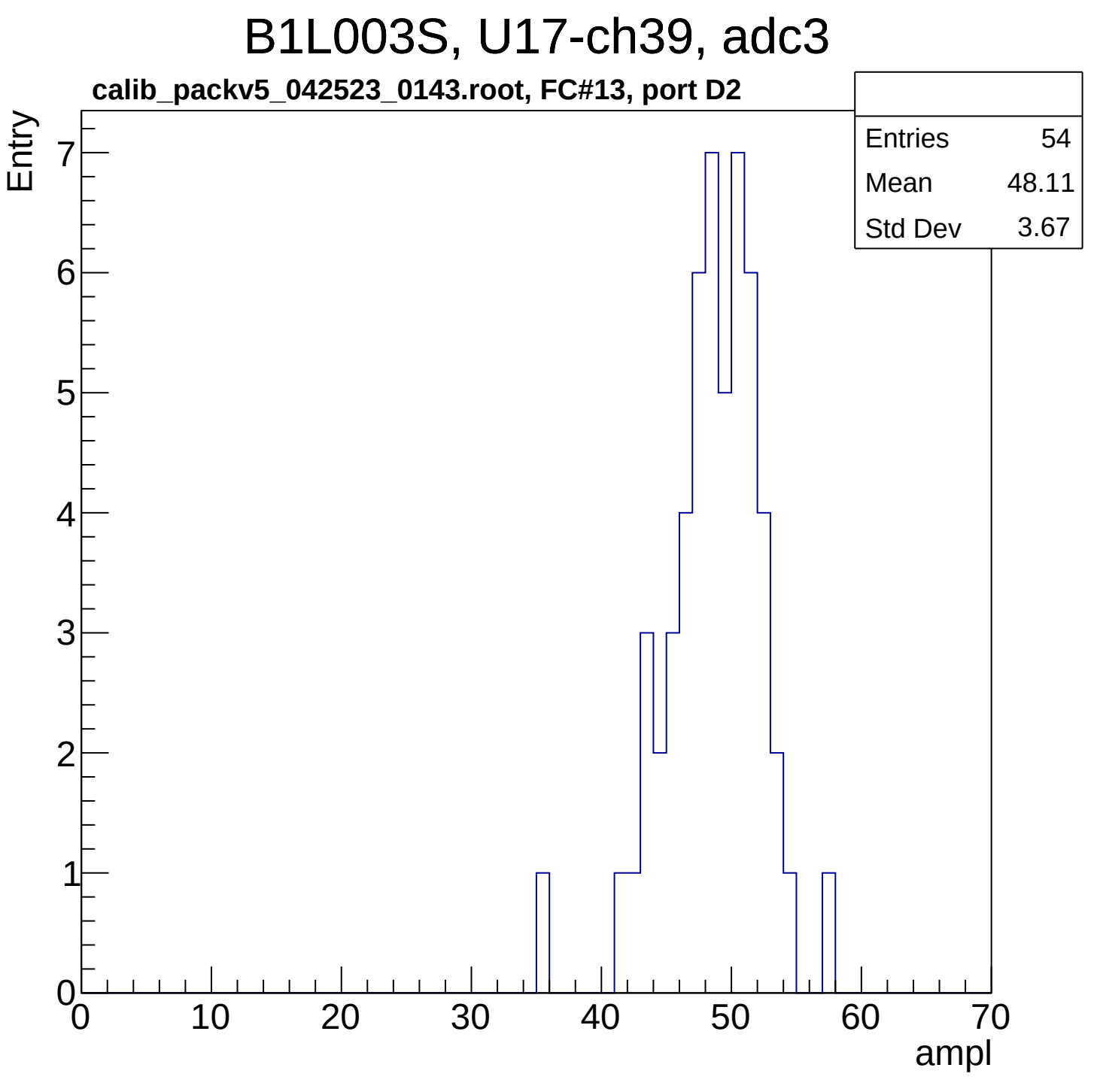
B1L003S, U17-ch39, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 48.11
Std Dev 3.67
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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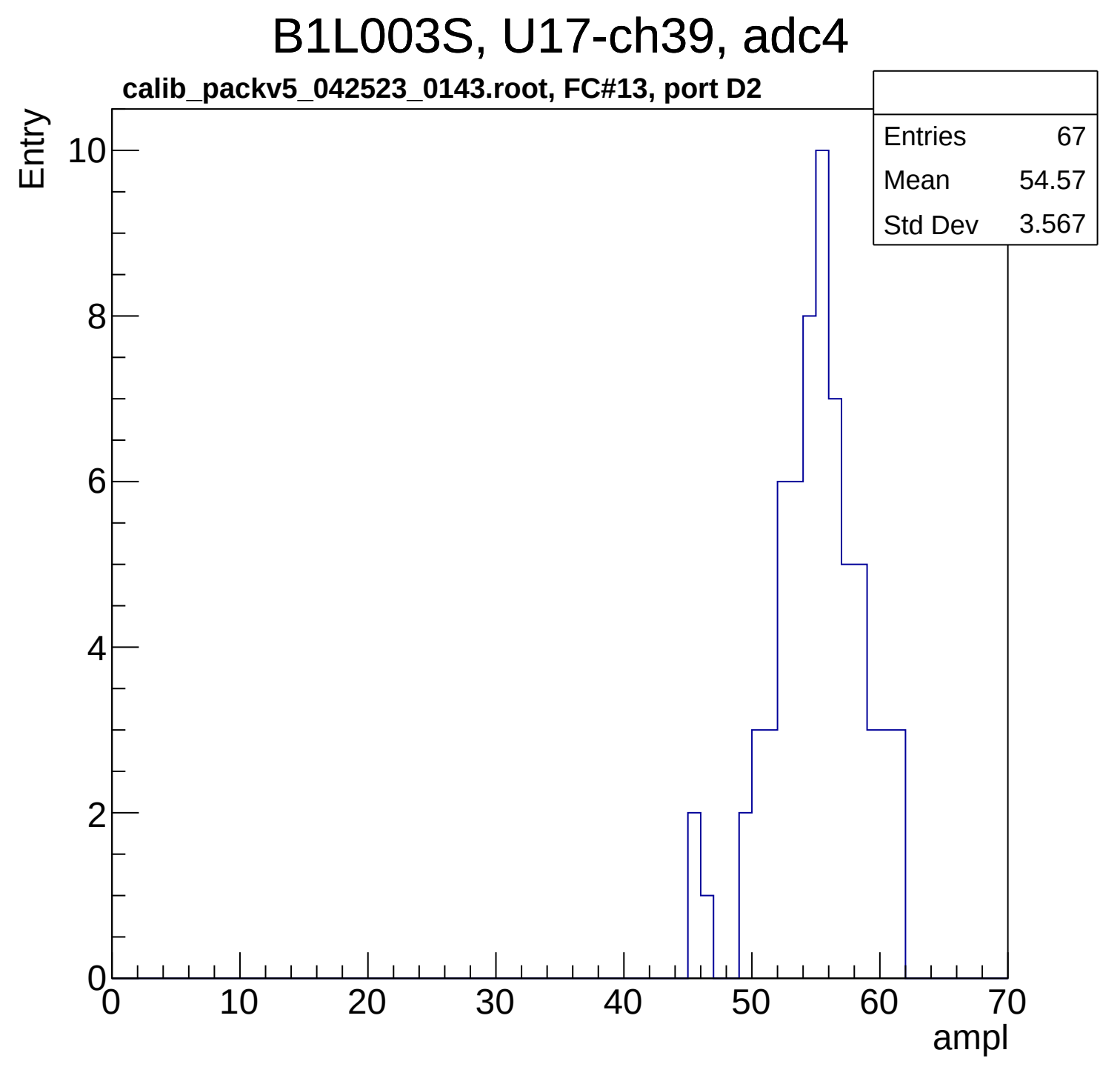
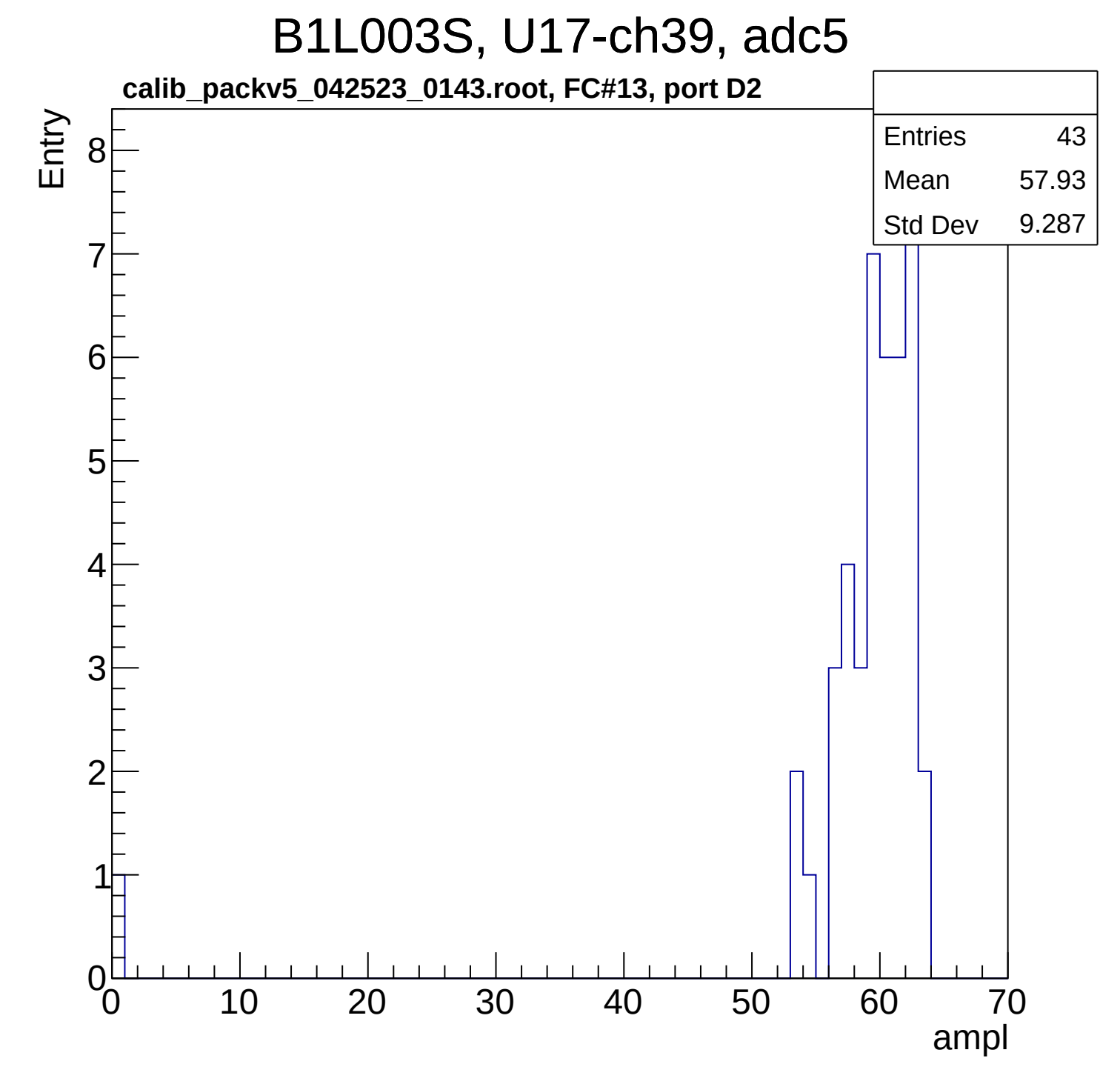

B1L003S, U17-ch39, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 54.57
Std Dev 3.567
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch39, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 57.93
Std Dev 9.287
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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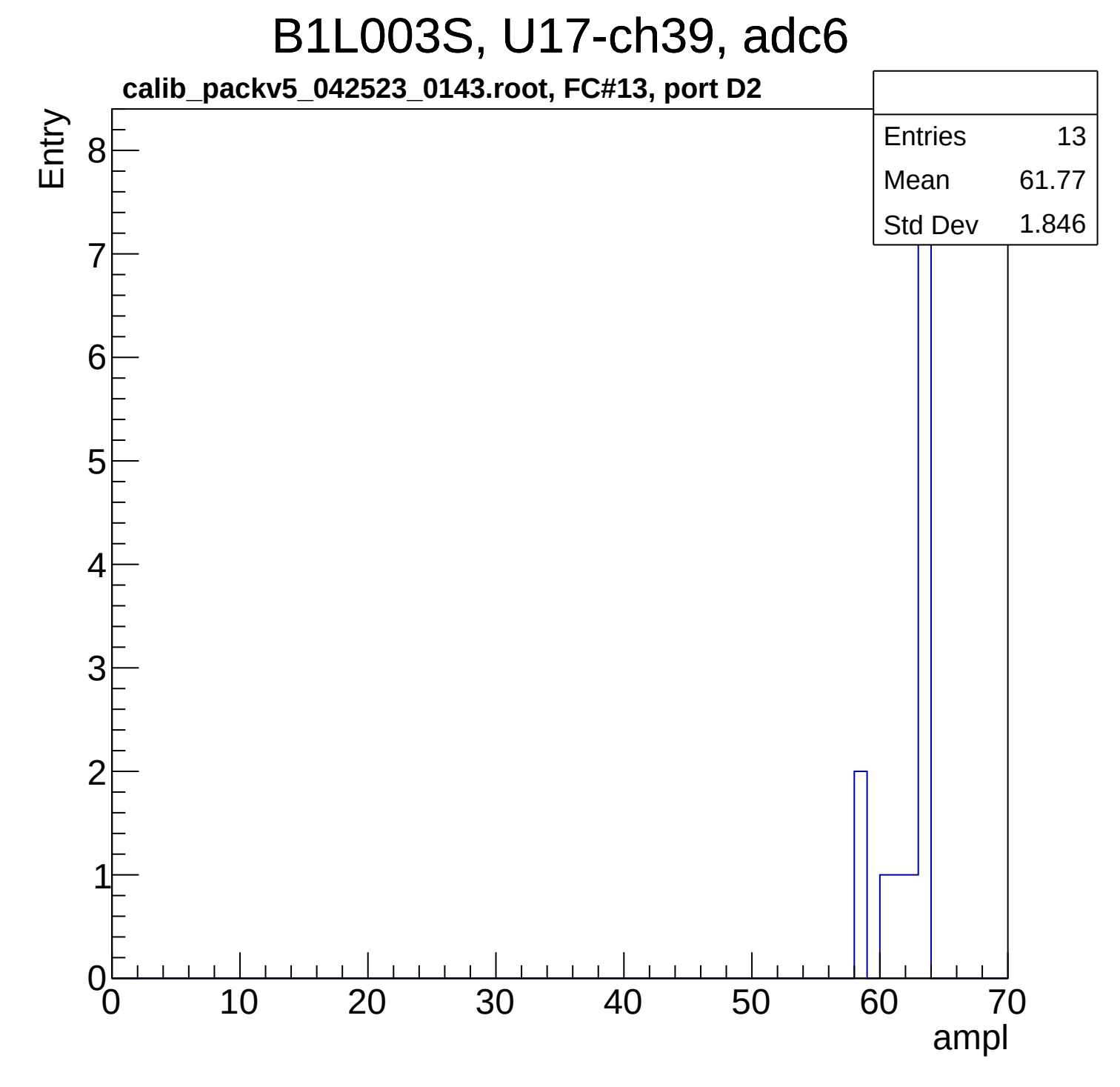
B1L003S, U17-ch39, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.77
Std Dev 1.846
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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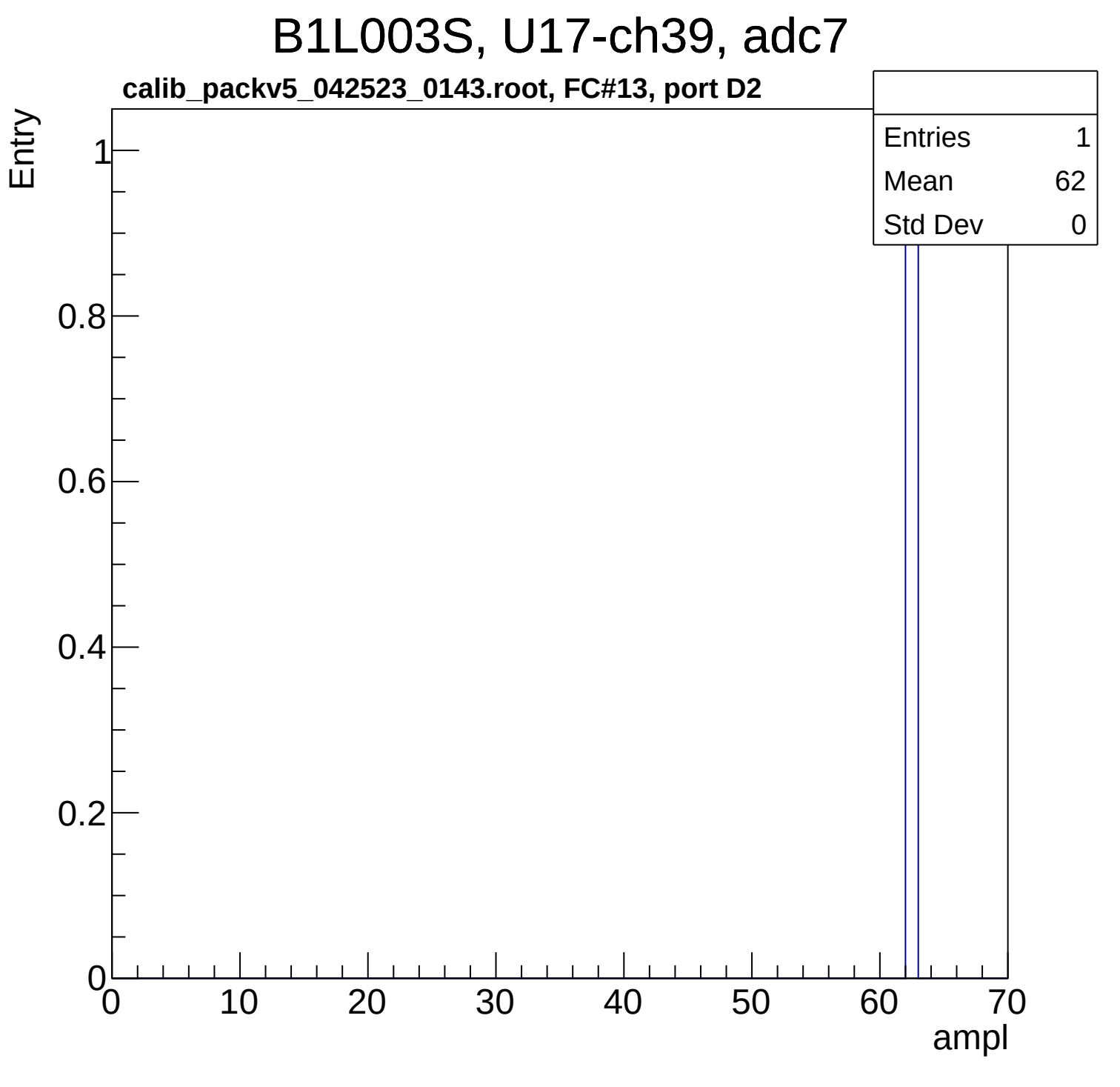
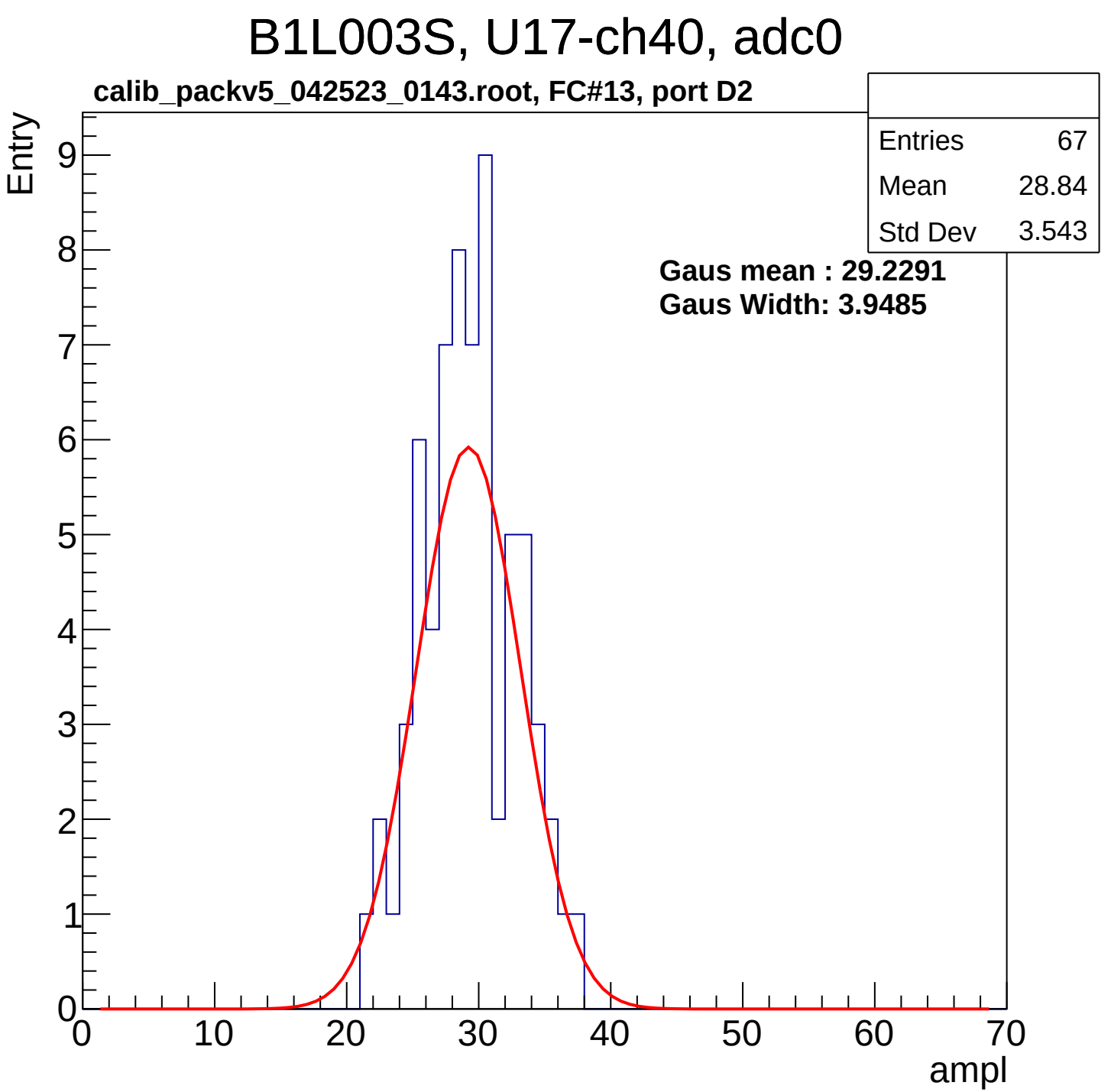
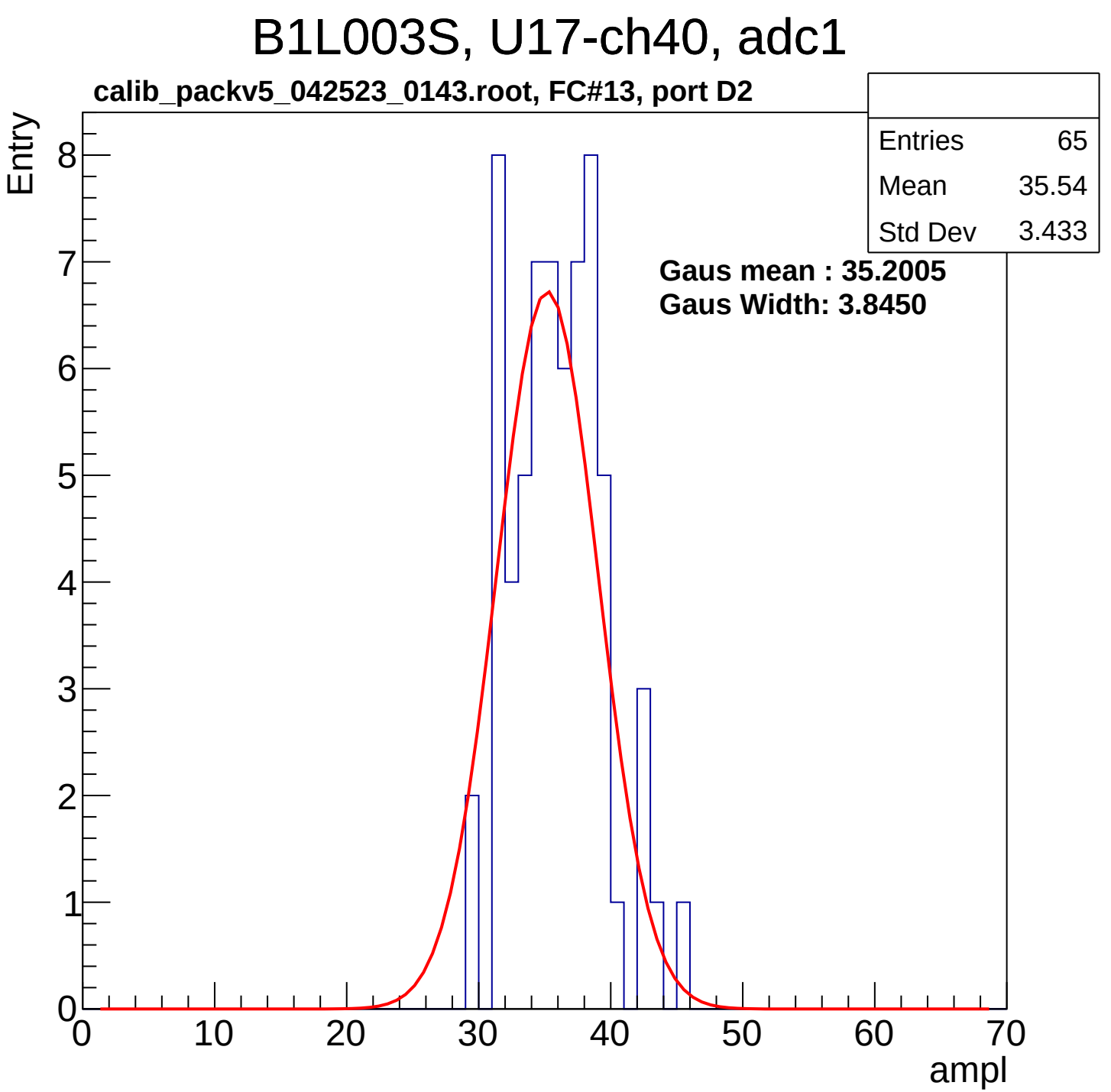

B1L003S, U17-ch39, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch40, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 28.84
Std Dev 3.543
Gaus mean : 29.2291
Gaus Width: 3.9485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch40, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.54
Std Dev 3.433
Gaus mean : 35.2005
Gaus Width: 3.8450
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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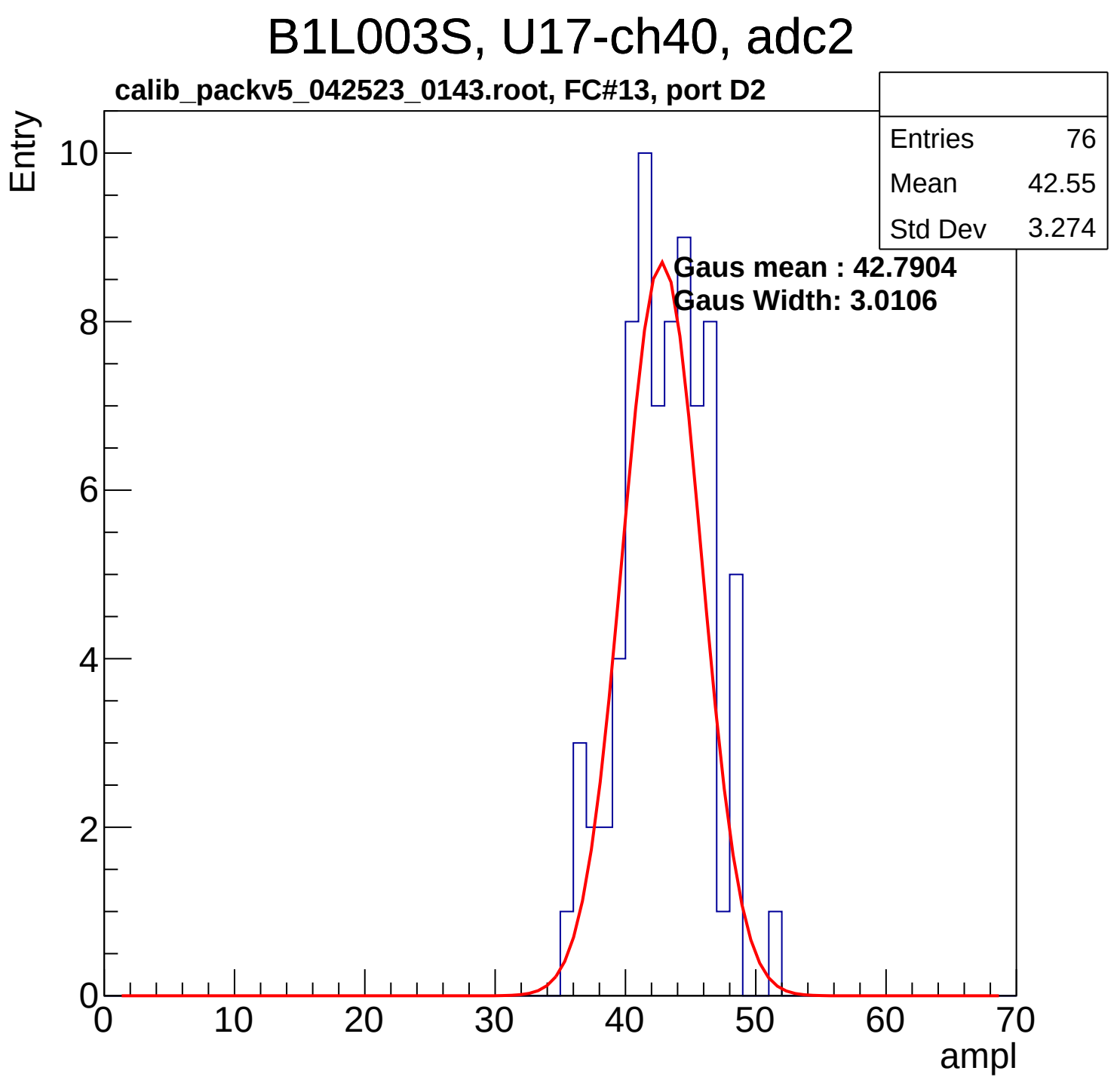
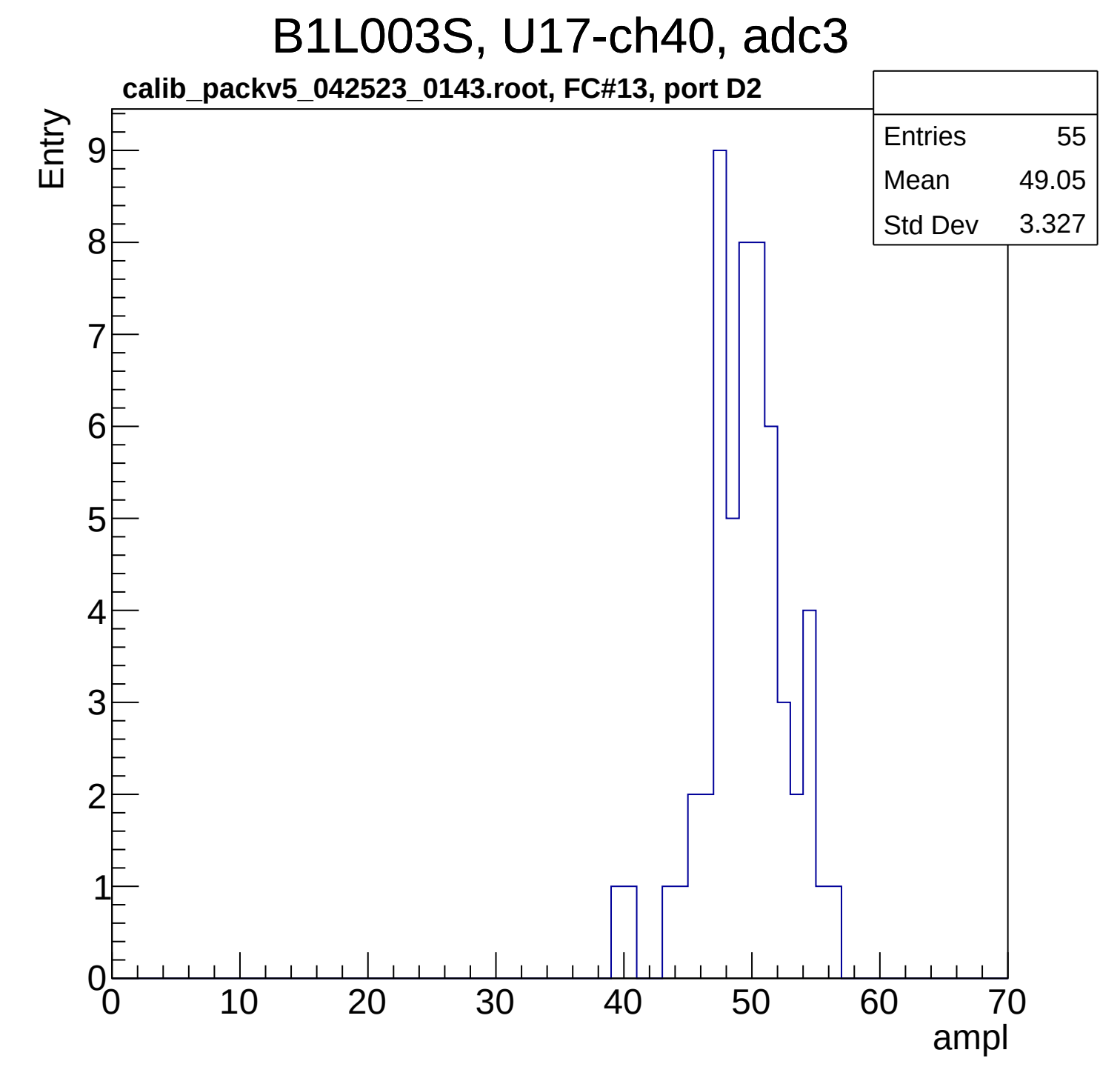

B1L003S, U17-ch40, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 42.55
Std Dev 3.274
Gaus mean : 42.7904
Gaus Width: 3.0106
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch40, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 49.05
Std Dev 3.327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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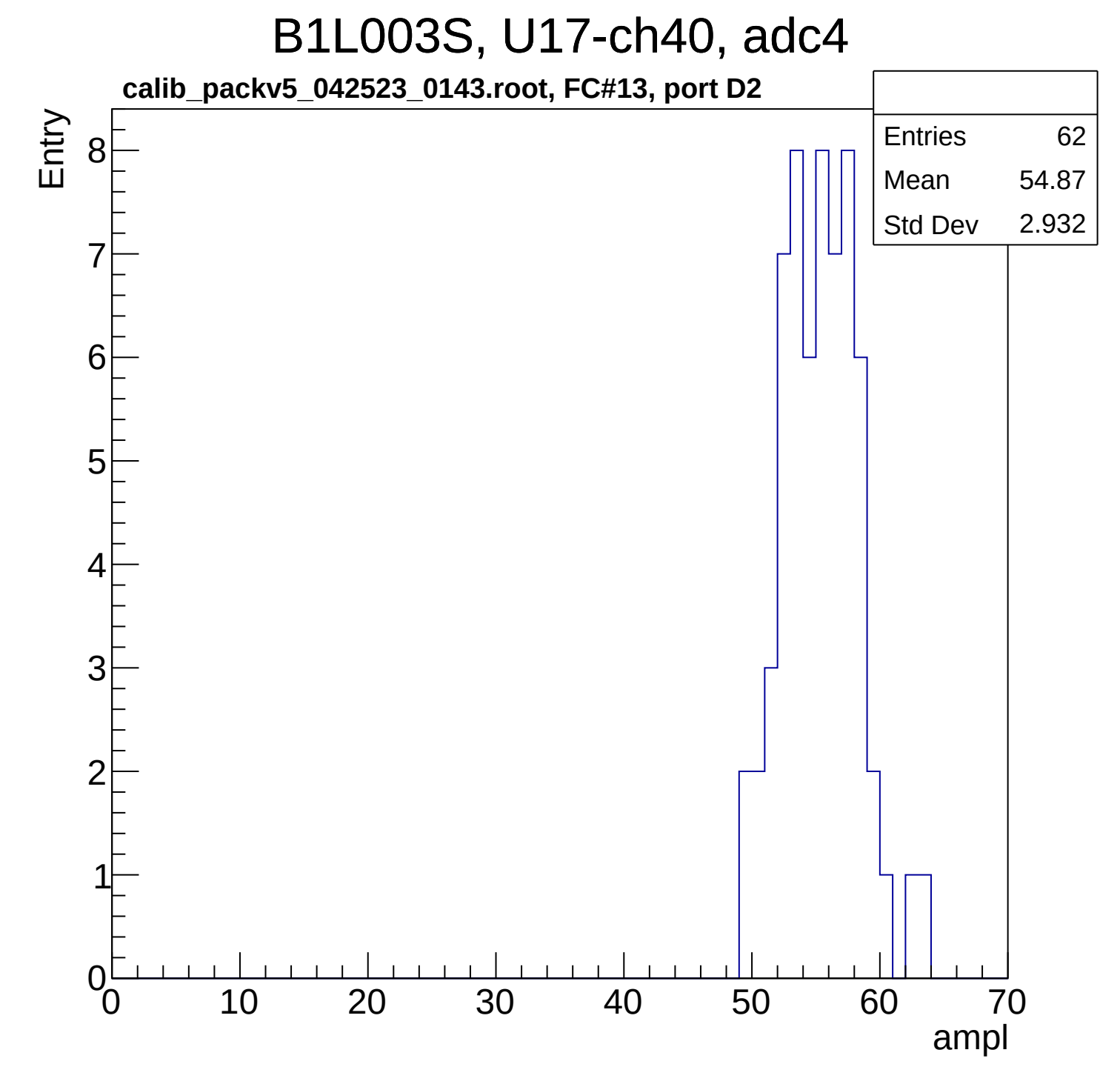
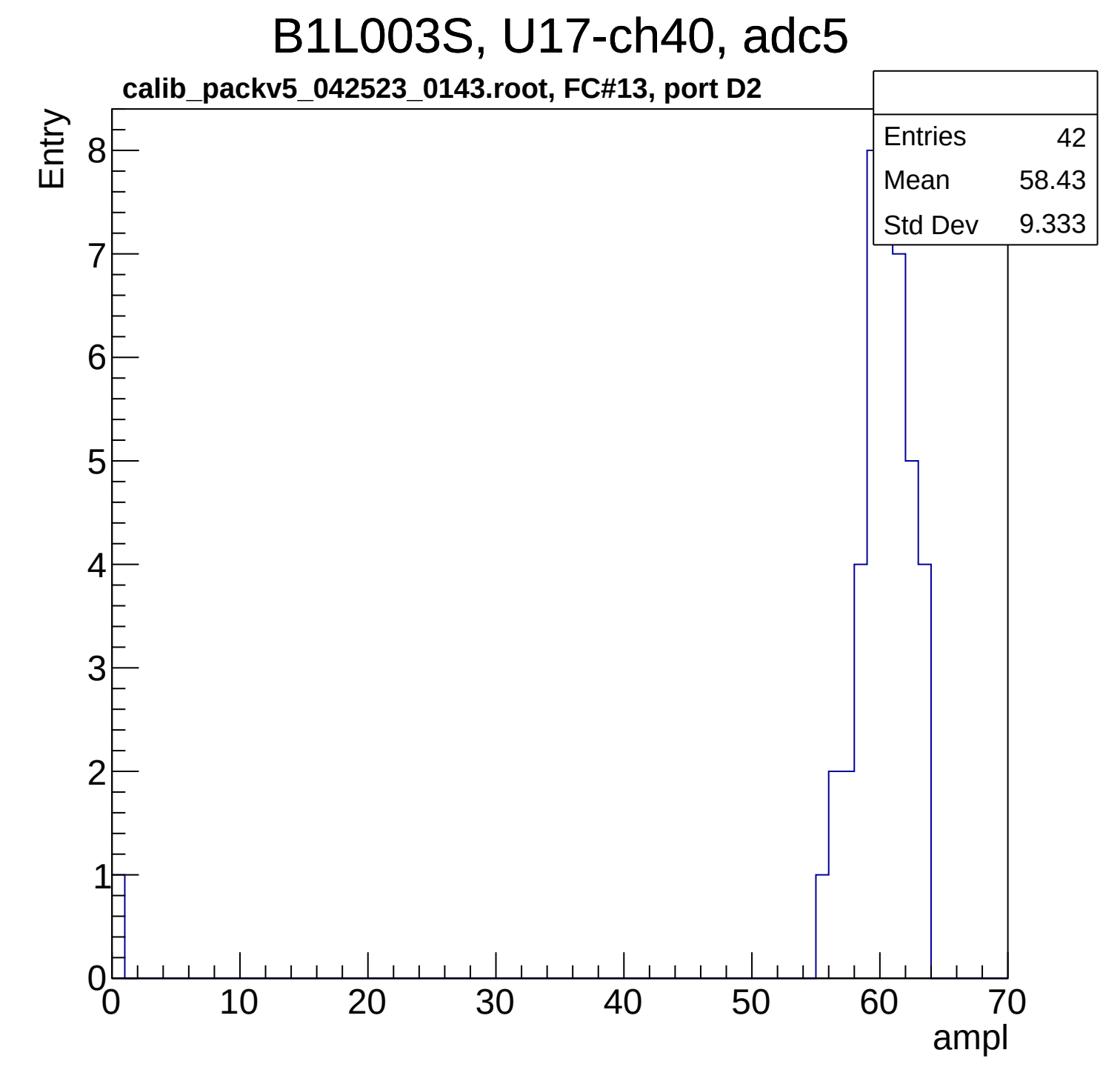
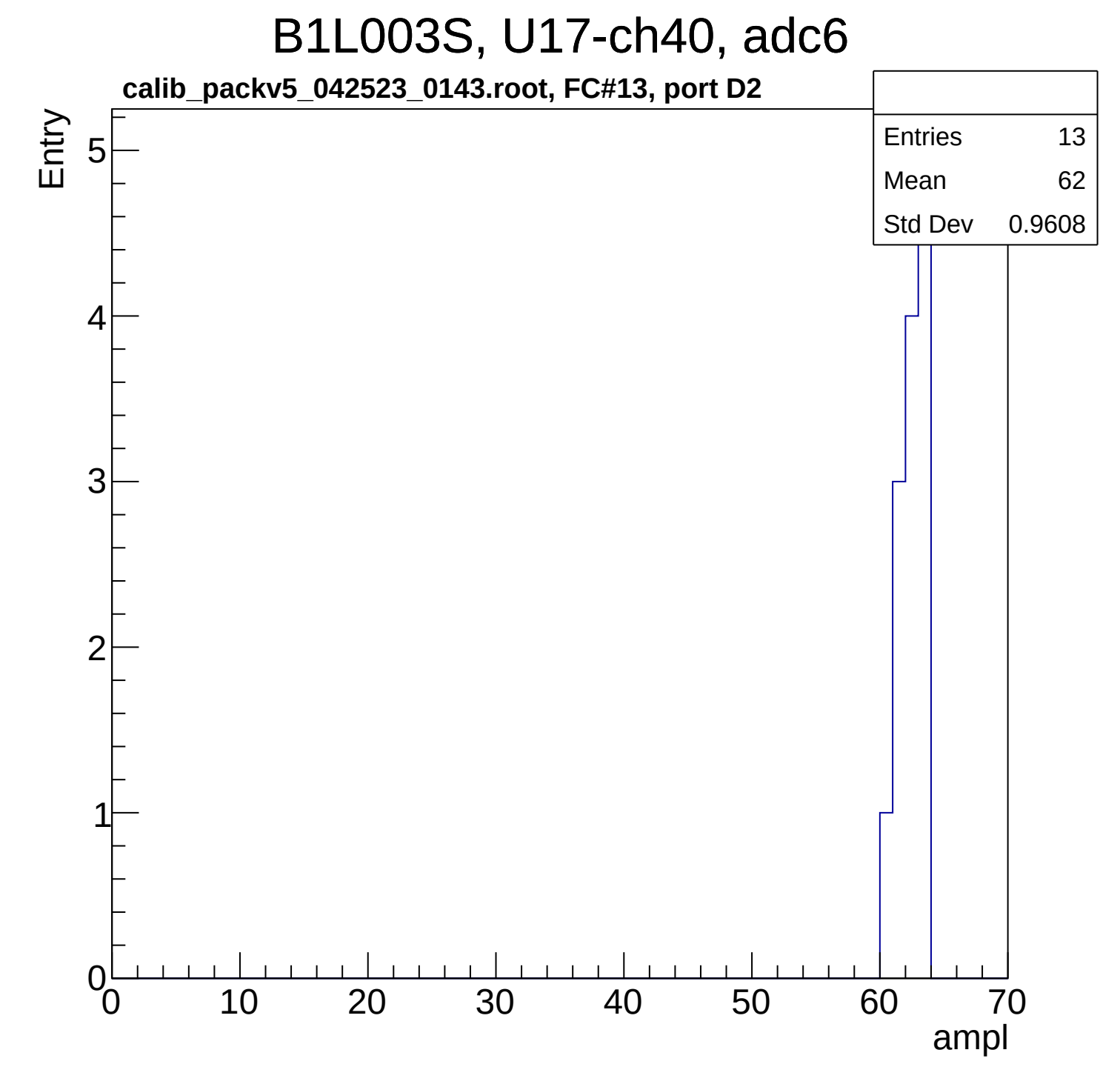
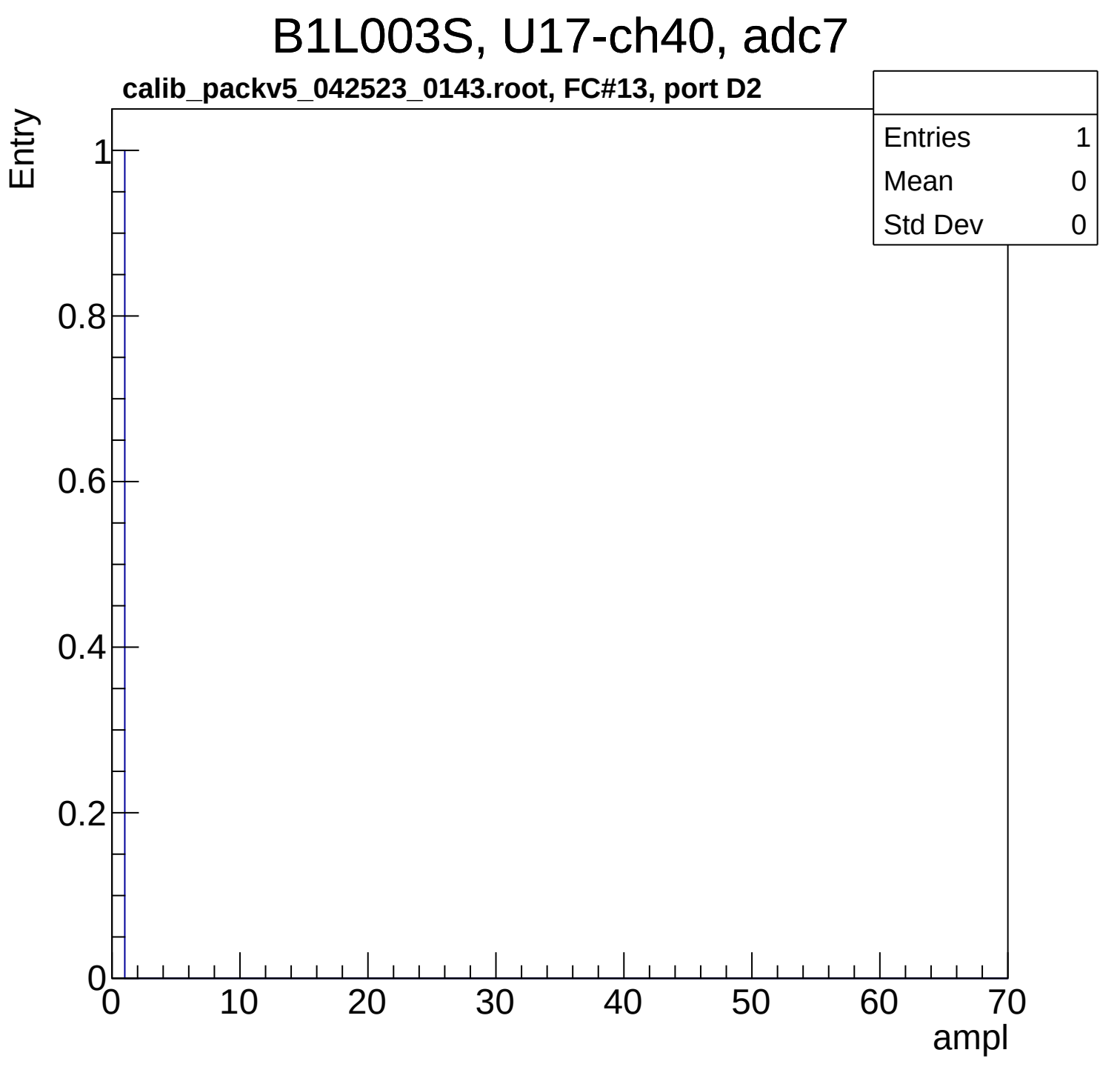

B1L003S, U17-ch40, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 54.87
Std Dev 2.932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch40, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 58.43
Std Dev 9.333
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch40, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 62
Std Dev 0.9608
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch40, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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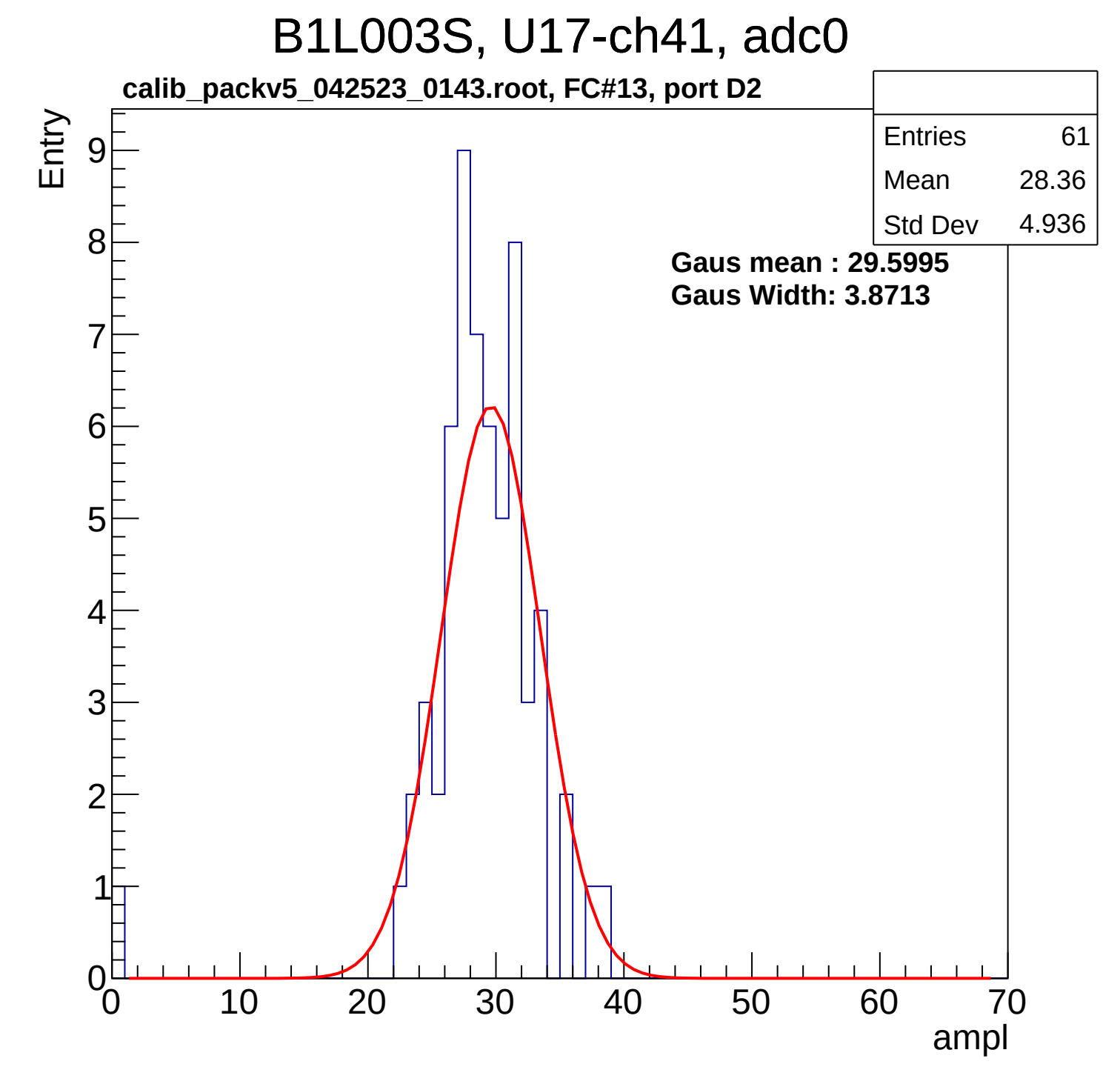
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch41, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 28.36
Std Dev 4.936
Gaus mean : 29.5995
Gaus Width: 3.8713
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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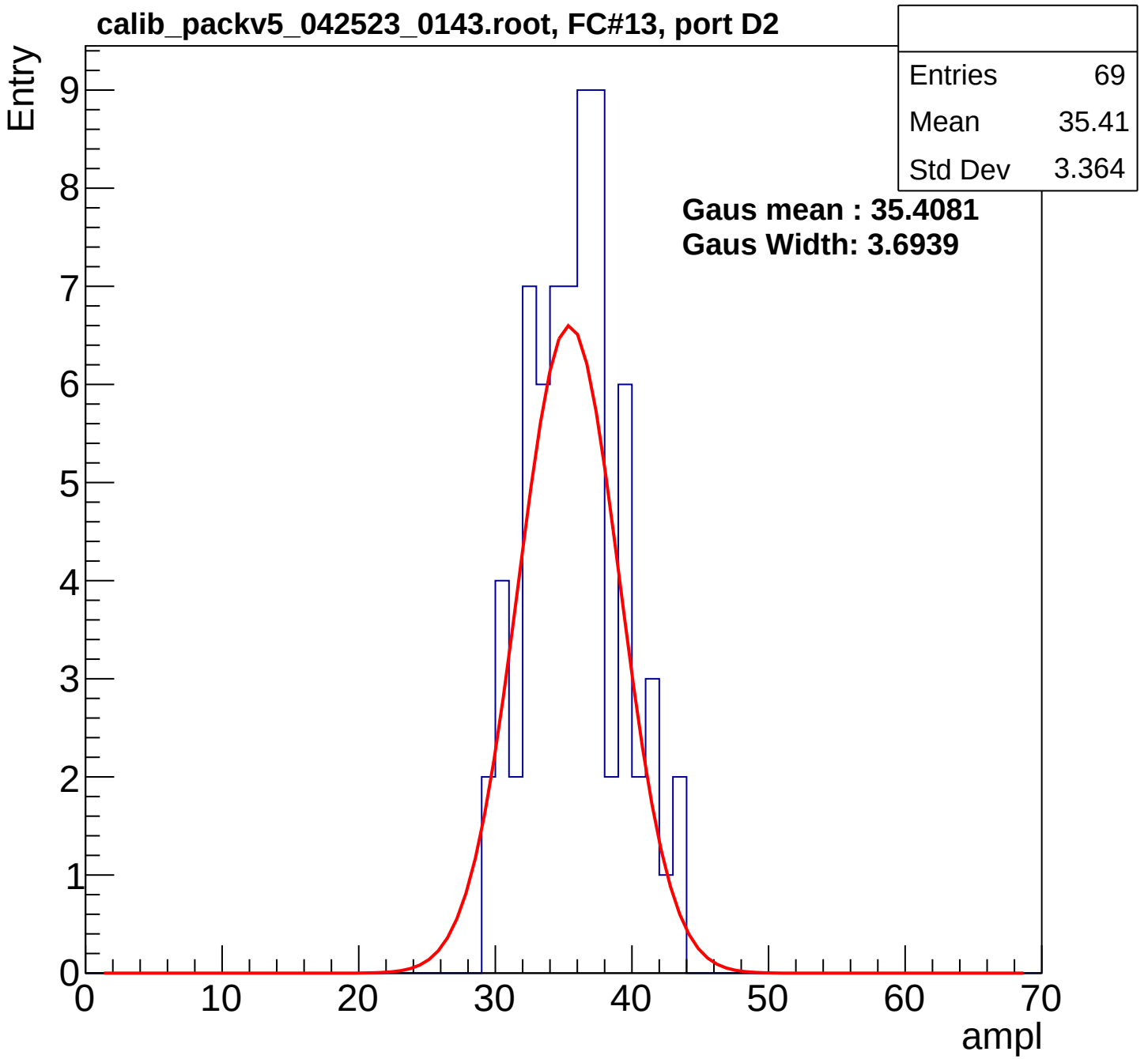
20
30
40
50
60
70


B1L003S, U17-ch41, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.41
Std Dev 3.364
Gaus mean : 35.4081
Gaus Width: 3.6939
Entry
ampl
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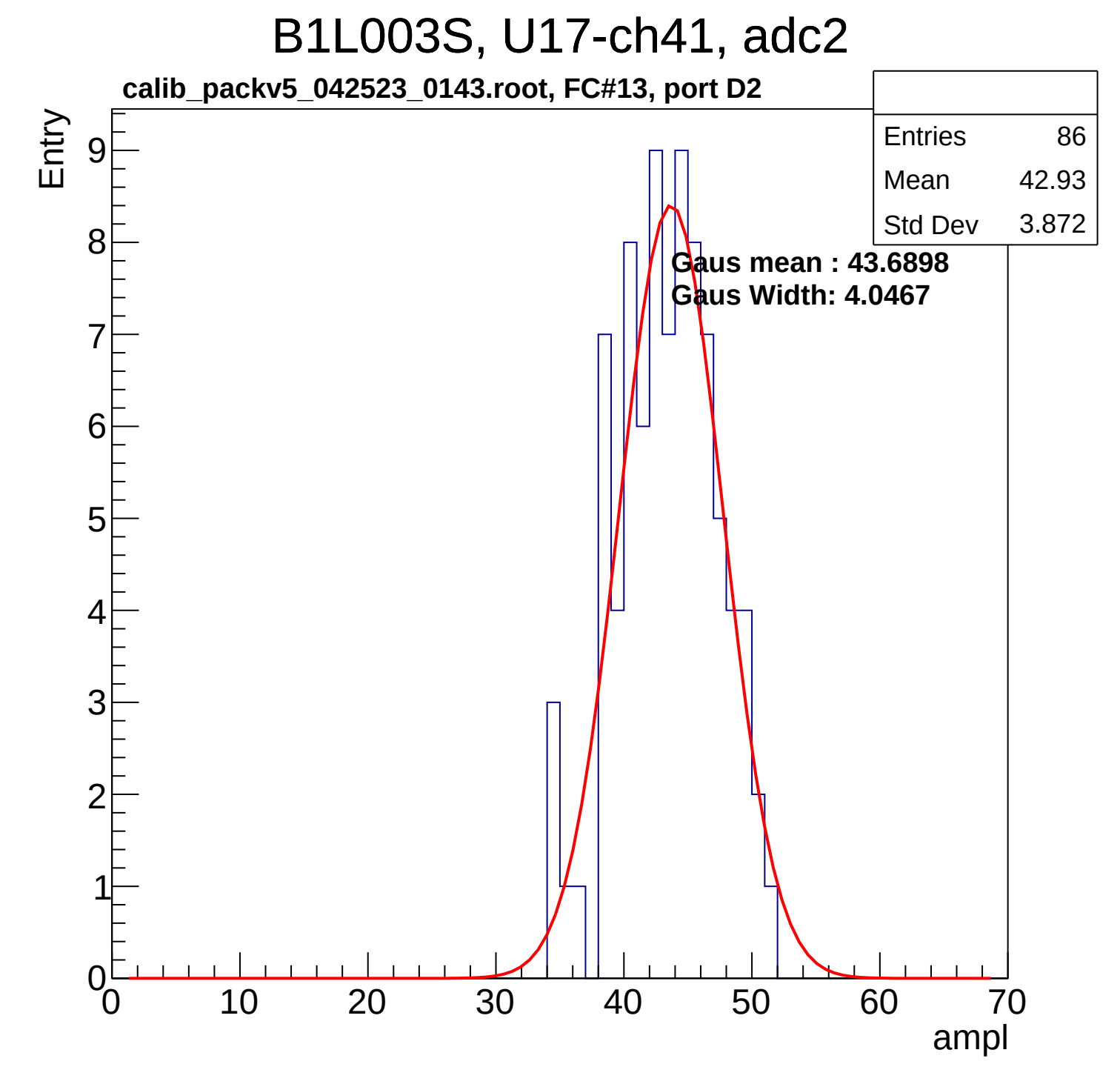
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch41, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 42.93
Std Dev 3.872
Gaus mean : 43.6898
Gaus Width: 4.0467
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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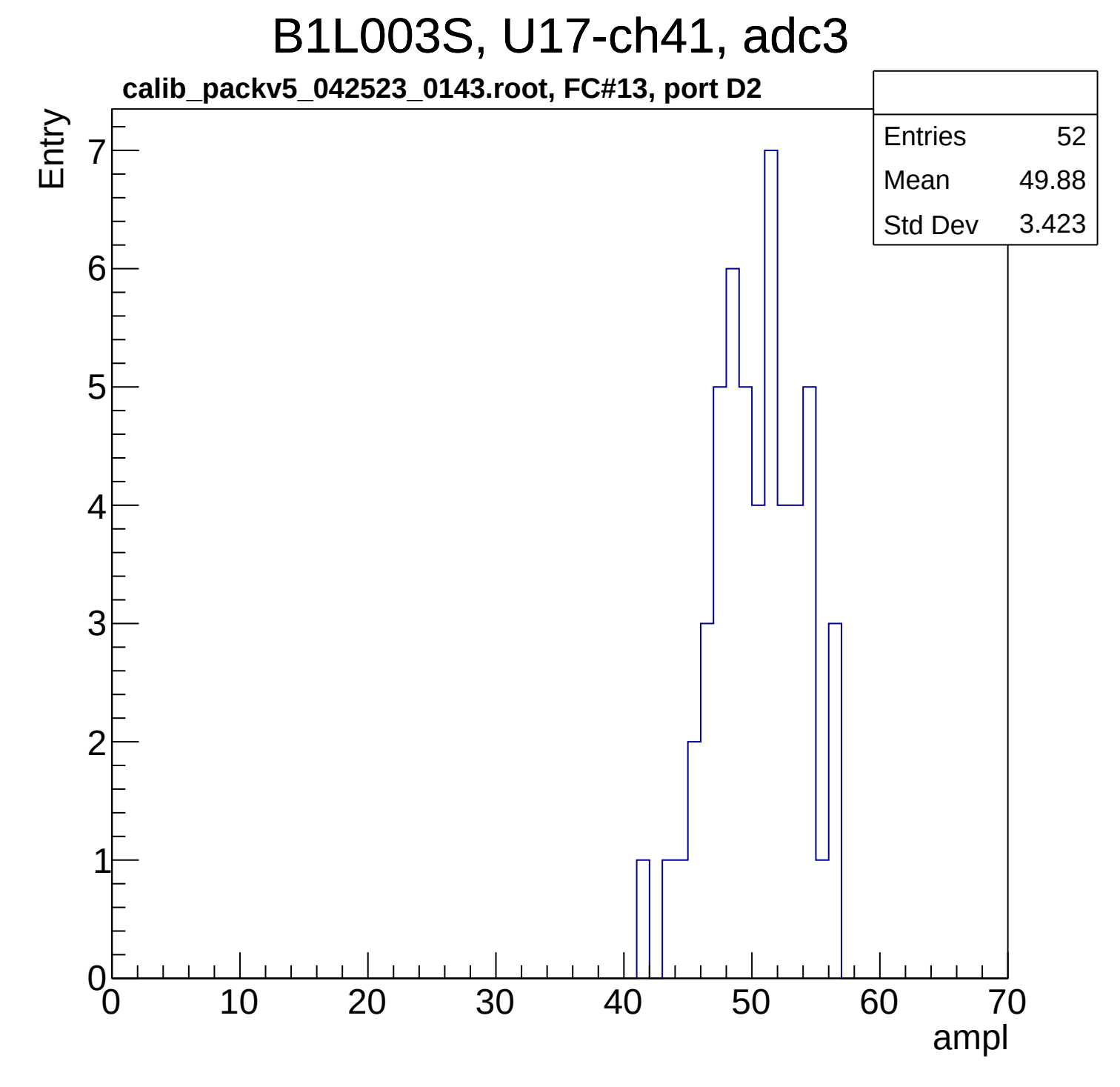
B1L003S, U17-ch41, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 49.88
Std Dev 3.423
Entry
ampl
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1
2
3
4
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0
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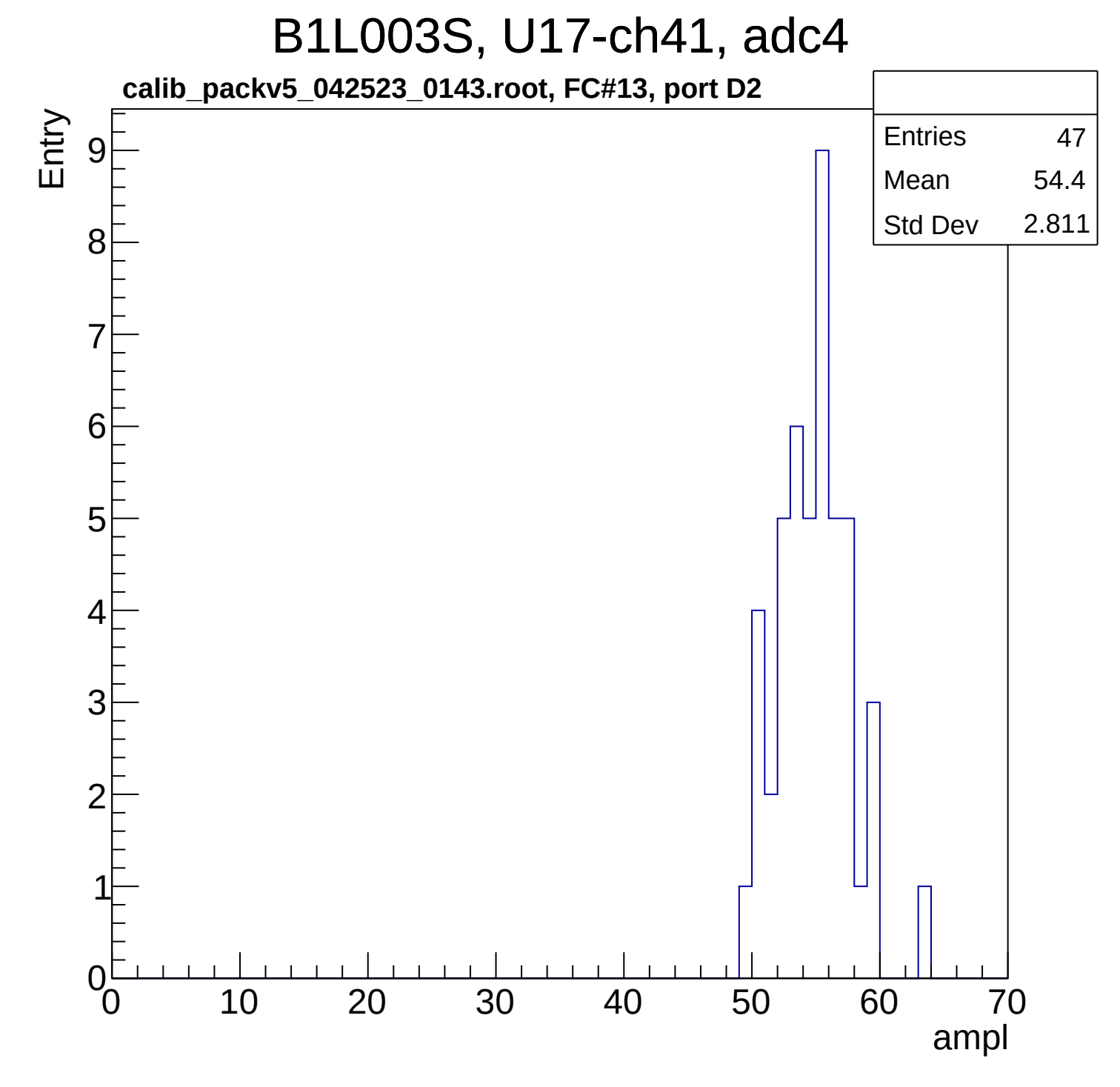
20
30
40
50
60
70


B1L003S, U17-ch41, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.4
Std Dev 2.811
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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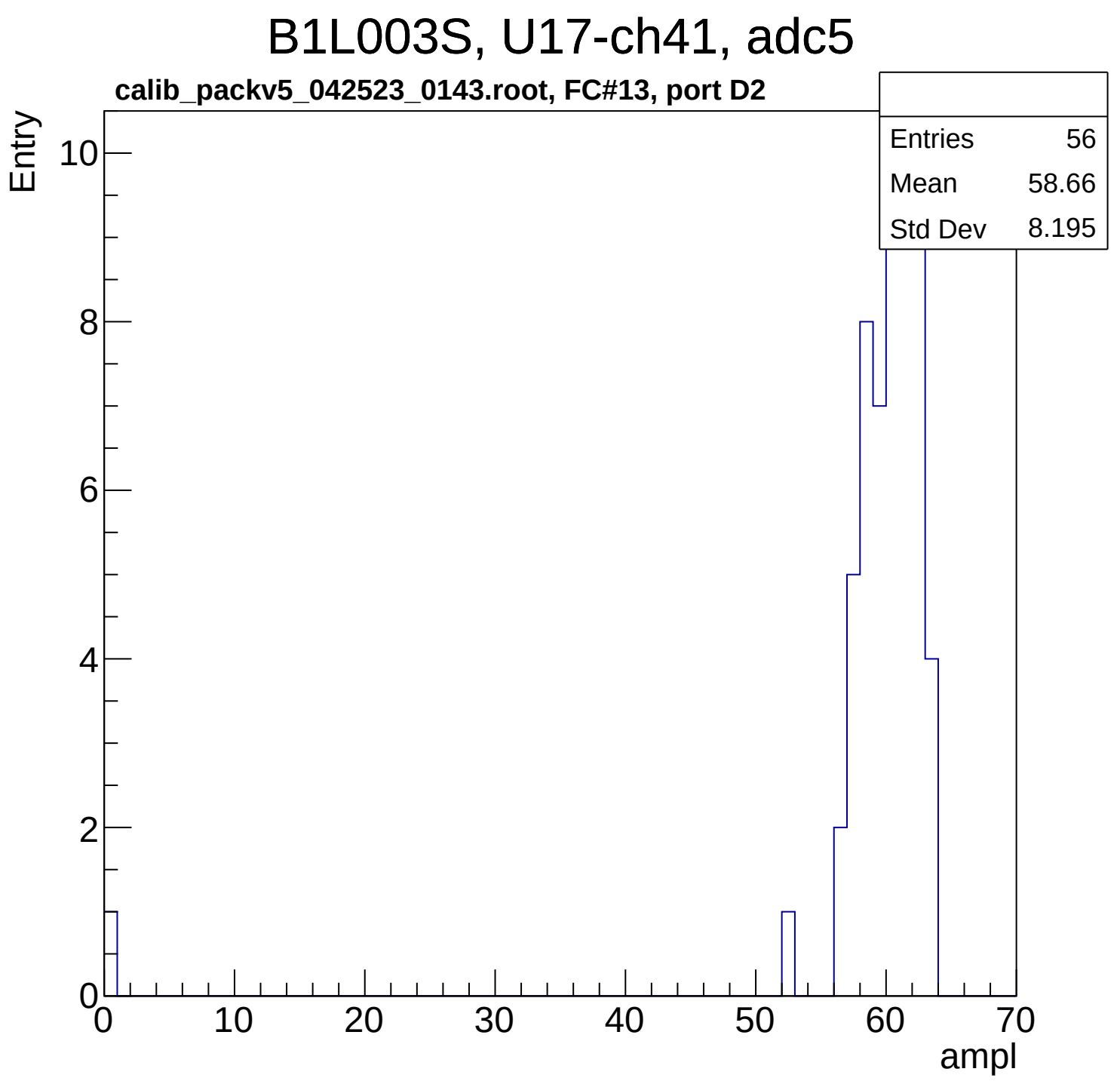
B1L003S, U17-ch41, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.66
Std Dev 8.195
Entry
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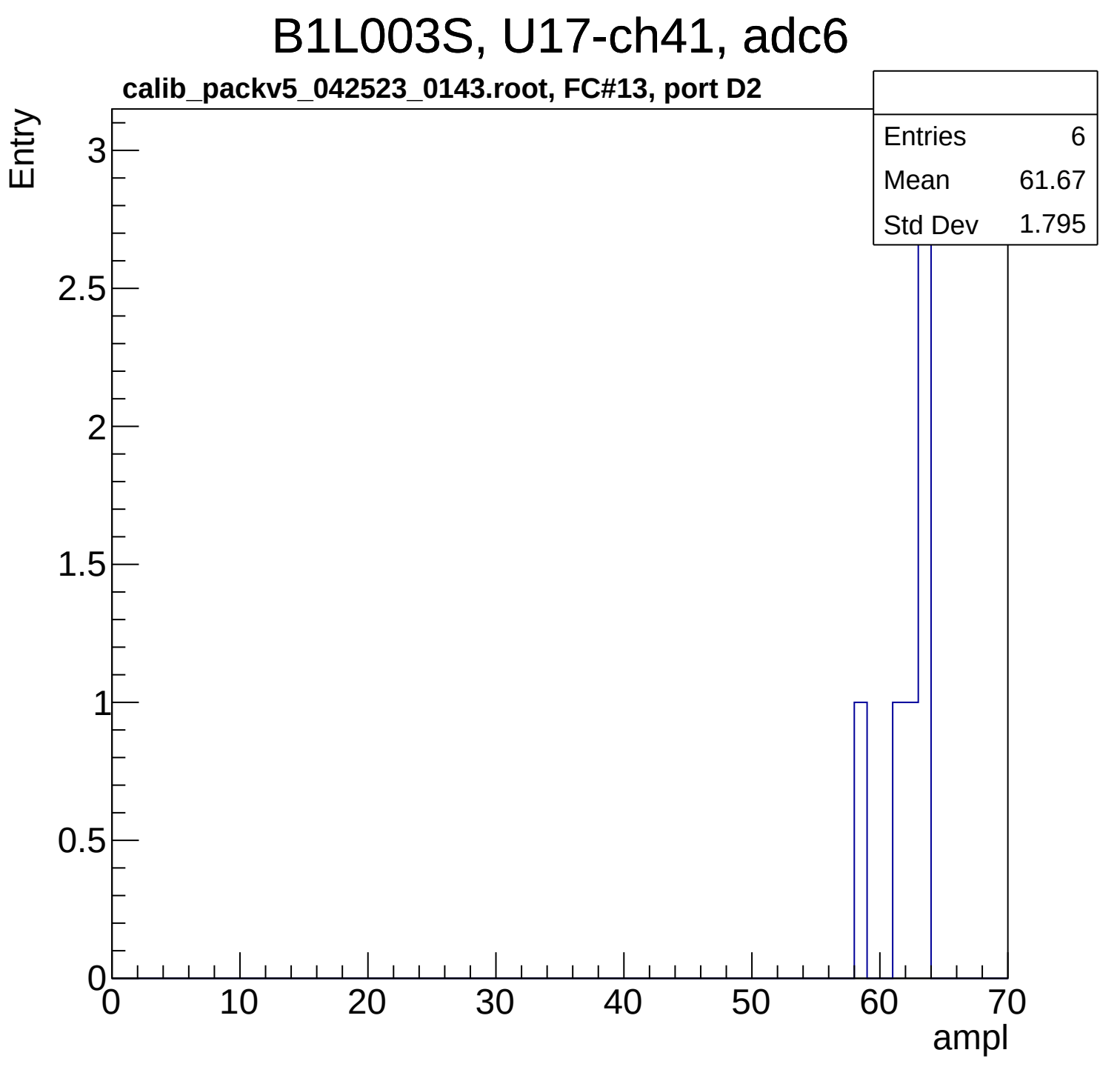
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch41, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.67
Std Dev 1.795
Entry
ampl
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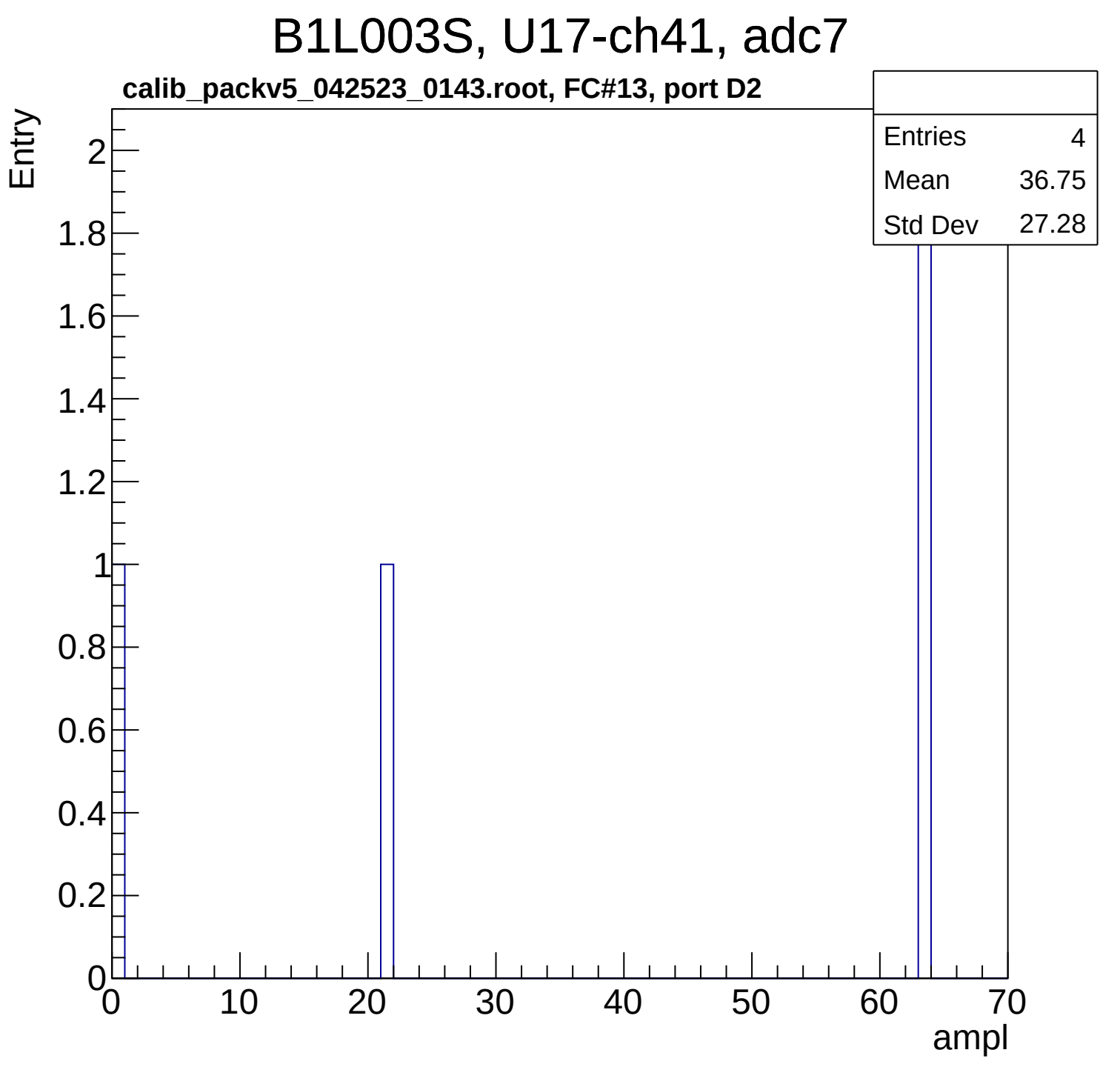
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch41, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 36.75
Std Dev 27.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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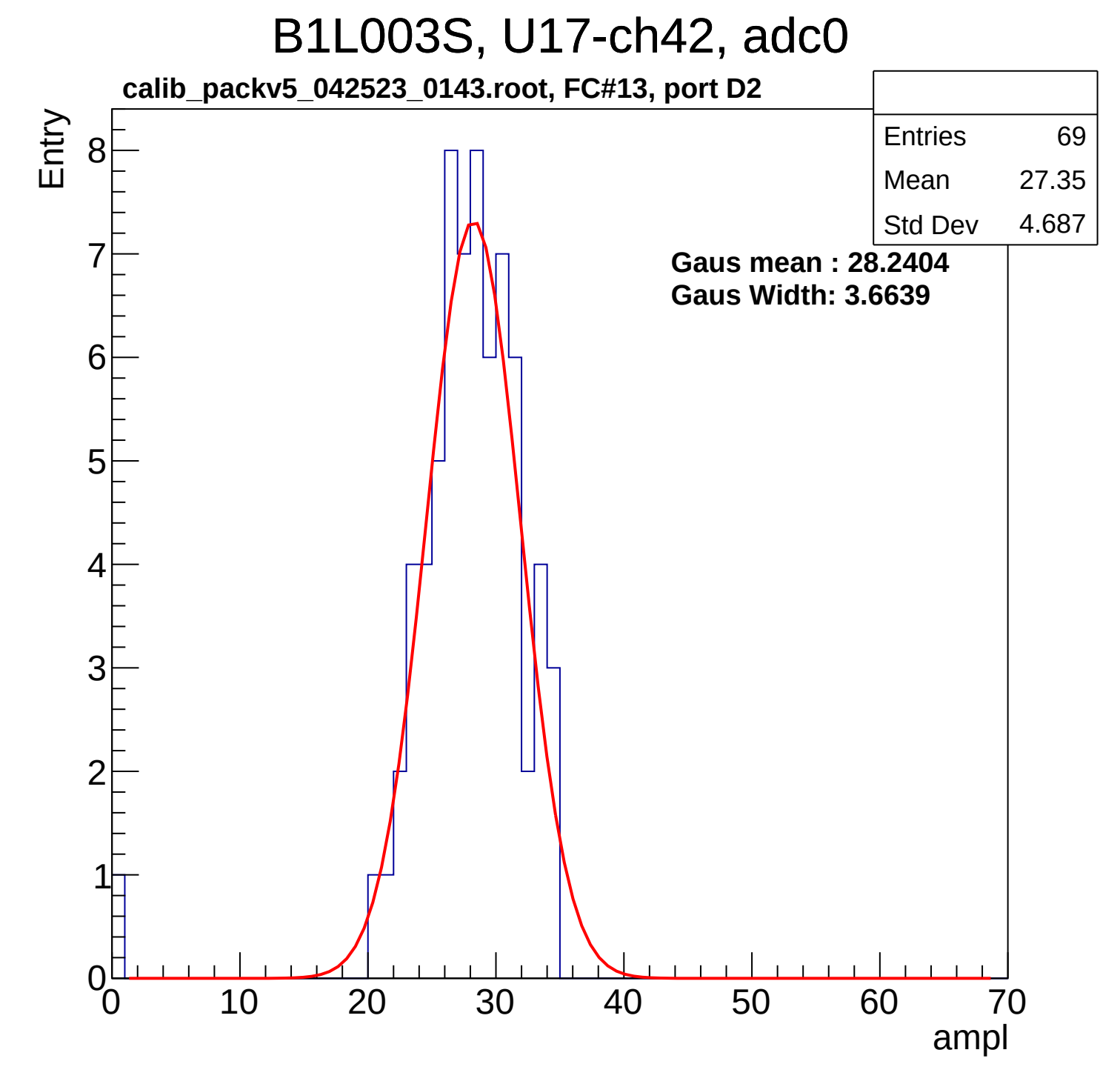
B1L003S, U17-ch42, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 27.35
Std Dev 4.687
Gaus mean : 28.2404
Gaus Width: 3.6639
Entry
ampl
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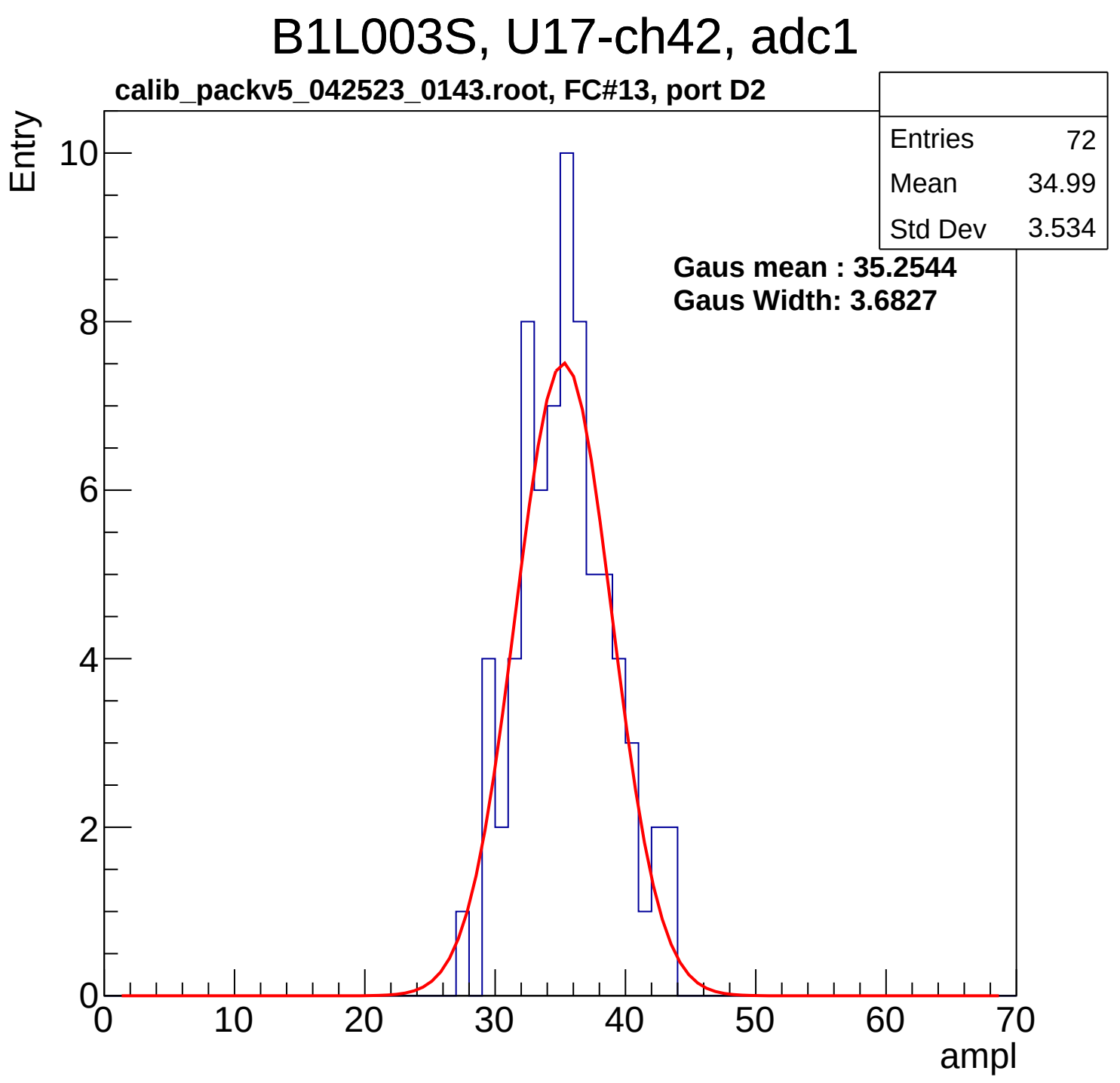
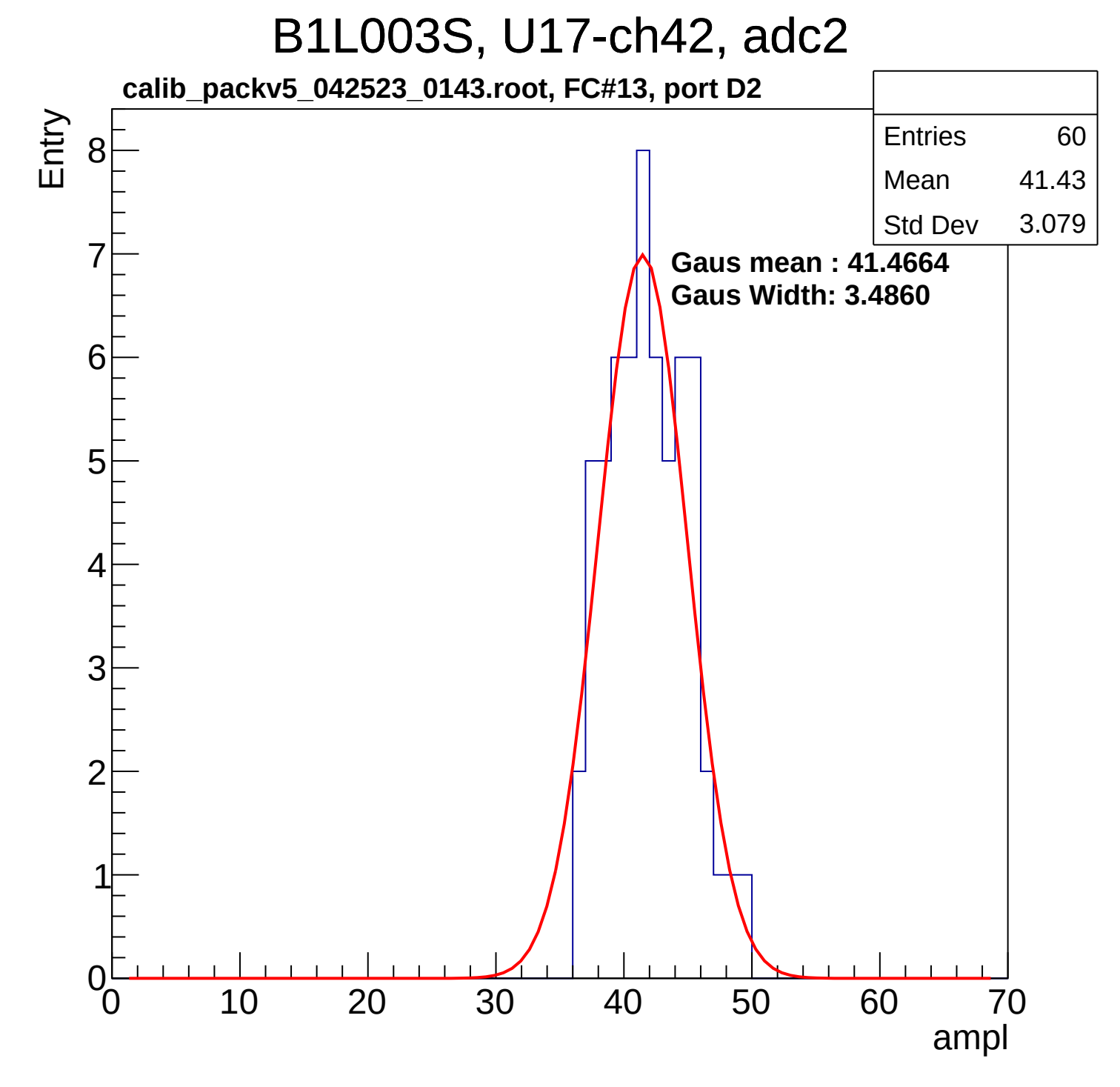
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch42, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 34.99
Std Dev 3.534
Gaus mean : 35.2544
Gaus Width: 3.6827
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch42, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.43
Std Dev 3.079
Gaus mean : 41.4664
Gaus Width: 3.4860
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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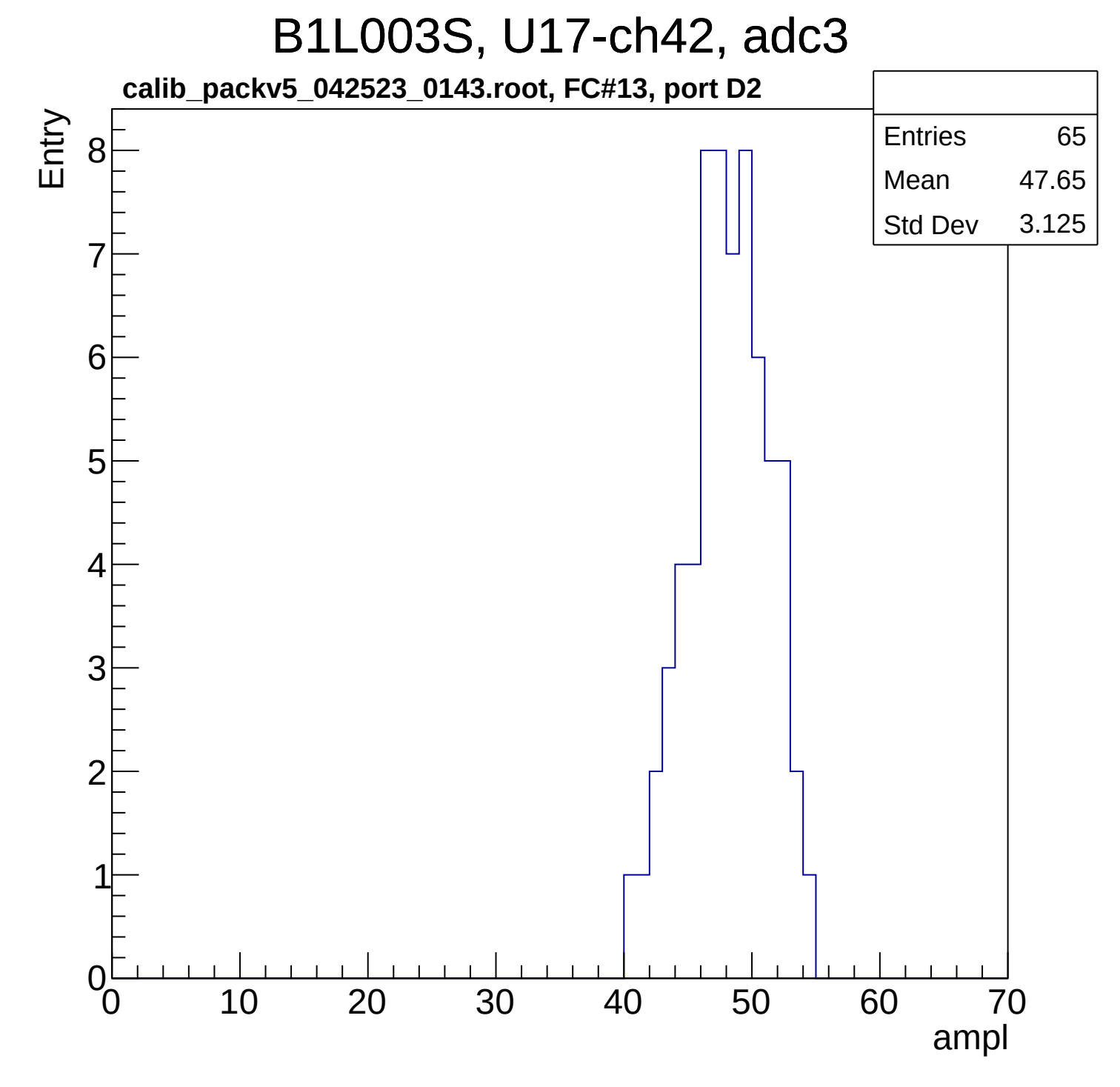
B1L003S, U17-ch42, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.65
Std Dev 3.125
Entry
ampl
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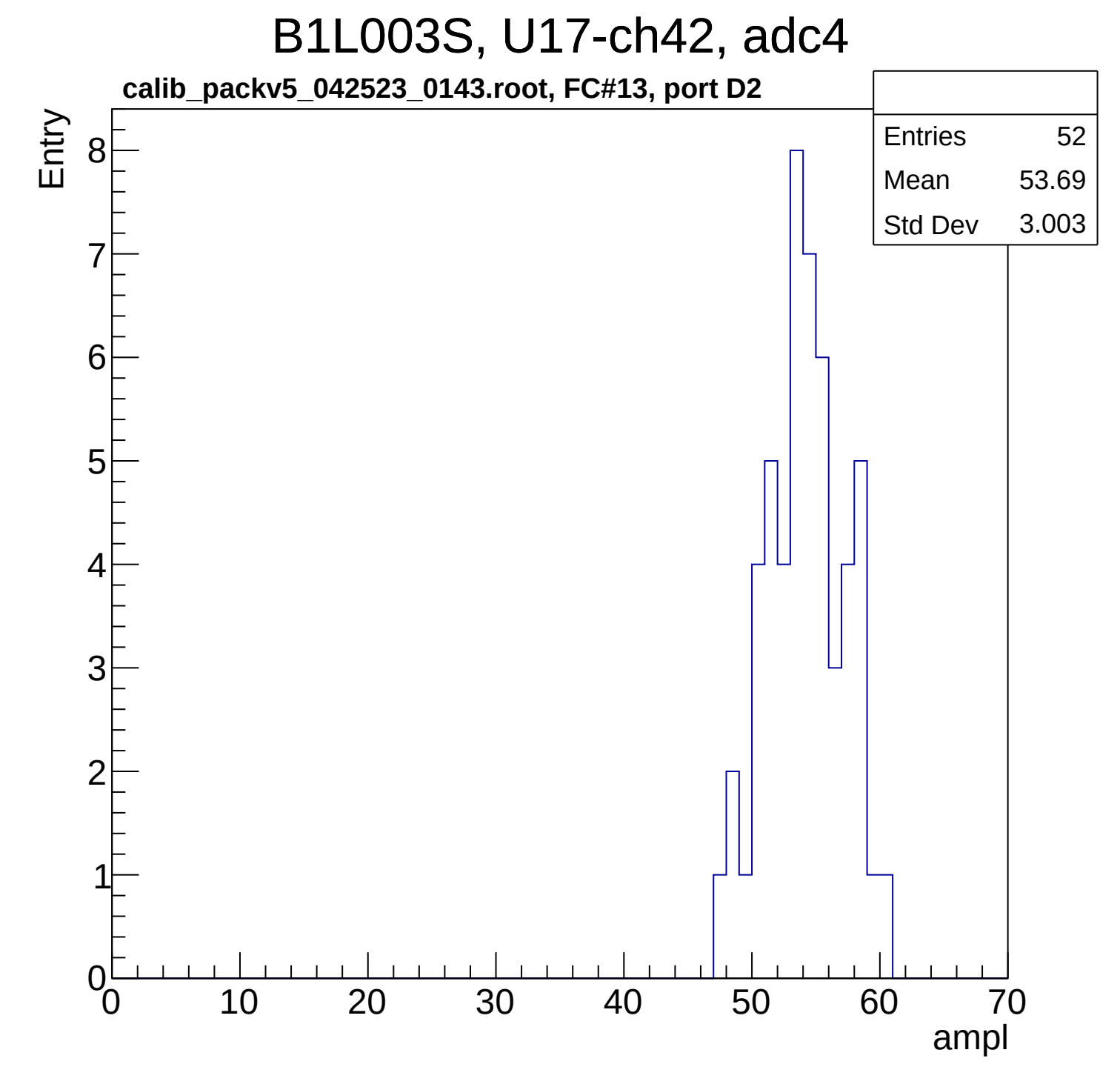
B1L003S, U17-ch42, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 53.69
Std Dev 3.003
Entry
ampl
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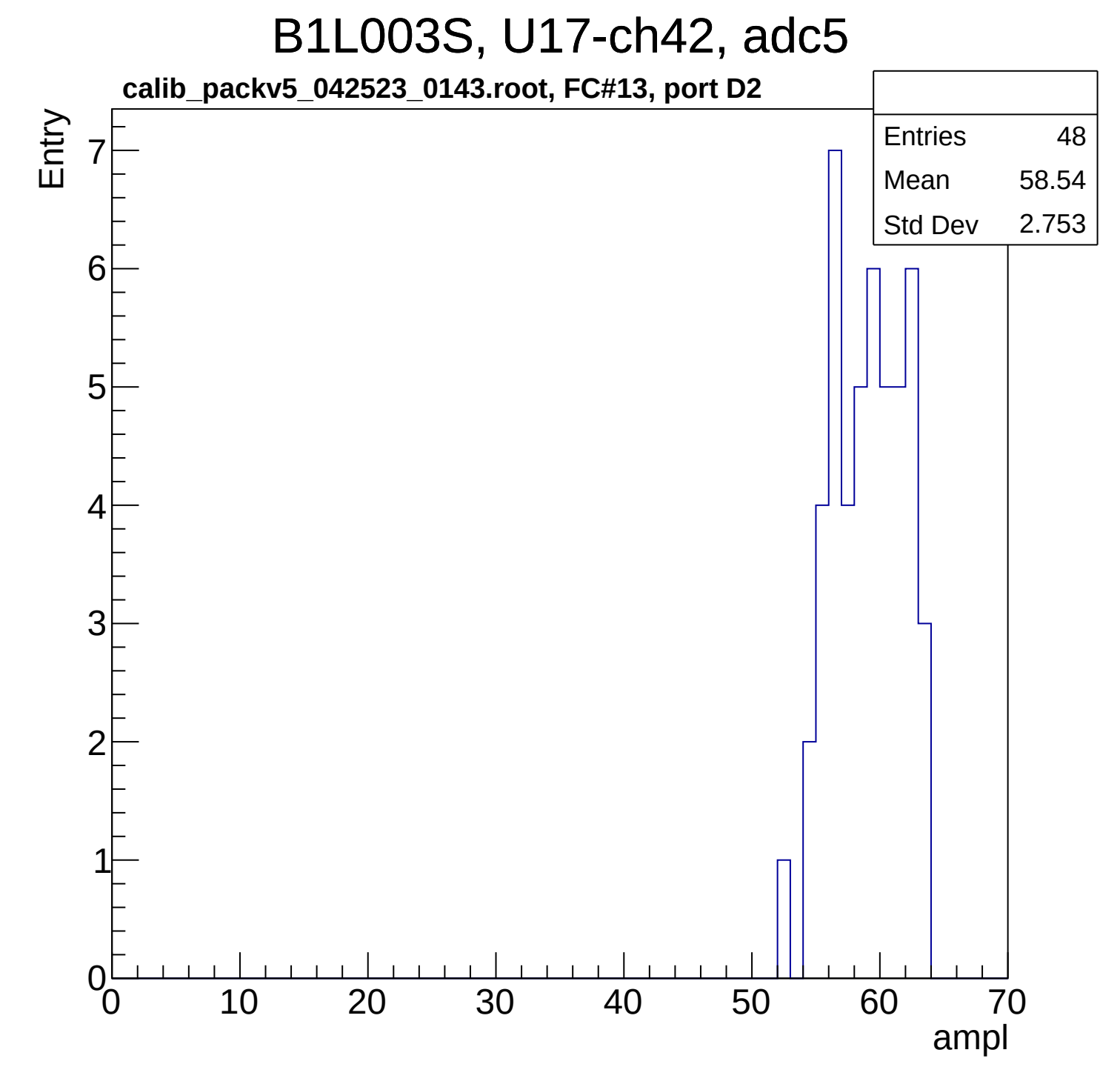
B1L003S, U17-ch42, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.54
Std Dev 2.753
Entry
ampl
0
1
2
3
4
5
6
7
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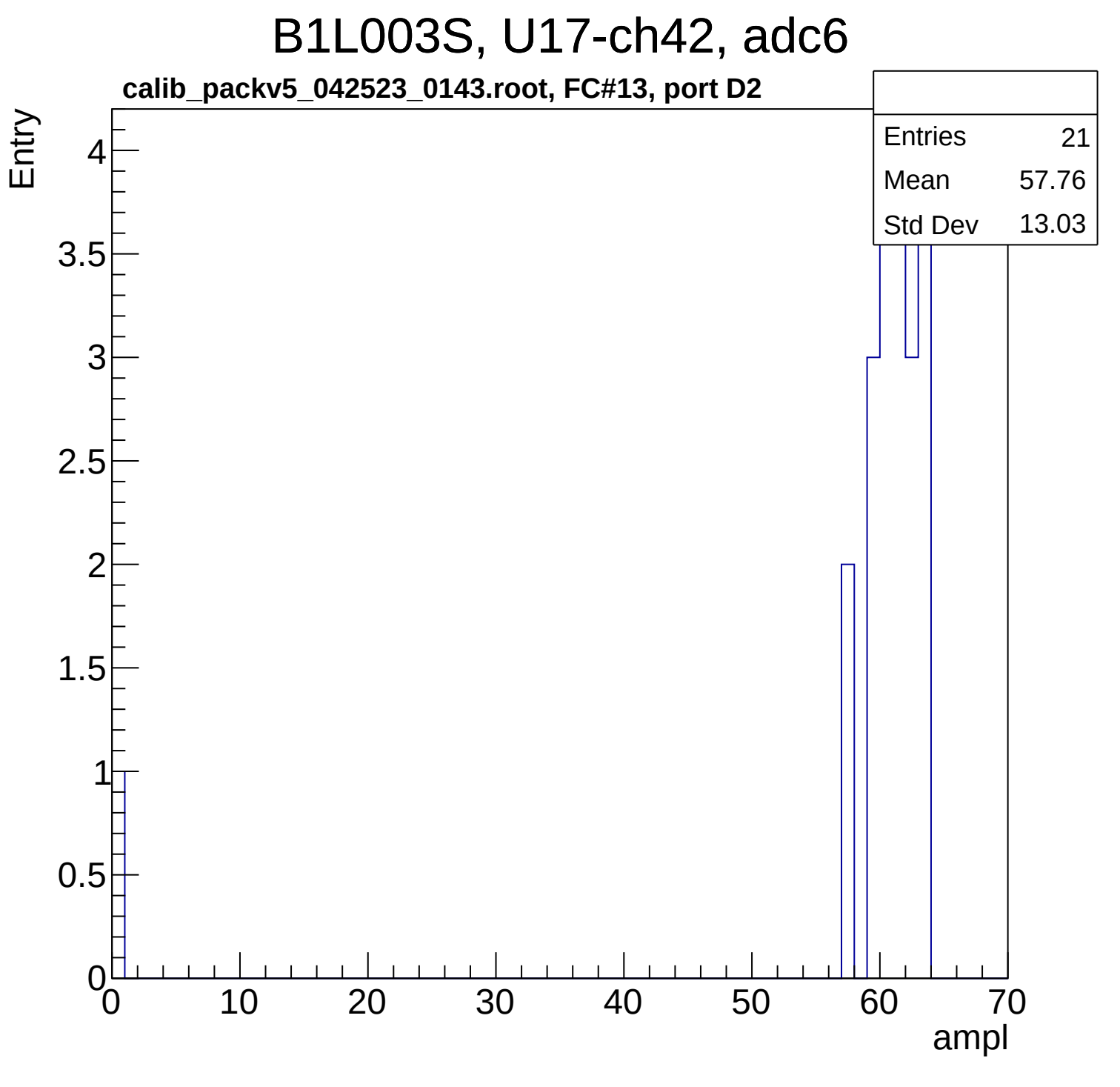
B1L003S, U17-ch42, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 57.76
Std Dev 13.03
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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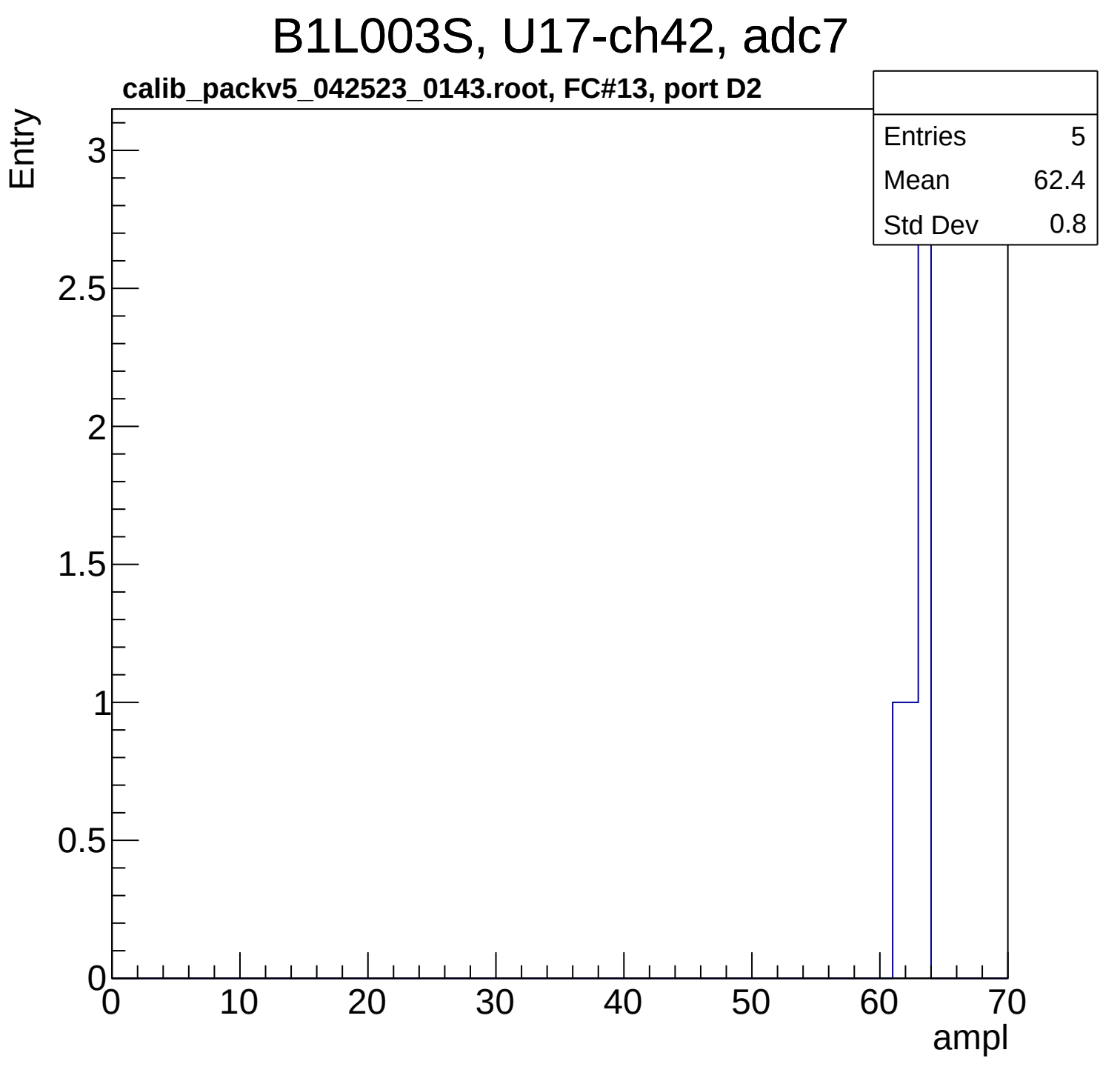
B1L003S, U17-ch42, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
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10
20
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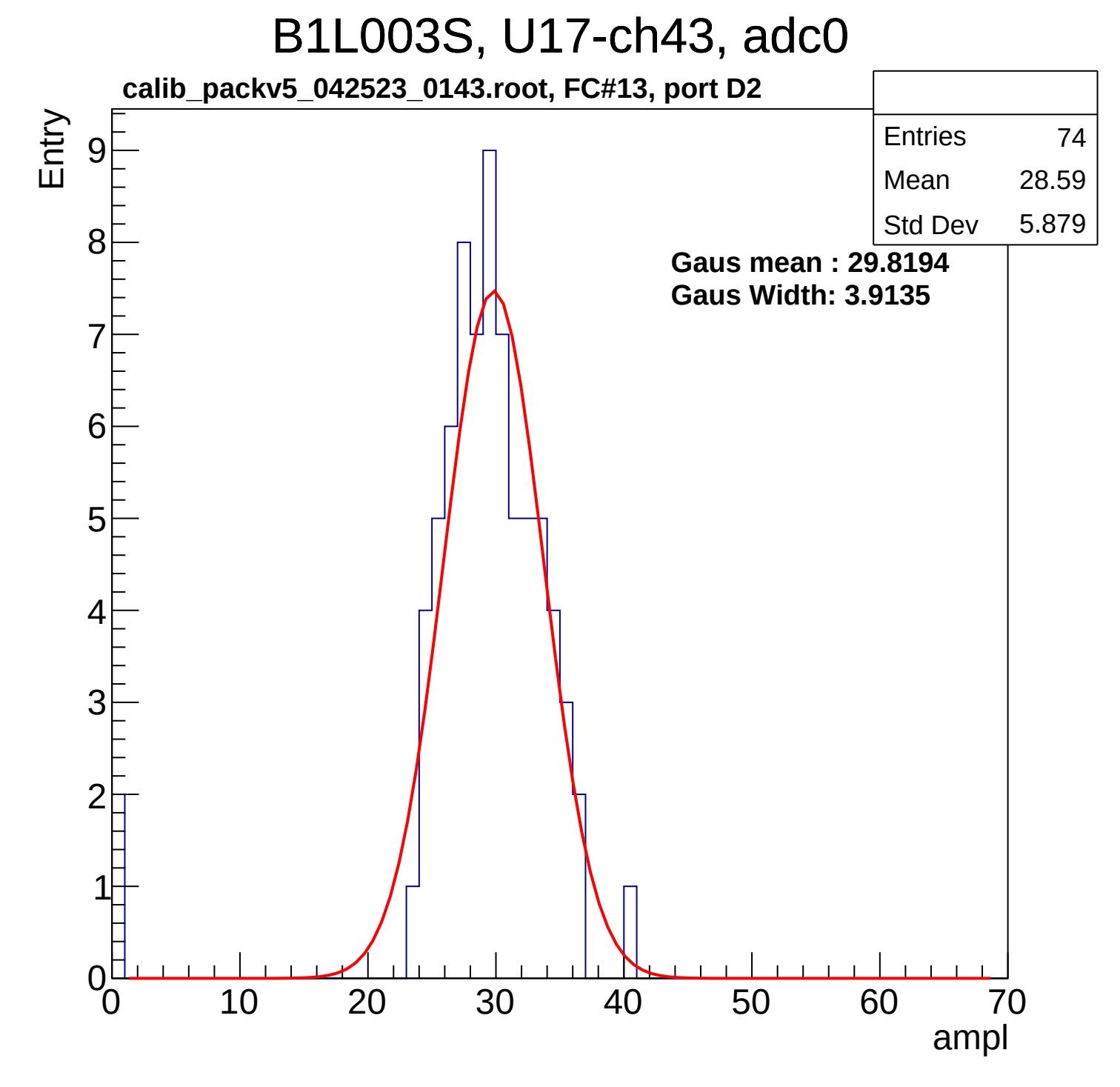
50
60
70
ampl


B1L003S, U17-ch43, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 28.59
Std Dev 5.879
Gaus mean : 29.8194
Gaus Width: 3.9135
Entry
ampl
0
10
20
30
40
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60
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1
2
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6
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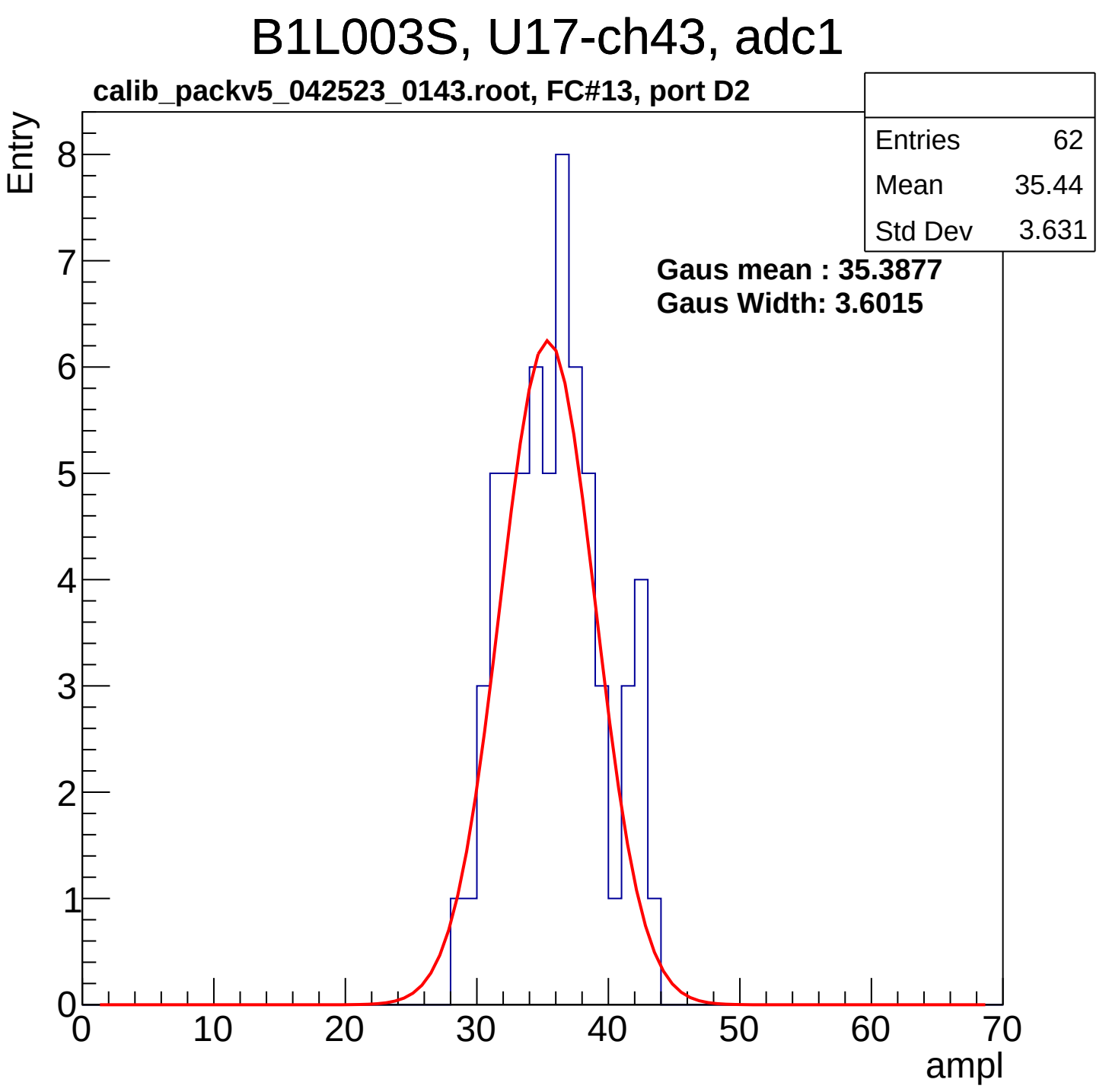
8
9


B1L003S, U17-ch43, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.44
Std Dev 3.631
Gaus mean : 35.3877
Gaus Width: 3.6015
Entry
ampl
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1
2
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5
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7
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0
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40
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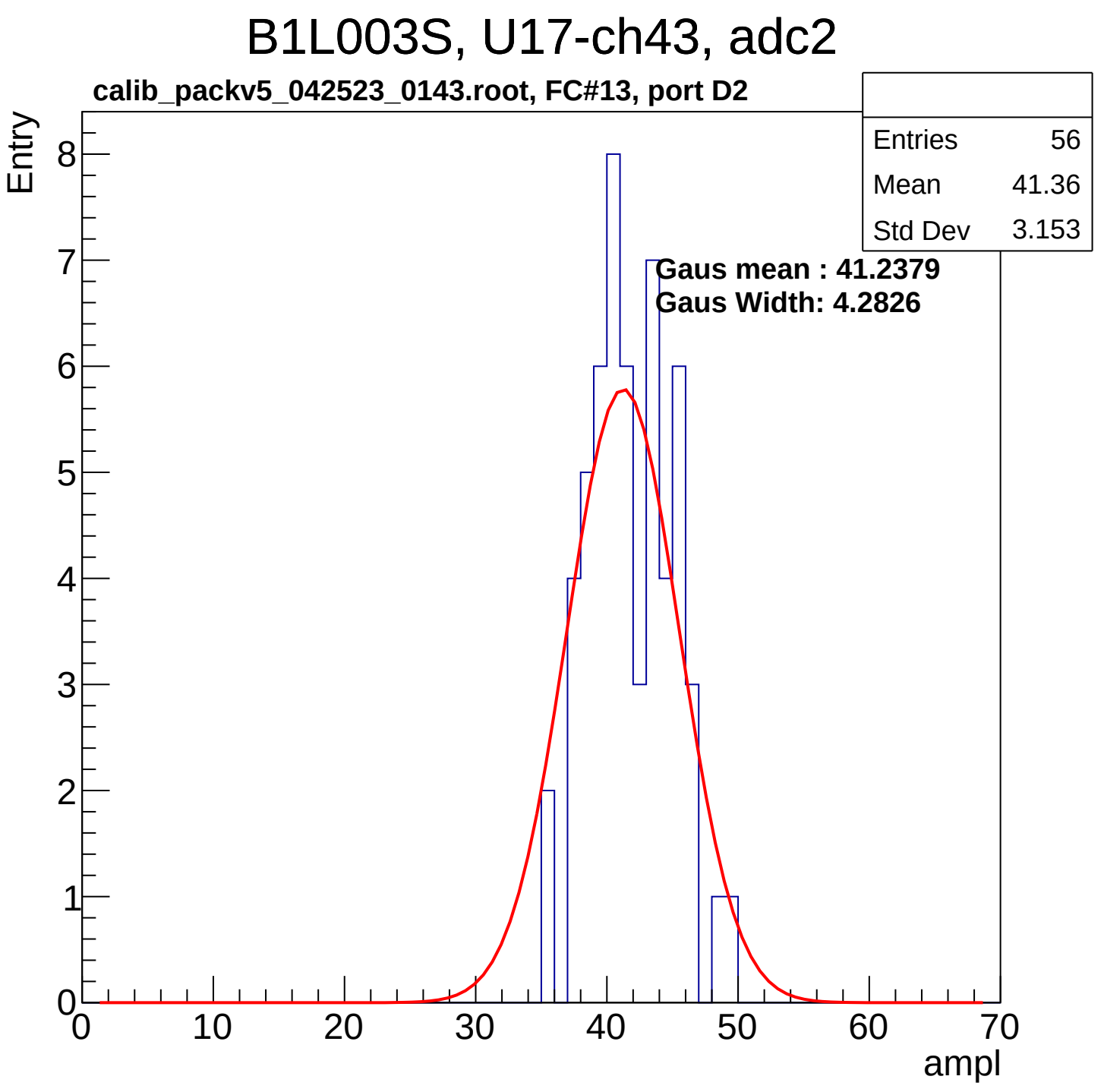
B1L003S, U17-ch43, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 41.36
Std Dev 3.153
Gaus mean : 41.2379
Gaus Width: 4.2826
Entry
ampl
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5
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7
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0
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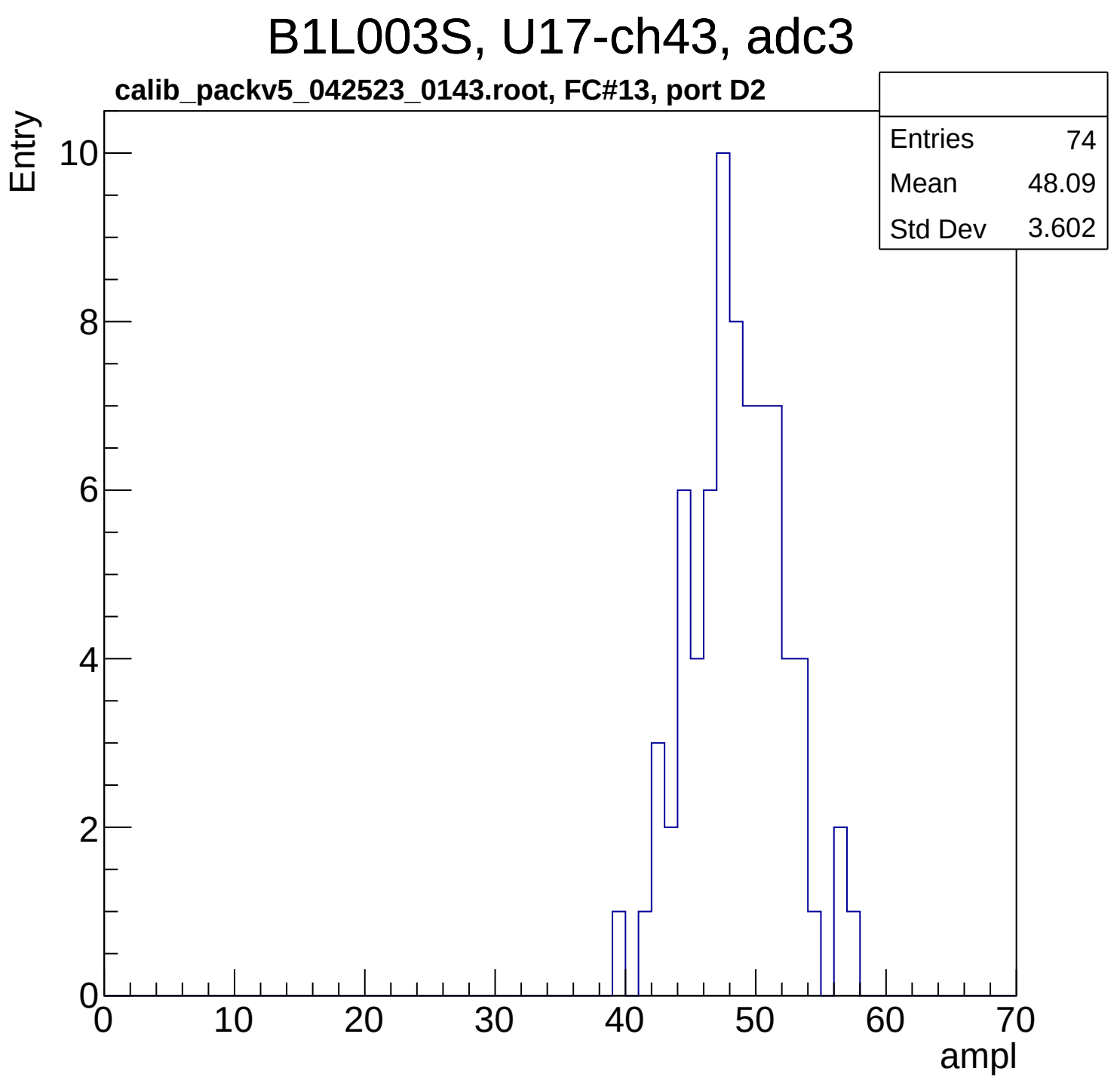
B1L003S, U17-ch43, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.09
Std Dev 3.602
Entry
0
2
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6
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10
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10
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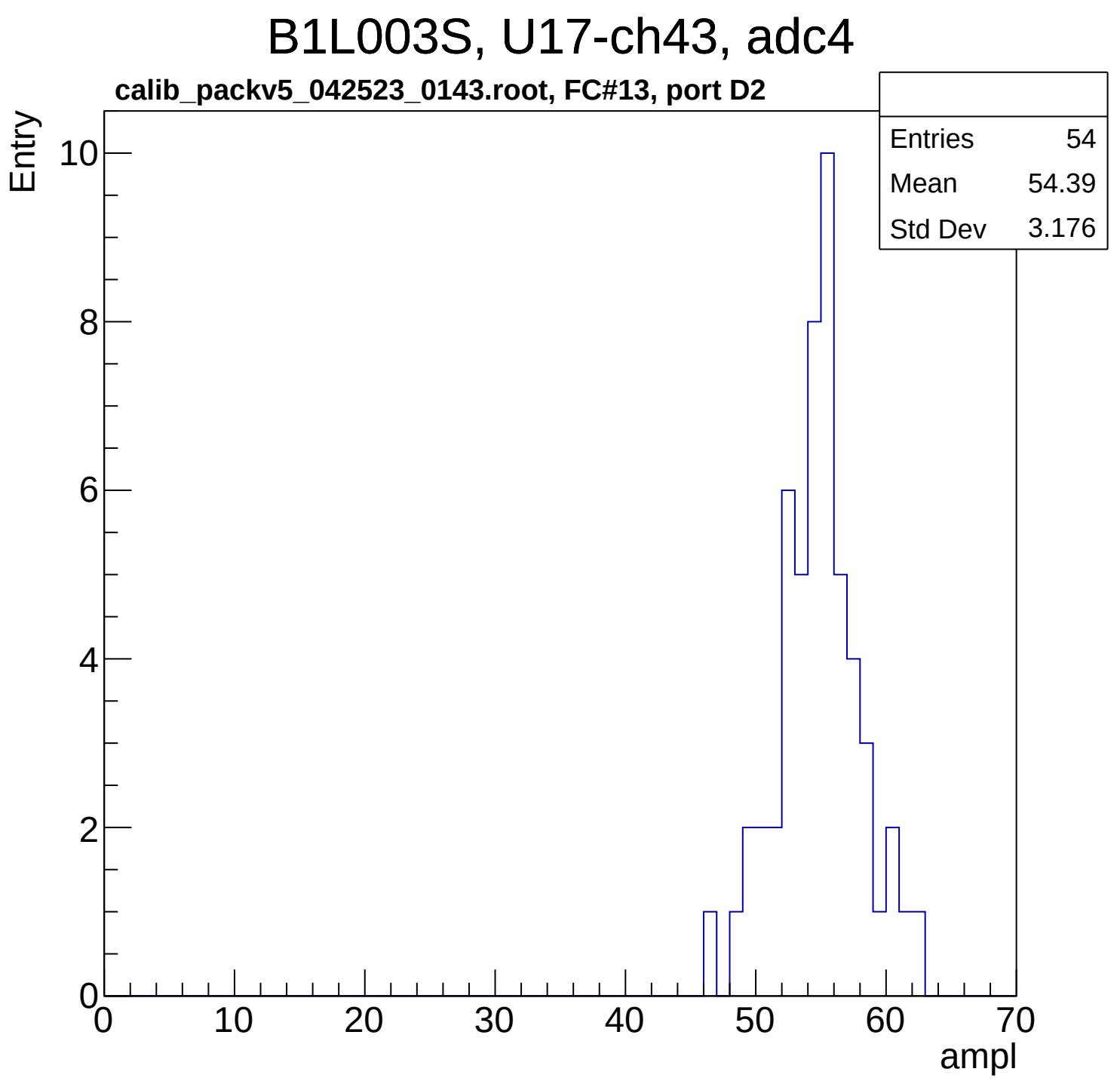
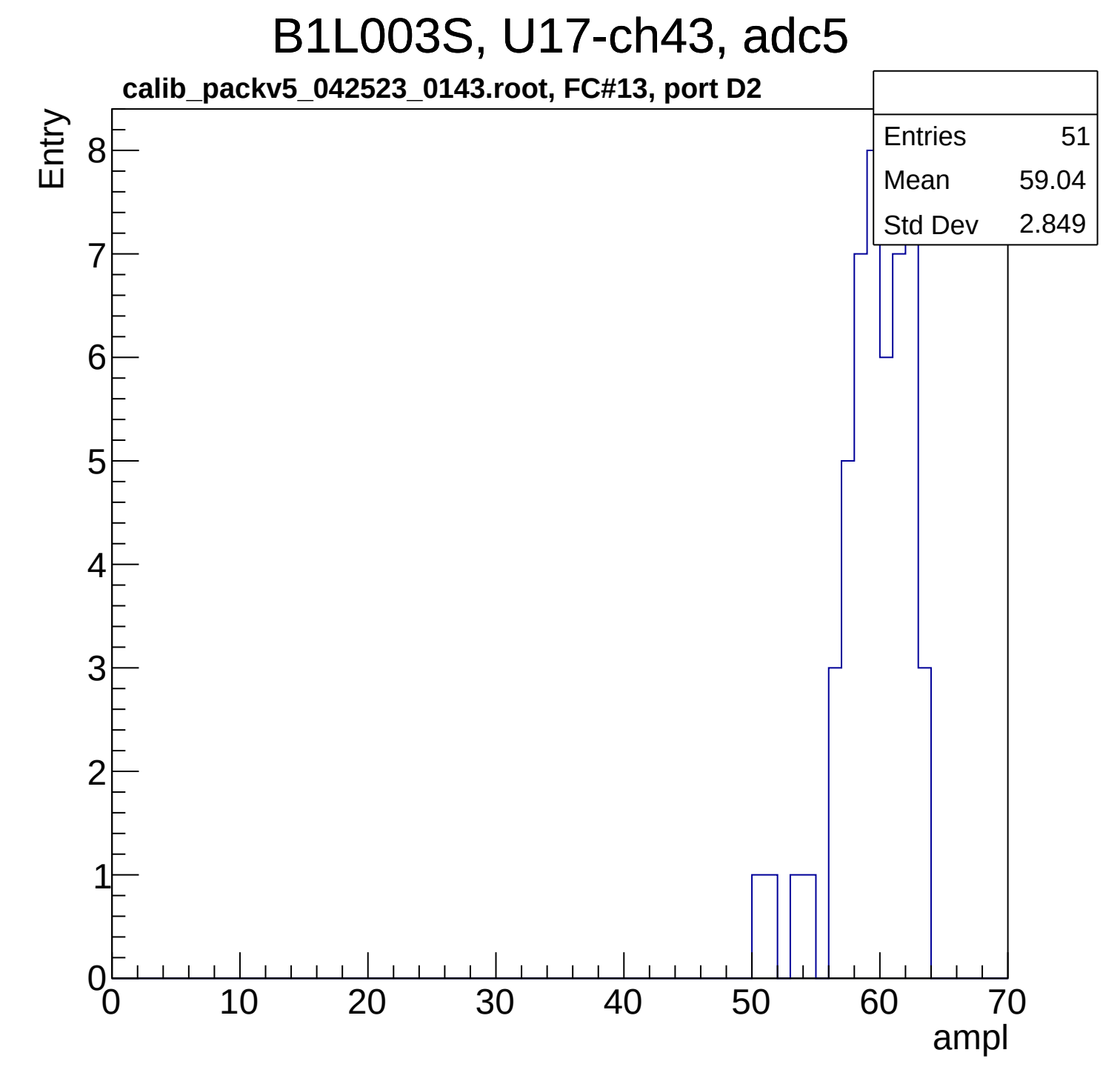
50
60
70
ampl


B1L003S, U17-ch43, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.39
Std Dev 3.176
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch43, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.04
Std Dev 2.849
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
50
60
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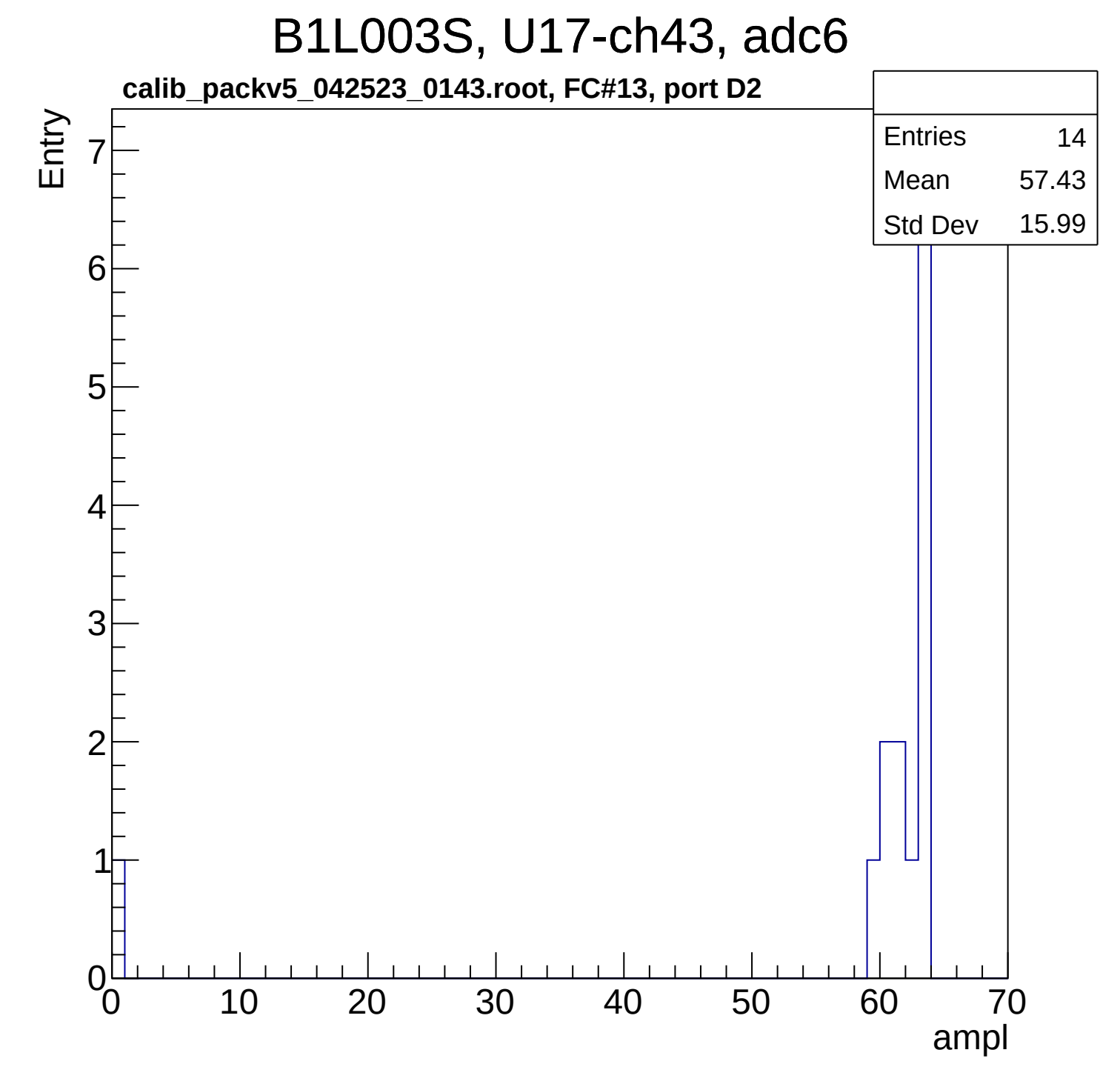
B1L003S, U17-ch43, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.43
Std Dev 15.99
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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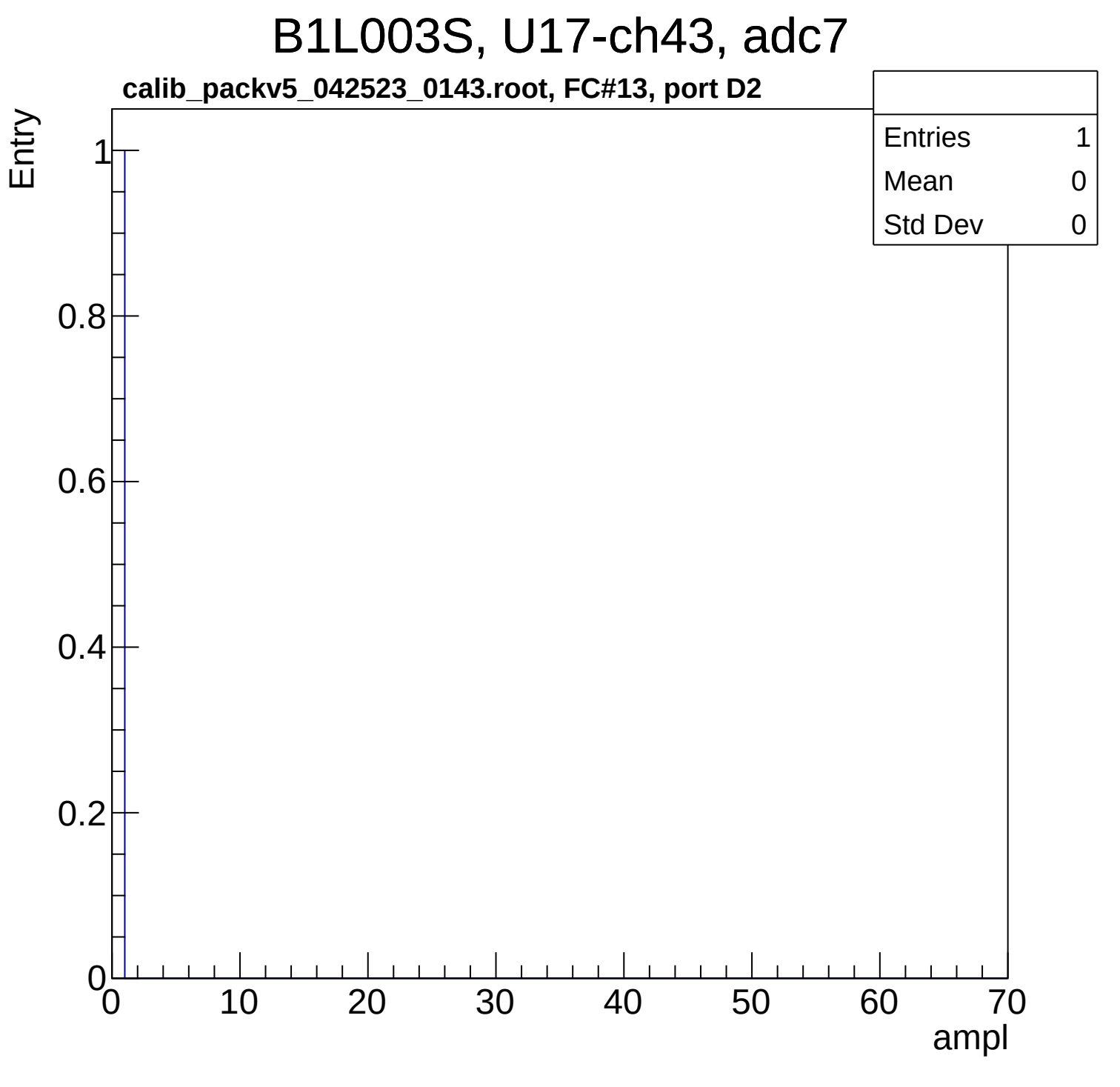
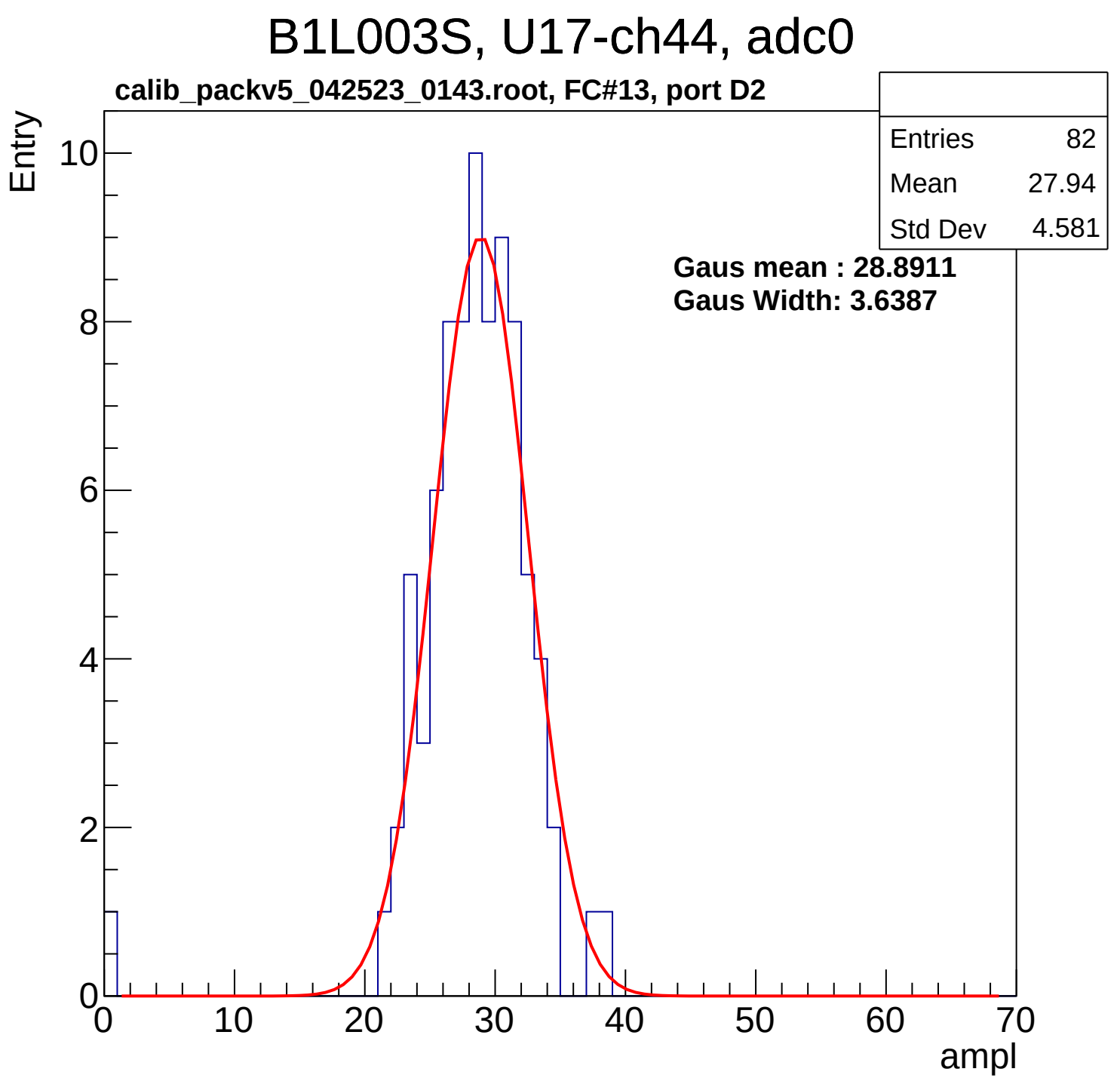
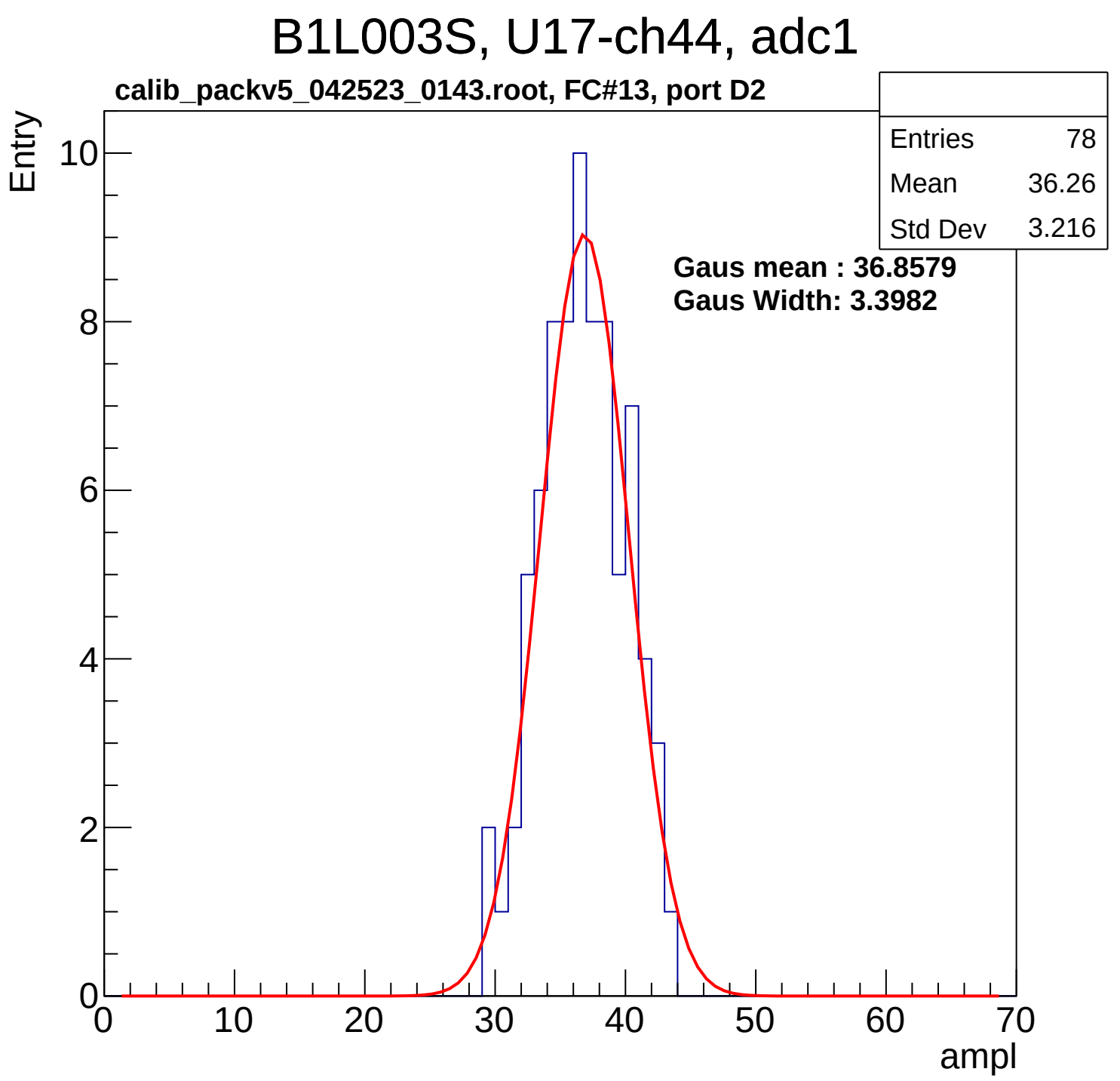

B1L003S, U17-ch43, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch44, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 27.94
Std Dev 4.581
Gaus mean : 28.8911
Gaus Width: 3.6387
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch44, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 36.26
Std Dev 3.216
Gaus mean : 36.8579
Gaus Width: 3.3982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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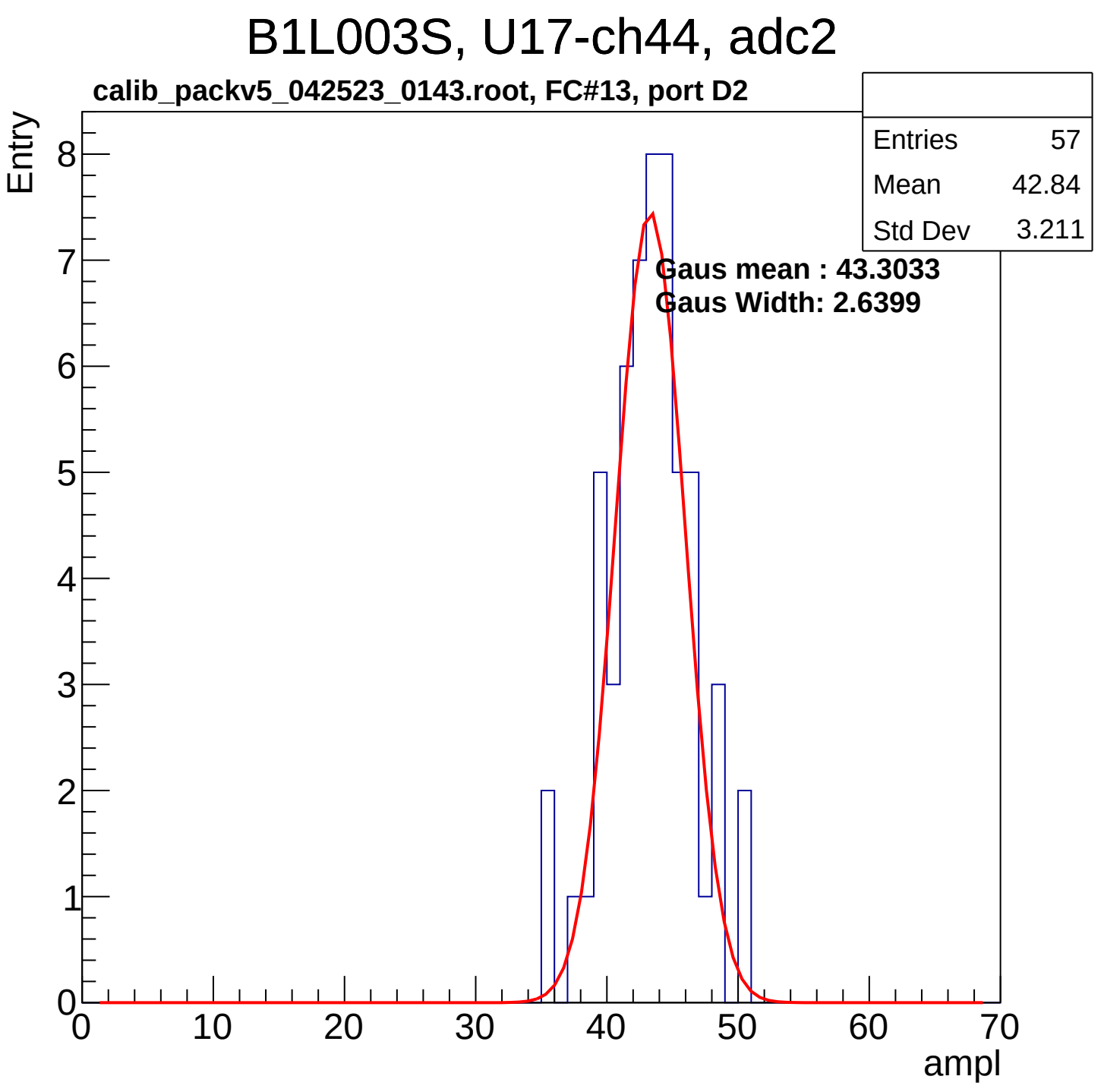
B1L003S, U17-ch44, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 42.84
Std Dev 3.211
Gaus mean : 43.3033
Gaus Width: 2.6399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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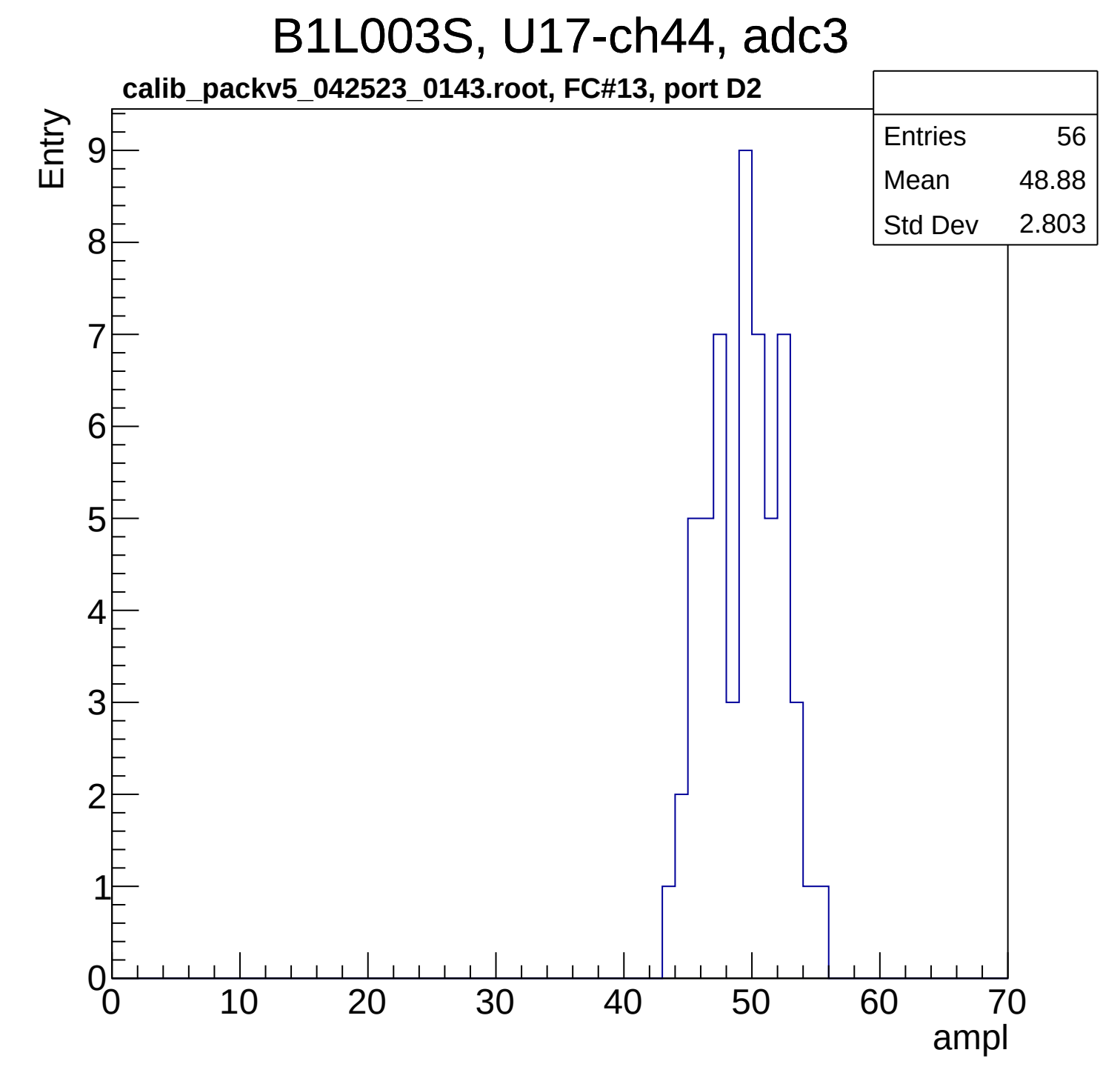
B1L003S, U17-ch44, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.88
Std Dev 2.803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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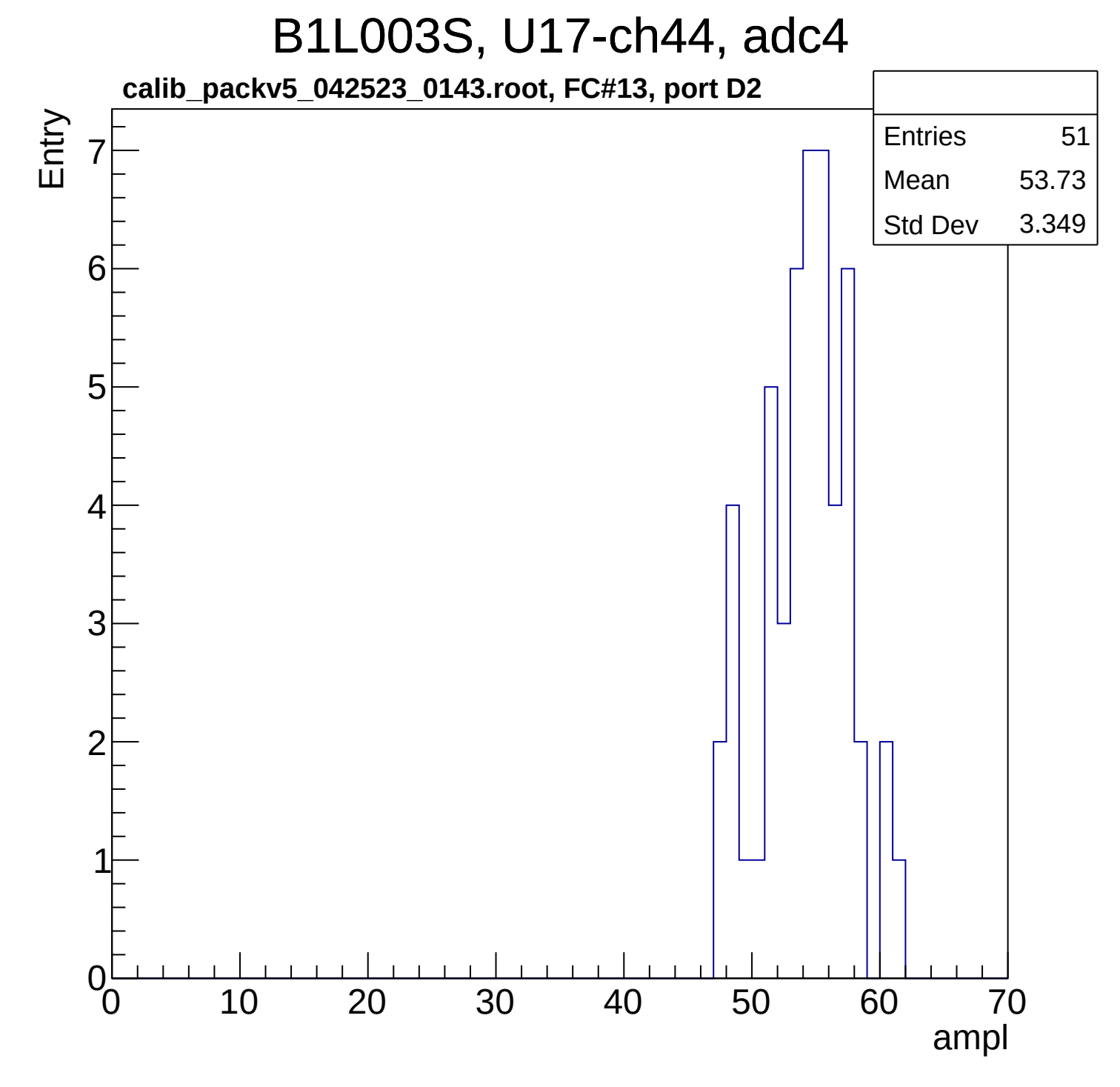
B1L003S, U17-ch44, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 53.73
Std Dev 3.349
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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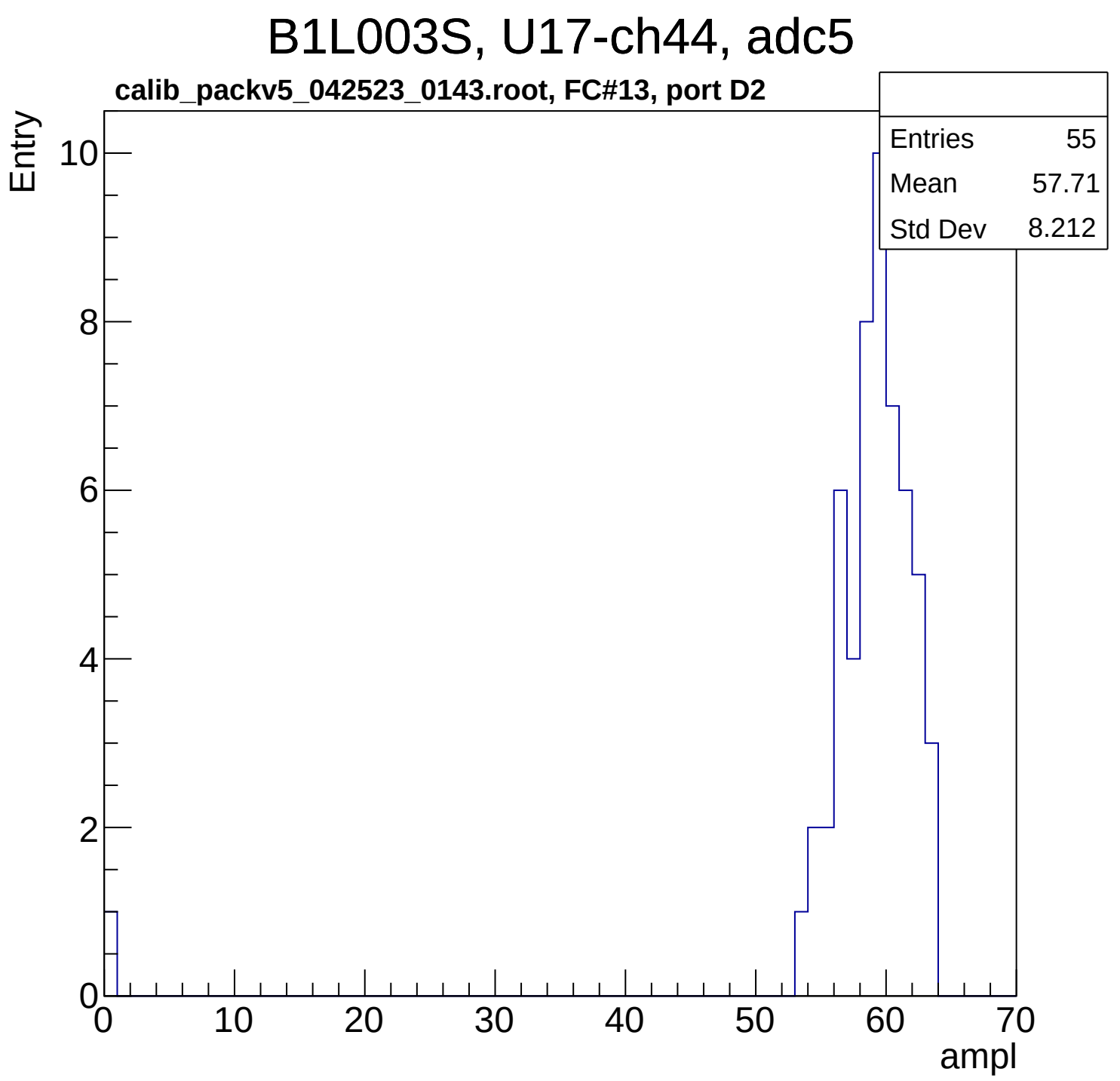
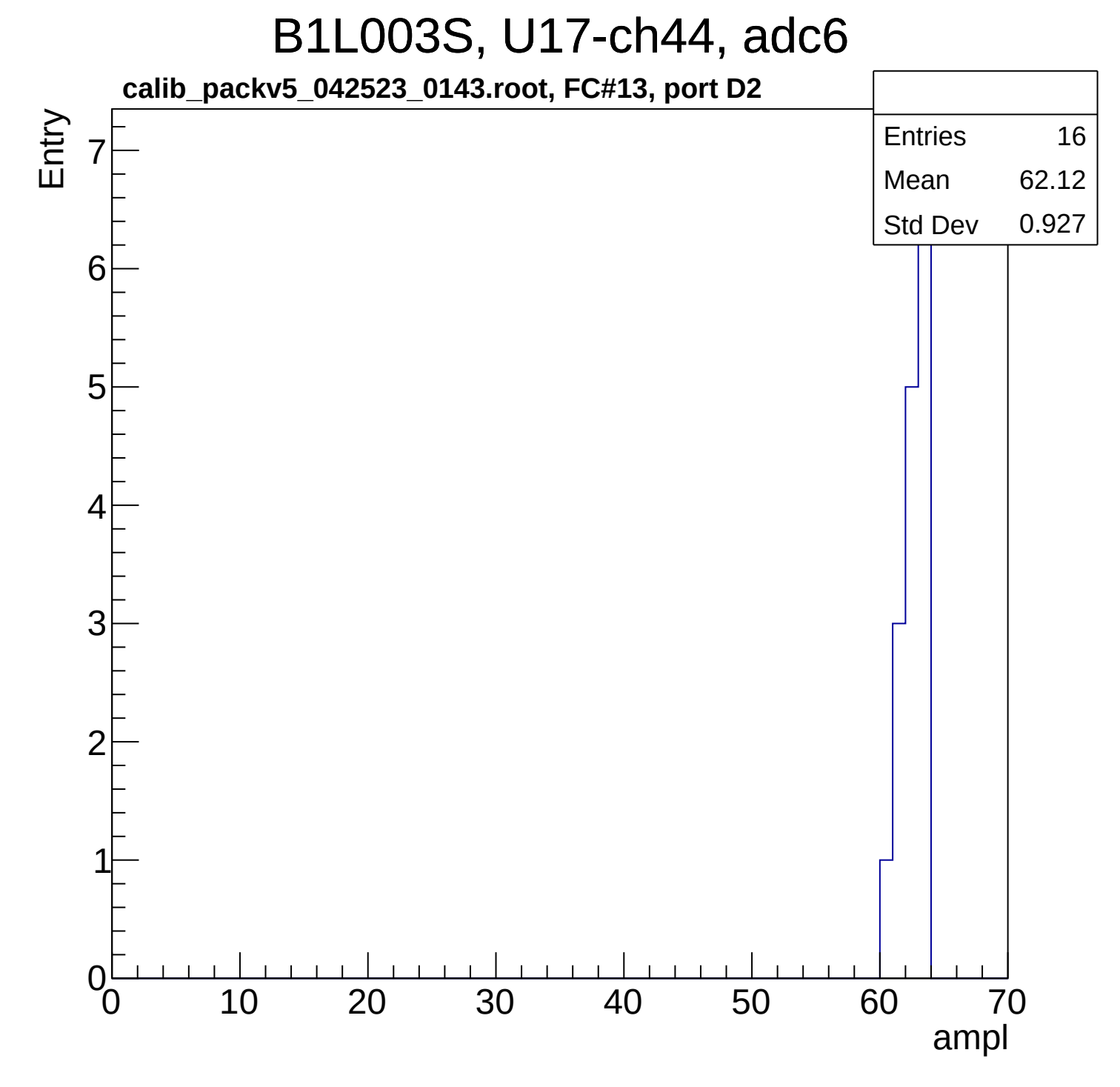
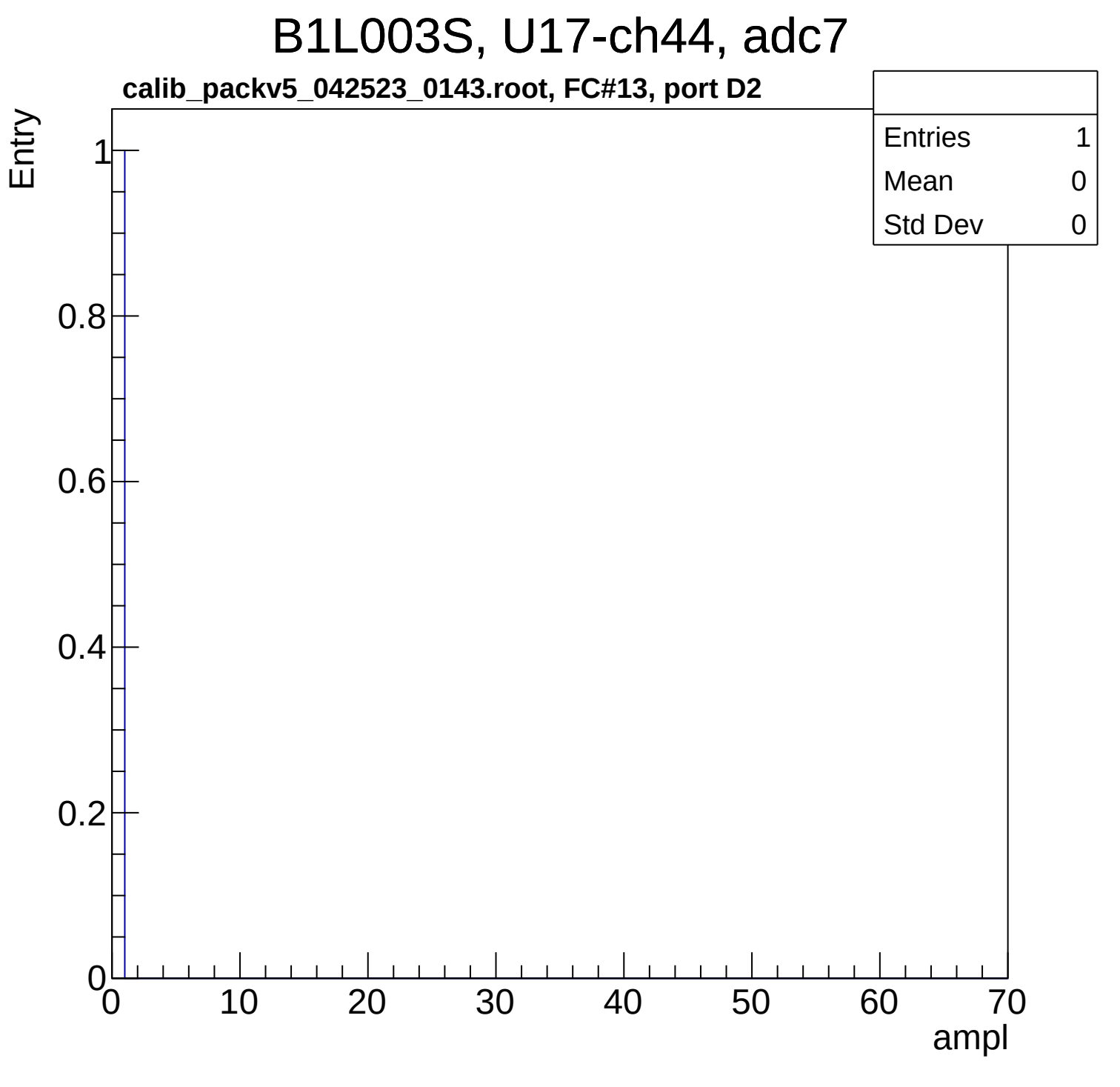

B1L003S, U17-ch44, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.71
Std Dev 8.212
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch44, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 62.12
Std Dev 0.927
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch44, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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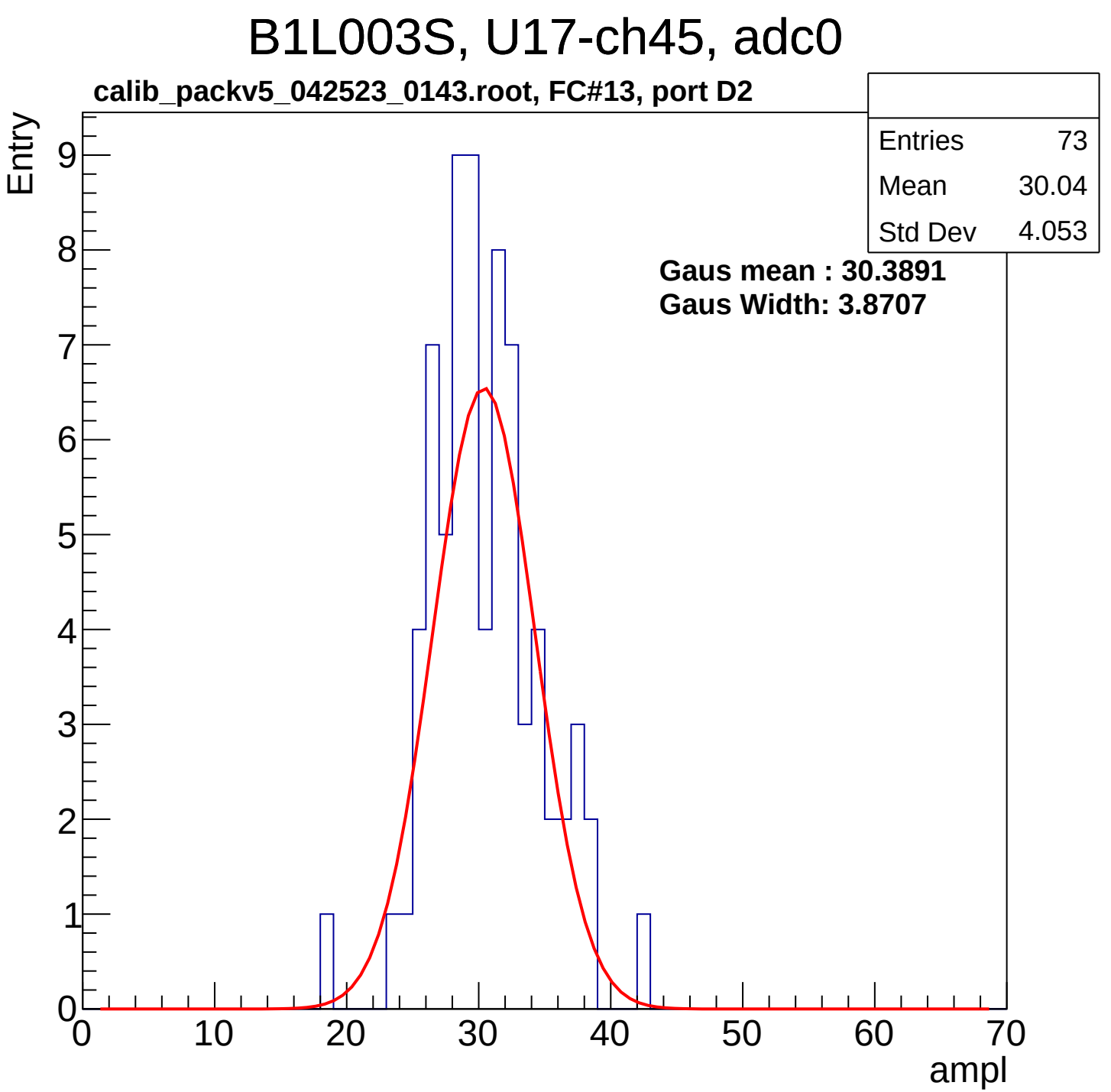
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch45, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 30.04
Std Dev 4.053
Gaus mean : 30.3891
Gaus Width: 3.8707
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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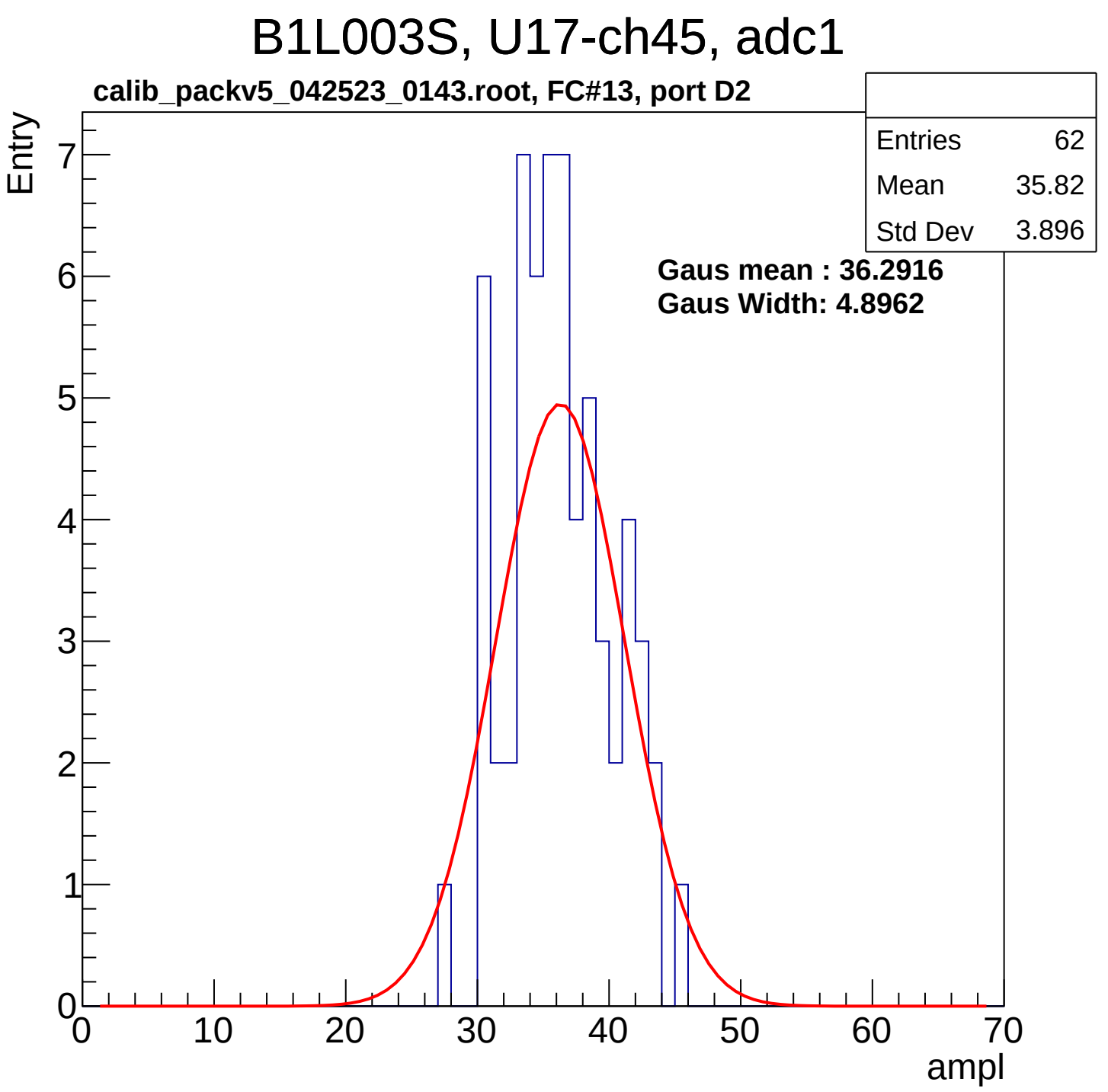
B1L003S, U17-ch45, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.82
Std Dev 3.896
Gaus mean : 36.2916
Gaus Width: 4.8962
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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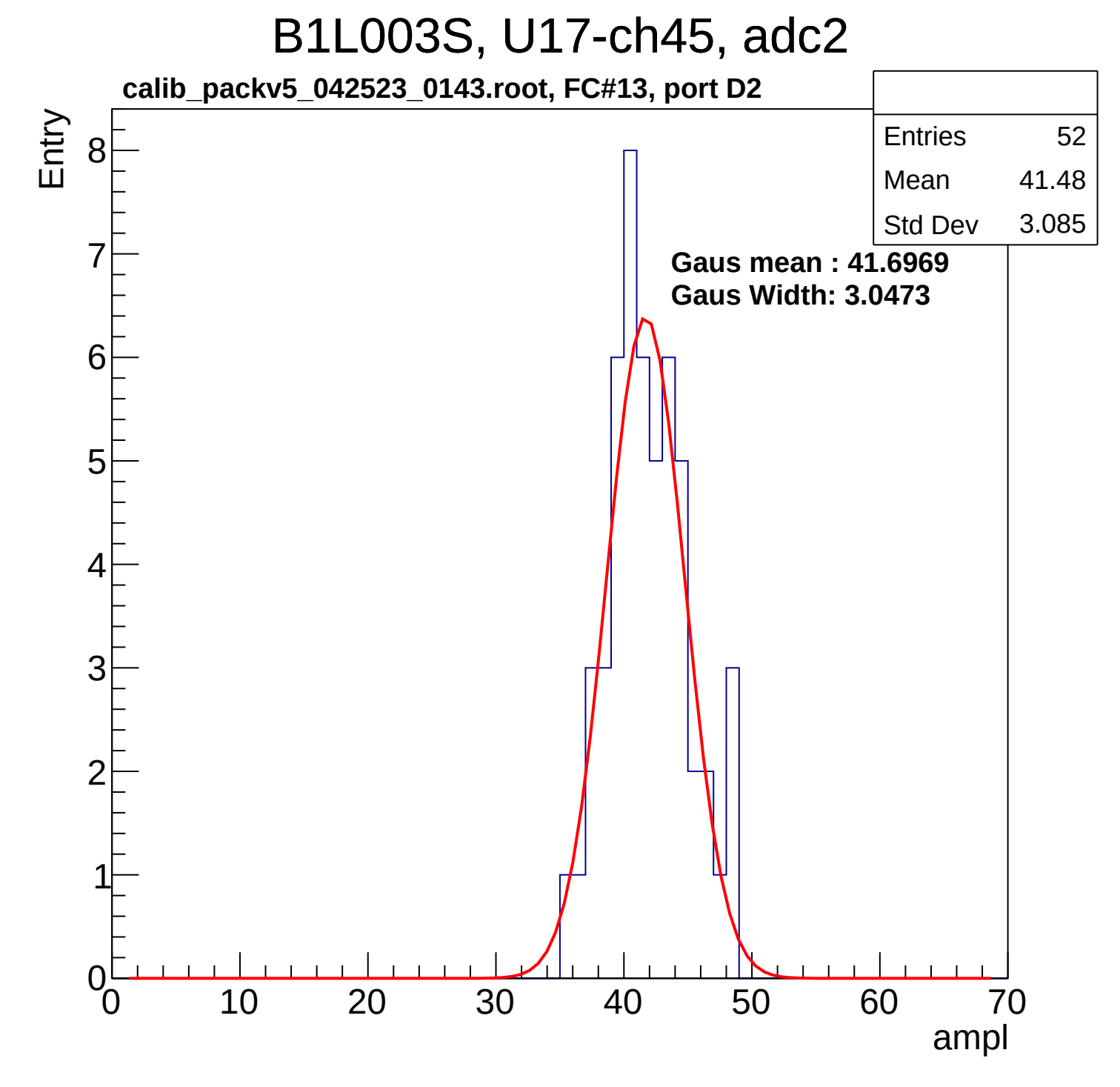
B1L003S, U17-ch45, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 41.48
Std Dev 3.085
Gaus mean : 41.6969
Gaus Width: 3.0473
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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50
60
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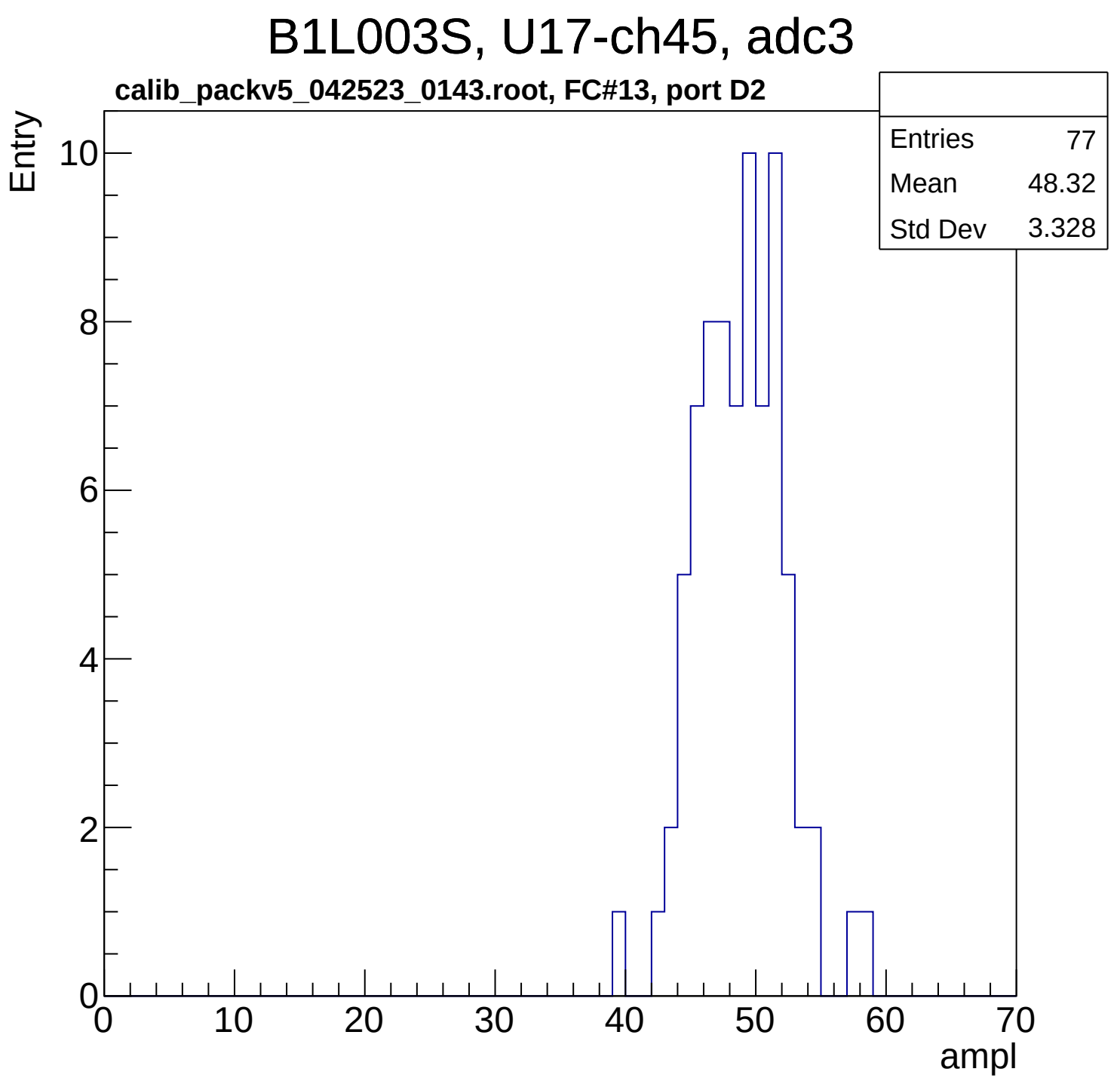
B1L003S, U17-ch45, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 48.32
Std Dev 3.328
Entry
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2
4
6
8
10
0
10
20
30
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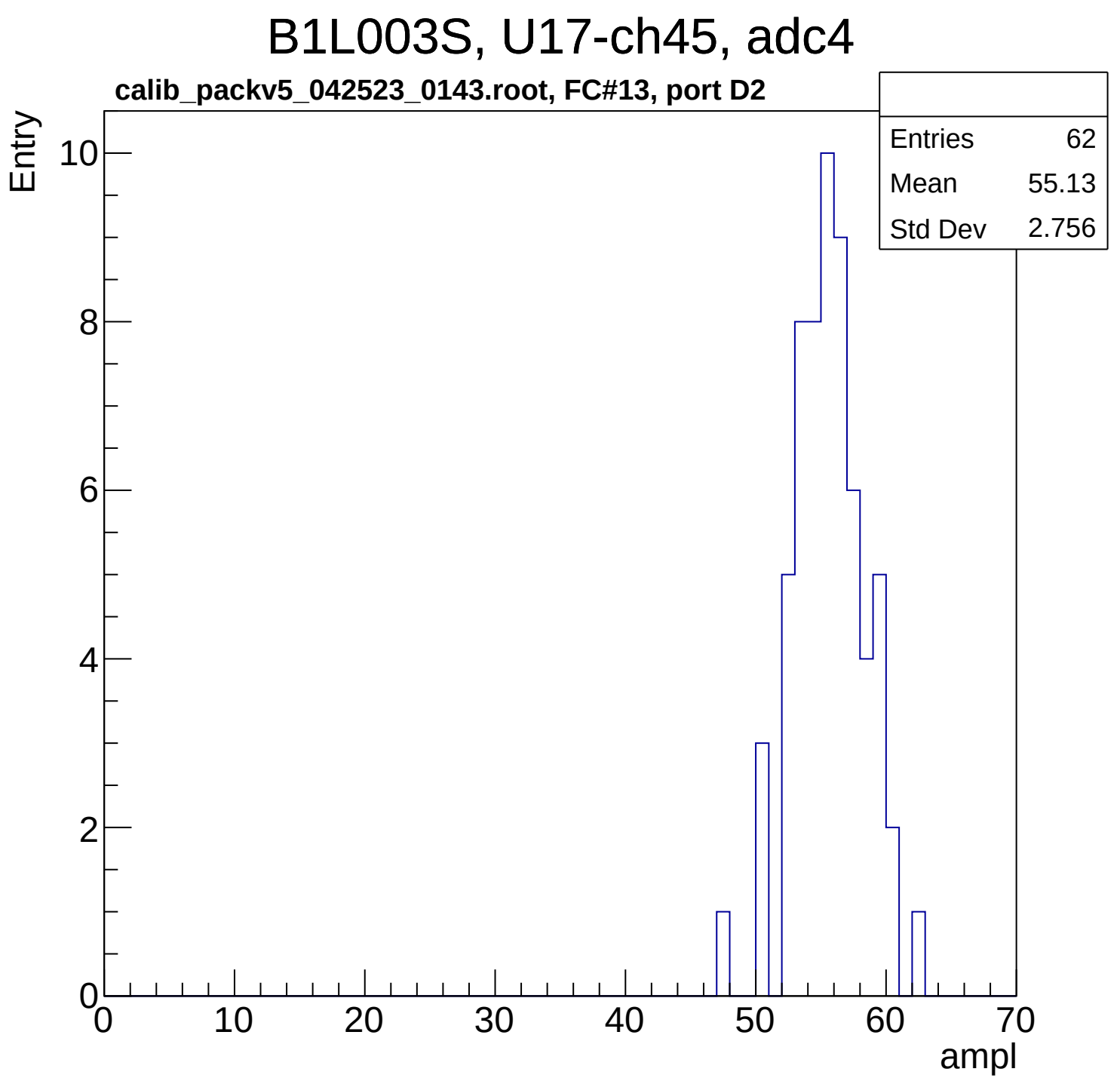
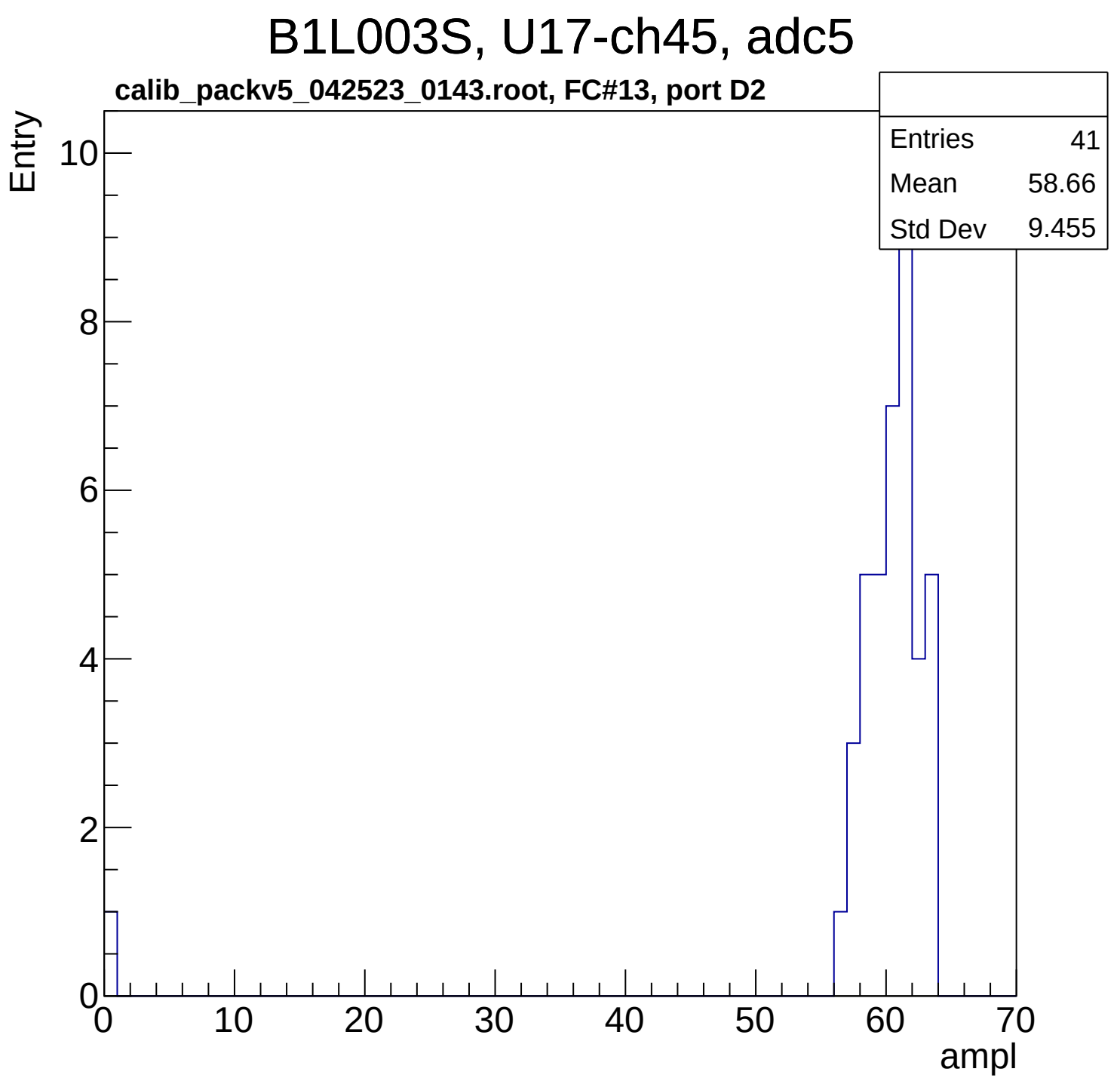
50
60
70
ampl


B1L003S, U17-ch45, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 55.13
Std Dev 2.756
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch45, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.66
Std Dev 9.455
Entry
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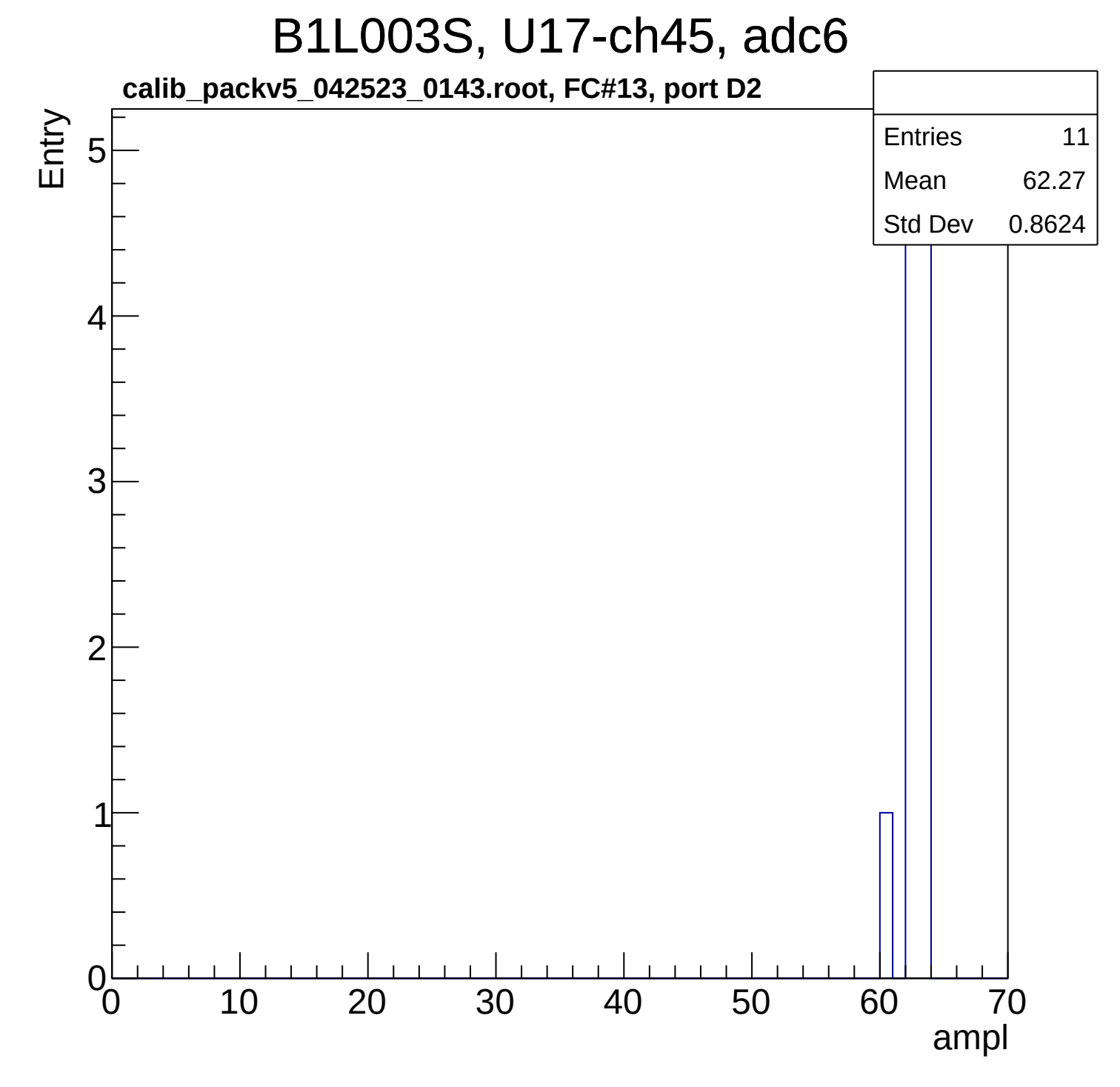
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch45, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 62.27
Std Dev 0.8624
Entry
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2
3
4
5
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10
20
30
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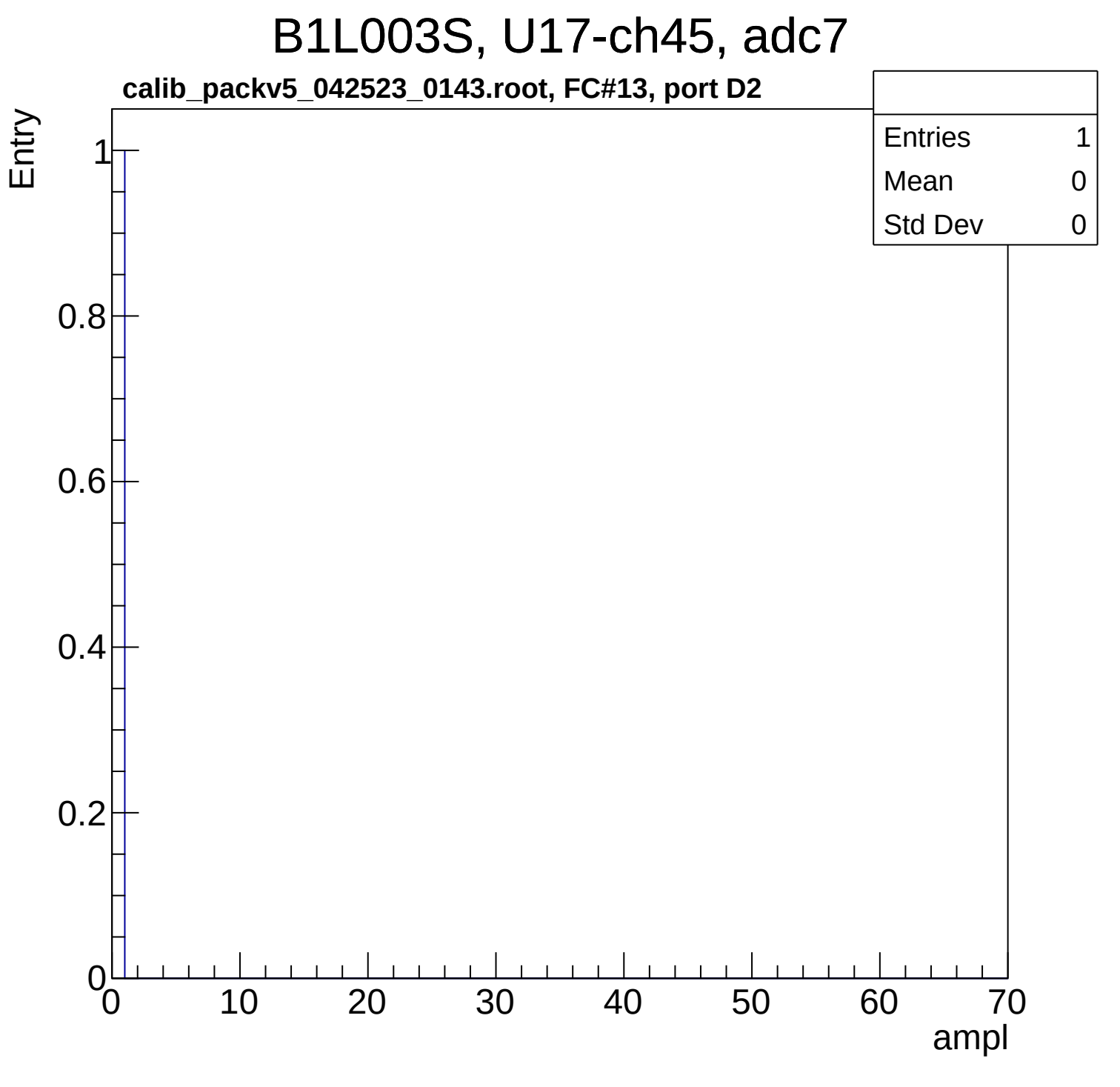
50
60
70
ampl


B1L003S, U17-ch45, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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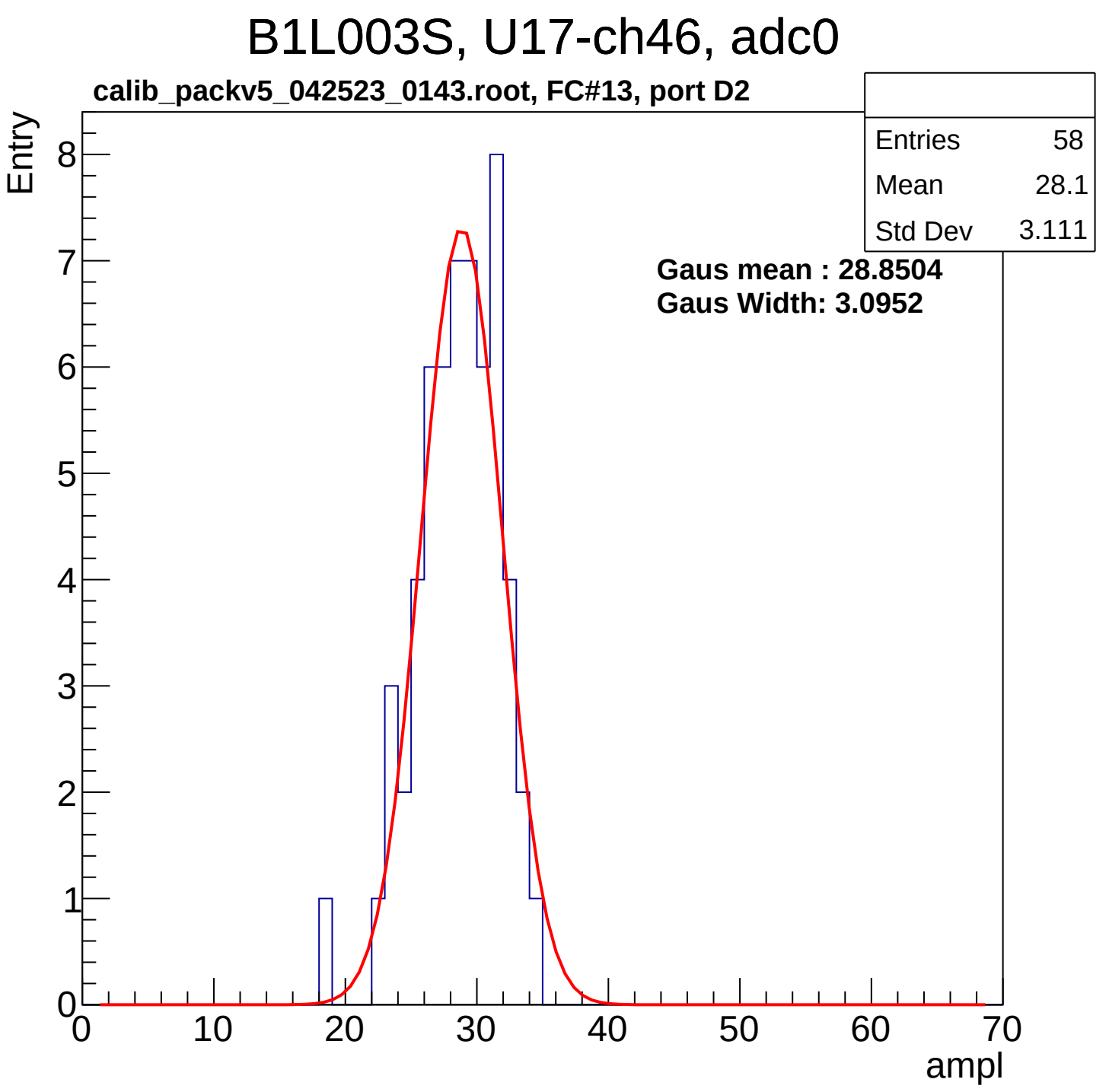
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch46, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.1
Std Dev 3.111
Gaus mean : 28.8504
Gaus Width: 3.0952
Entry
ampl
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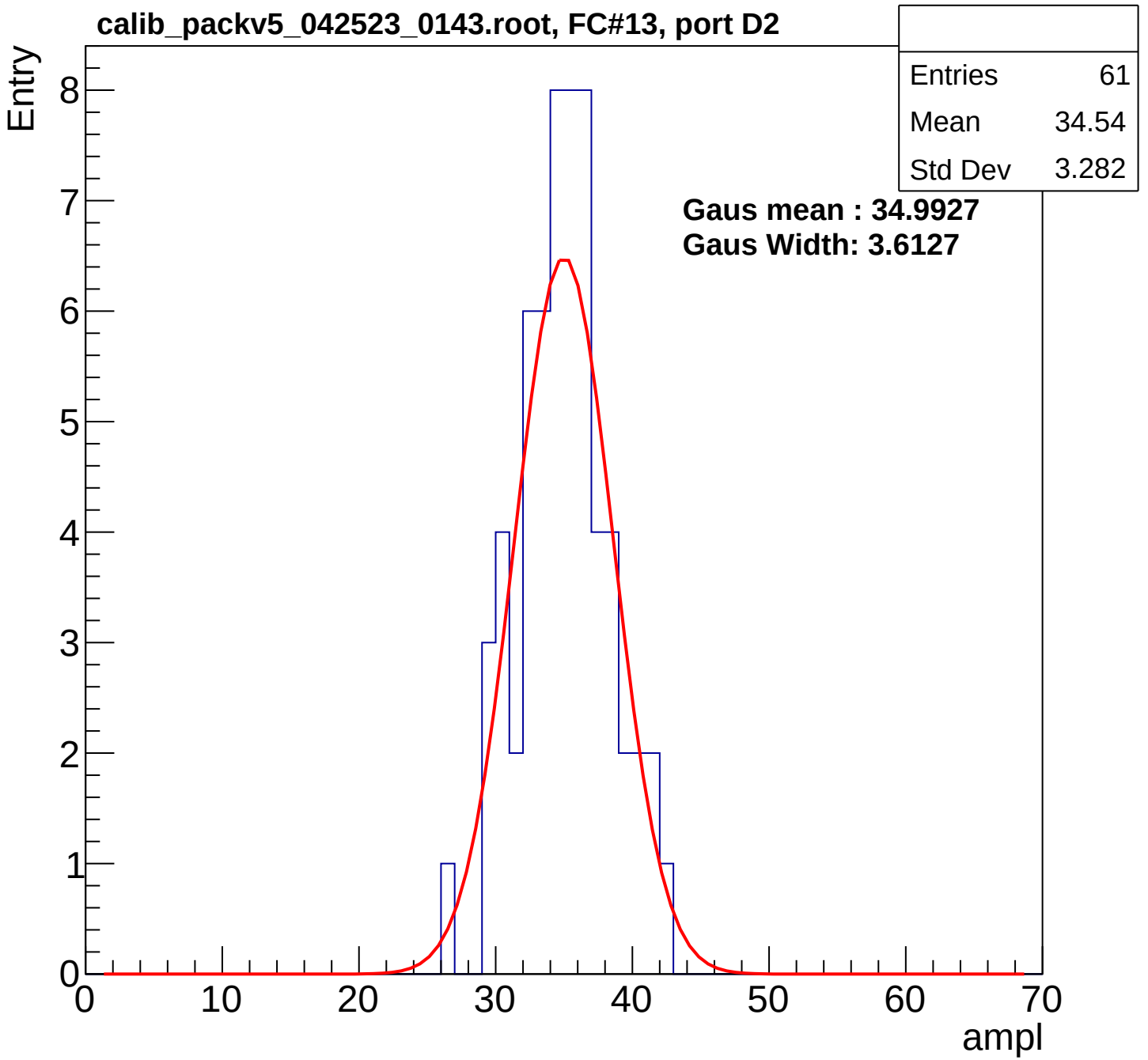
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch46, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 34.54
Std Dev 3.282
Gaus mean : 34.9927
Gaus Width: 3.6127
Entry
ampl
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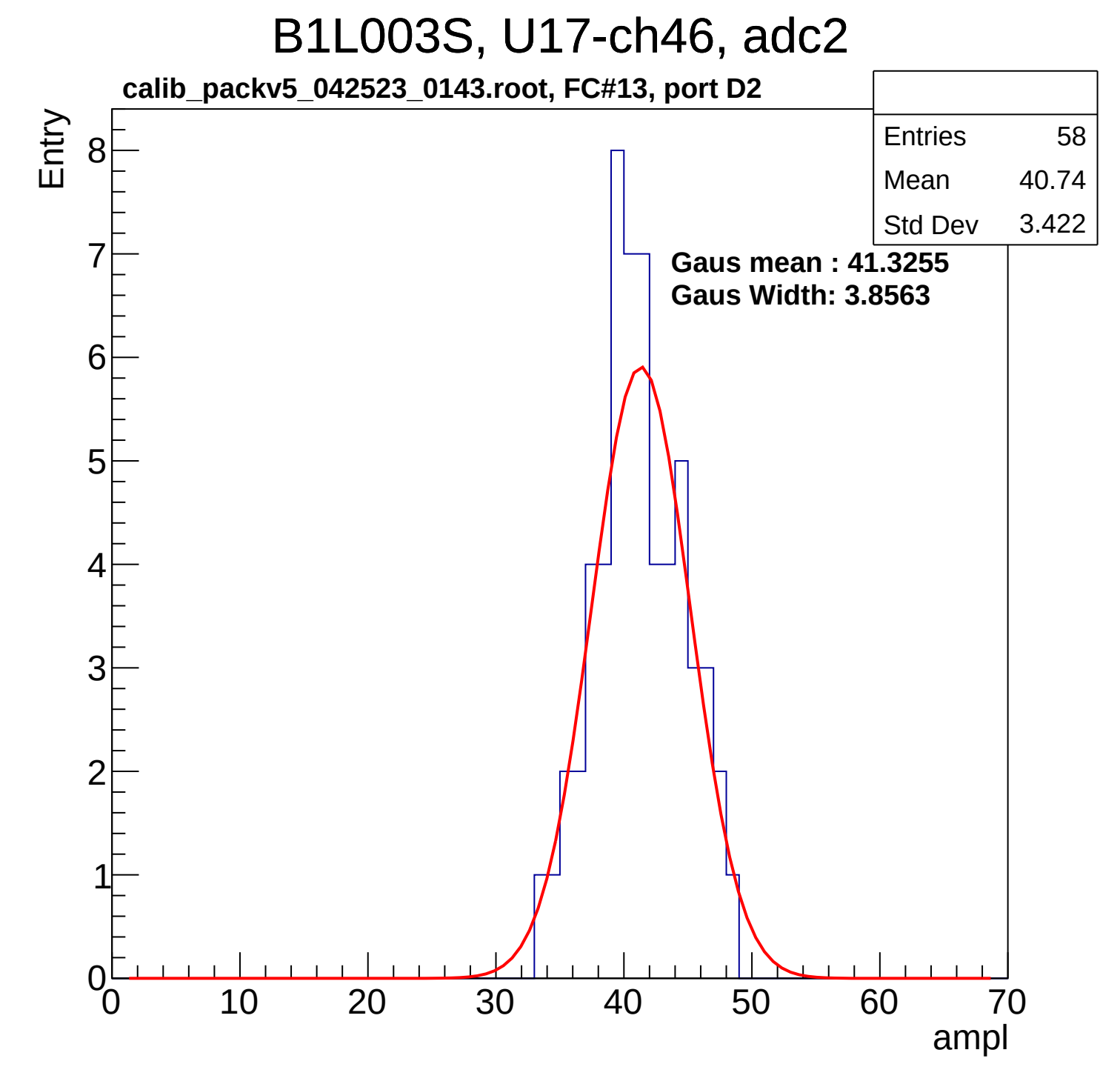
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch46, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 40.74
Std Dev 3.422
Gaus mean : 41.3255
Gaus Width: 3.8563
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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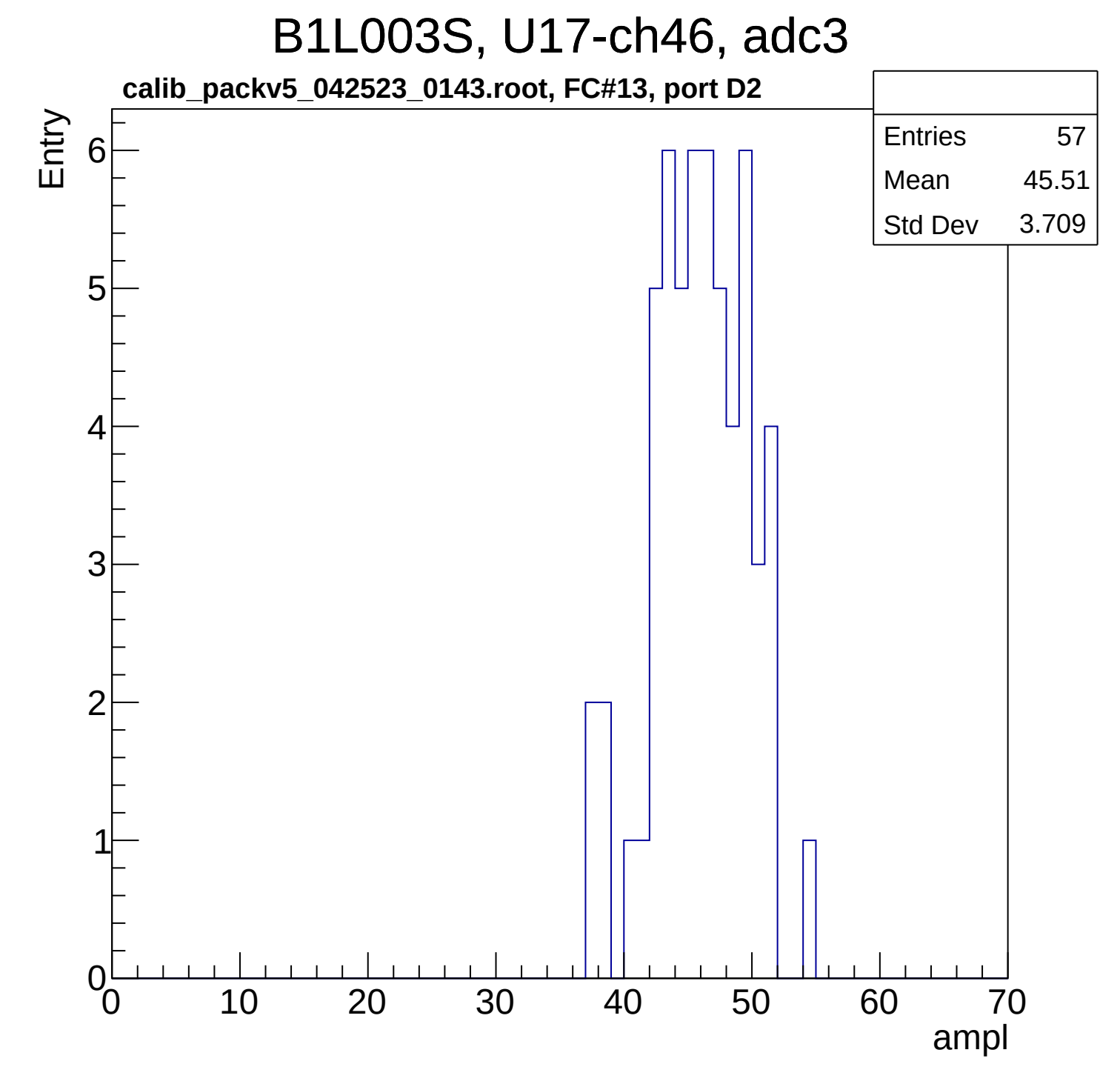
B1L003S, U17-ch46, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 45.51
Std Dev 3.709
Entry
ampl
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3
4
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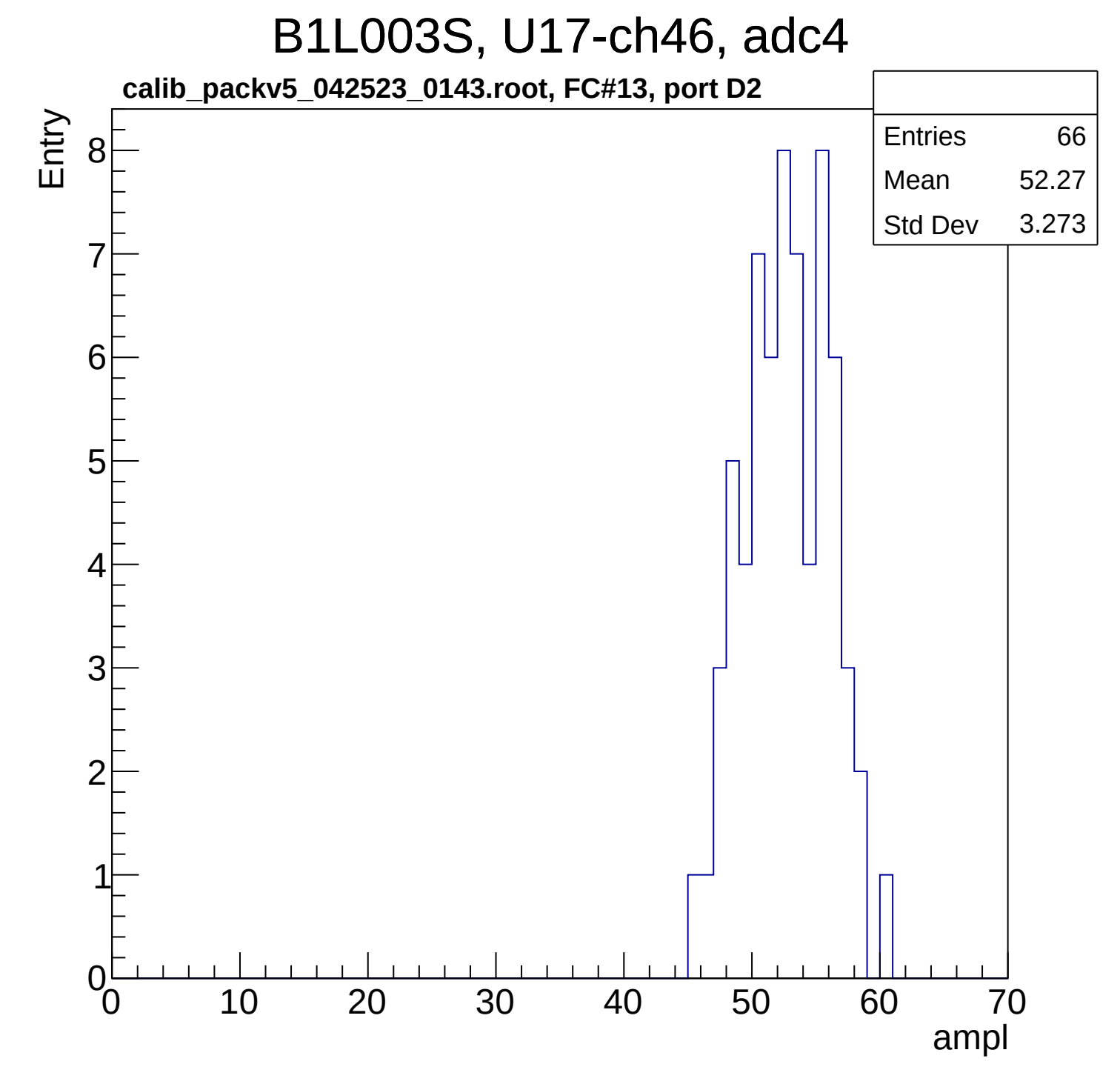
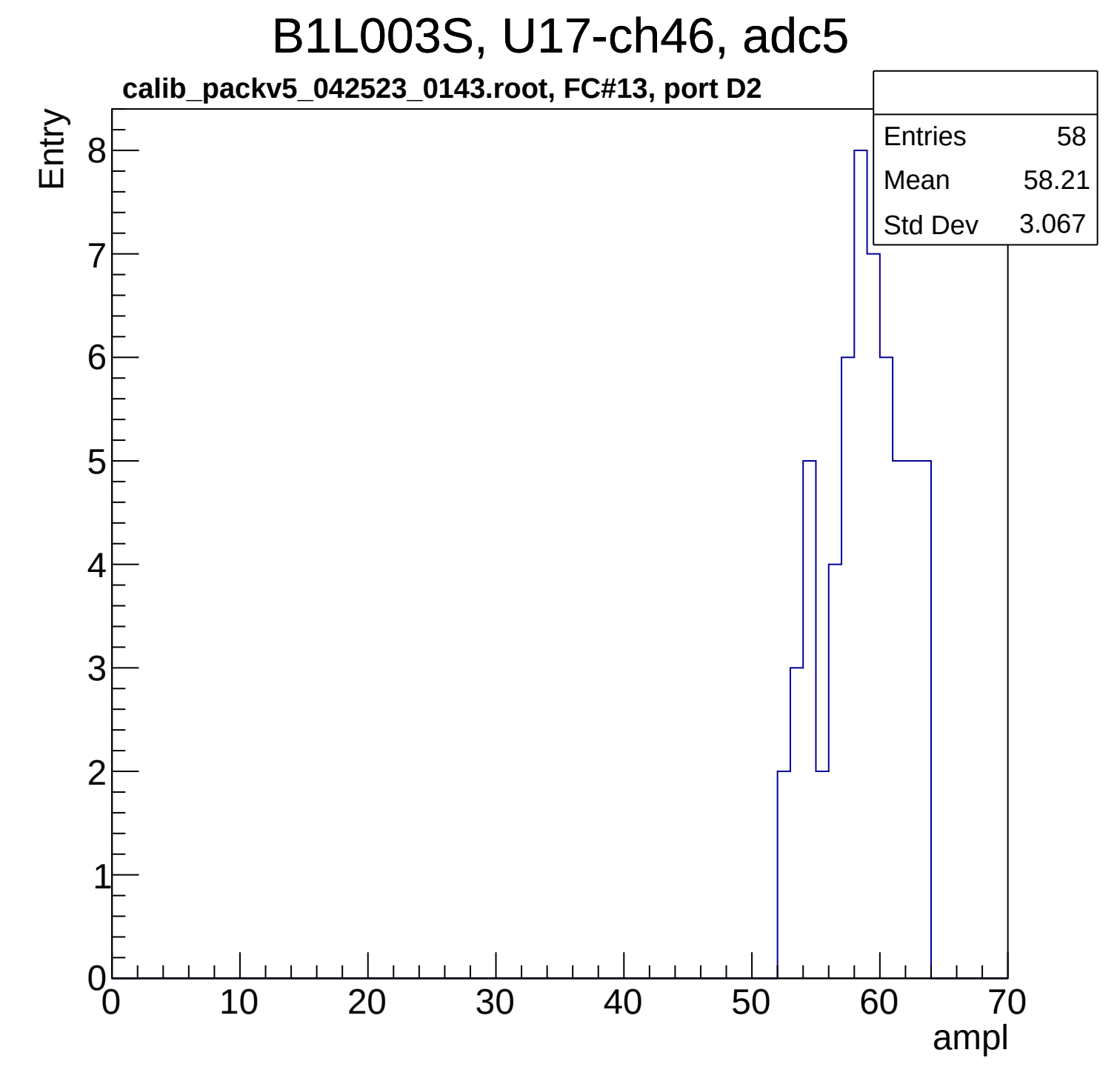
0
10
20
30
40
50
60
70


B1L003S, U17-ch46, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 52.27
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch46, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 58.21
Std Dev 3.067
Entry
ampl
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2
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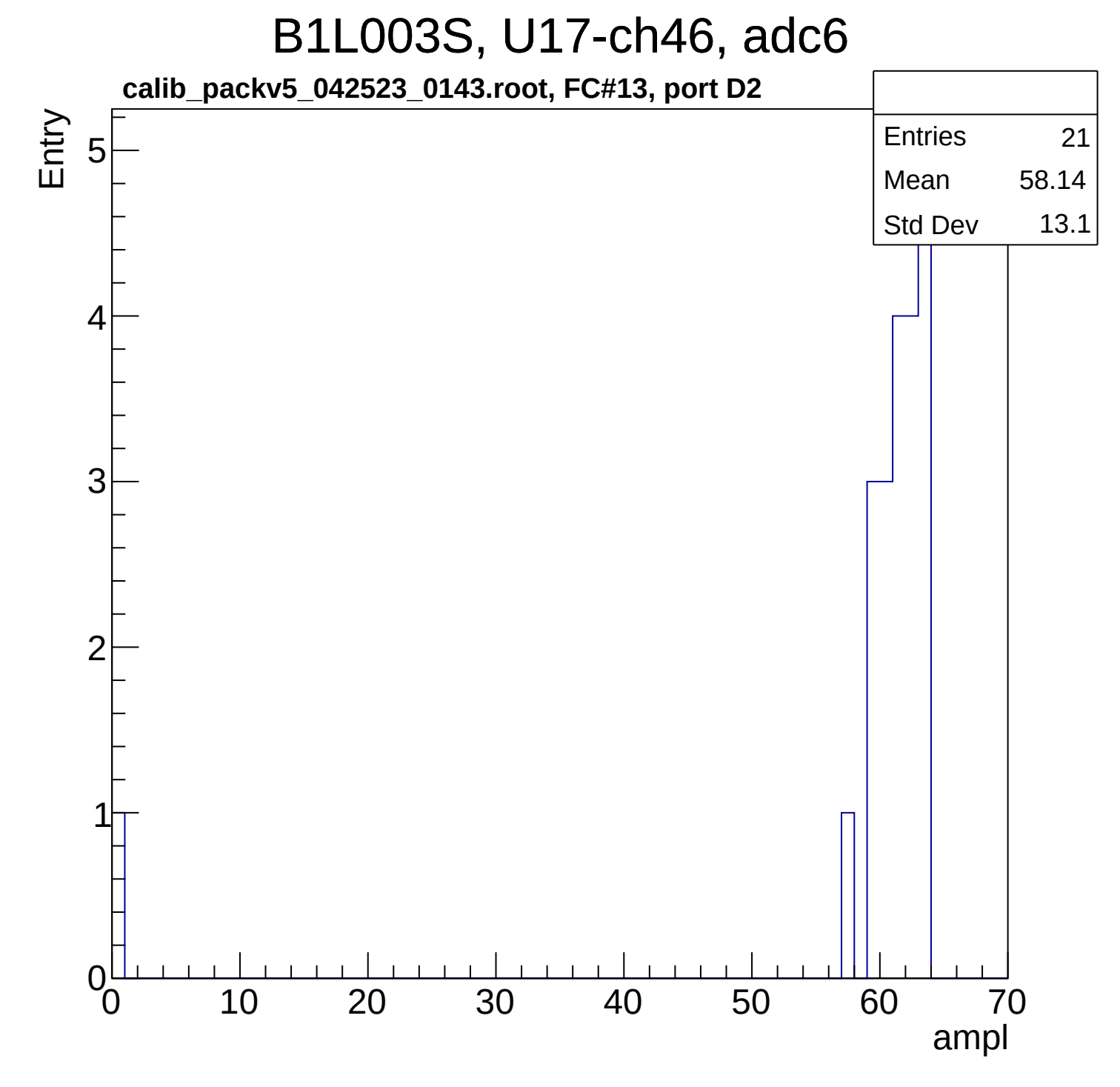
B1L003S, U17-ch46, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 58.14
Std Dev 13.1
Entry
0
1
2
3
4
5
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10
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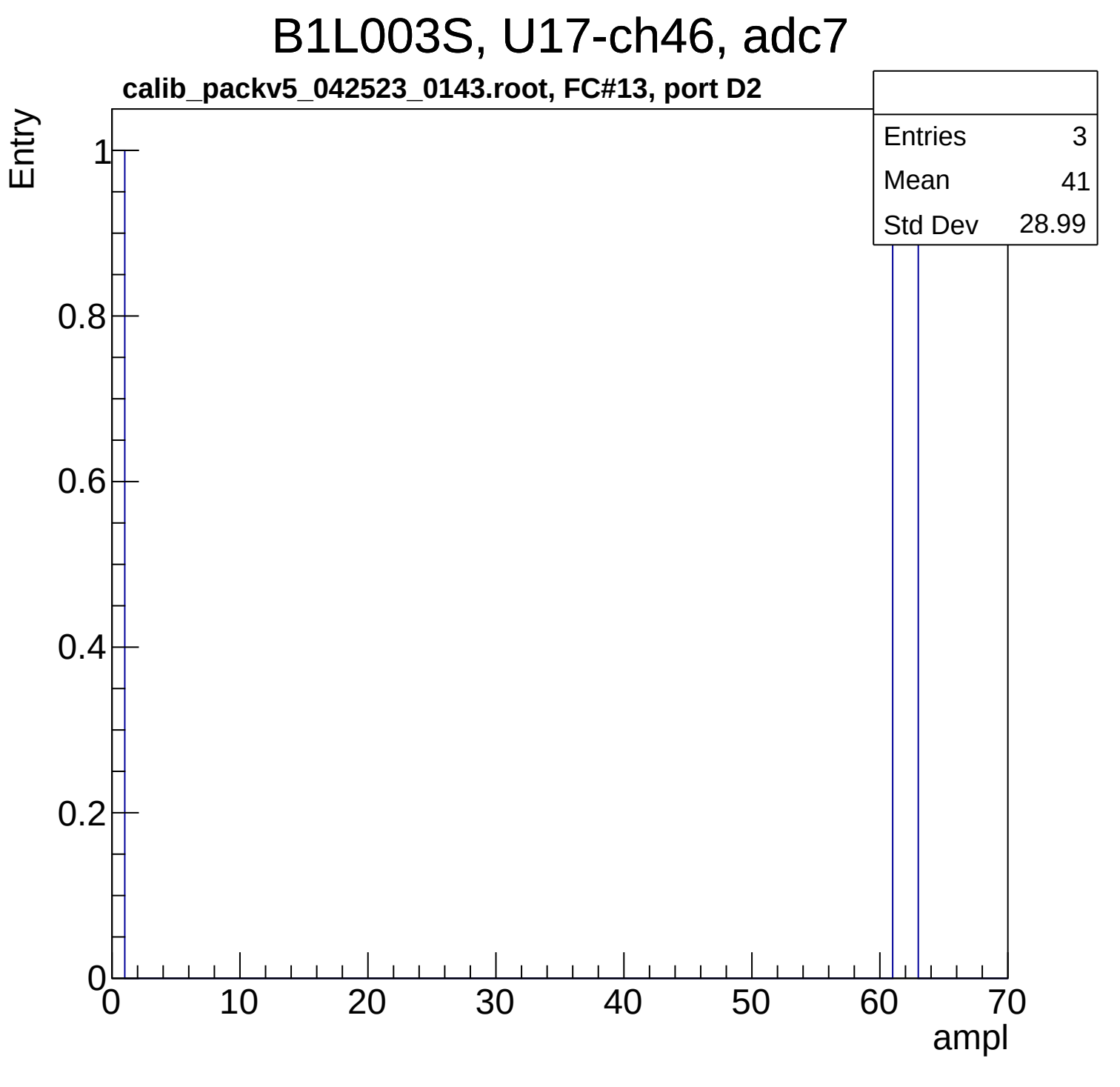
70
ampl


B1L003S, U17-ch46, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41
Std Dev 28.99
Entry
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0.6
0.4
0.2
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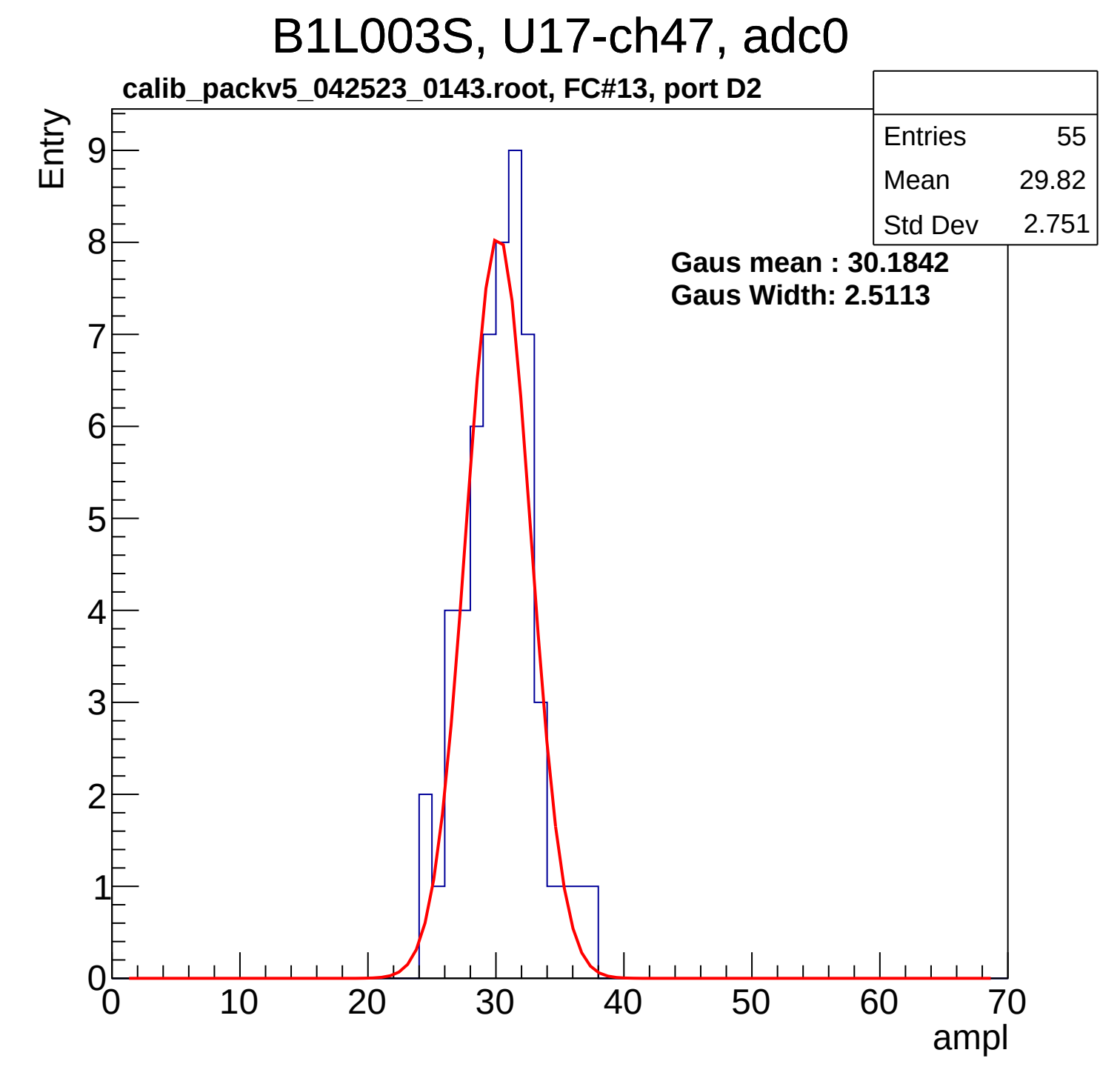
70
ampl


B1L003S, U17-ch47, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.82
Std Dev 2.751
Gaus mean : 30.1842
Gaus Width: 2.5113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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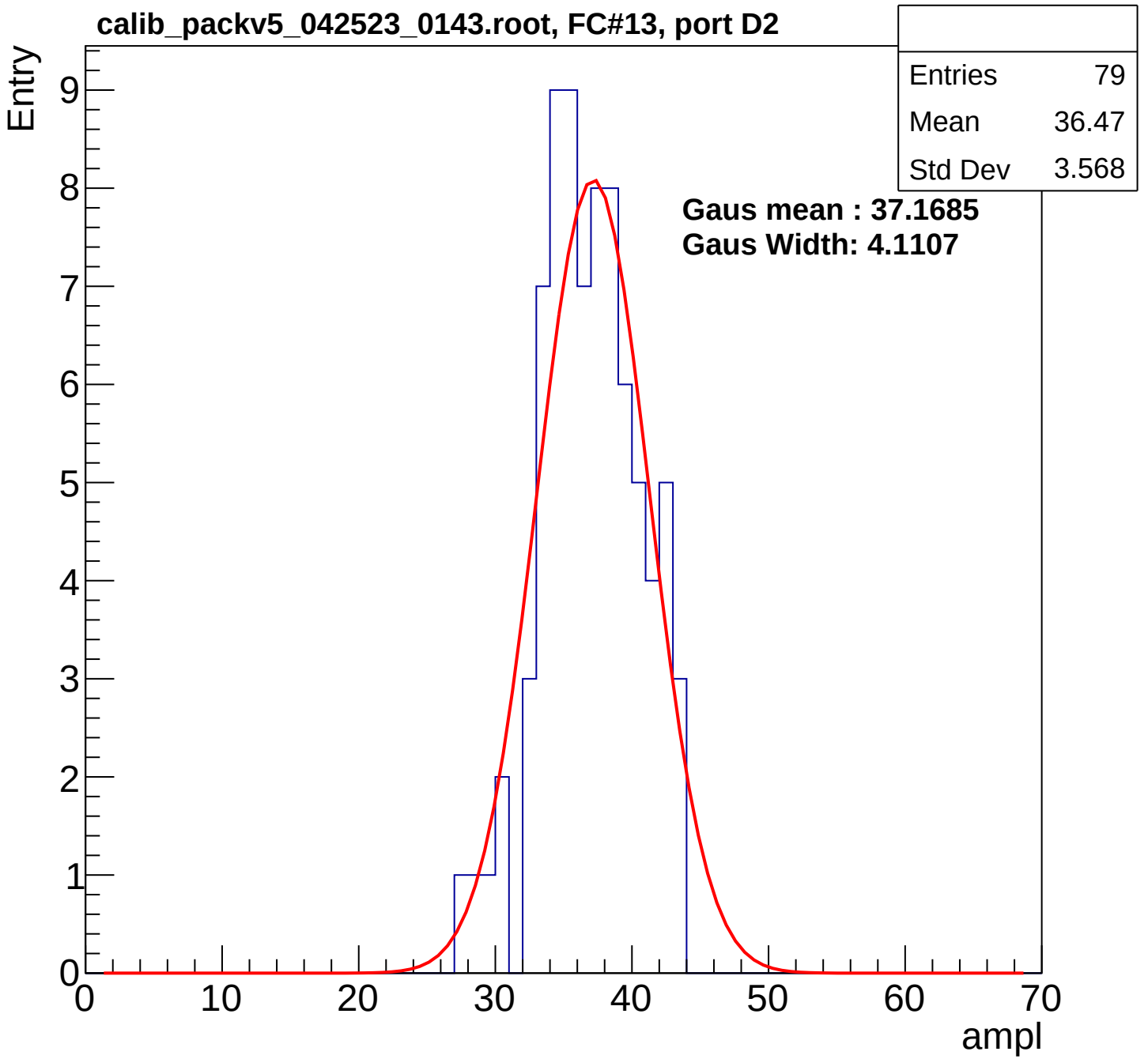
B1L003S, U17-ch47, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 36.47
Std Dev 3.568
Gaus mean : 37.1685
Gaus Width: 4.1107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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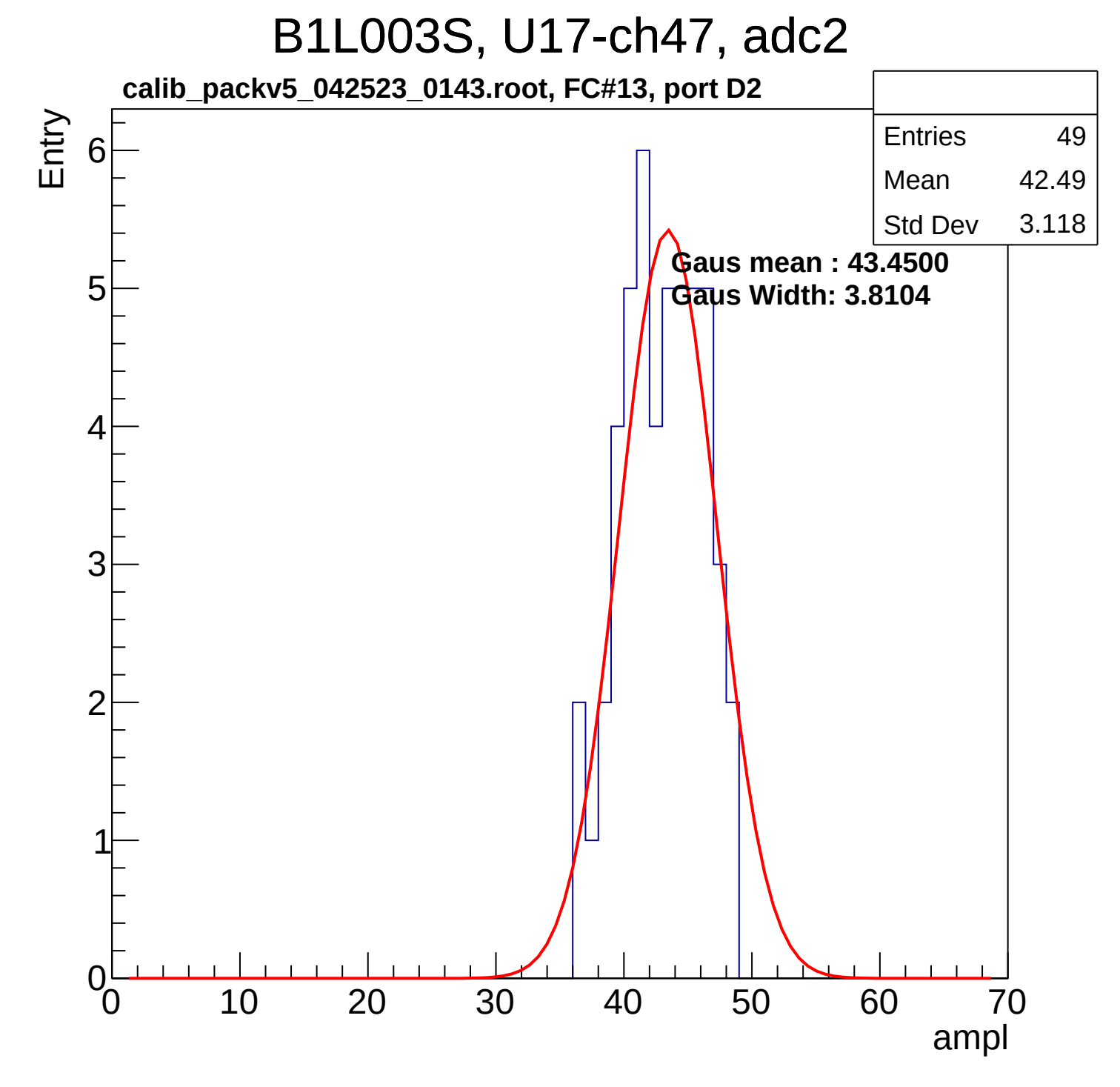
B1L003S, U17-ch47, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 42.49
Std Dev 3.118
Gaus mean : 43.4500
Gaus Width: 3.8104
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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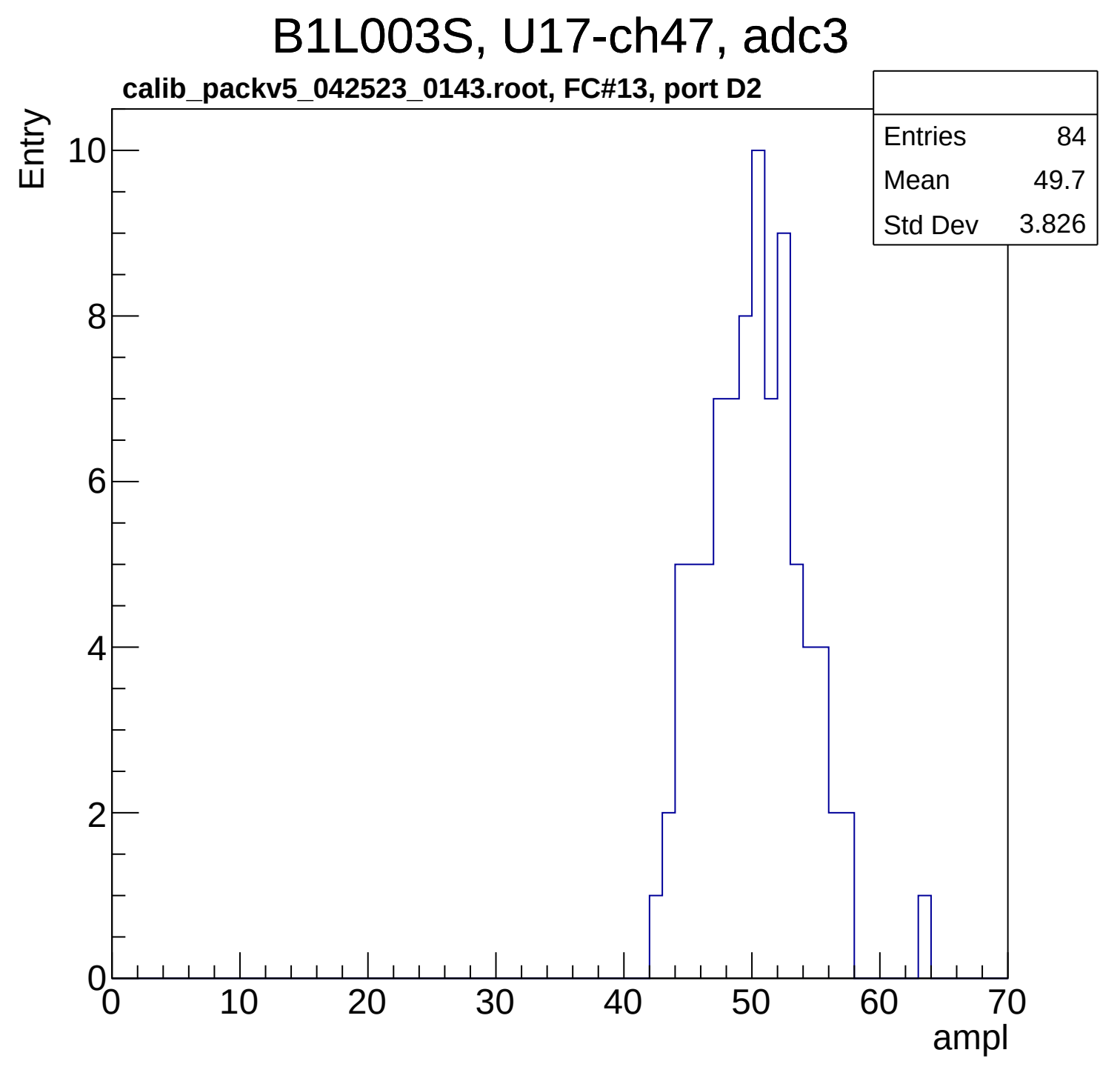
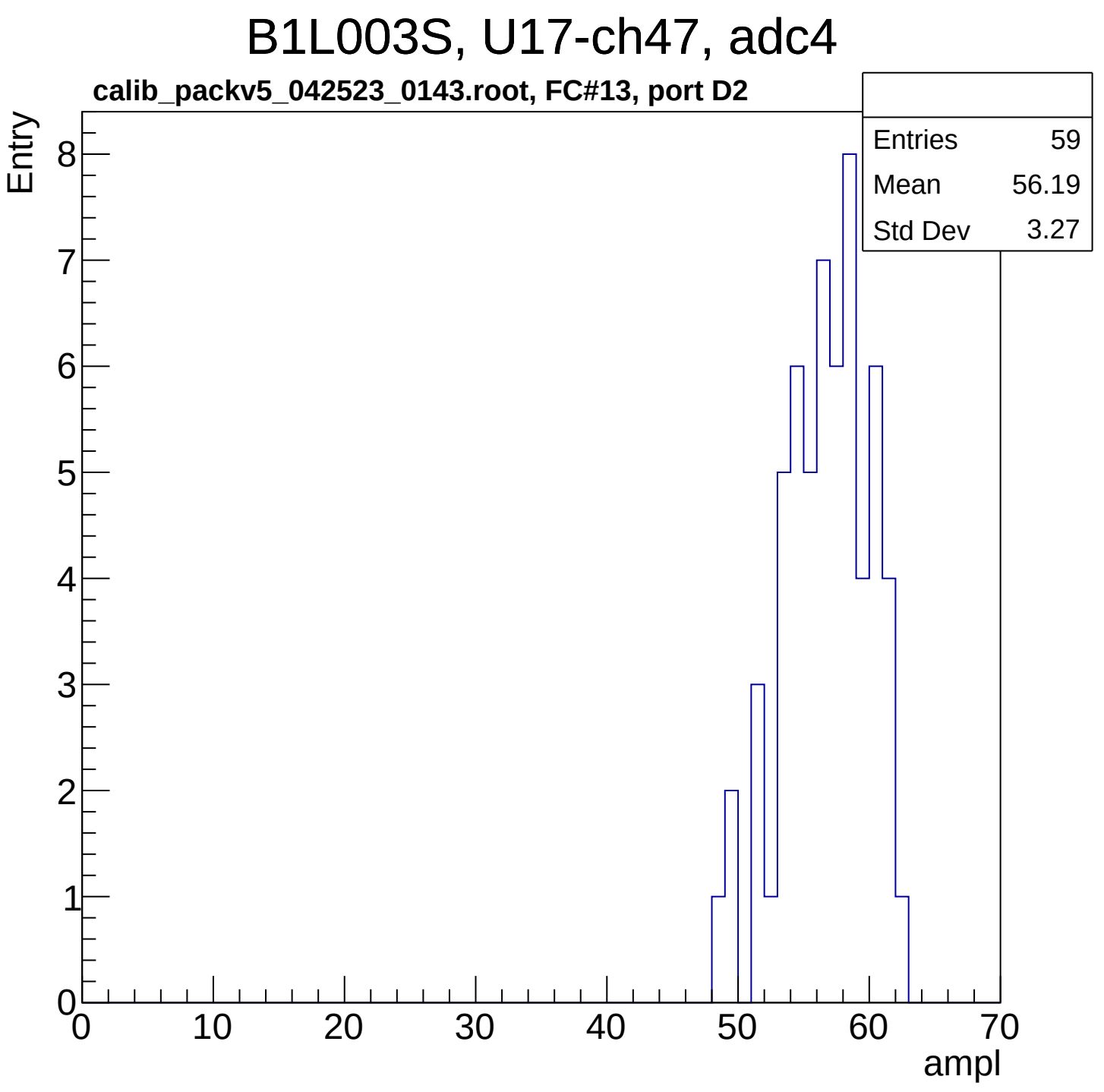
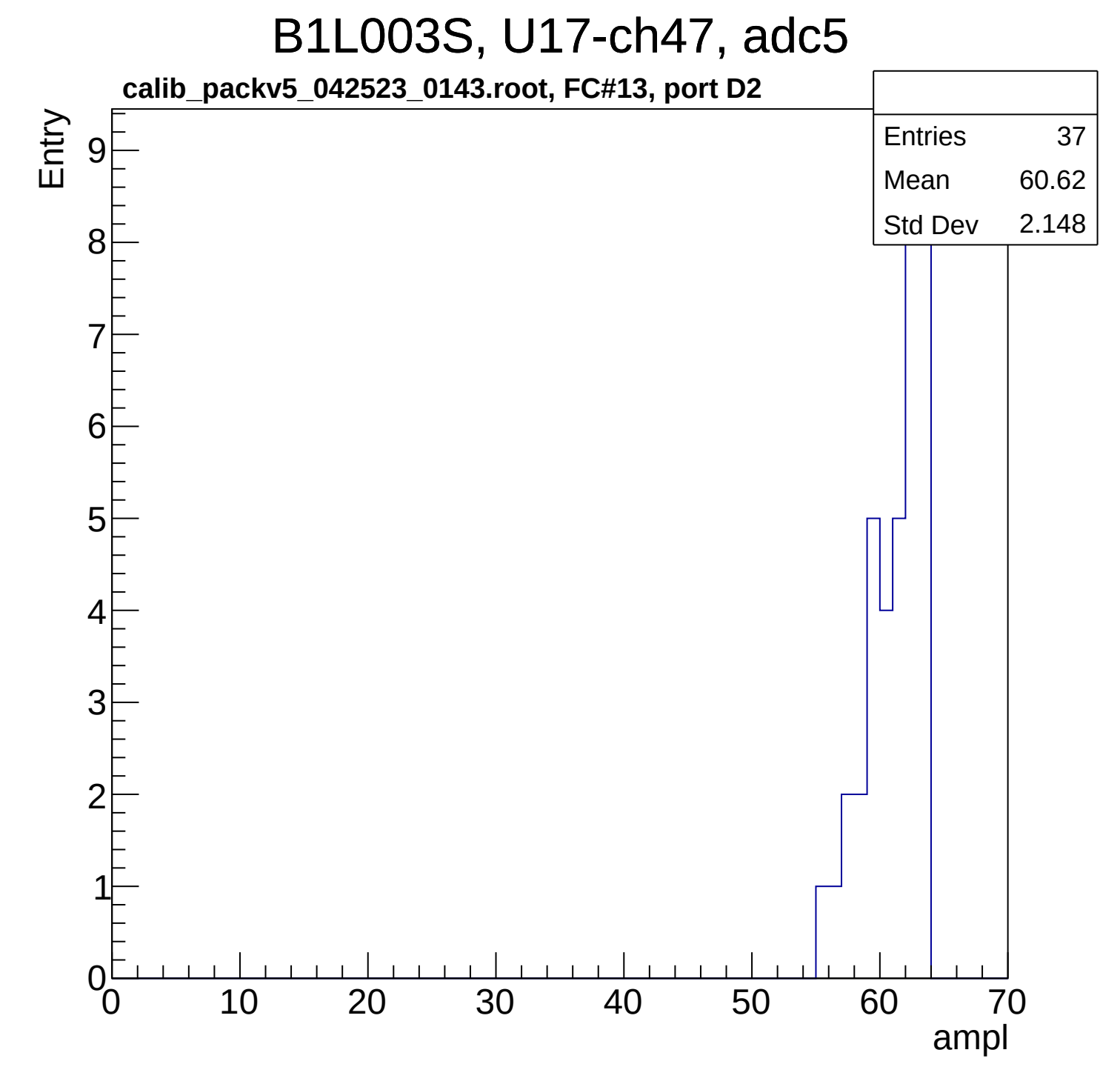
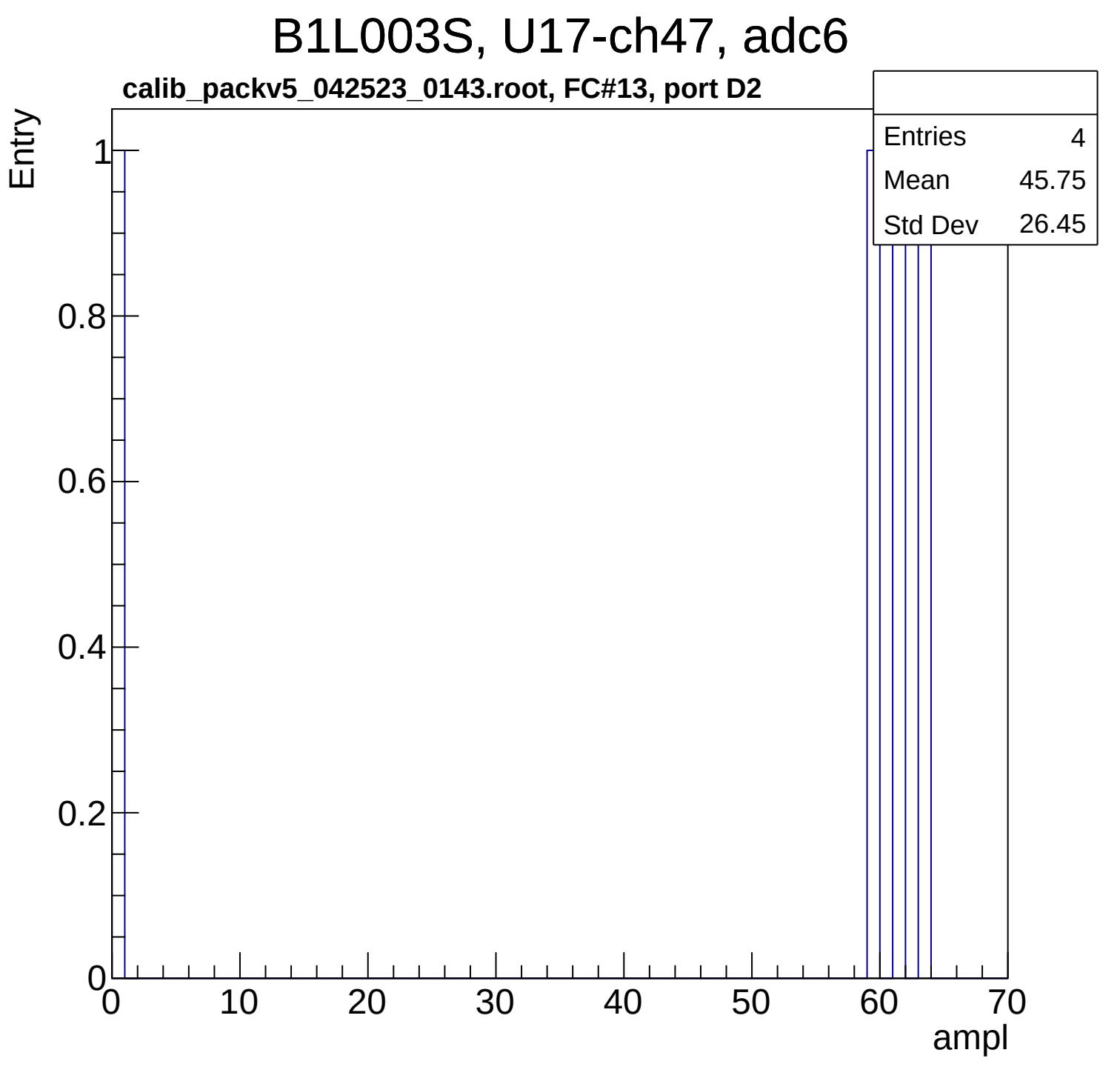
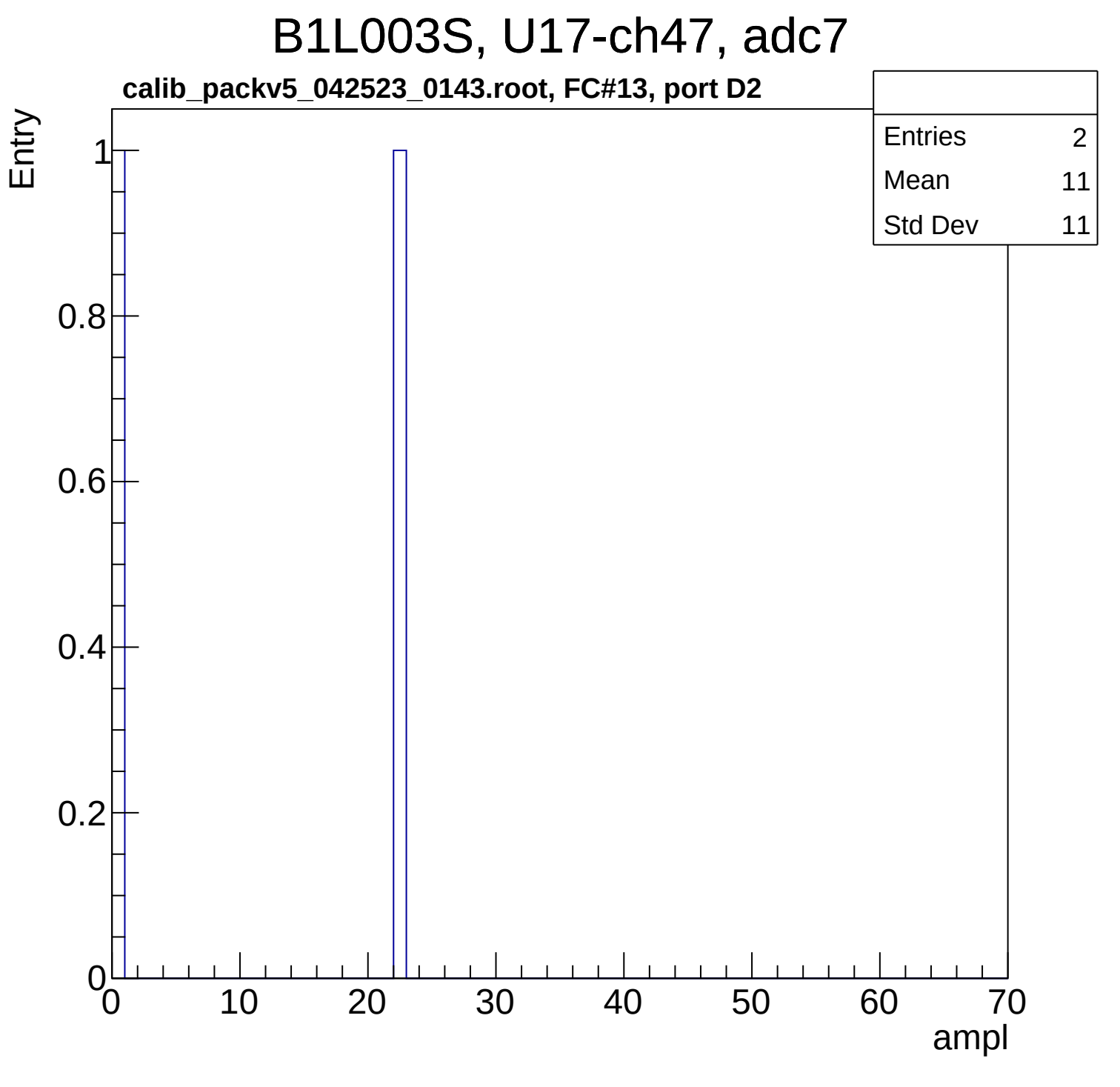
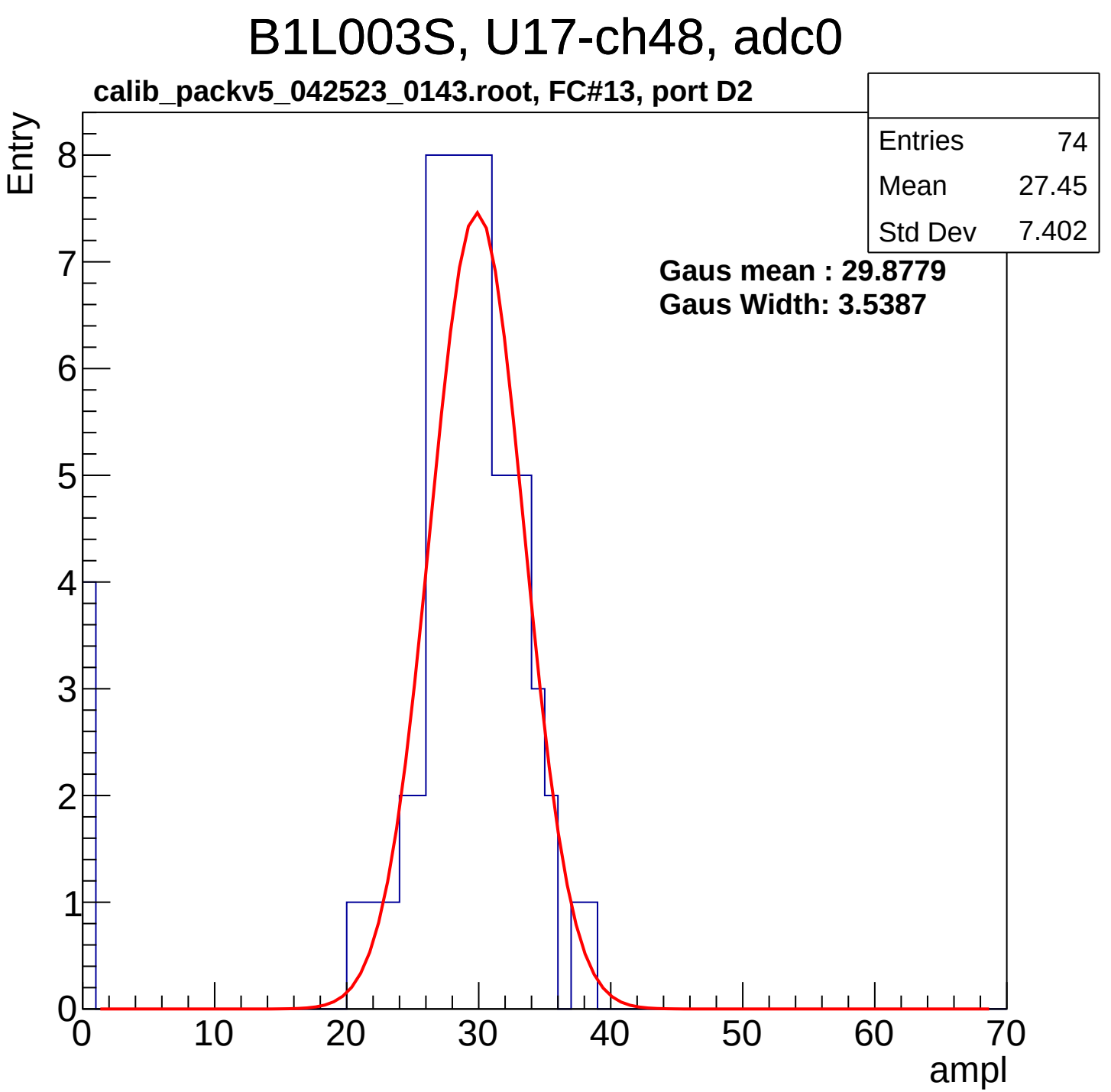

B1L003S, U17-ch47, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 49.7
Std Dev 3.826
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch47, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 56.19
Std Dev 3.27
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch47, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 60.62
Std Dev 2.148
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch47, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 45.75
Std Dev 26.45
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch47, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch48, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 27.45
Std Dev 7.402
Gaus mean : 29.8779
Gaus Width: 3.5387
Entry
ampl
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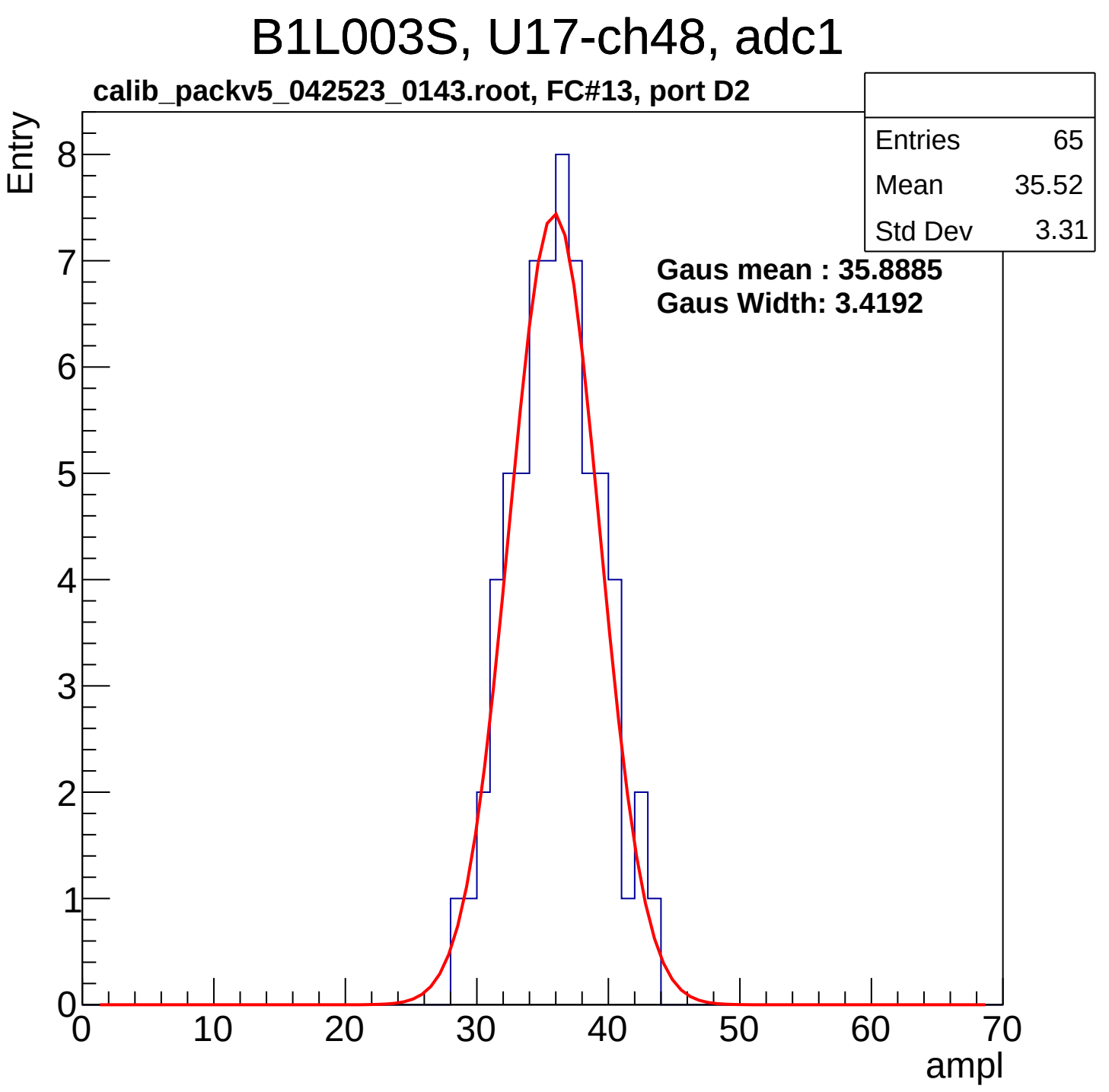
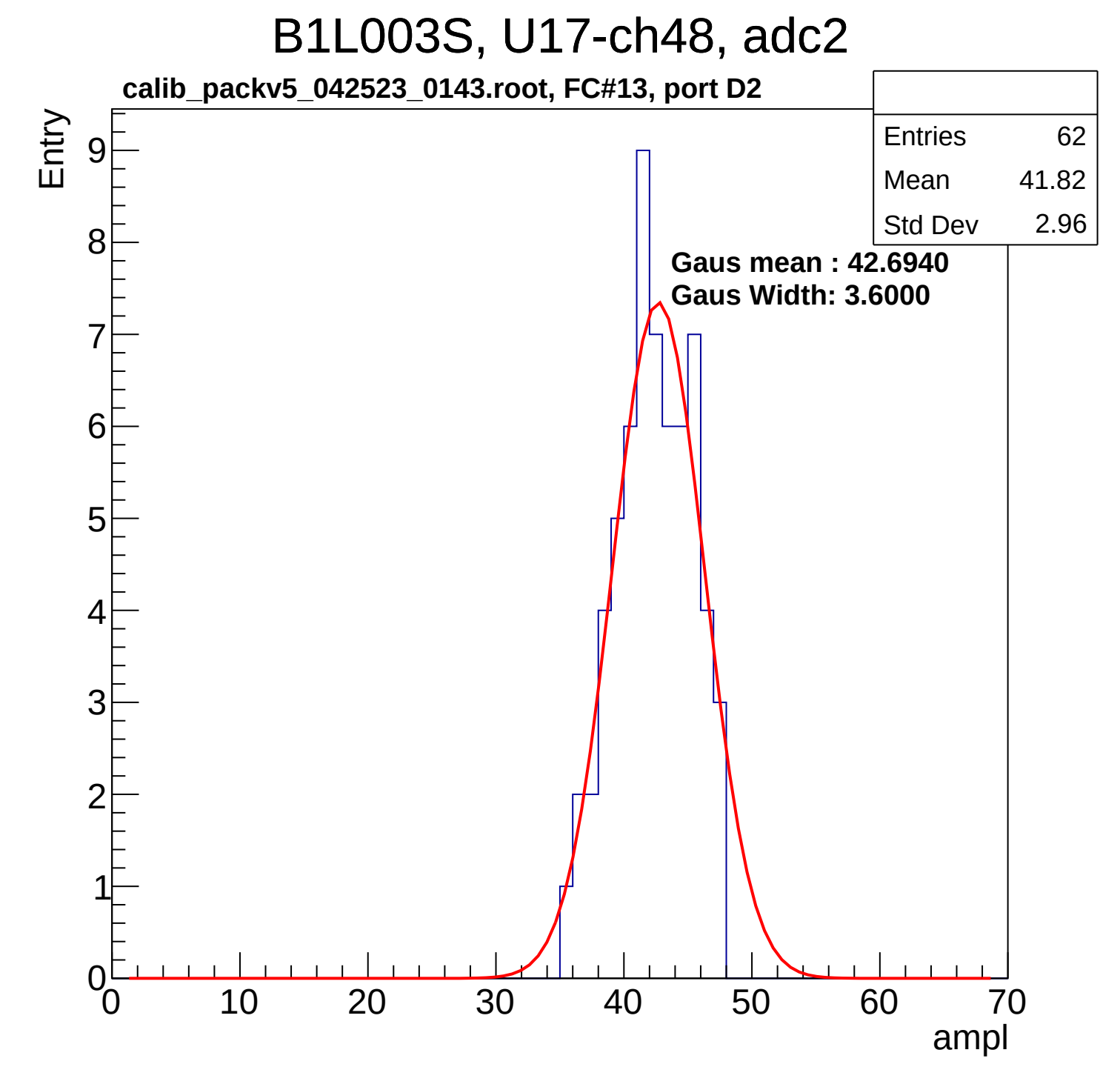
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch48, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.52
Std Dev 3.31
Gaus mean : 35.8885
Gaus Width: 3.4192
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch48, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 41.82
Std Dev 2.96
Gaus mean : 42.6940
Gaus Width: 3.6000
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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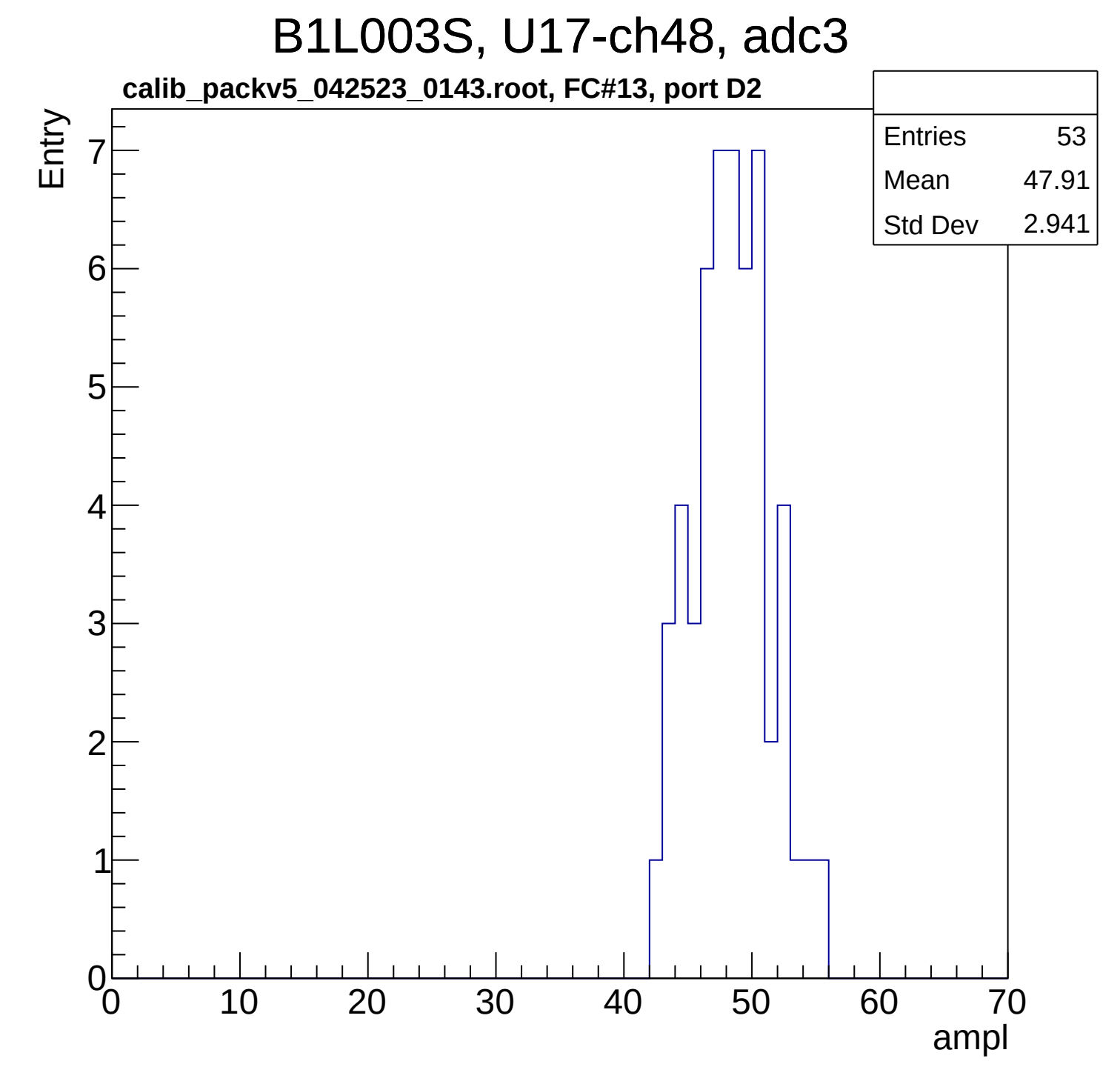
B1L003S, U17-ch48, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 47.91
Std Dev 2.941
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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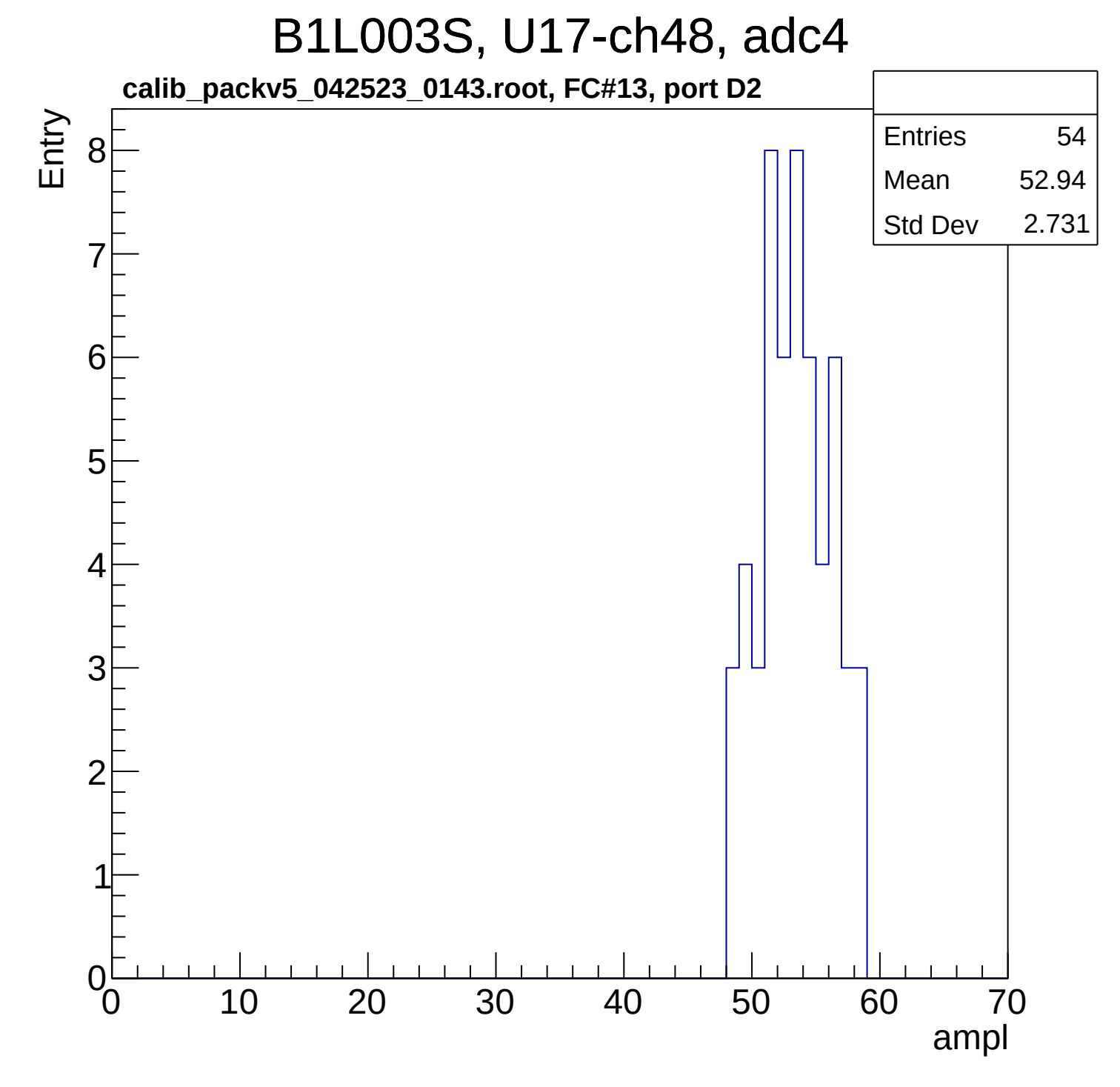
B1L003S, U17-ch48, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 52.94
Std Dev 2.731
Entry
ampl
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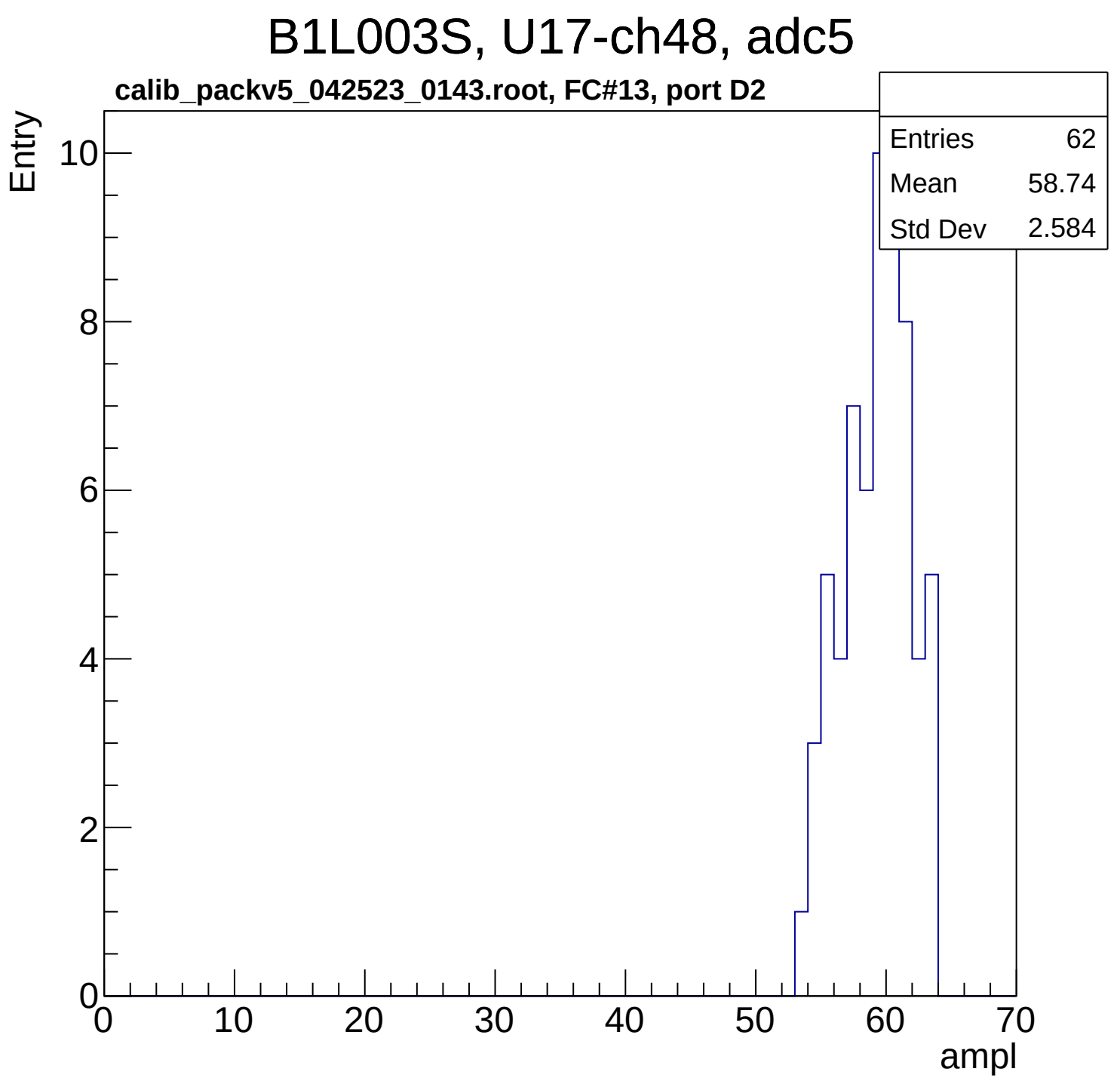
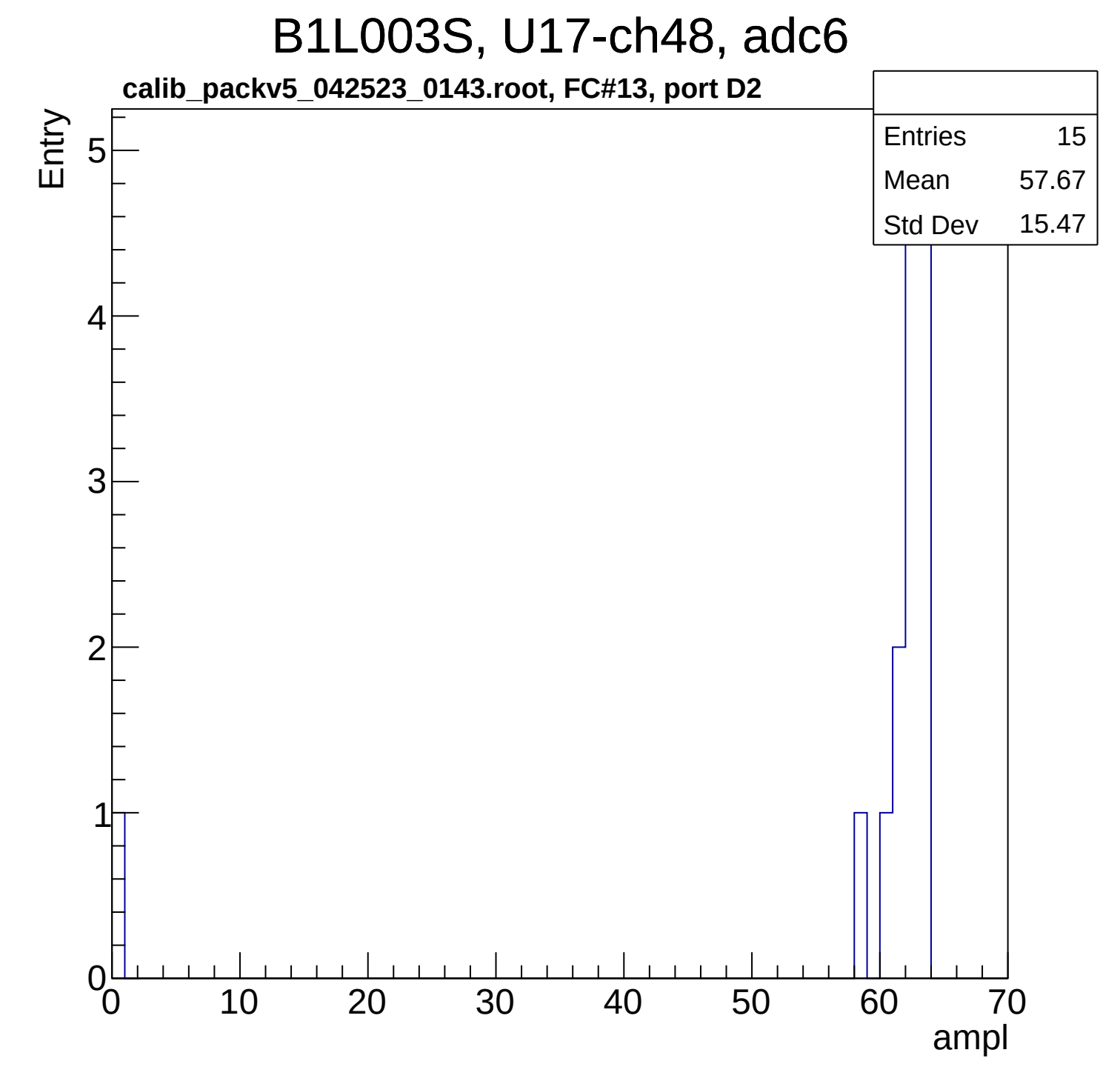
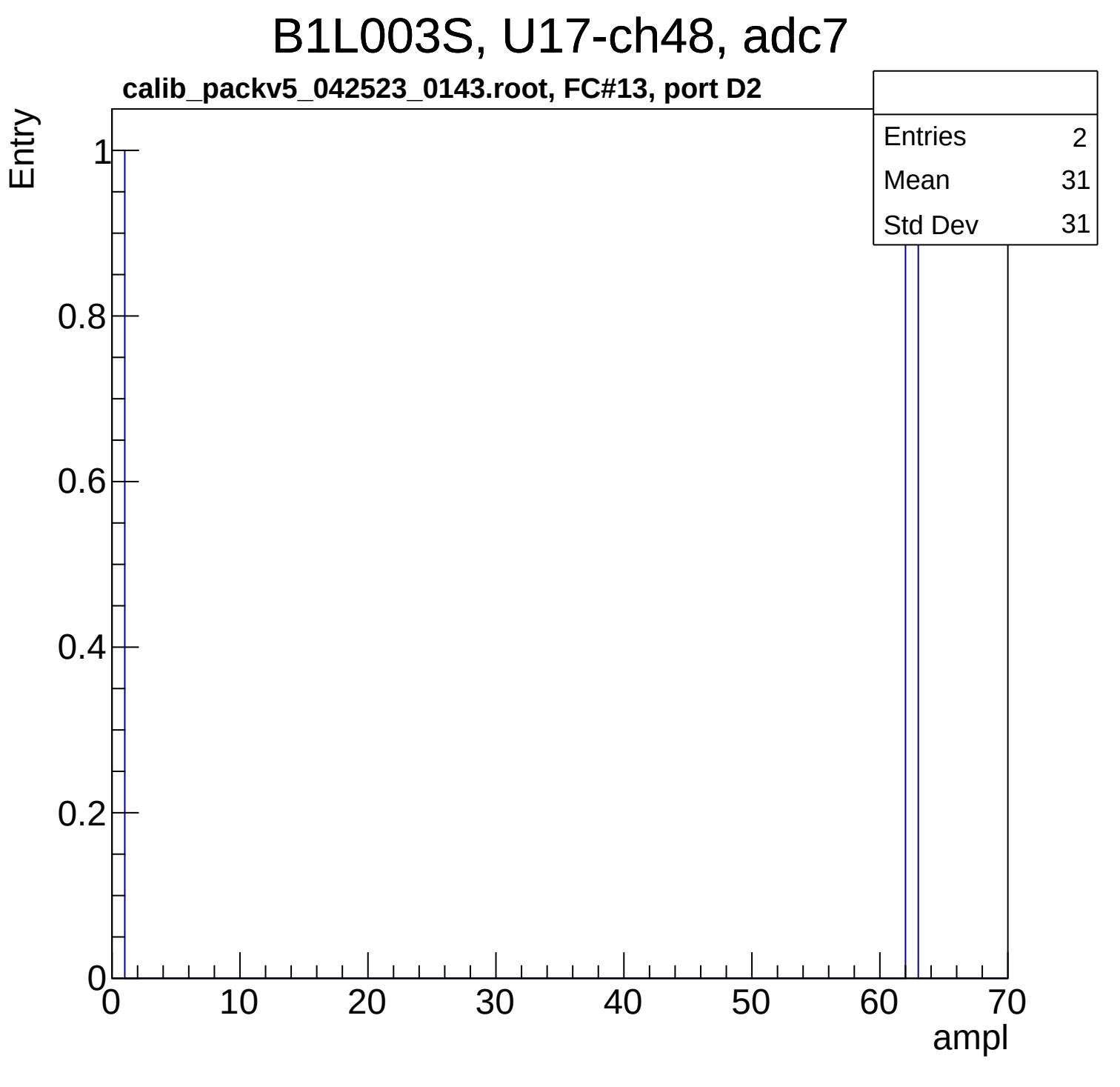
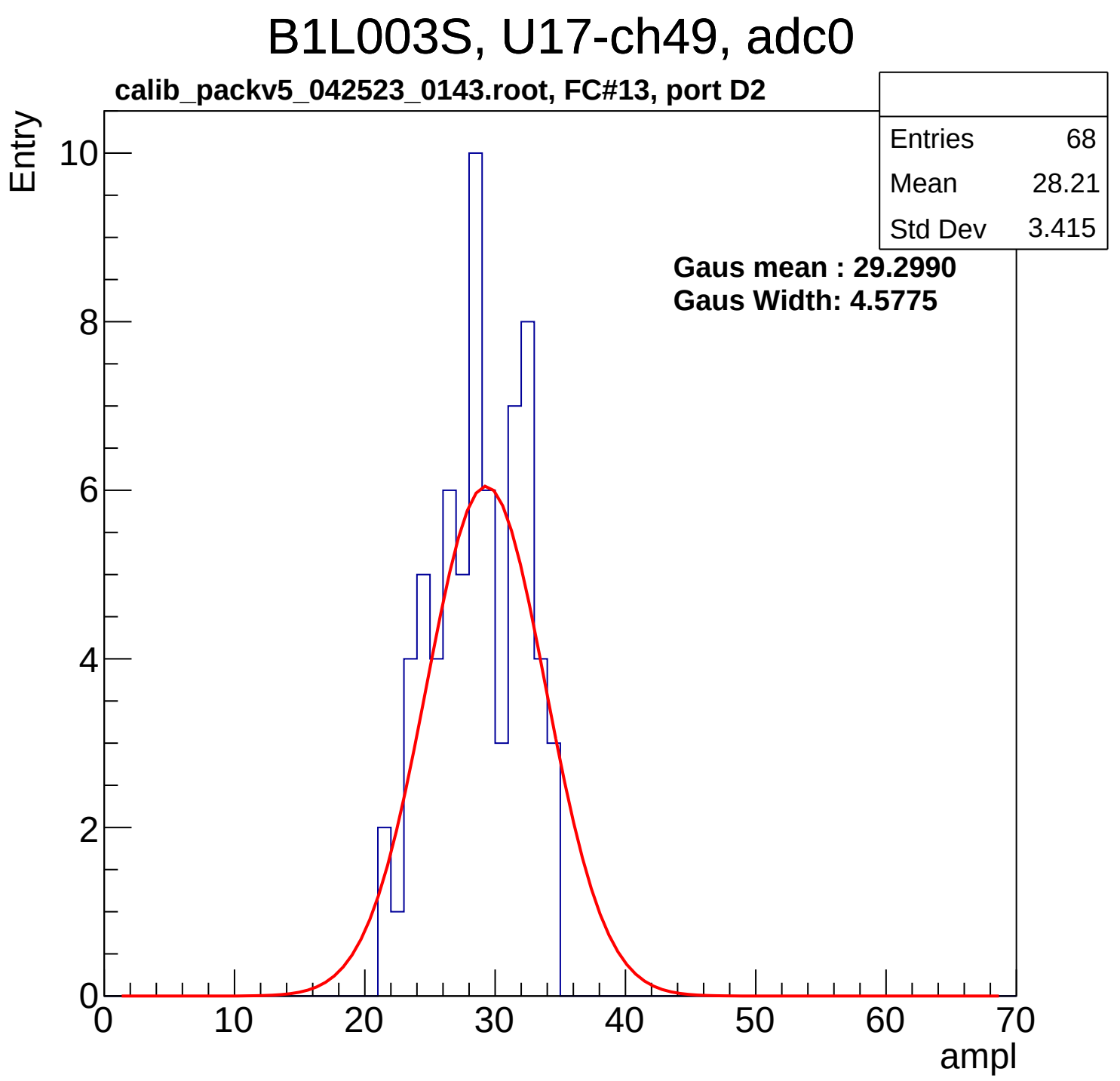
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch48, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.74
Std Dev 2.584
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch48, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.67
Std Dev 15.47
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch48, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch49, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.21
Std Dev 3.415
Gaus mean : 29.2990
Gaus Width: 4.5775
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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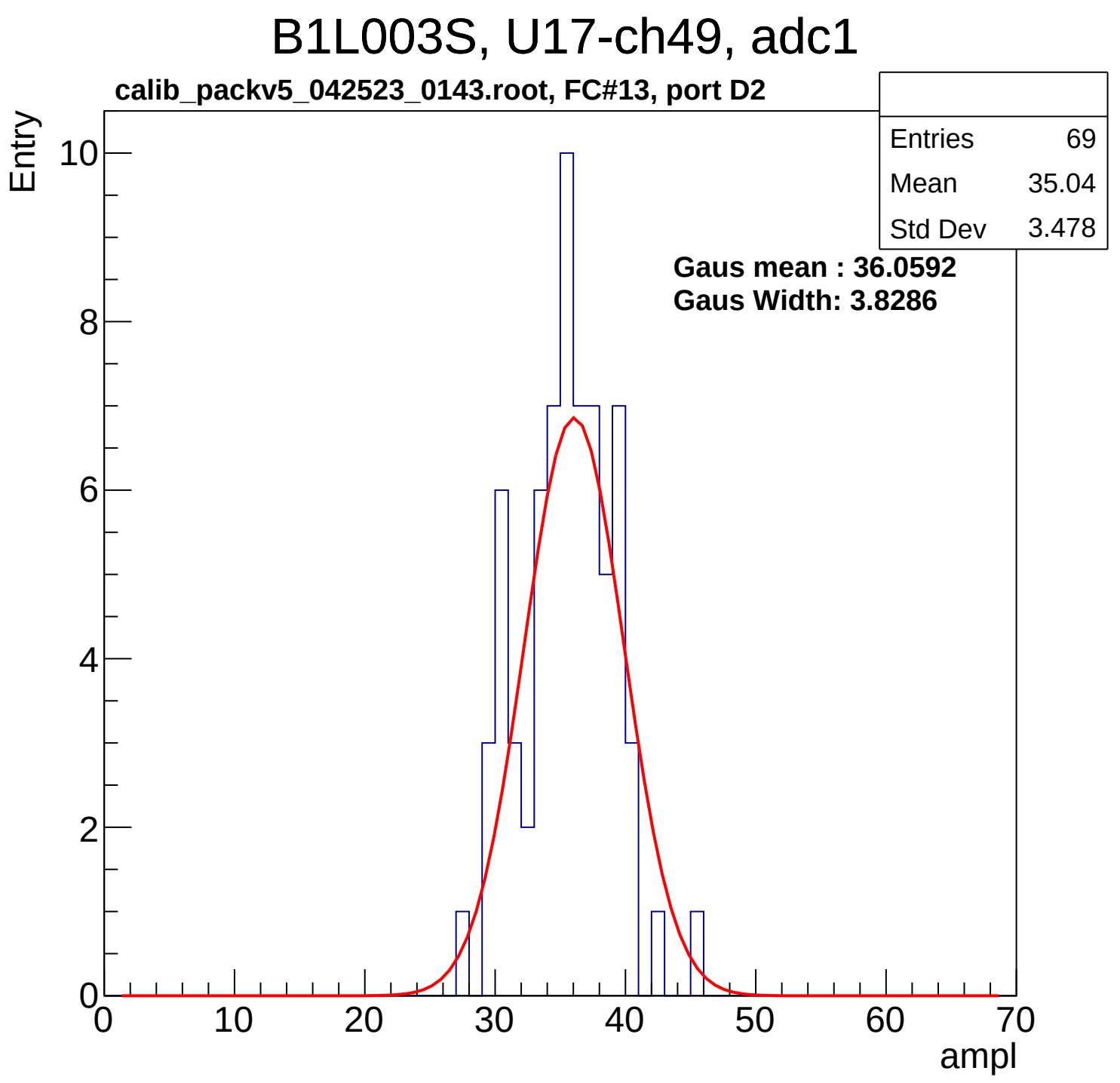
B1L003S, U17-ch49, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.04
Std Dev 3.478
Gaus mean : 36.0592
Gaus Width: 3.8286
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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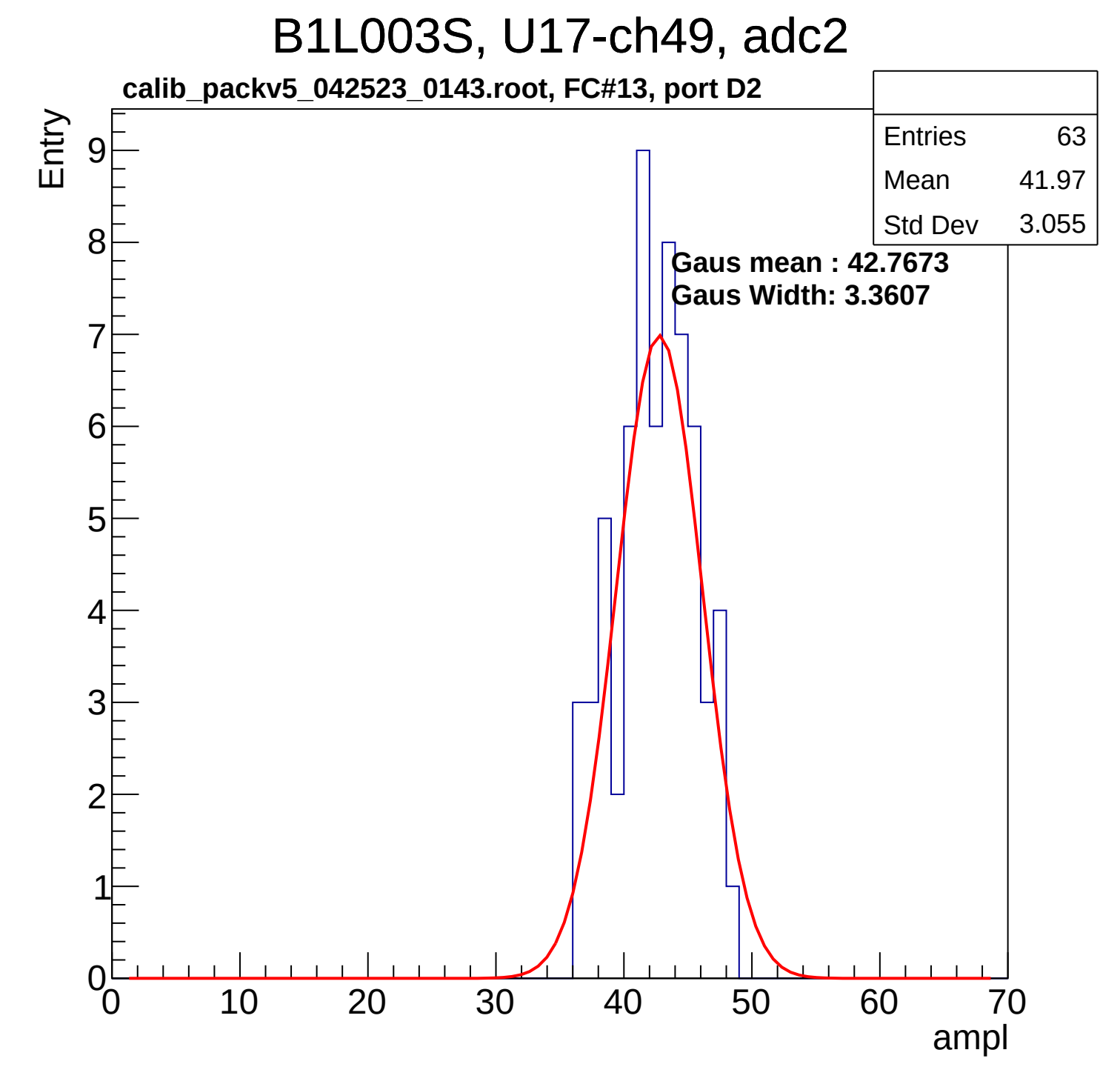
B1L003S, U17-ch49, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 41.97
Std Dev 3.055
Gaus mean : 42.7673
Gaus Width: 3.3607
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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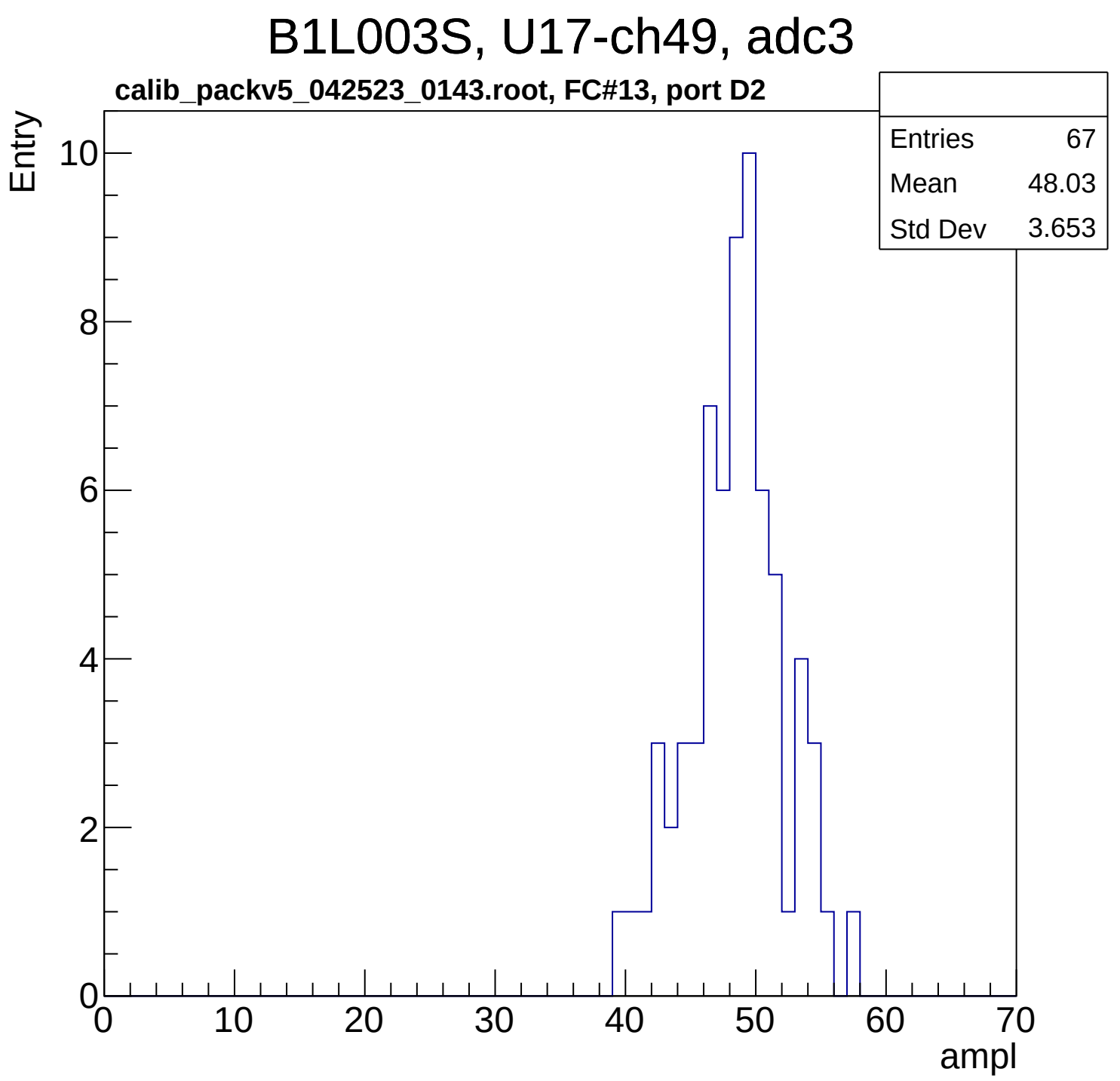
B1L003S, U17-ch49, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.03
Std Dev 3.653
Entry
0
2
4
6
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10
0
10
20
30
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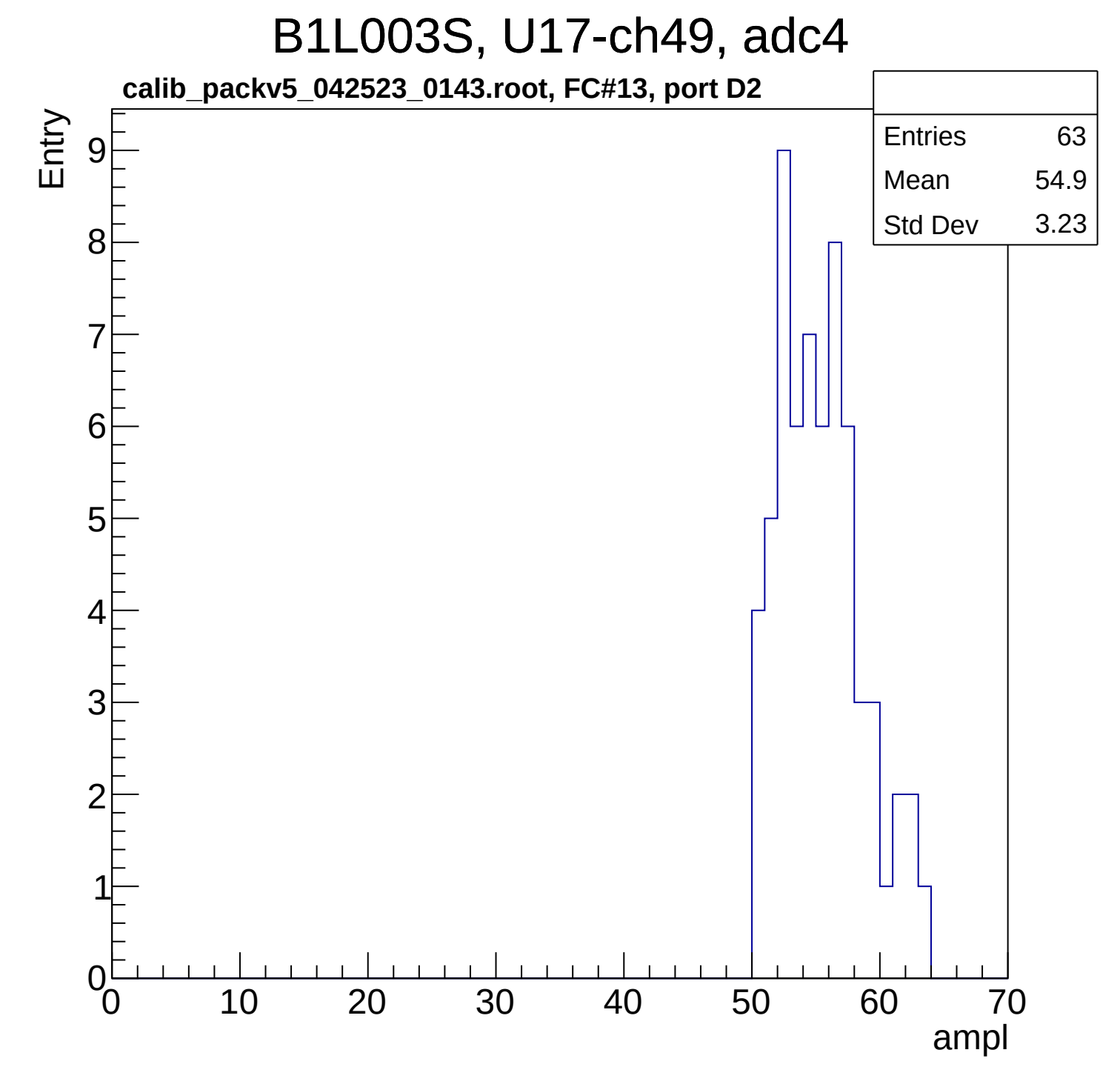
50
60
70
ampl


B1L003S, U17-ch49, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.9
Std Dev 3.23
Entry
ampl
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5
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7
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0
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60
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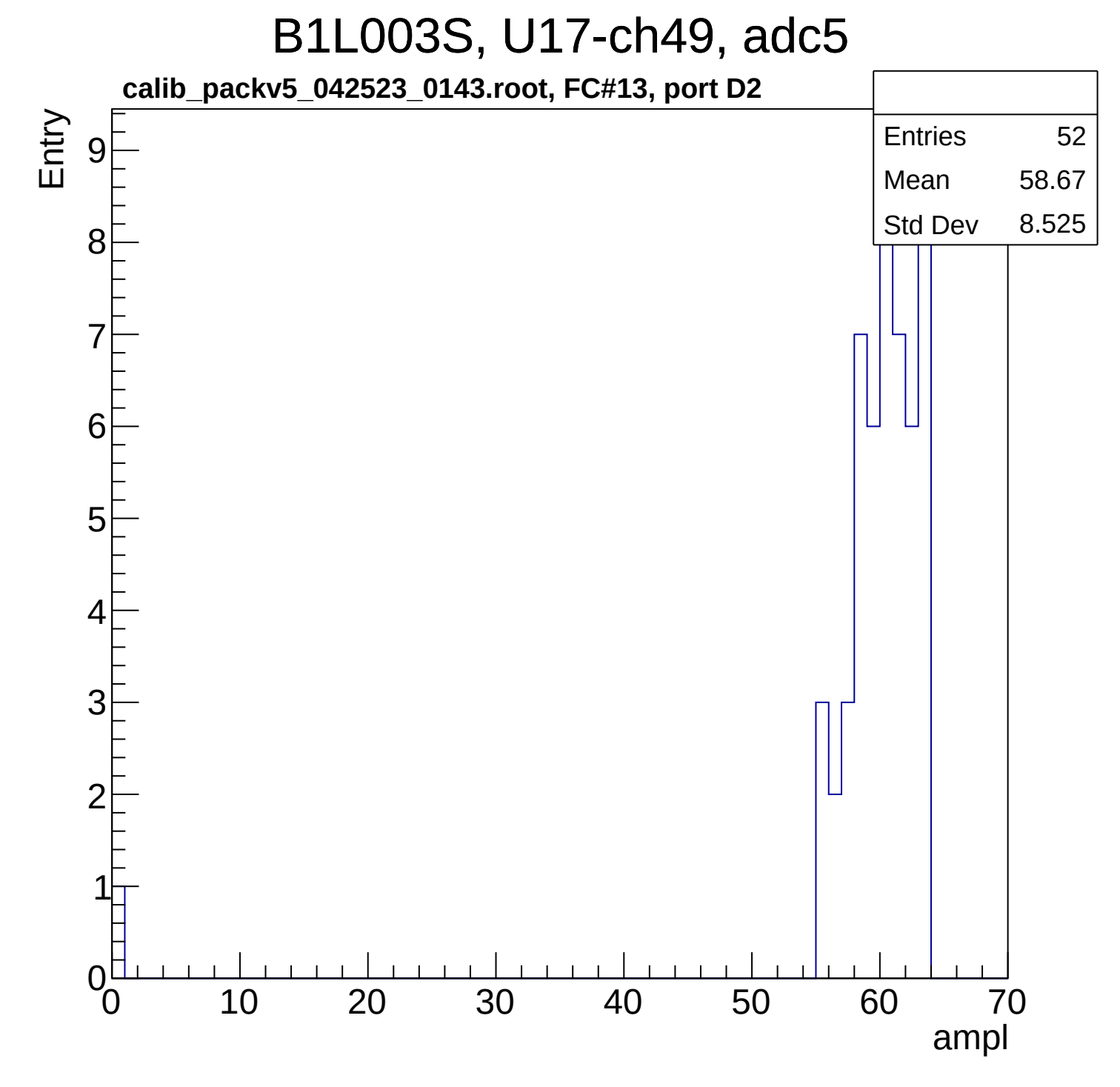
B1L003S, U17-ch49, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.67
Std Dev 8.525
Entry
ampl
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60
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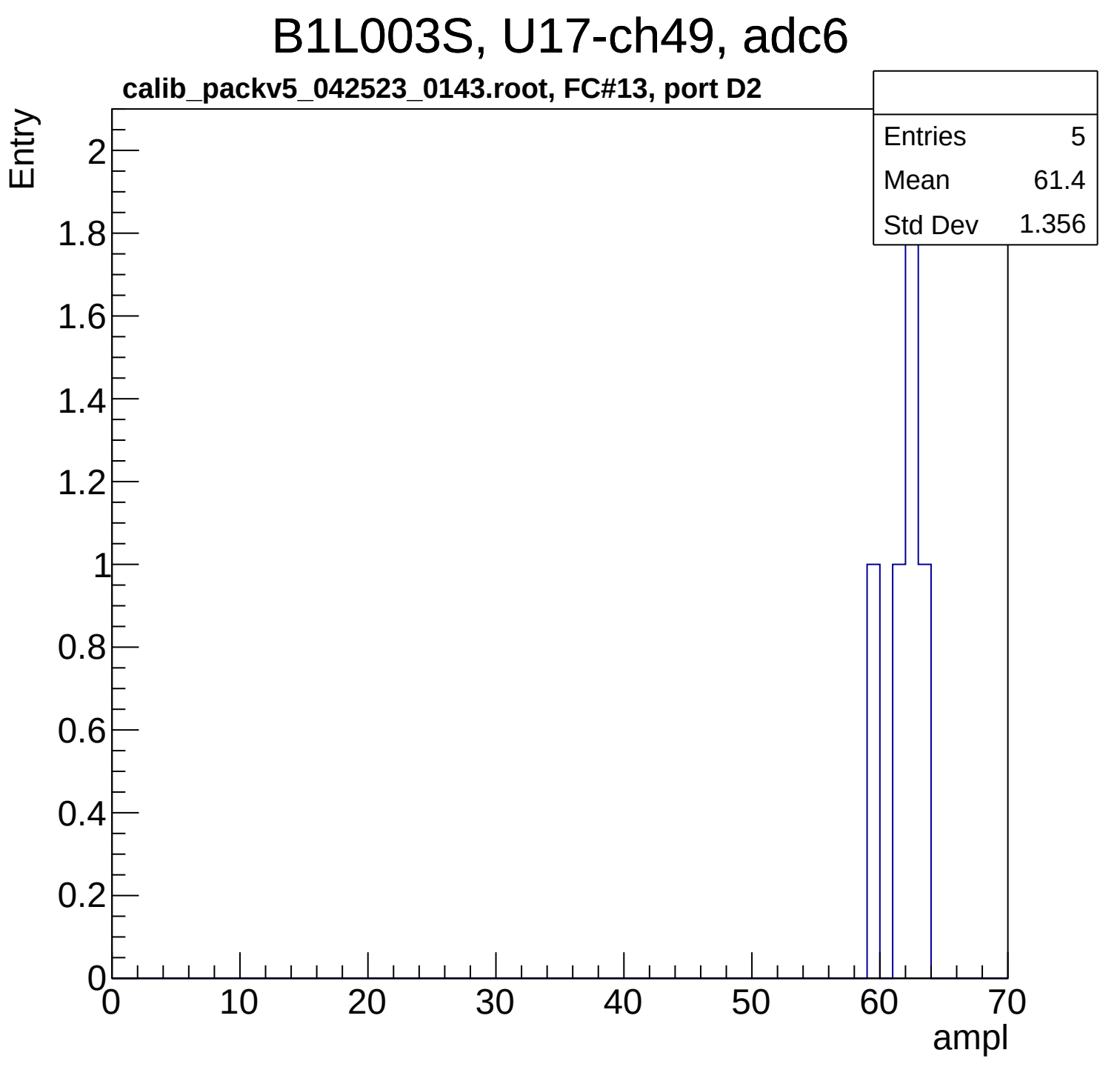
B1L003S, U17-ch49, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.4
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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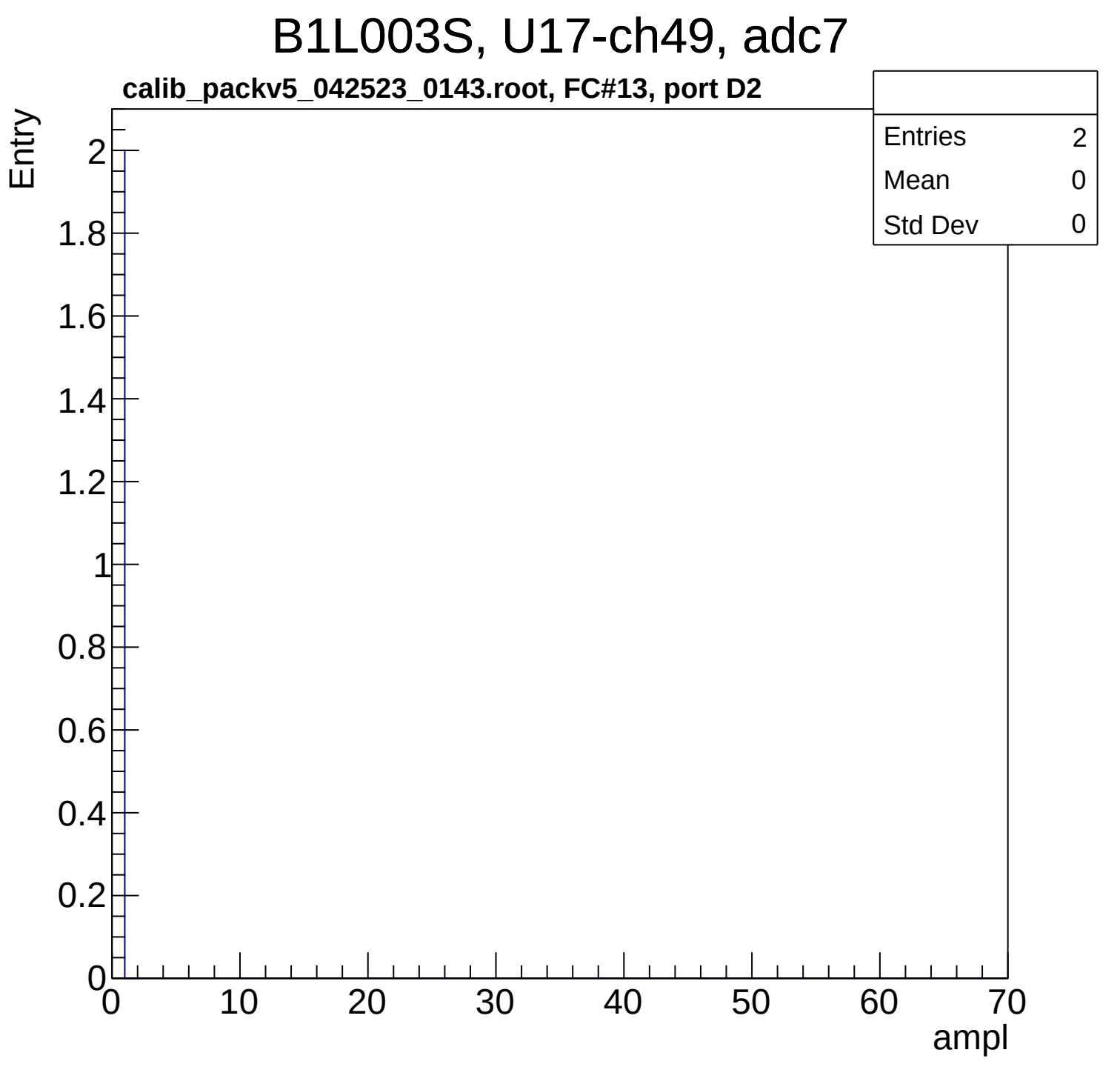
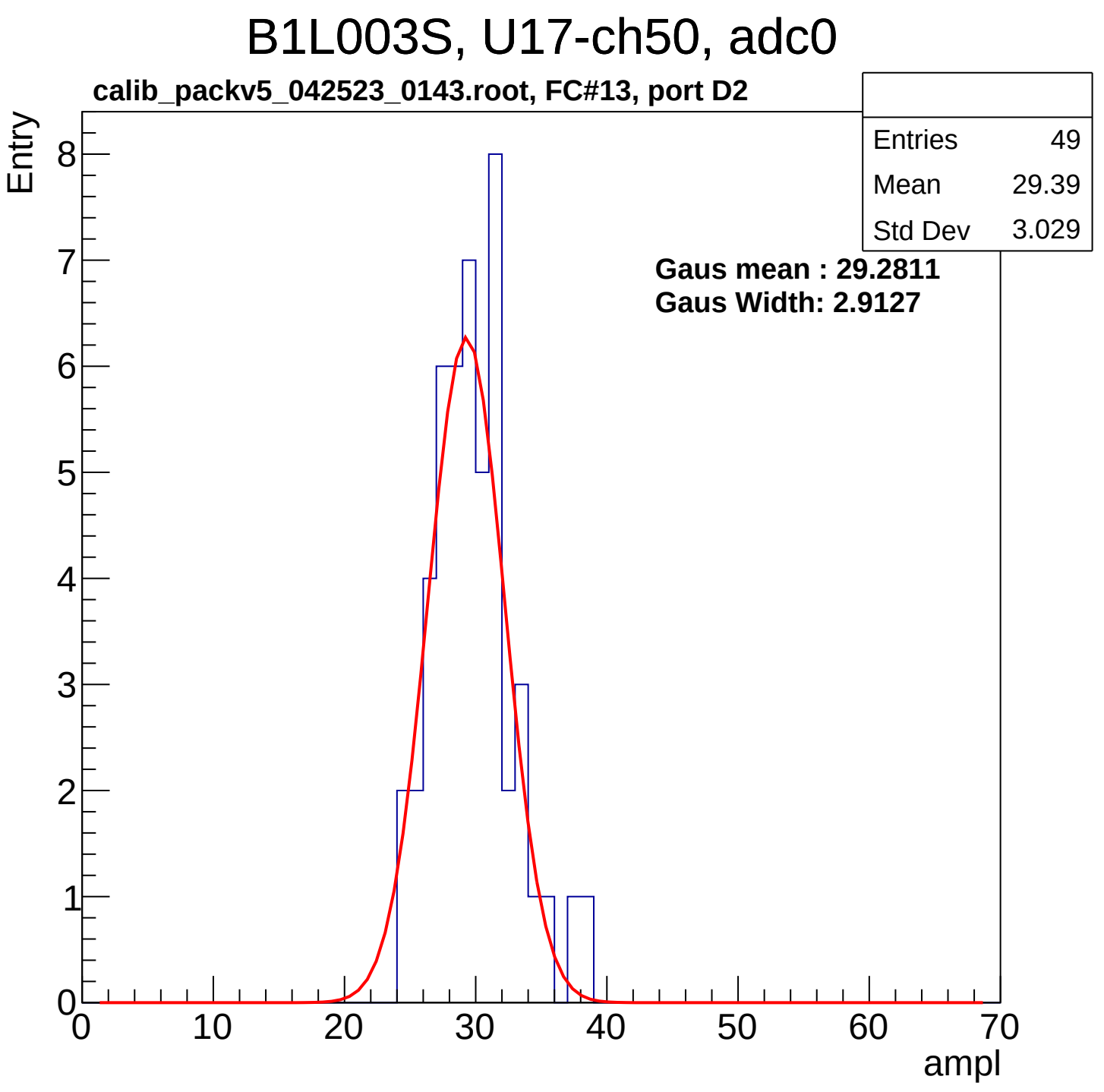
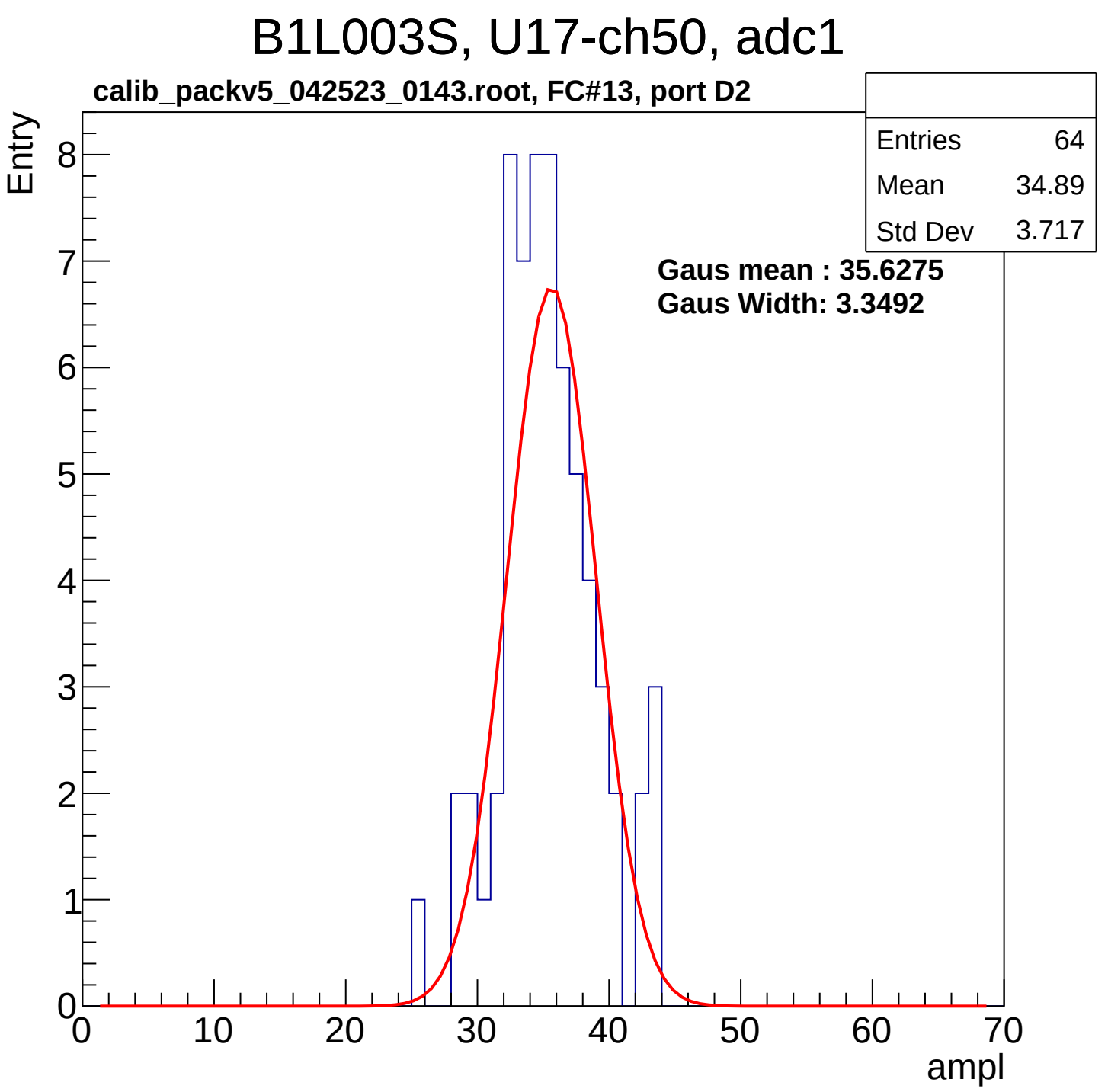

B1L003S, U17-ch49, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch50, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 29.39
Std Dev 3.029
Gaus mean : 29.2811
Gaus Width: 2.9127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch50, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 34.89
Std Dev 3.717
Gaus mean : 35.6275
Gaus Width: 3.3492
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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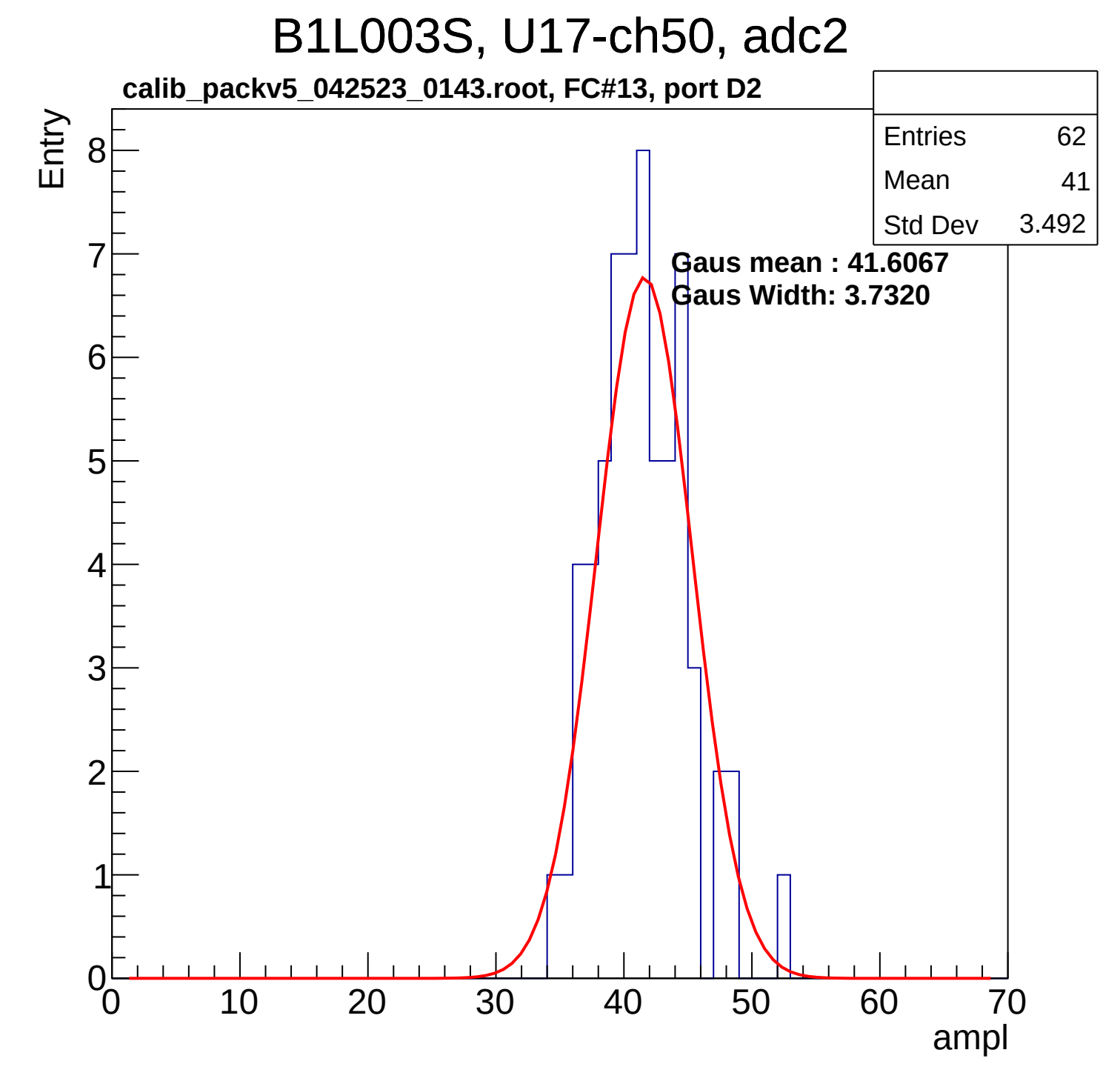
B1L003S, U17-ch50, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 41
Std Dev 3.492
Gaus mean : 41.6067
Gaus Width: 3.7320
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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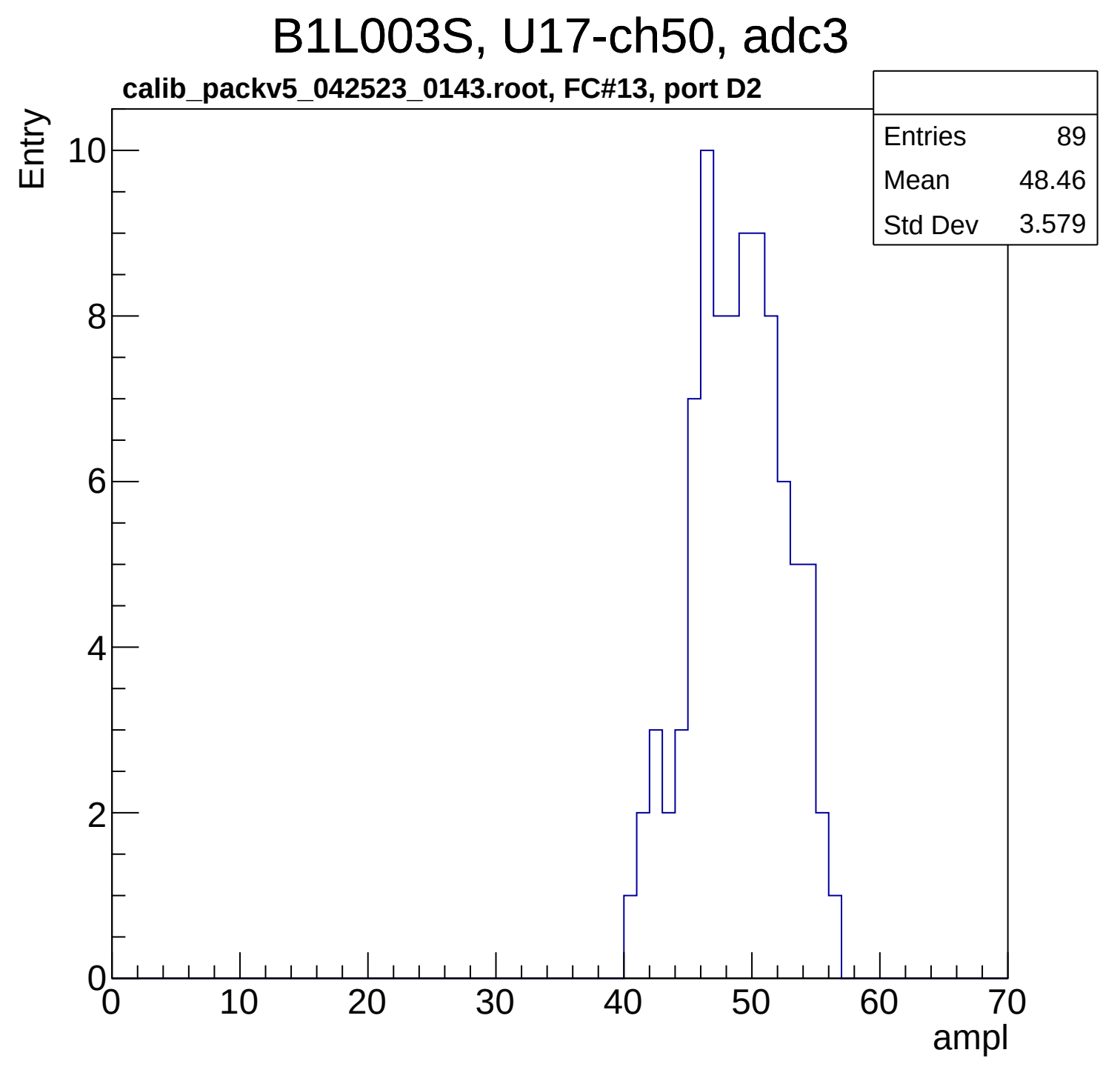
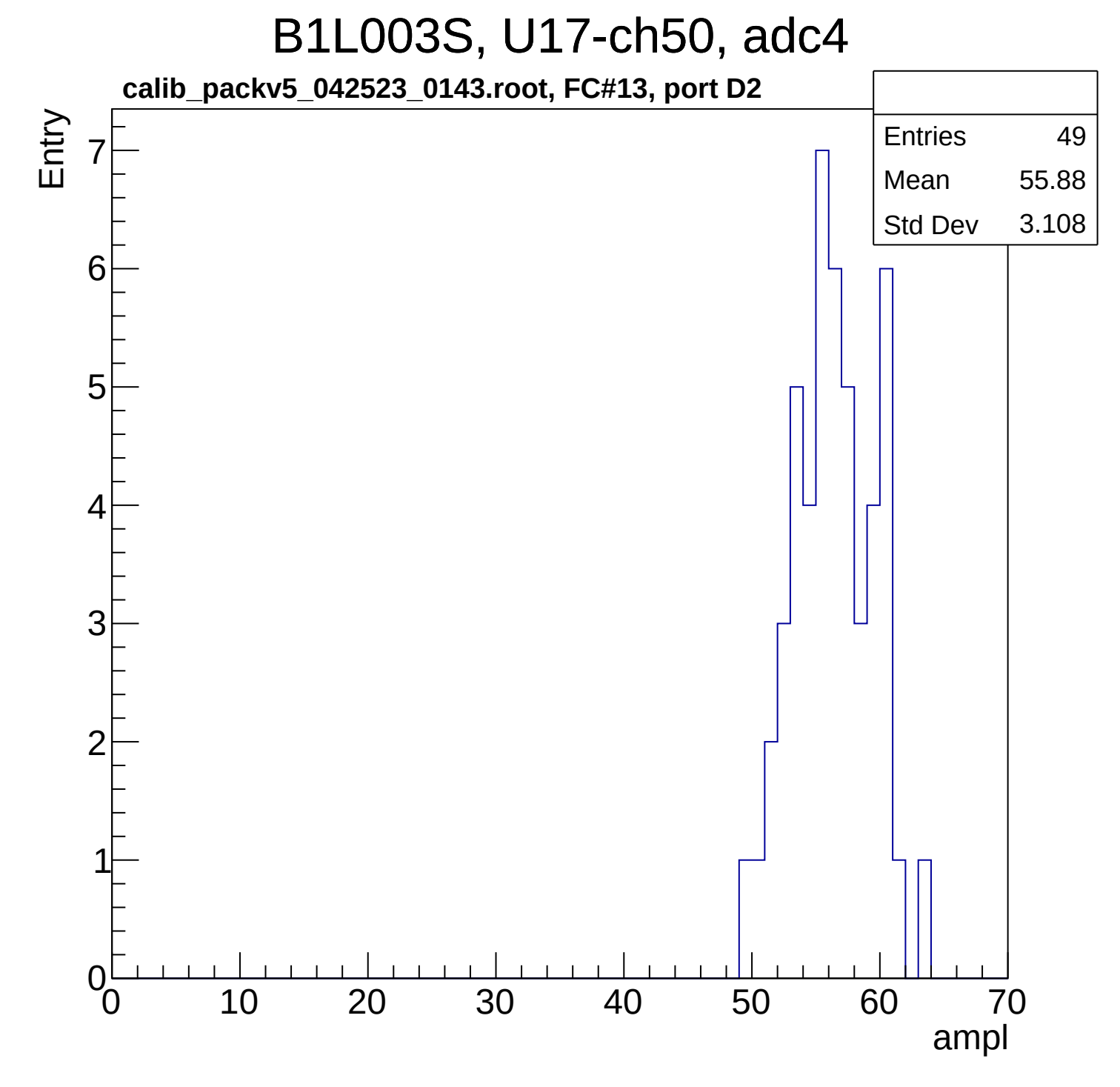

B1L003S, U17-ch50, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 89
Mean 48.46
Std Dev 3.579
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch50, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 55.88
Std Dev 3.108
Entry
ampl
0
1
2
3
4
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6
7
0
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60
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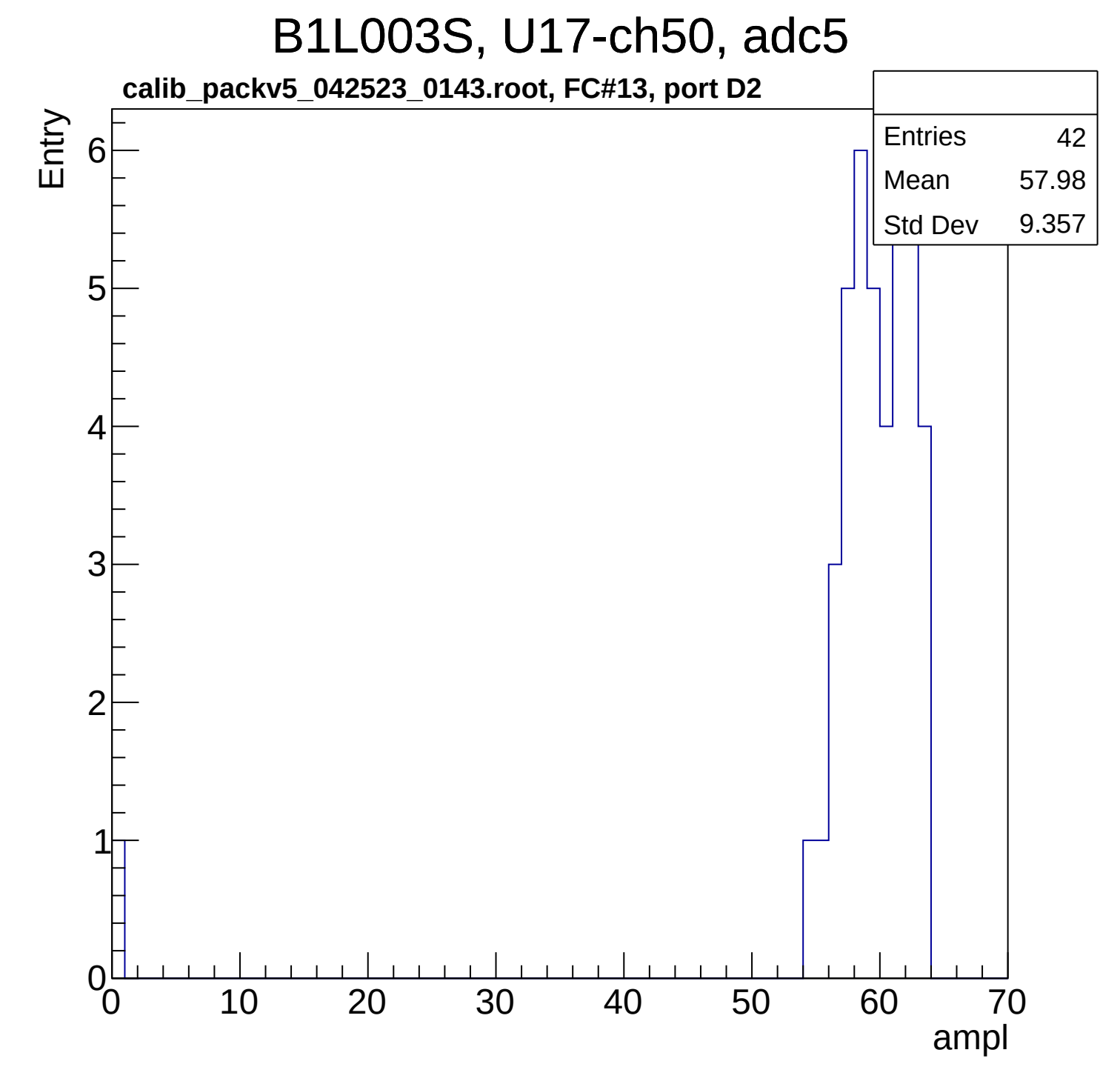
B1L003S, U17-ch50, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 57.98
Std Dev 9.357
Entry
ampl
0
1
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3
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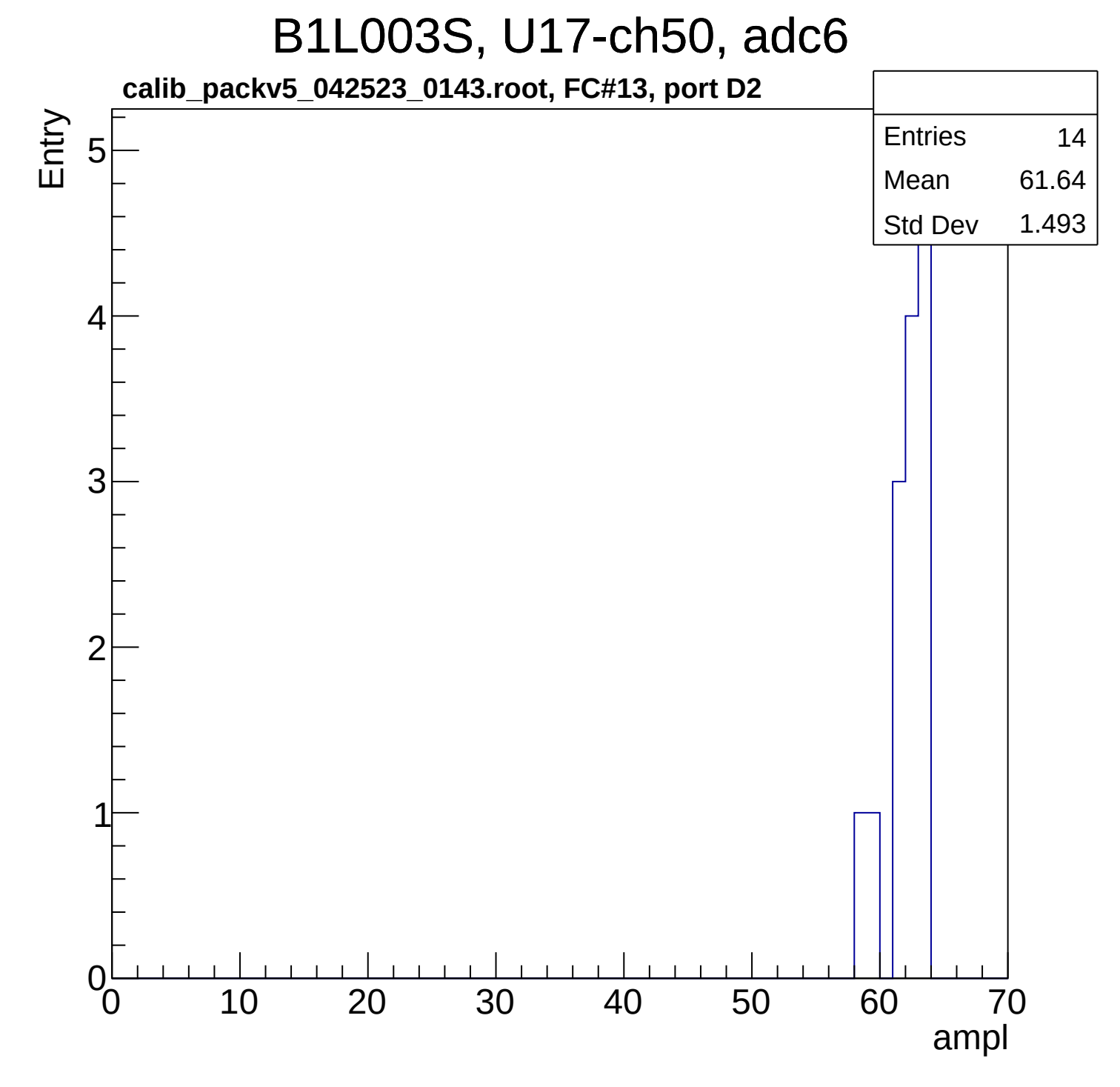
B1L003S, U17-ch50, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.64
Std Dev 1.493
Entry
ampl
0
1
2
3
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0
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40
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60
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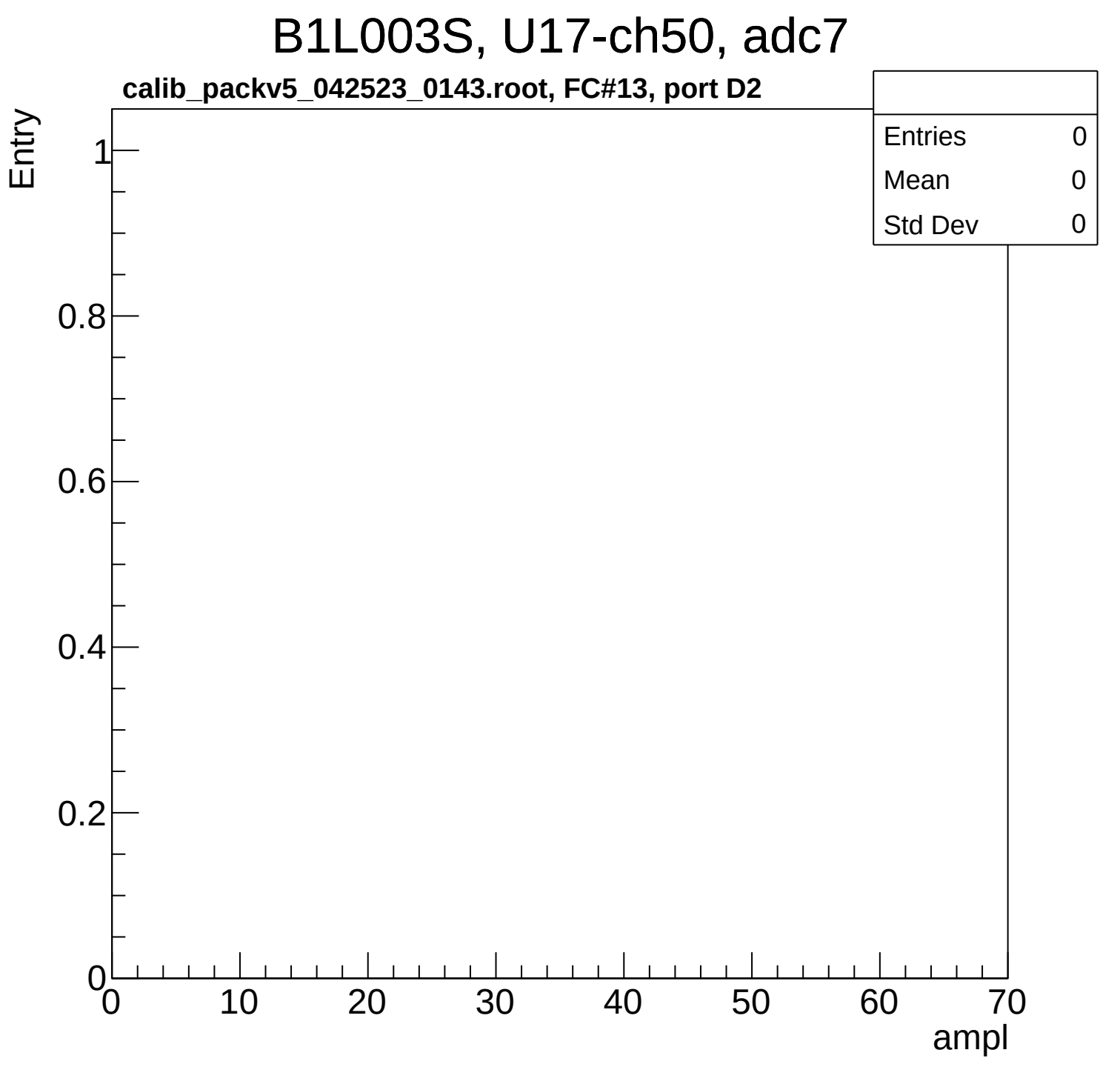
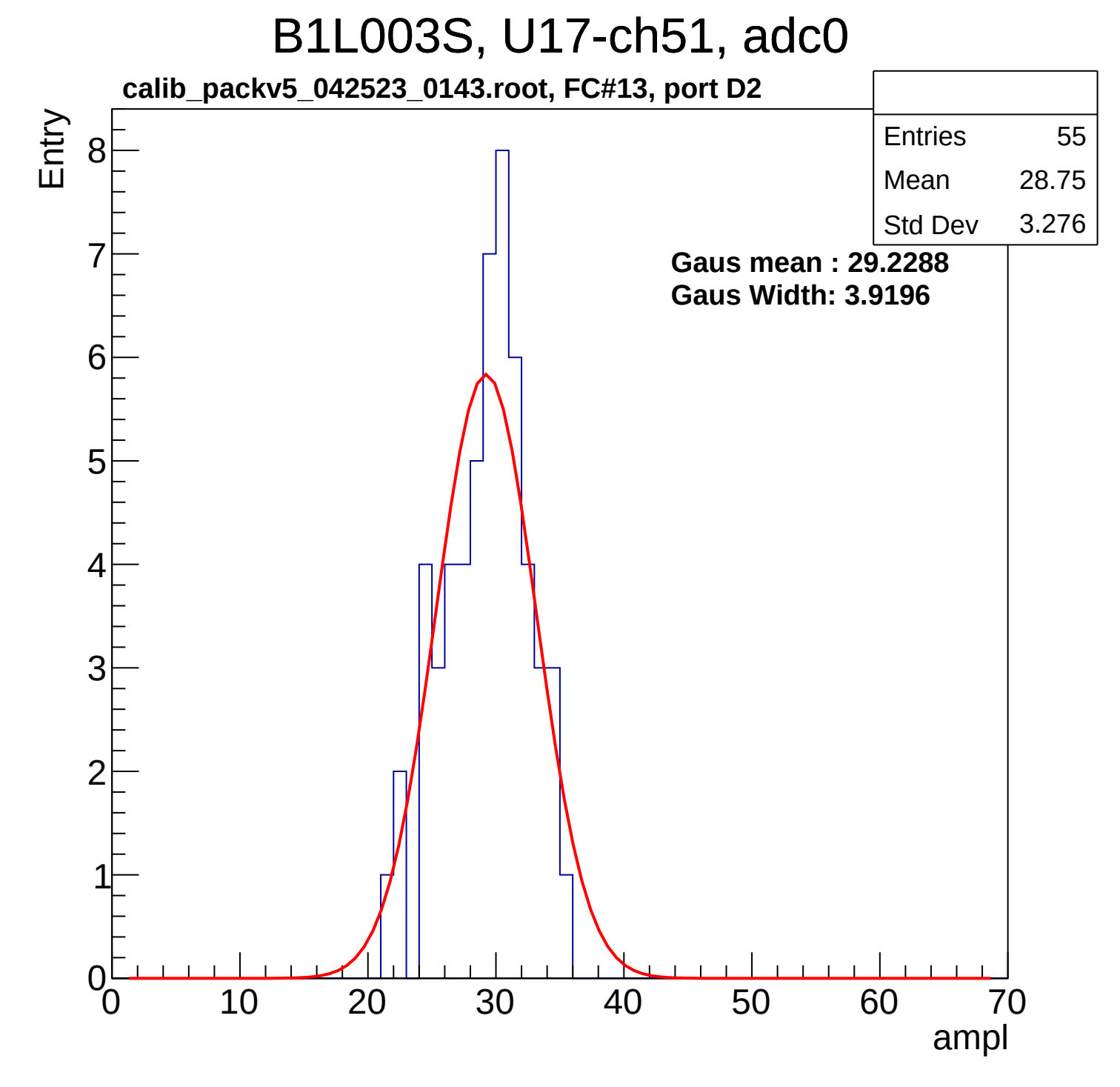

B1L003S, U17-ch50, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch51, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 28.75
Std Dev 3.276
Gaus mean : 29.2288
Gaus Width: 3.9196
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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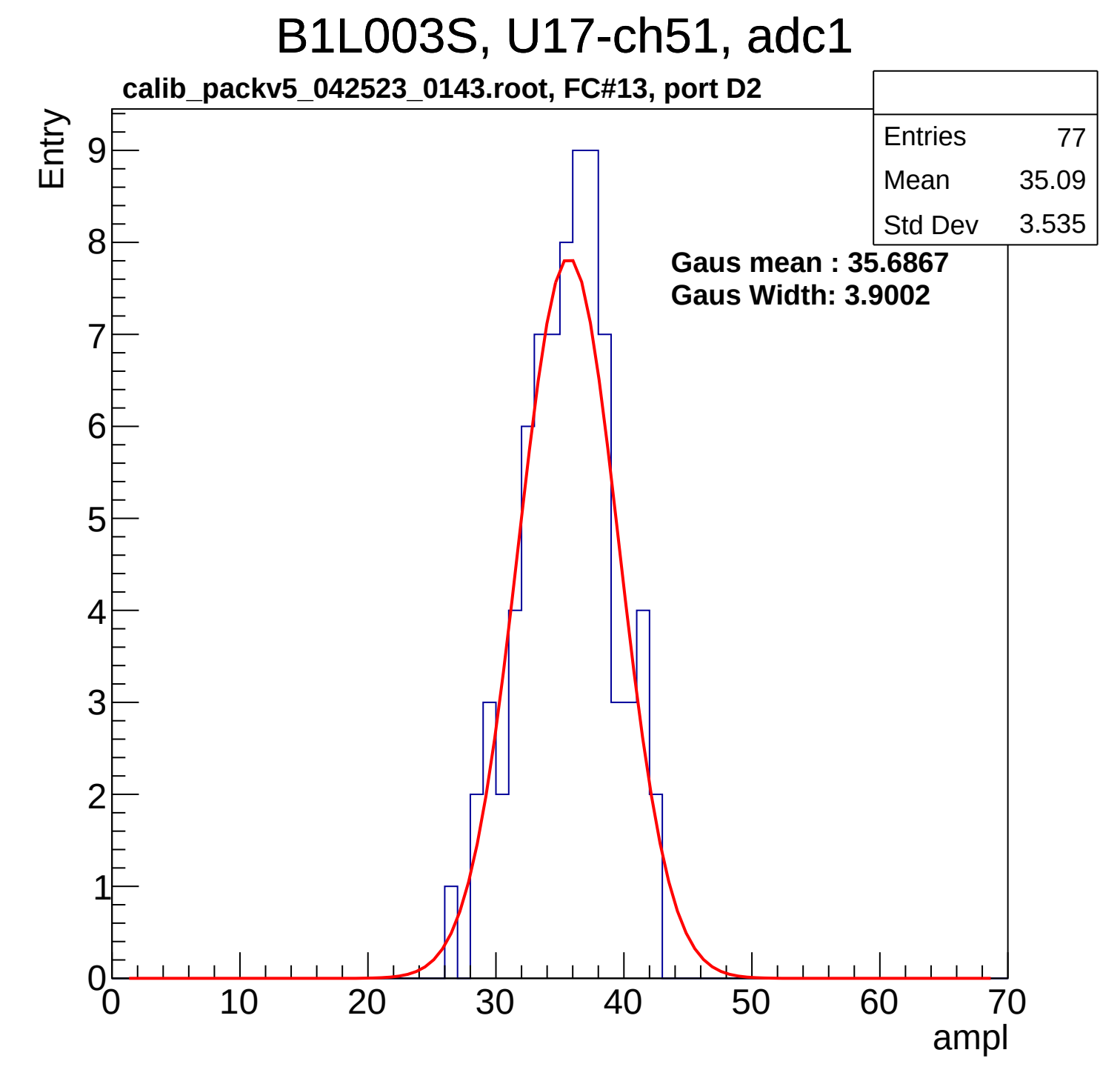
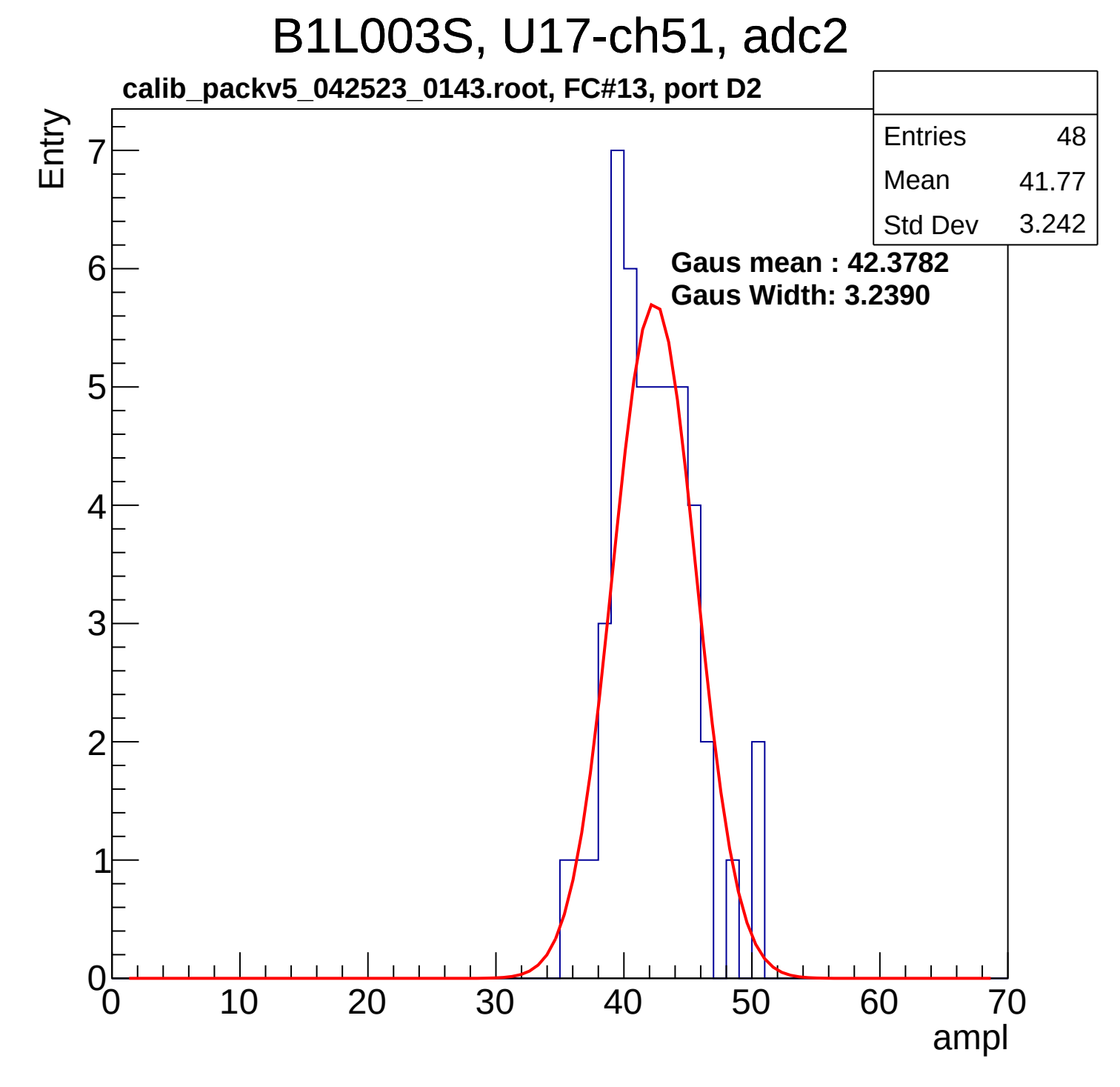

B1L003S, U17-ch51, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 35.09
Std Dev 3.535
Gaus mean : 35.6867
Gaus Width: 3.9002
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch51, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 41.77
Std Dev 3.242
Gaus mean : 42.3782
Gaus Width: 3.2390
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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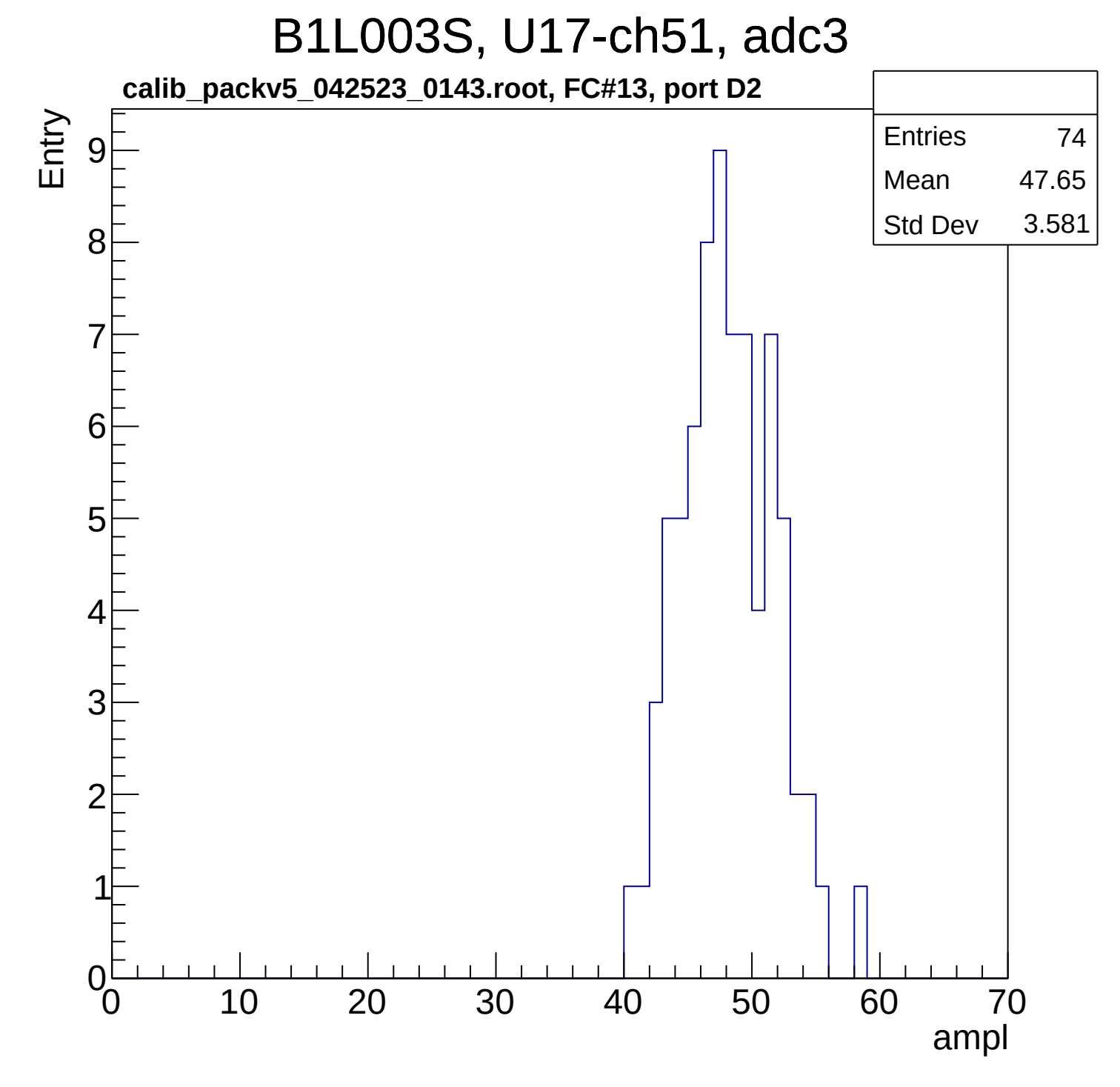
B1L003S, U17-ch51, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 47.65
Std Dev 3.581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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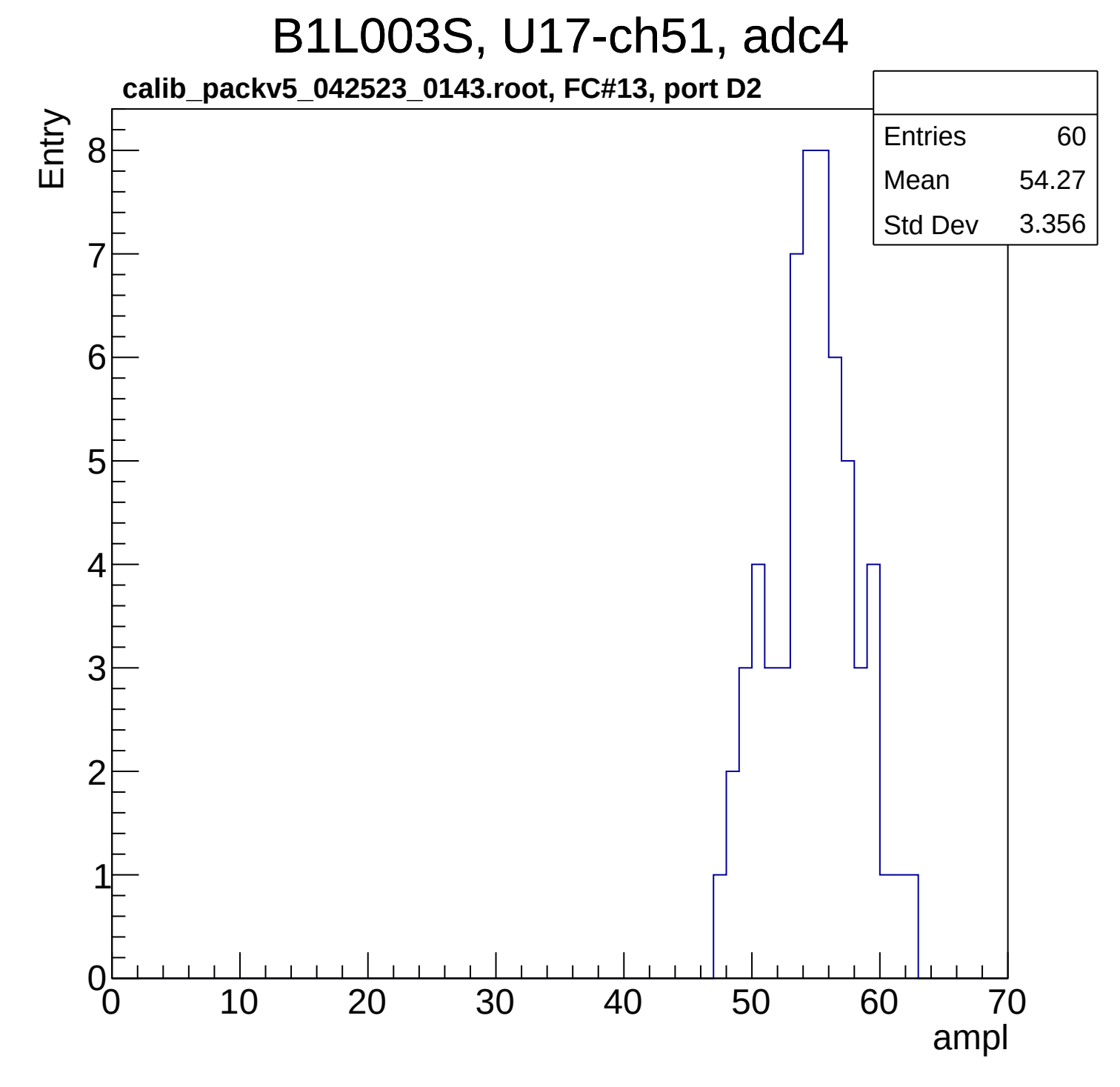
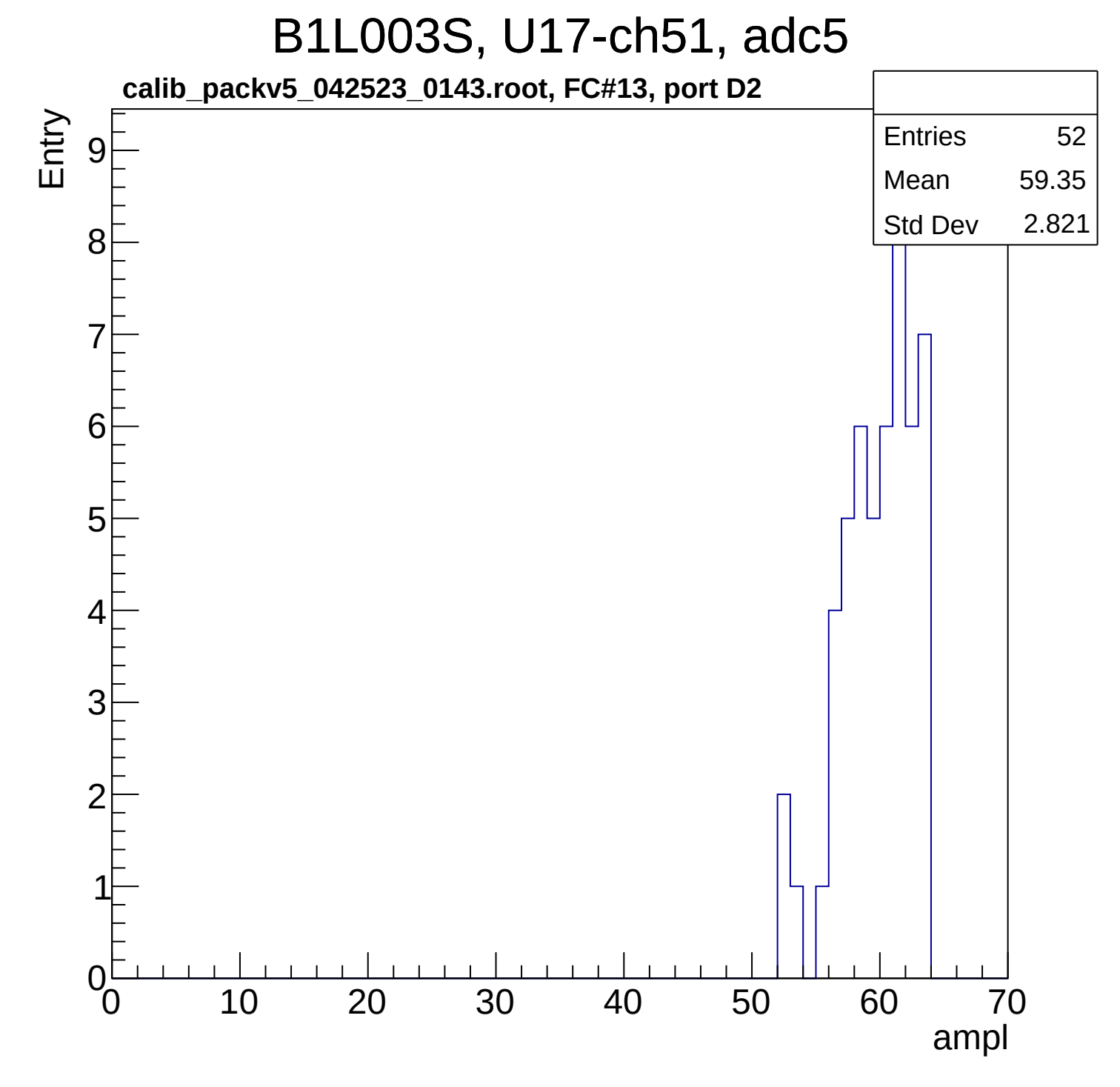
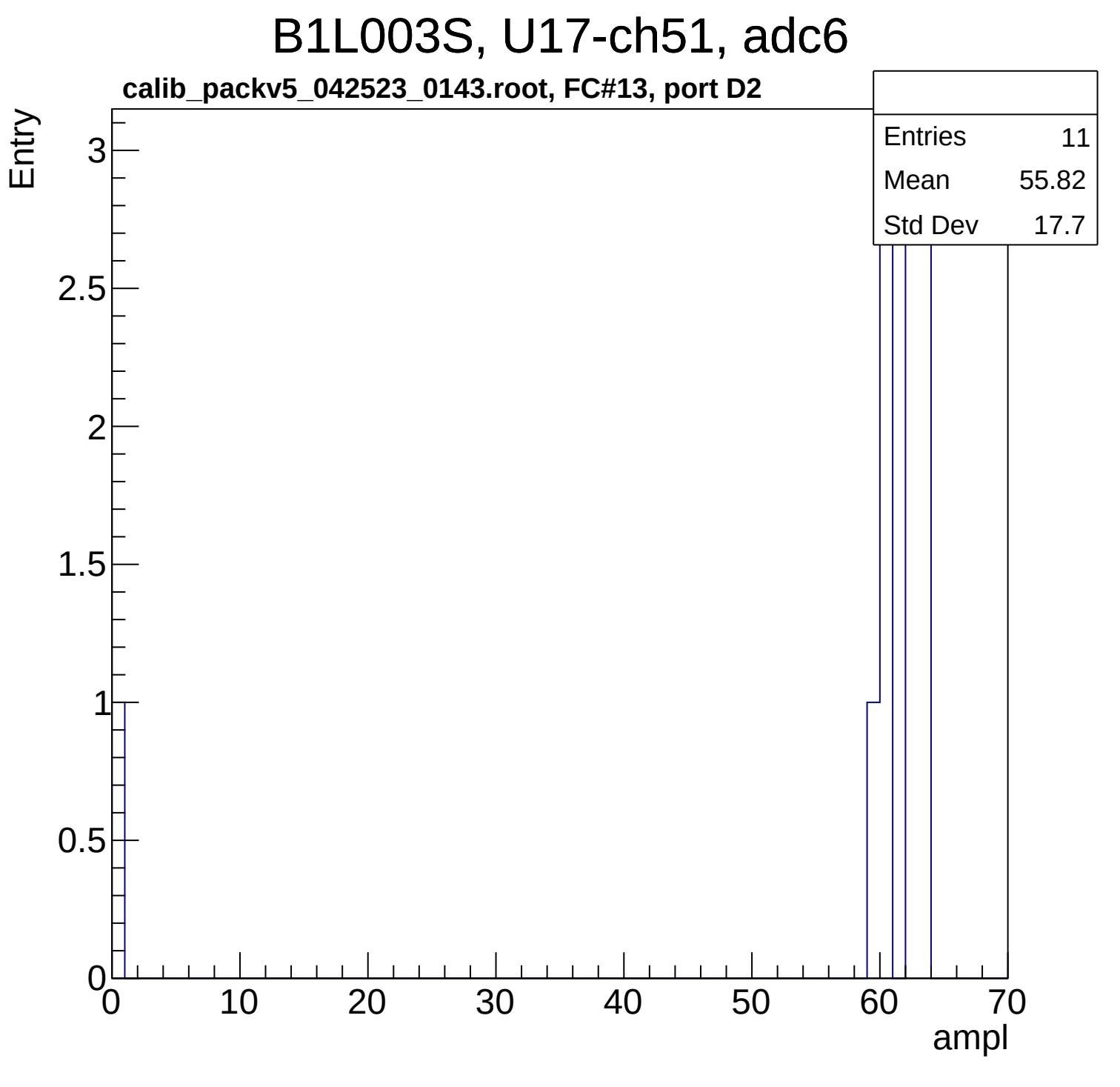
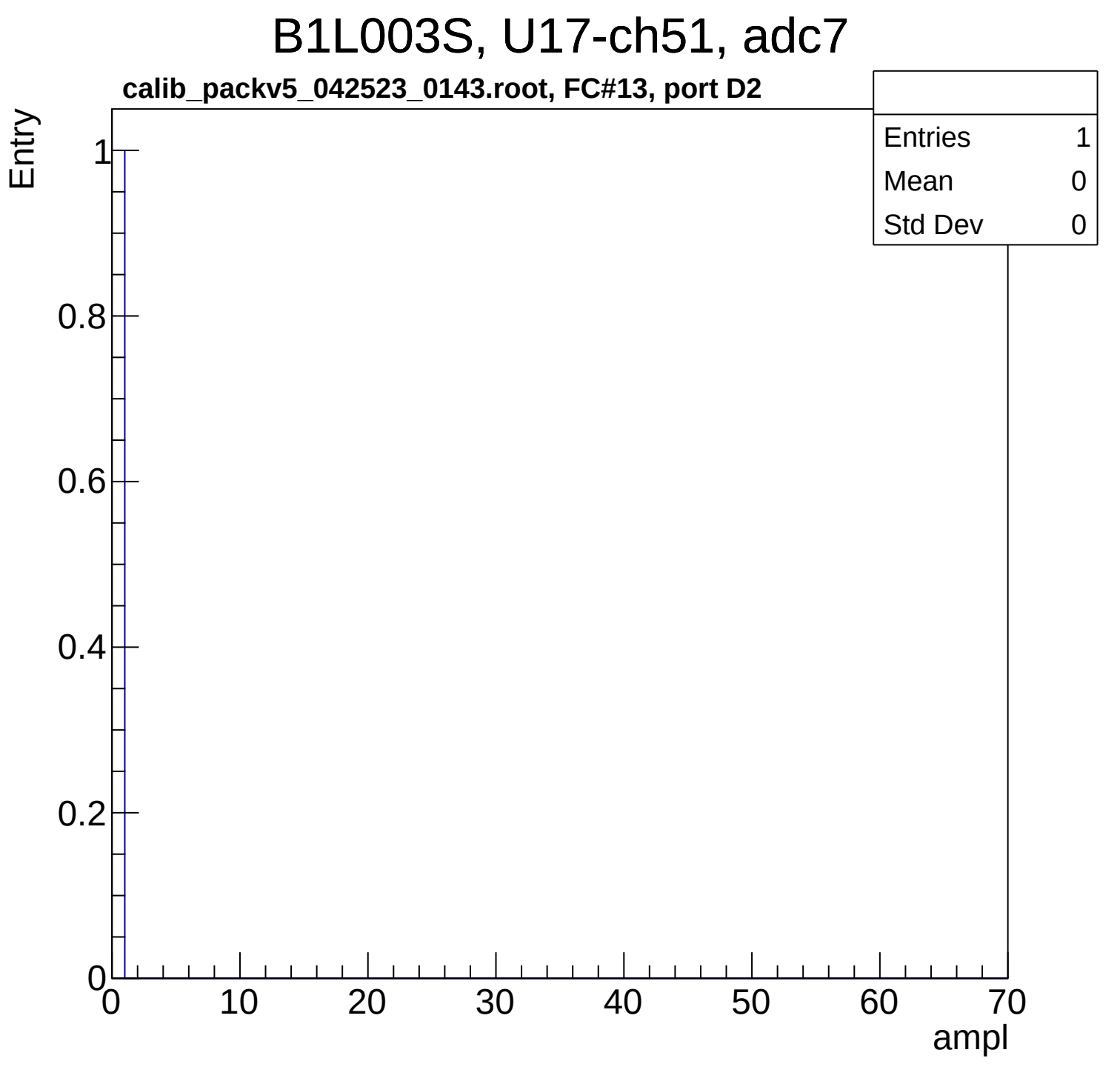
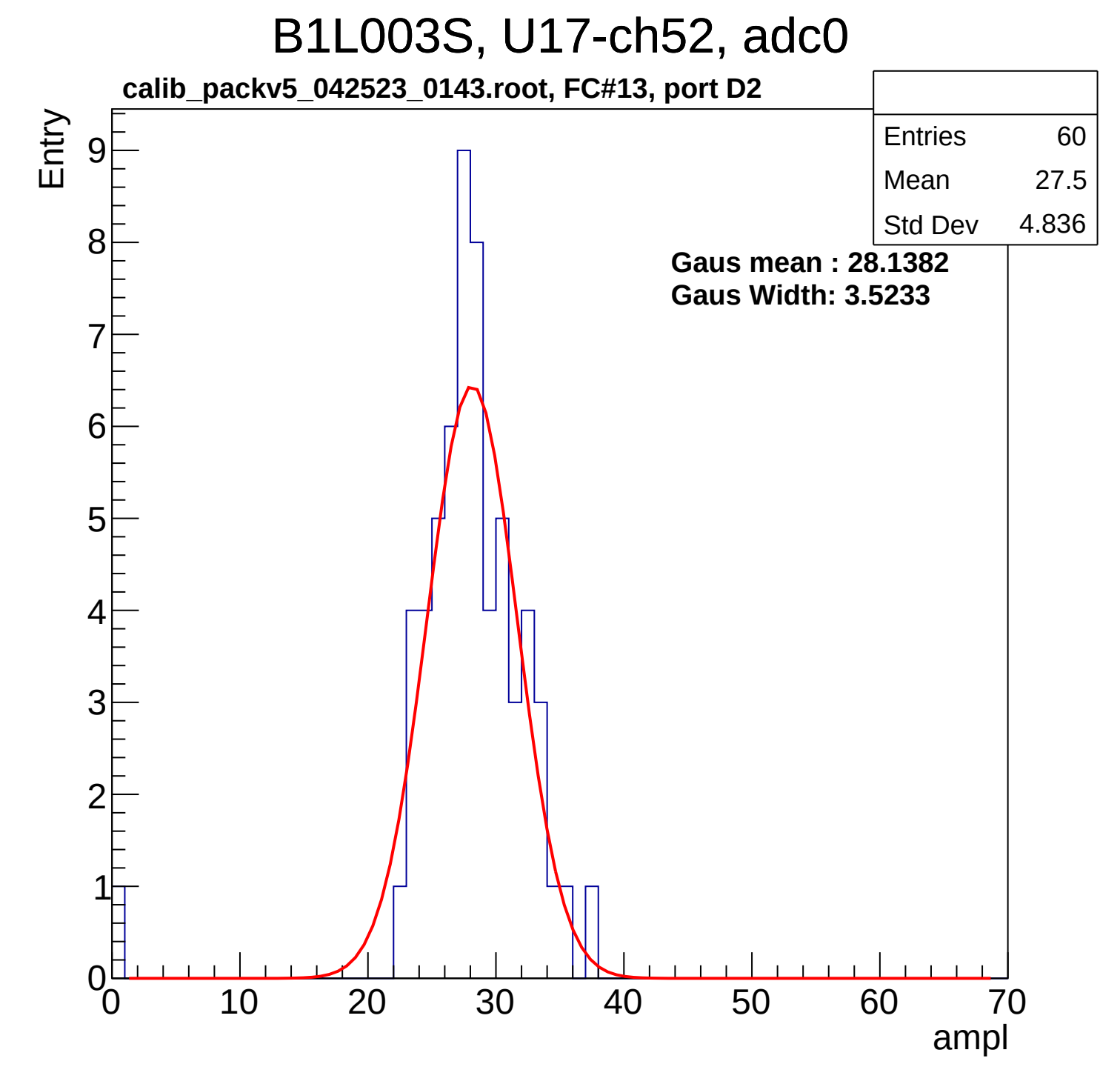
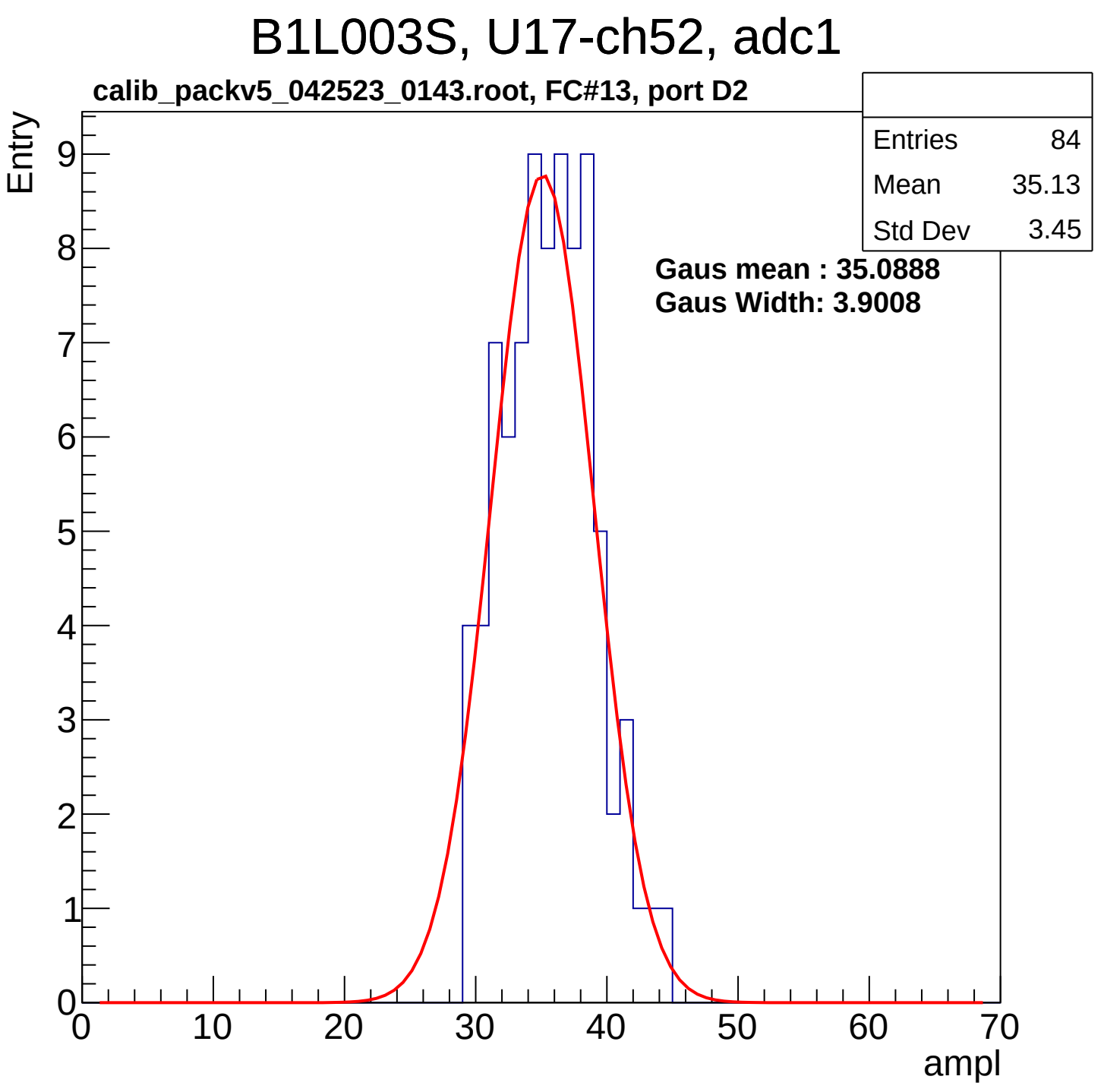

B1L003S, U17-ch51, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.27
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

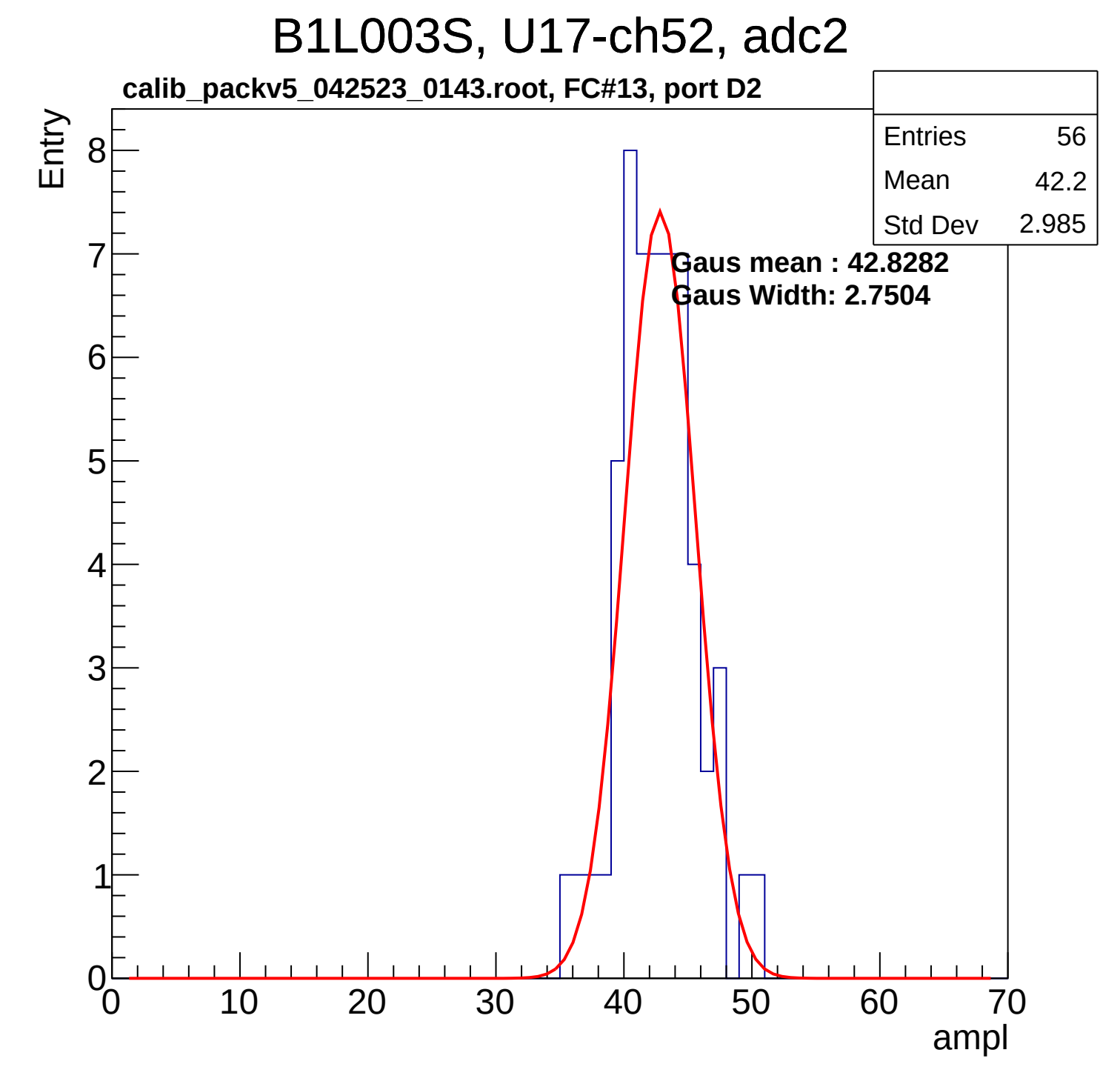

B1L003S, U17-ch51, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.35
Std Dev 2.821
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

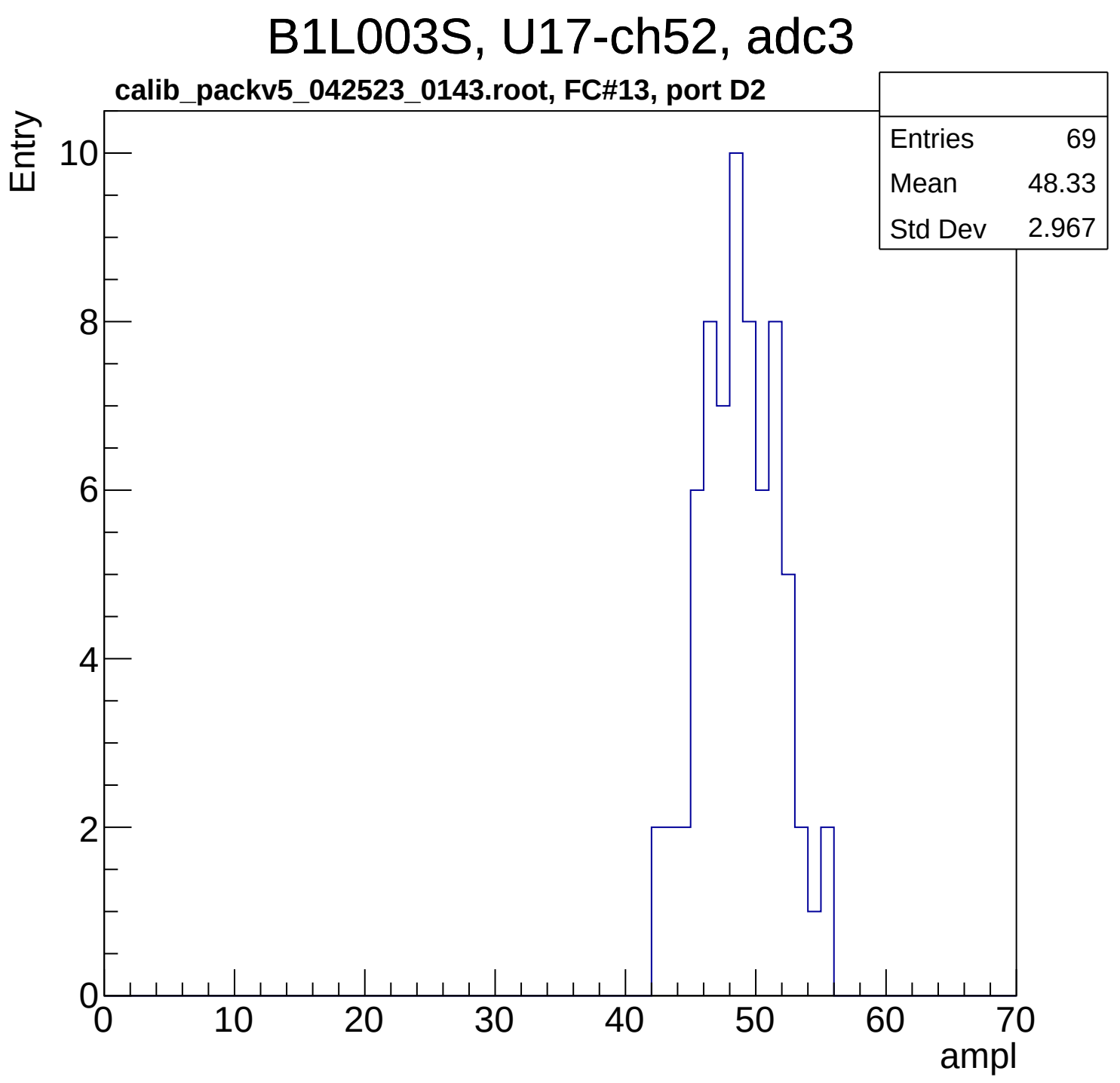

B1L003S, U17-ch51, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.82
Std Dev 17.7
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

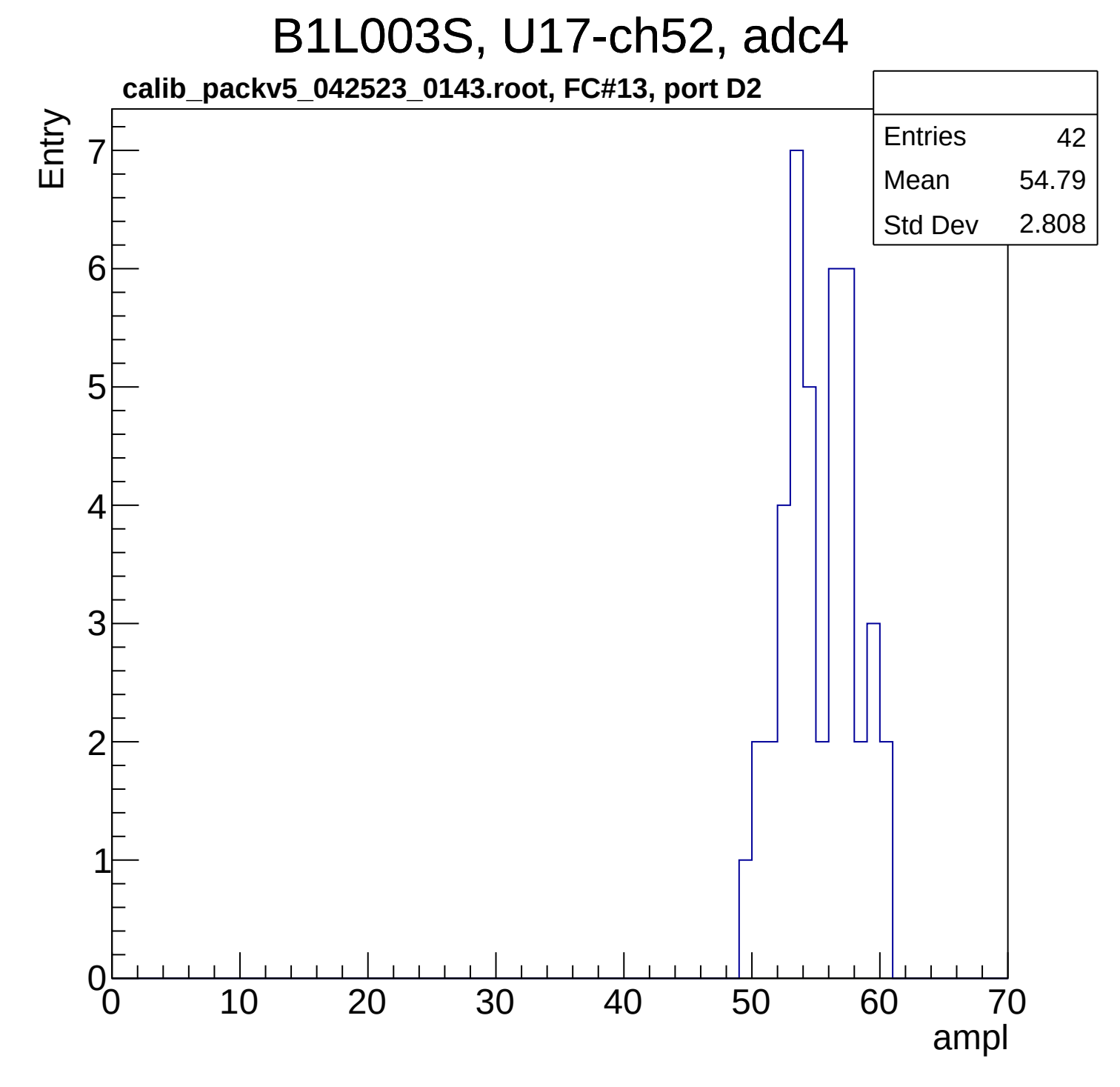

B1L003S, U17-ch51, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

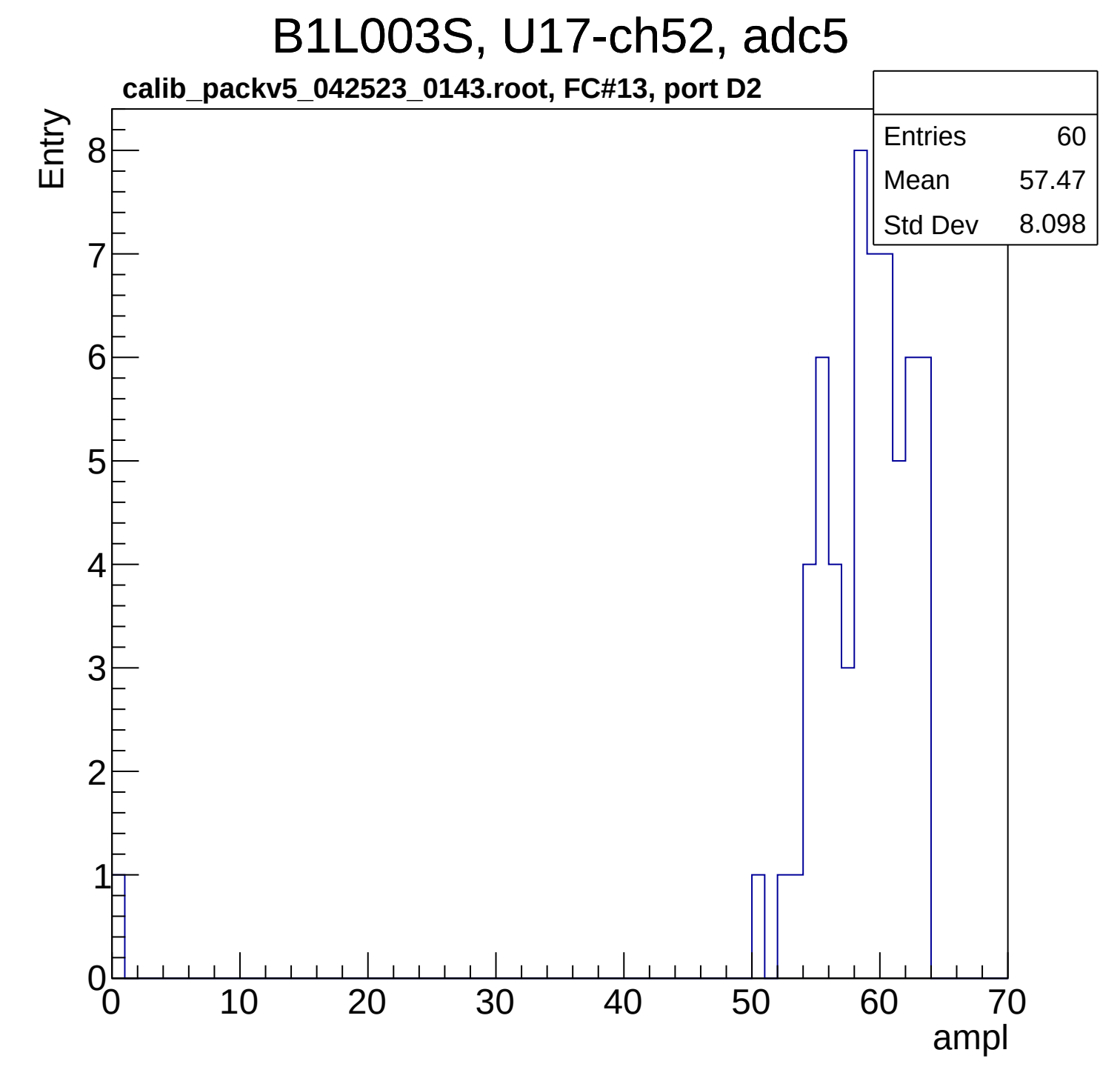

B1L003S, U17-ch52, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 27.5
Std Dev 4.836
Gaus mean : 28.1382
Gaus Width: 3.5233
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

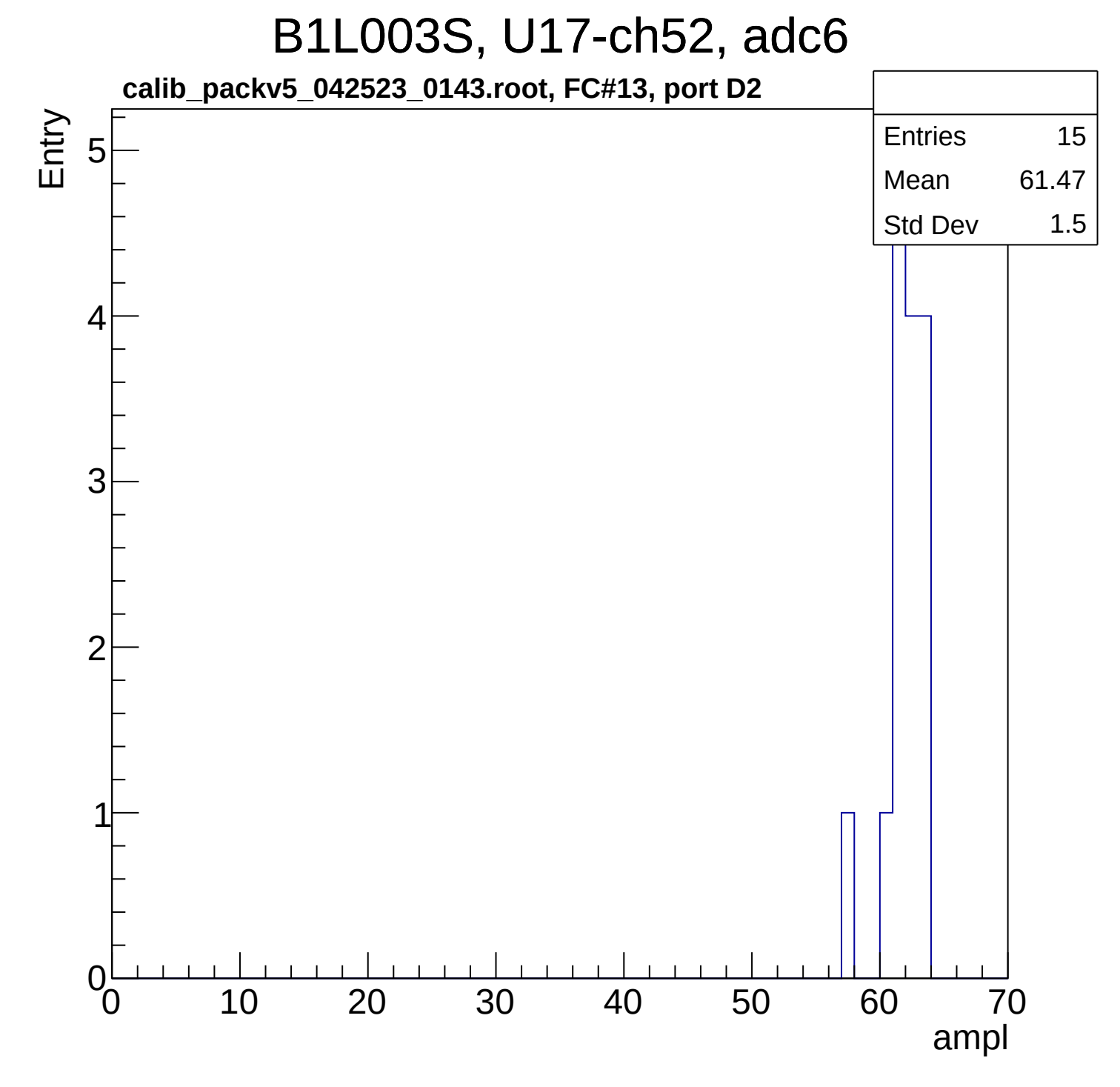

B1L003S, U17-ch52, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 35.13
Std Dev 3.45
Gaus mean : 35.0888
Gaus Width: 3.9008
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

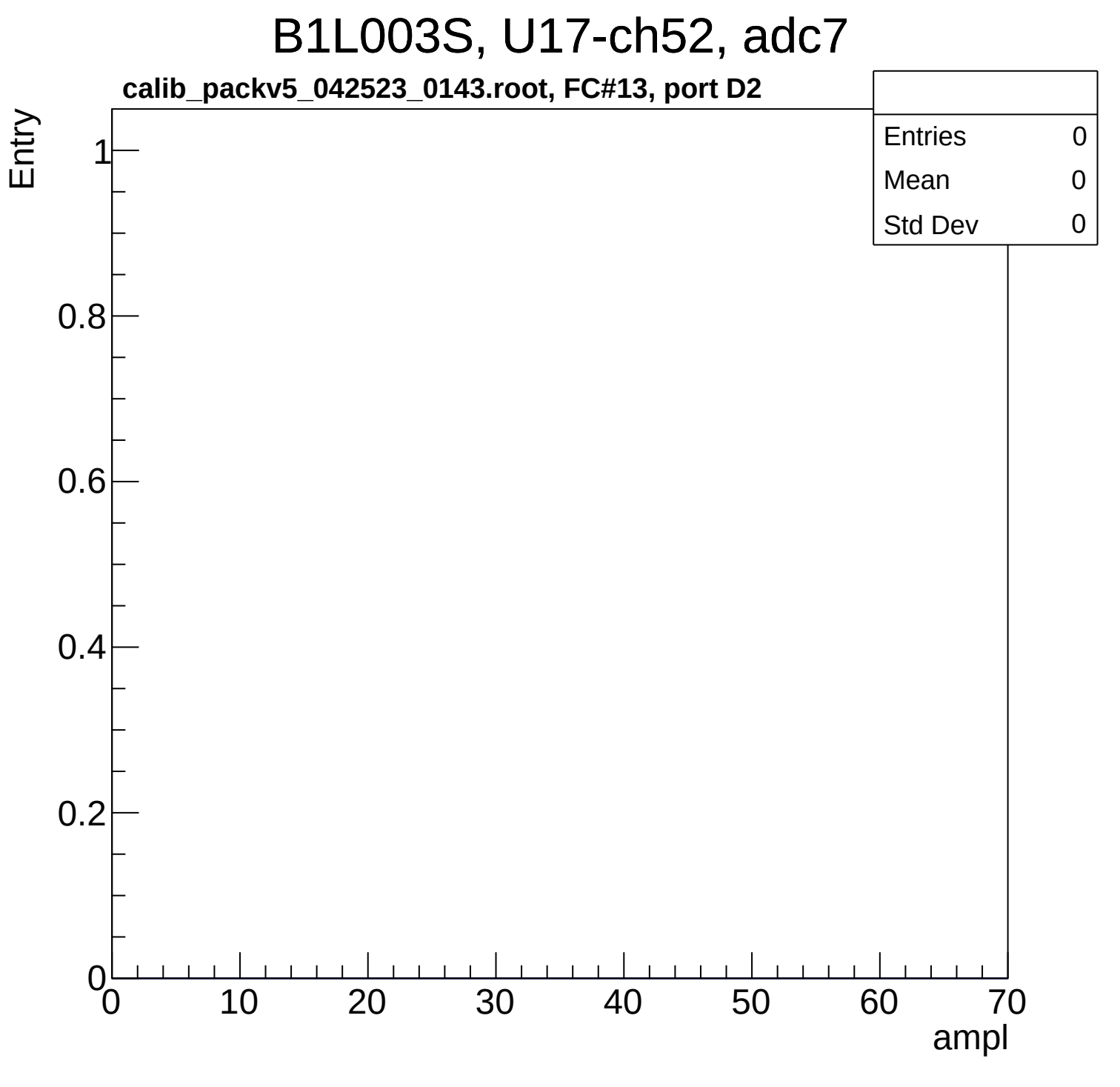

B1L003S, U17-ch52, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.2
Std Dev 2.985
Gaus mean : 42.8282
Gaus Width: 2.7504
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

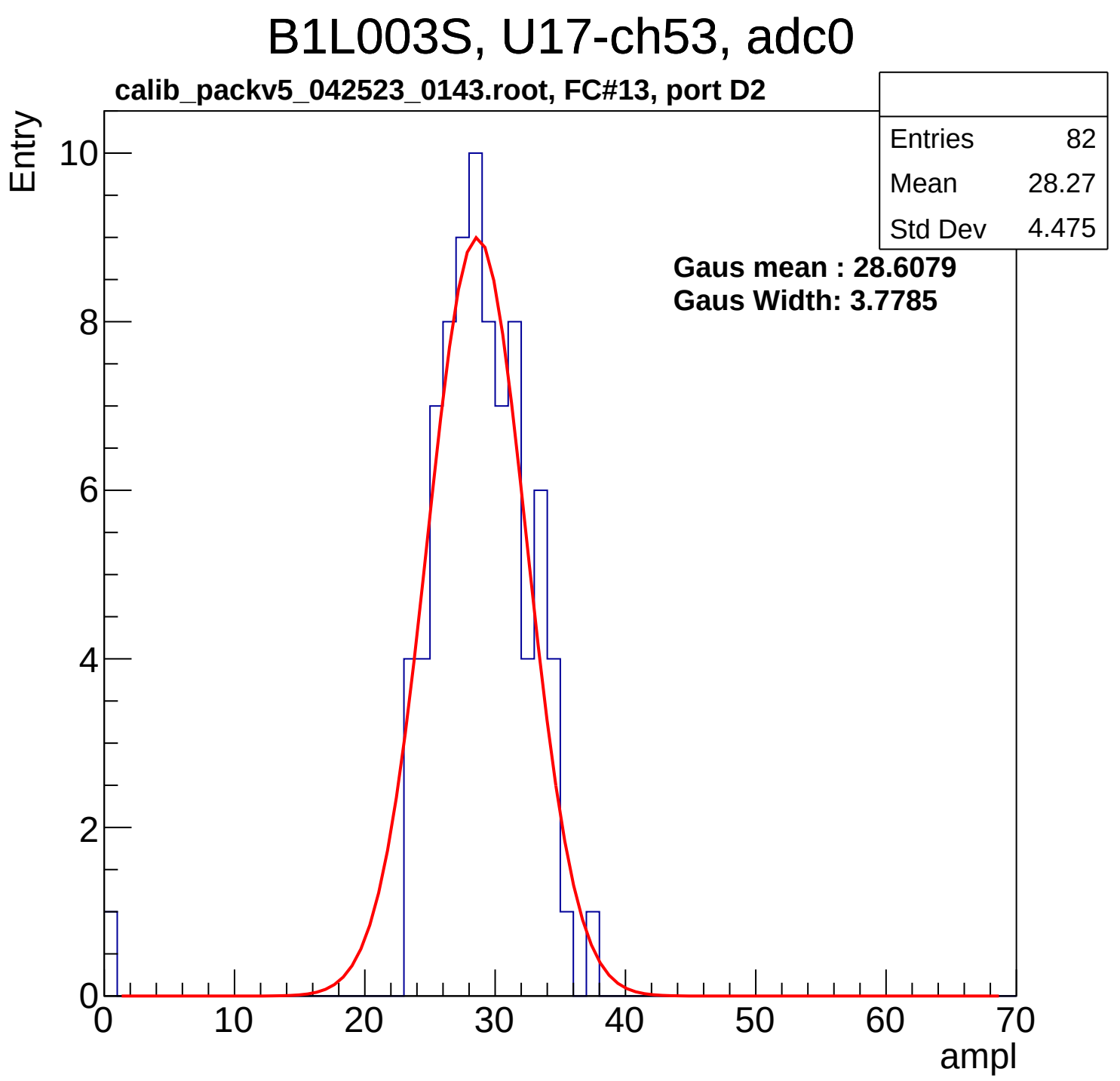

B1L003S, U17-ch52, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.33
Std Dev 2.967
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

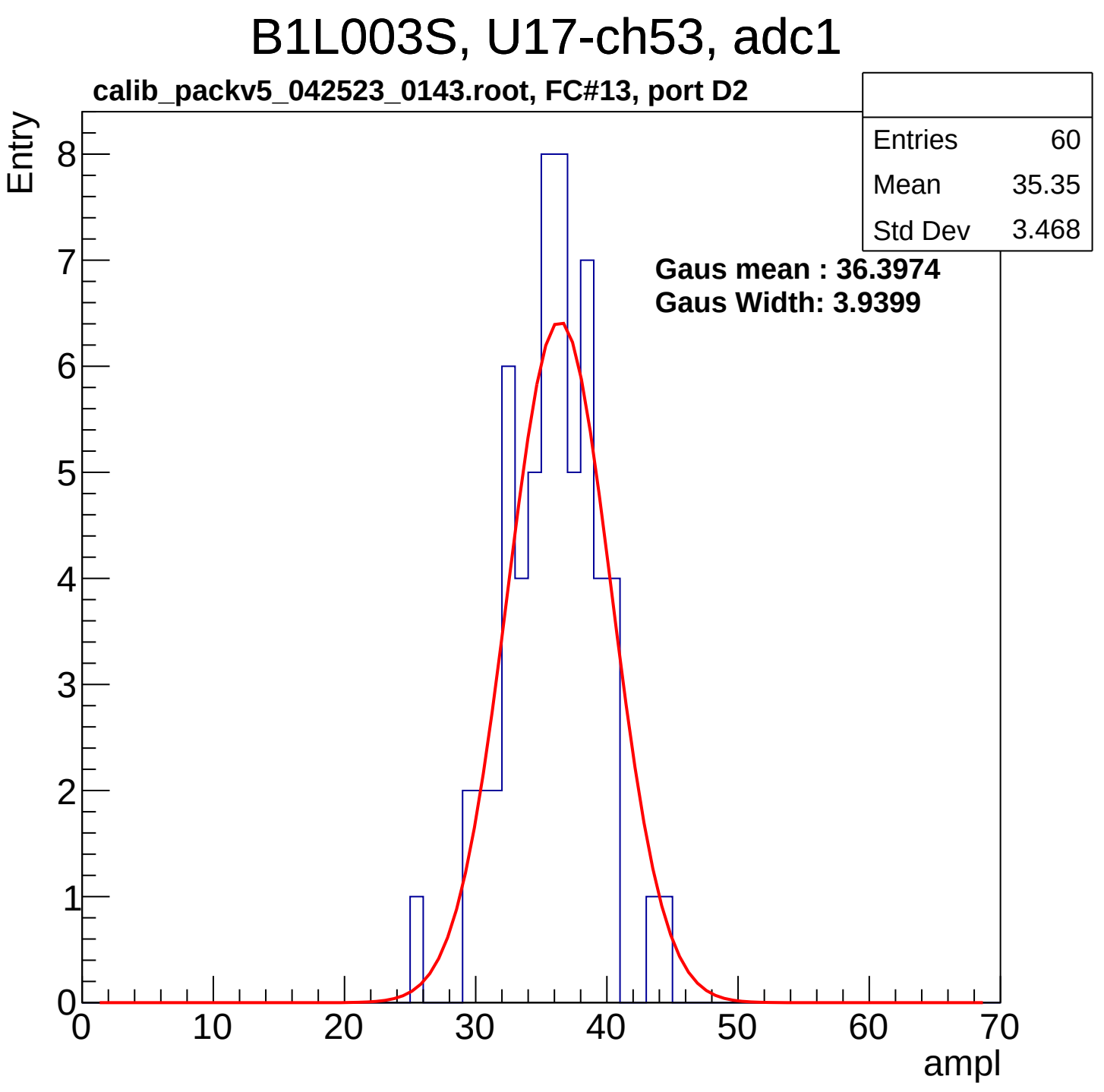

B1L003S, U17-ch52, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 54.79
Std Dev 2.808
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

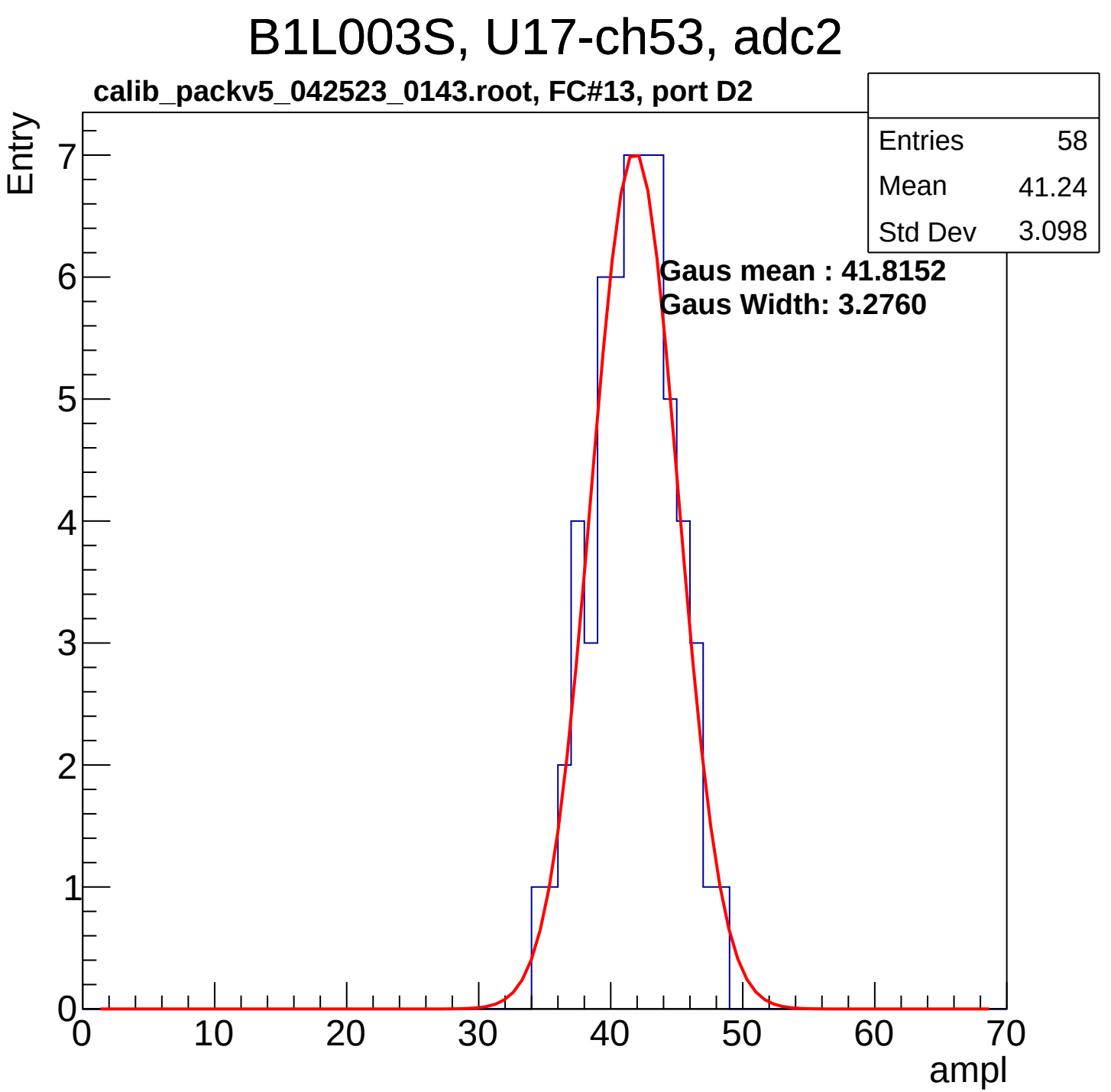

B1L003S, U17-ch52, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 57.47
Std Dev 8.098
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch52, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.47
Std Dev 1.5
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U17-ch52, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch53, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 28.27
Std Dev 4.475
Gaus mean : 28.6079
Gaus Width: 3.7785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch53, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 35.35
Std Dev 3.468
Gaus mean : 36.3974
Gaus Width: 3.9399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch53, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 41.24
Std Dev 3.098
Gaus mean : 41.8152
Gaus Width: 3.2760
Entry
ampl
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3
4
5
6
7
0
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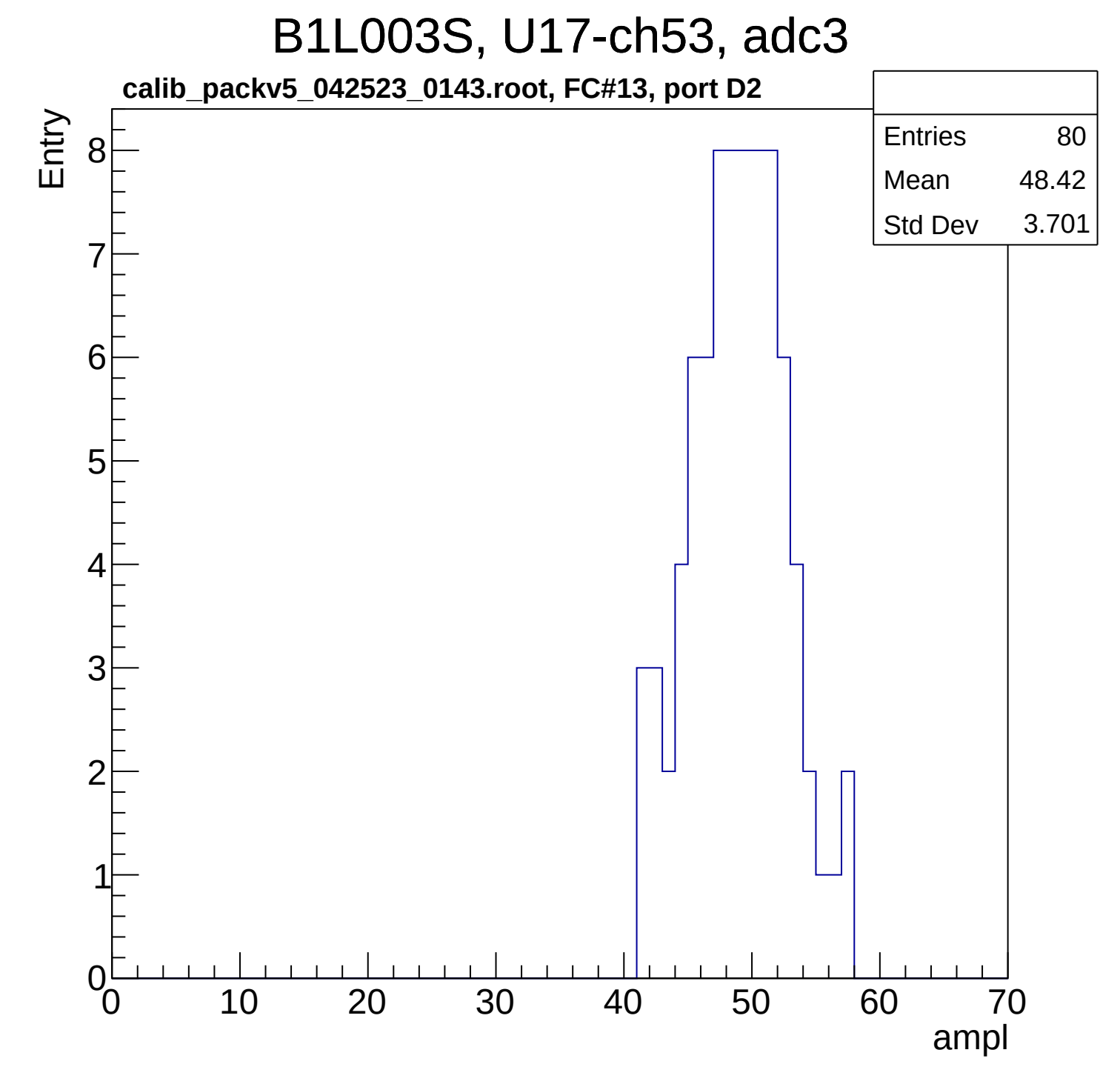
20
30
40
50
60
70


B1L003S, U17-ch53, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 48.42
Std Dev 3.701
Entry
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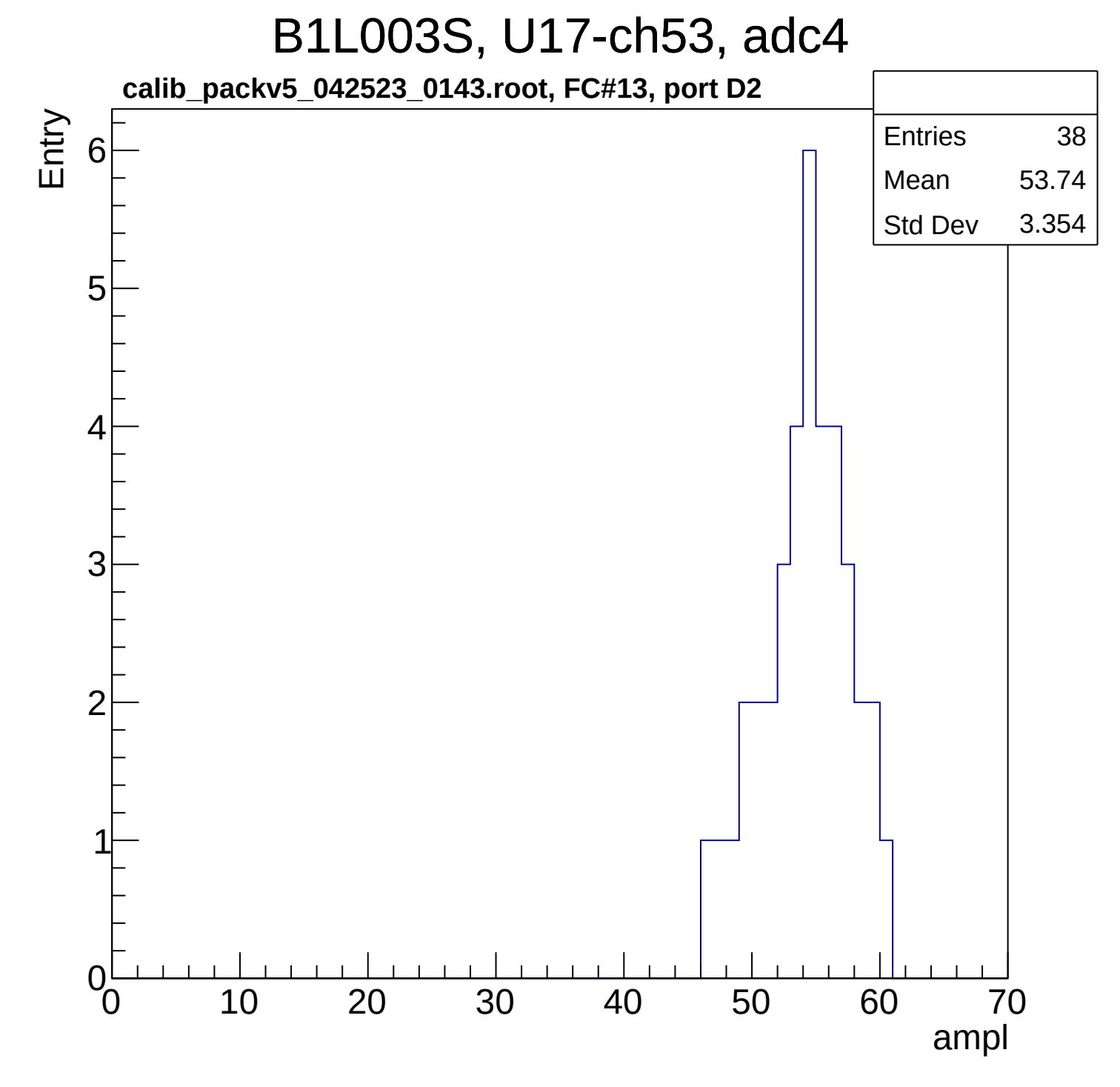
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch53, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 53.74
Std Dev 3.354
Entry
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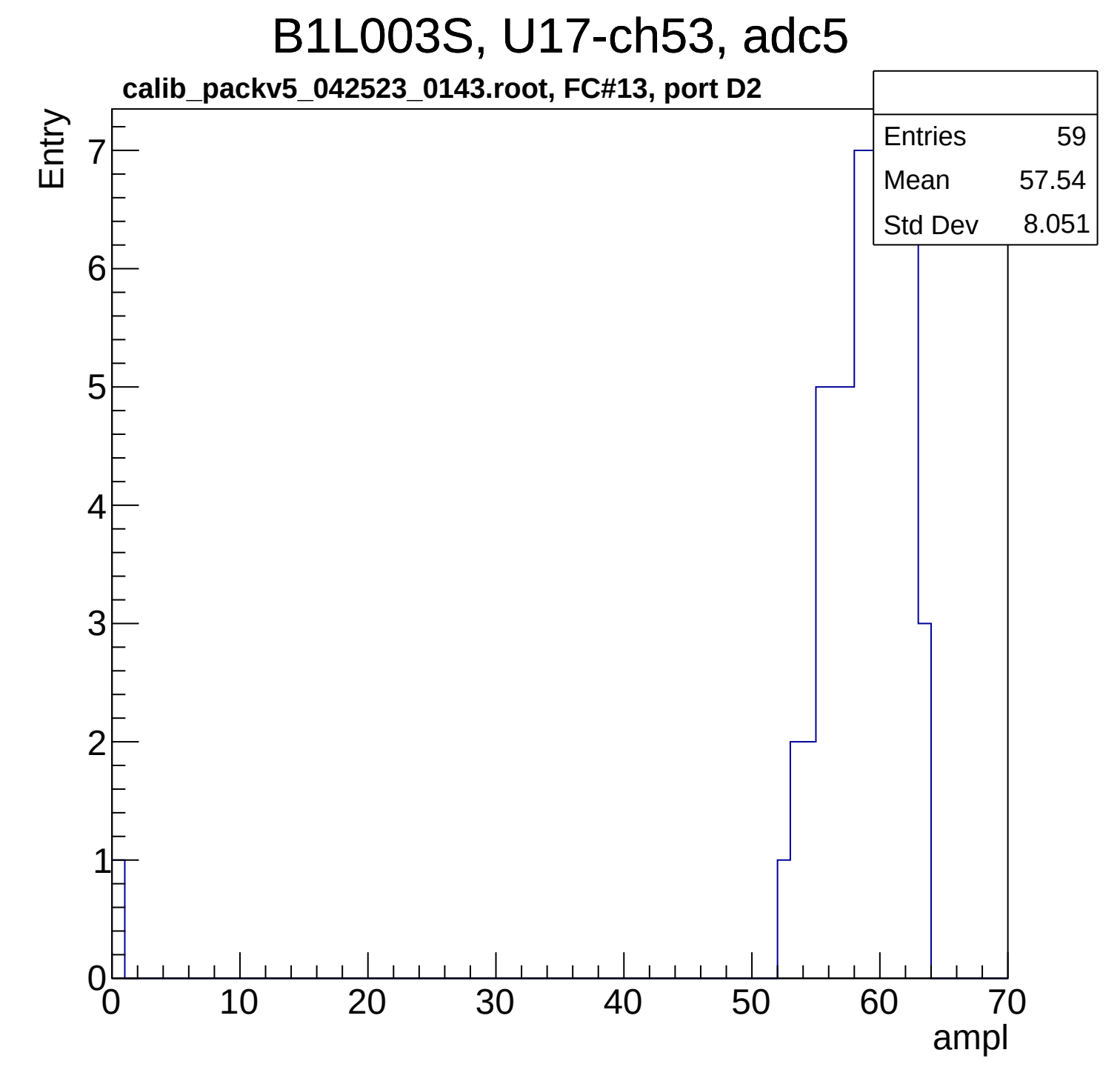
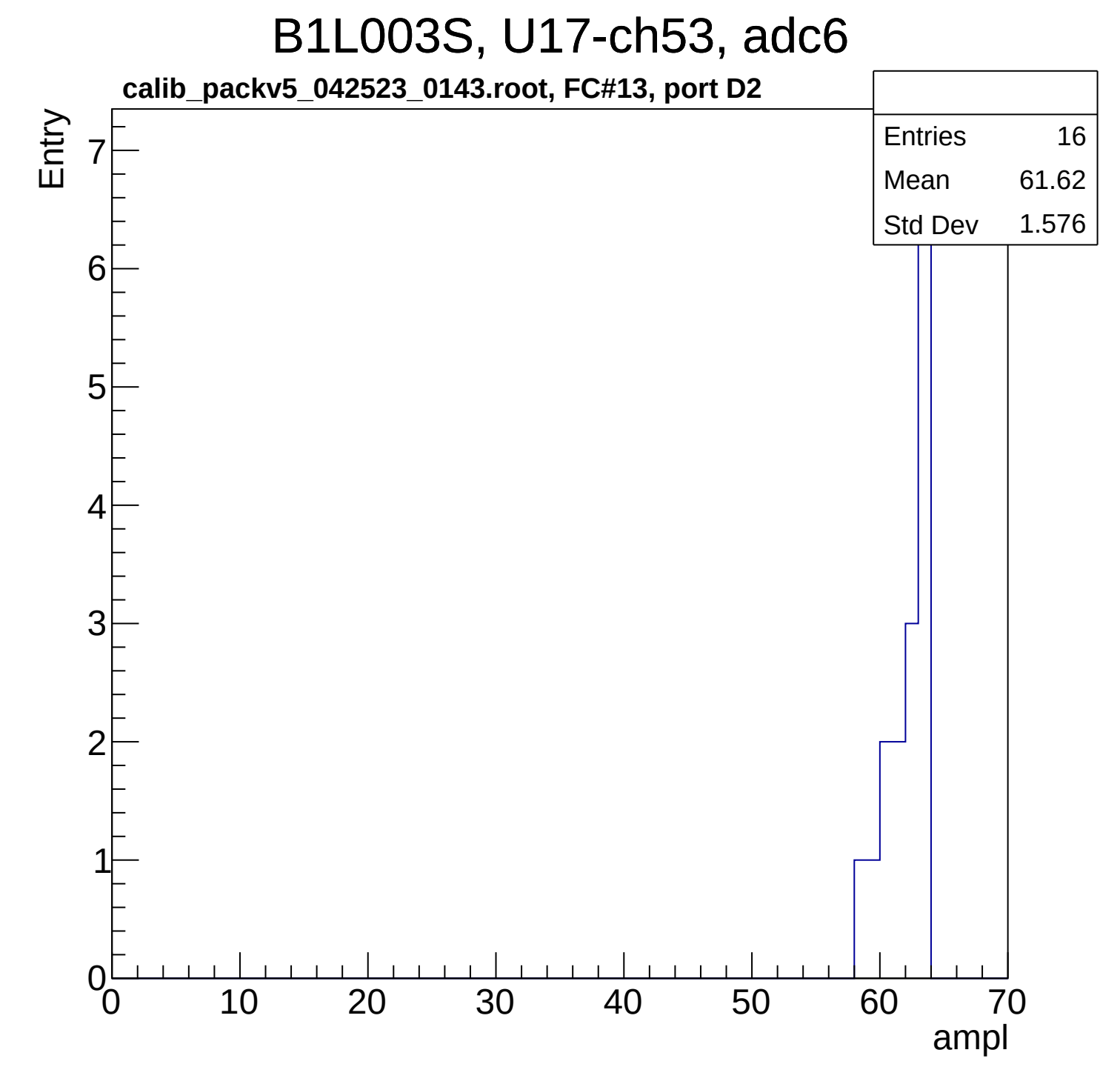
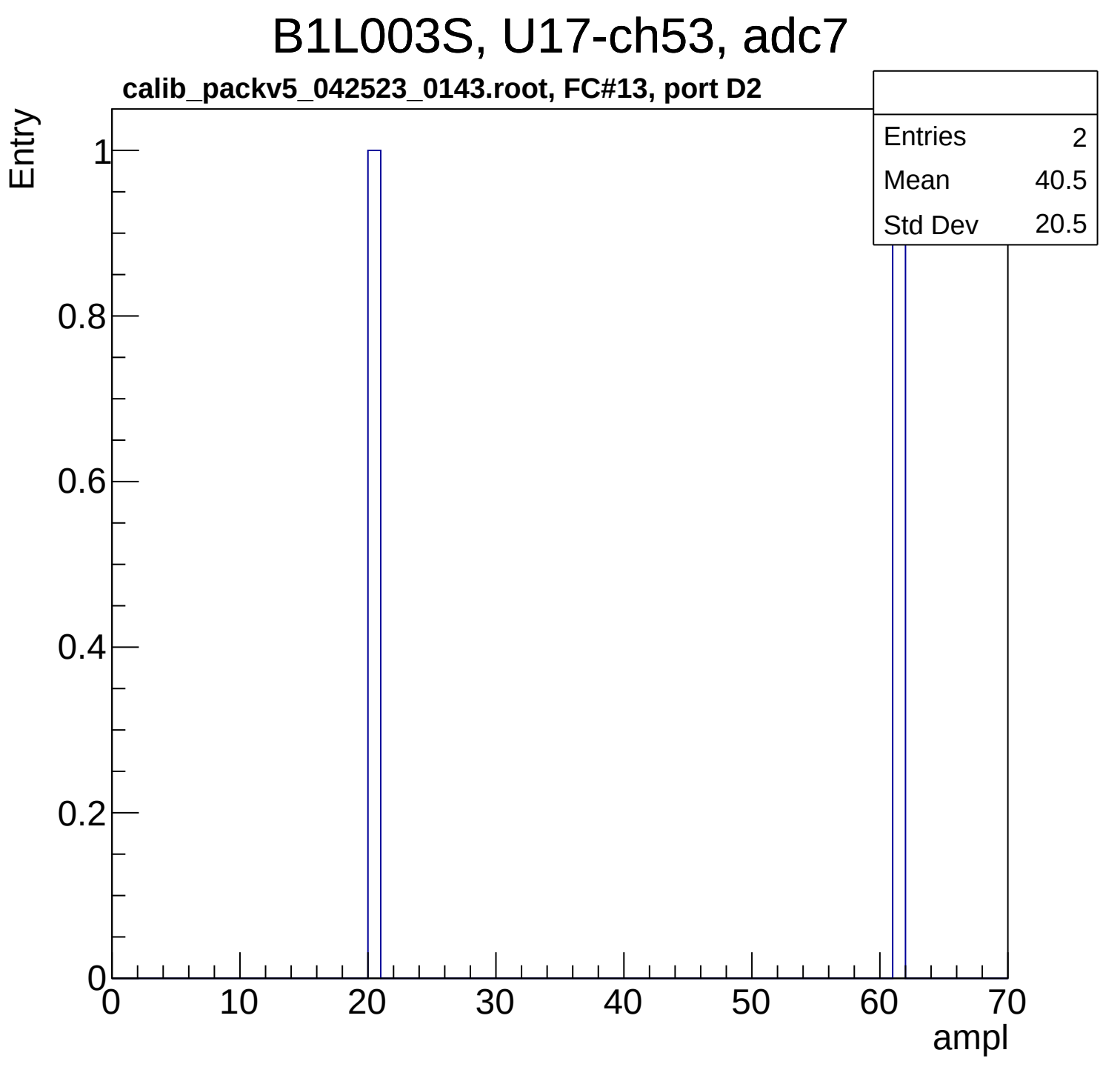
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch53, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 57.54
Std Dev 8.051
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch53, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 61.62
Std Dev 1.576
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch53, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 40.5
Std Dev 20.5
Entry
ampl
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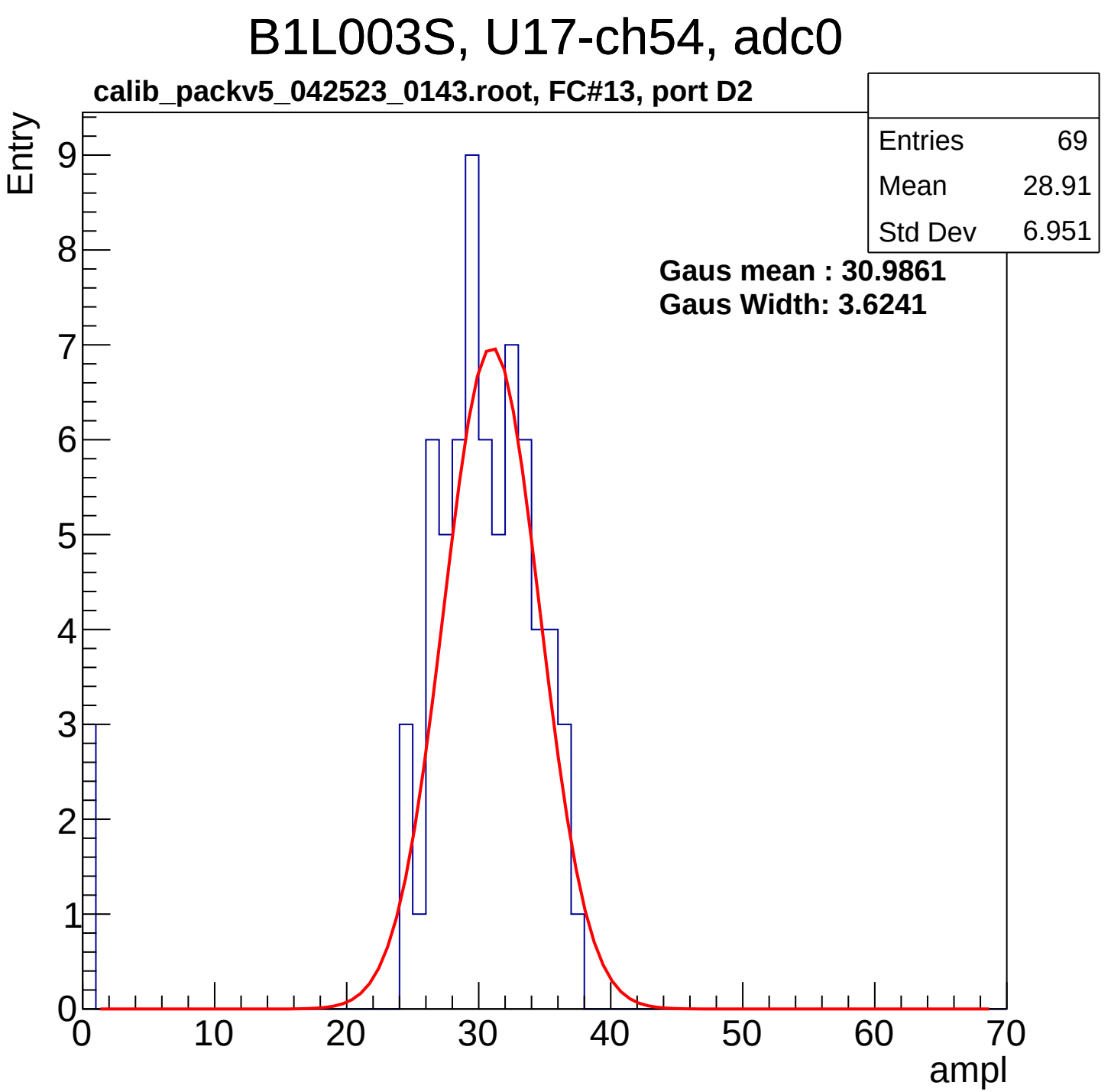
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L003S, U17-ch54, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 28.91
Std Dev 6.951
Gaus mean : 30.9861
Gaus Width: 3.6241
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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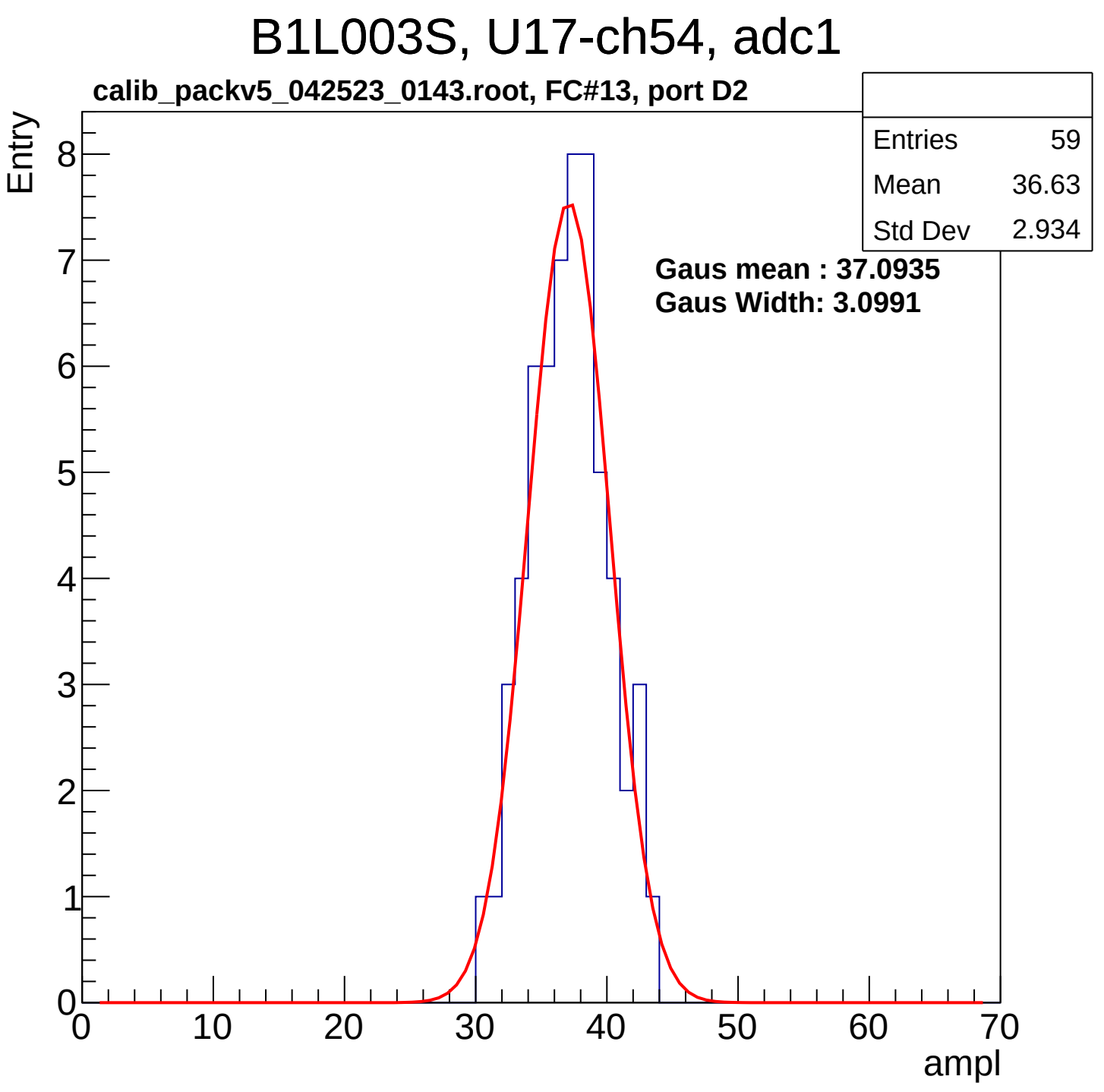
B1L003S, U17-ch54, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.63
Std Dev 2.934
Gaus mean : 37.0935
Gaus Width: 3.0991
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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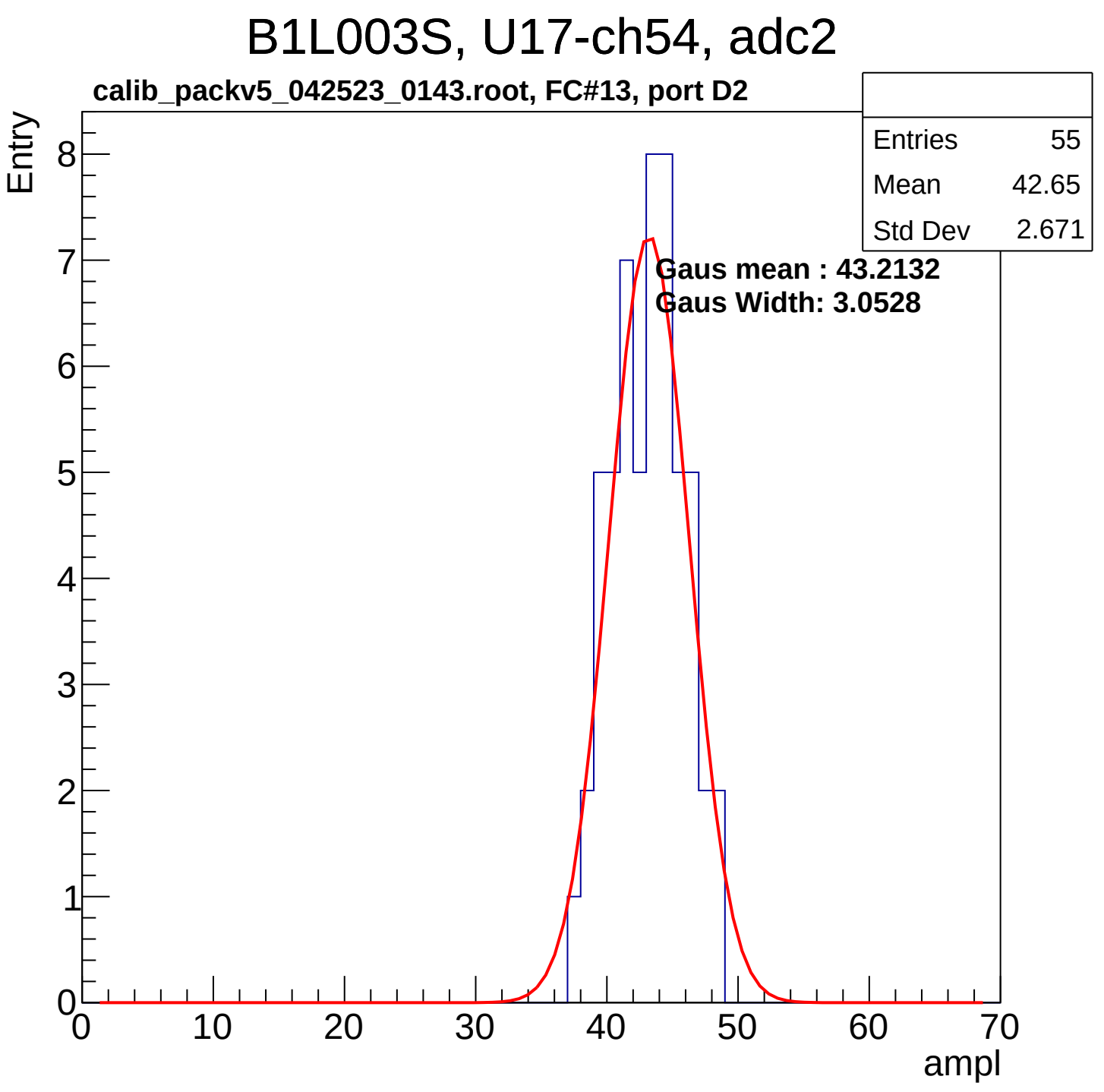
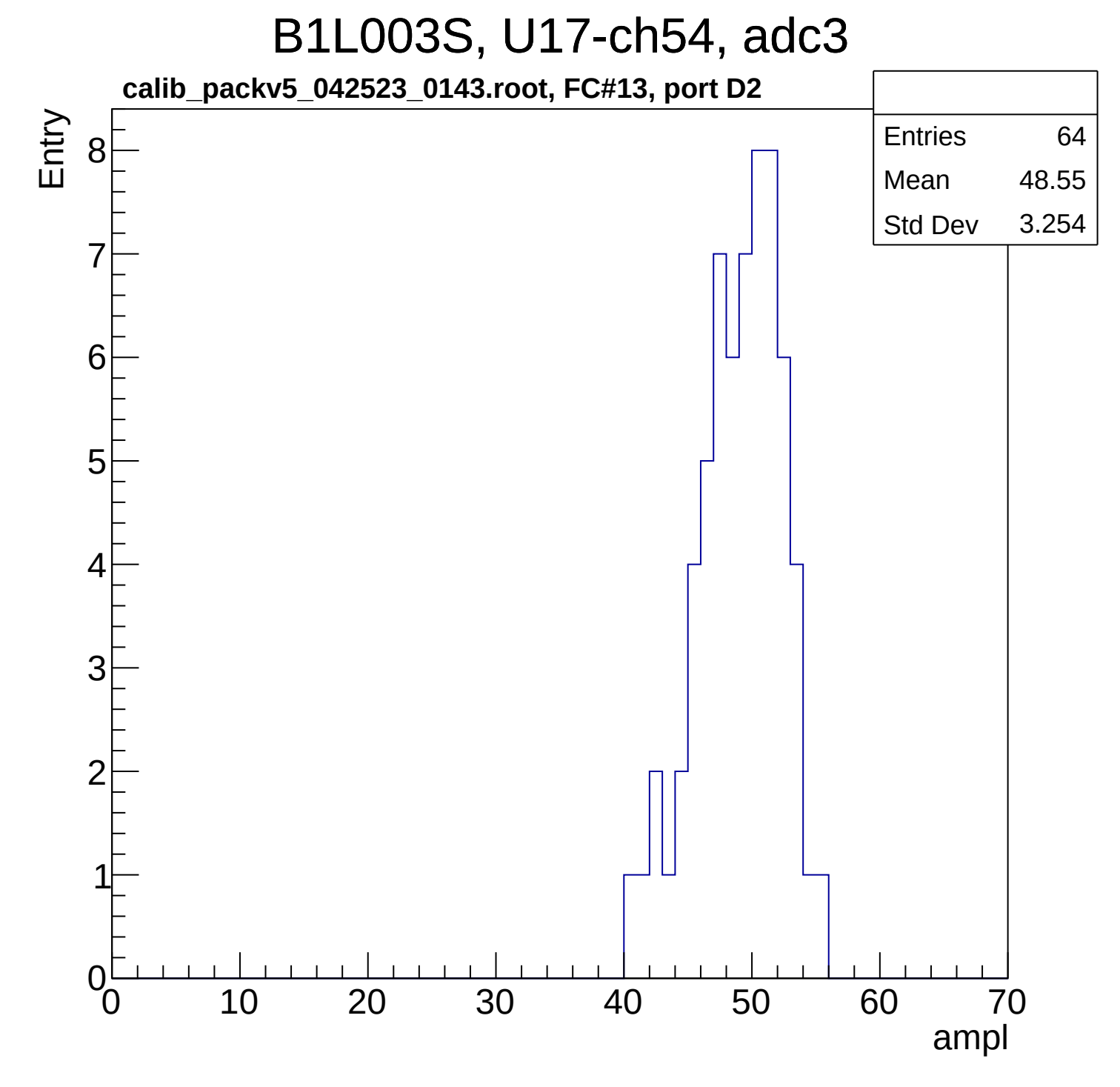

B1L003S, U17-ch54, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 42.65
Std Dev 2.671
Gaus mean : 43.2132
Gaus Width: 3.0528
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch54, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 48.55
Std Dev 3.254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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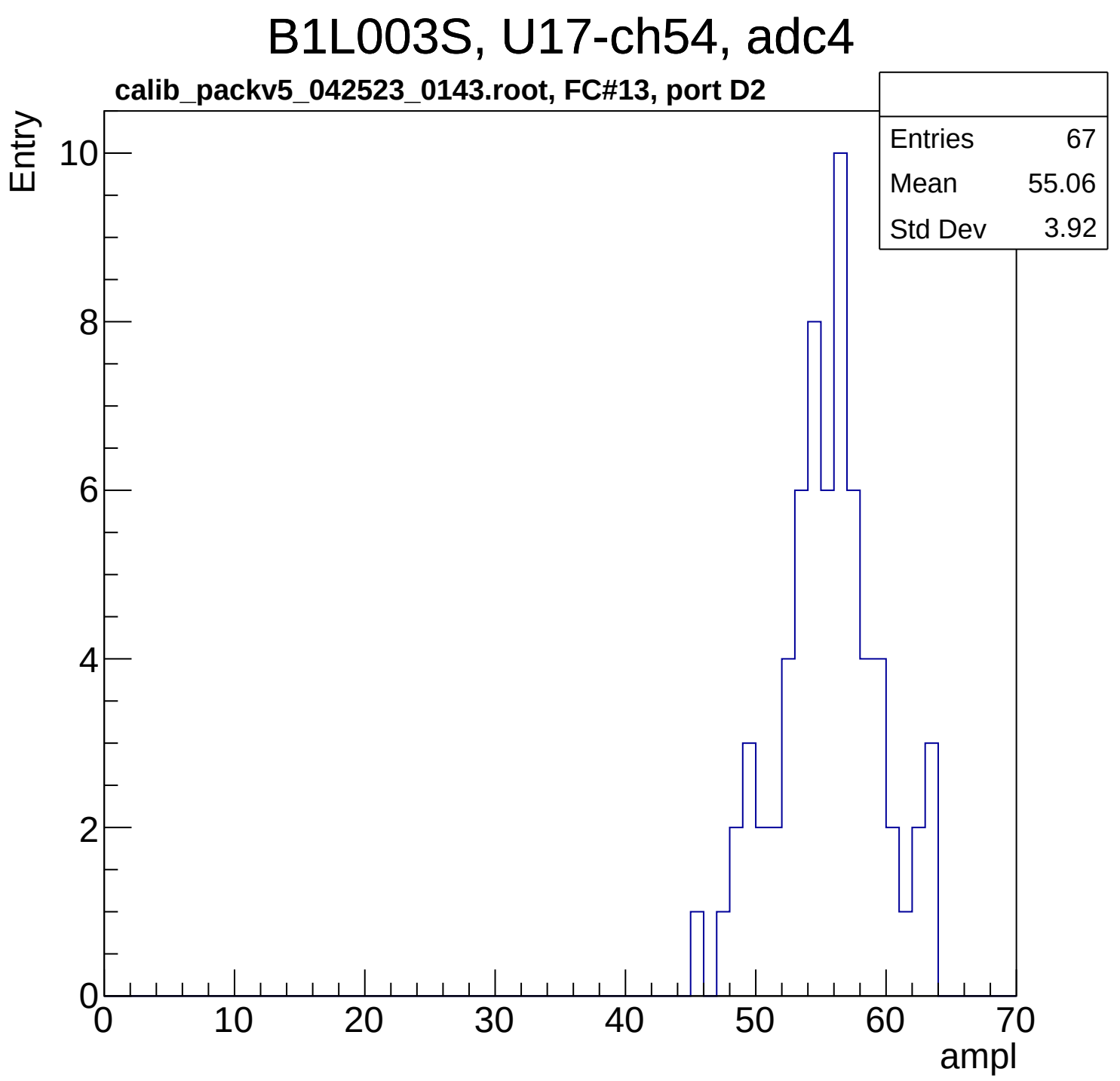
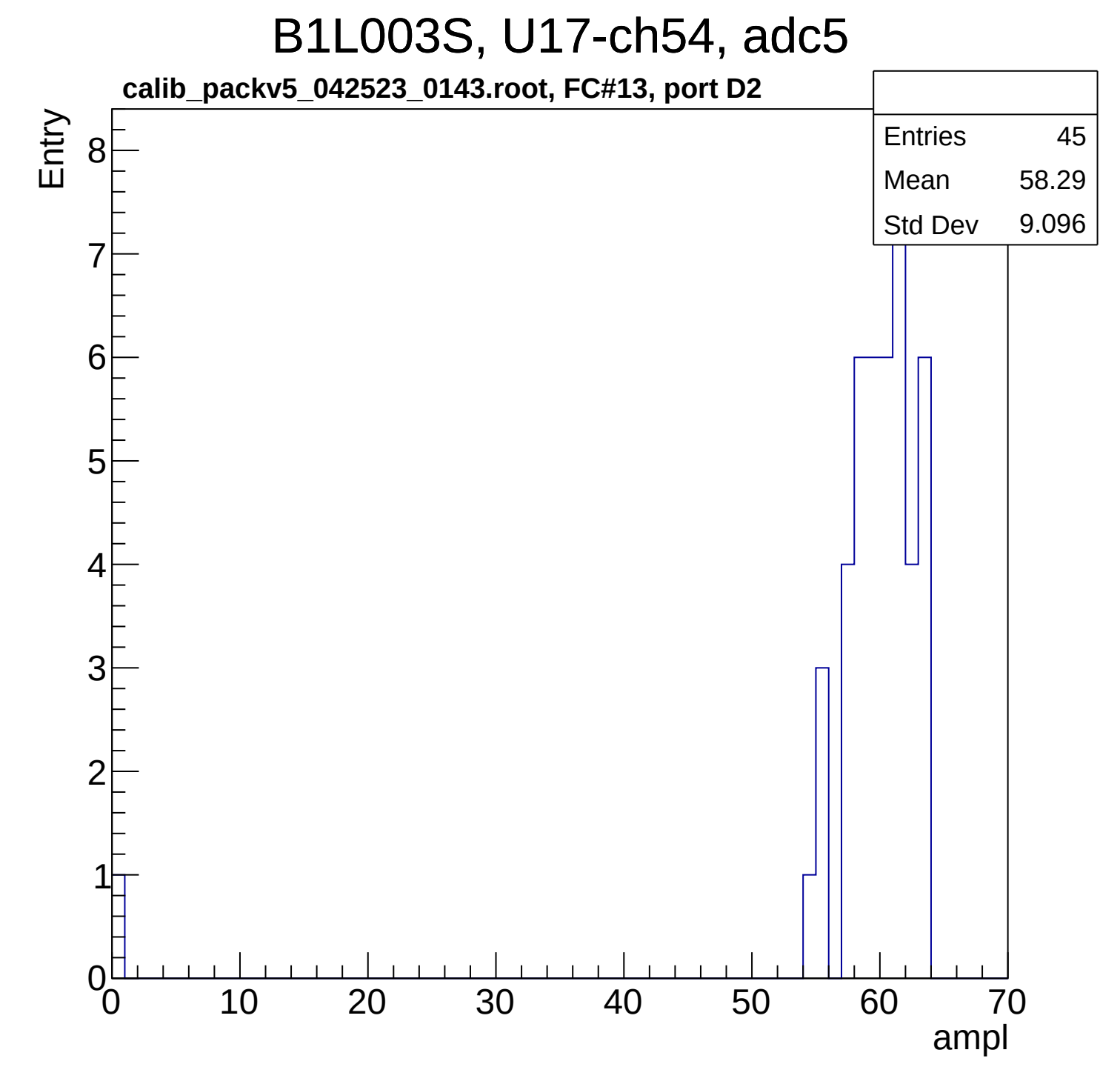

B1L003S, U17-ch54, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 55.06
Std Dev 3.92
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch54, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.29
Std Dev 9.096
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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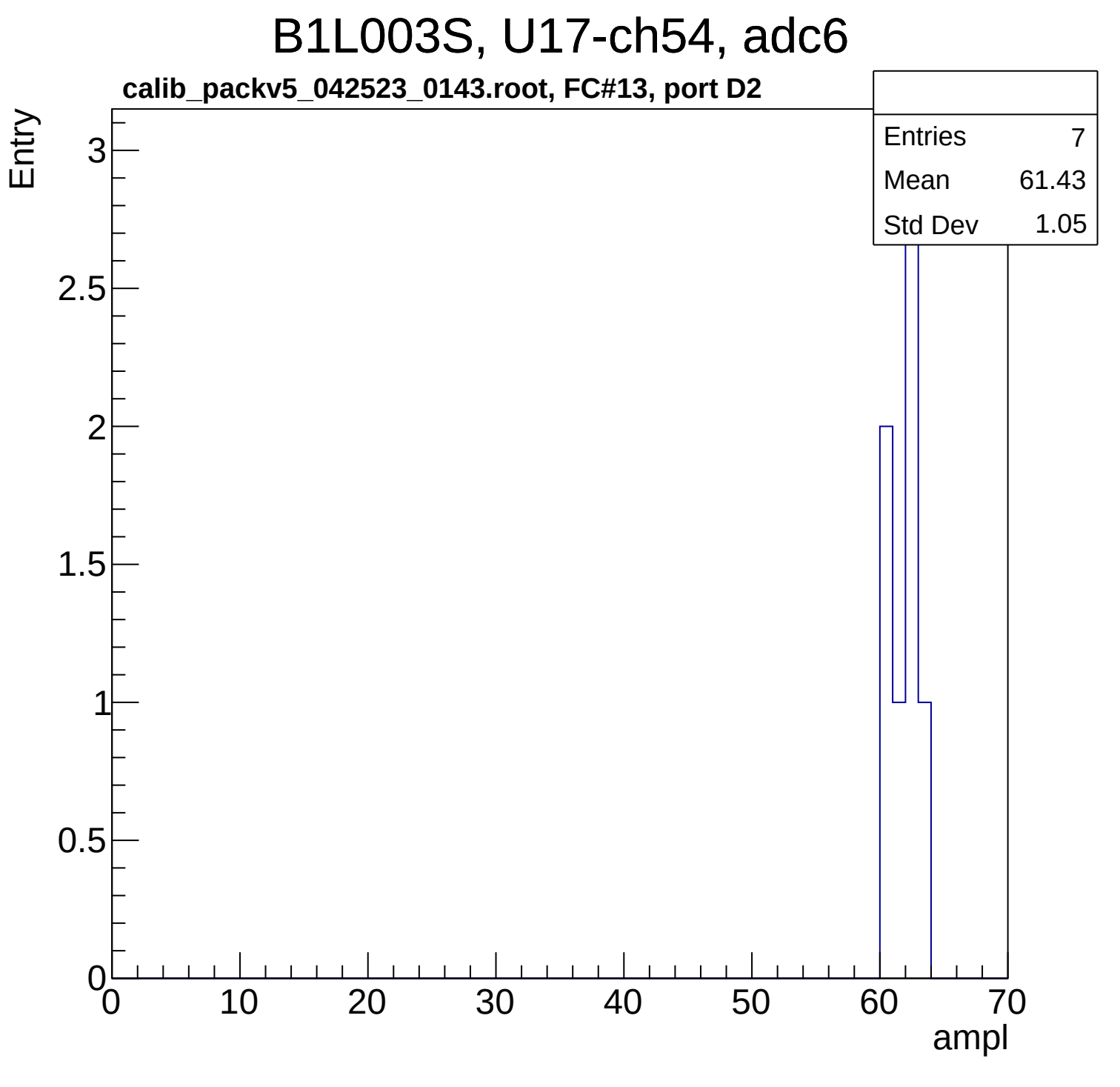
B1L003S, U17-ch54, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.43
Std Dev 1.05
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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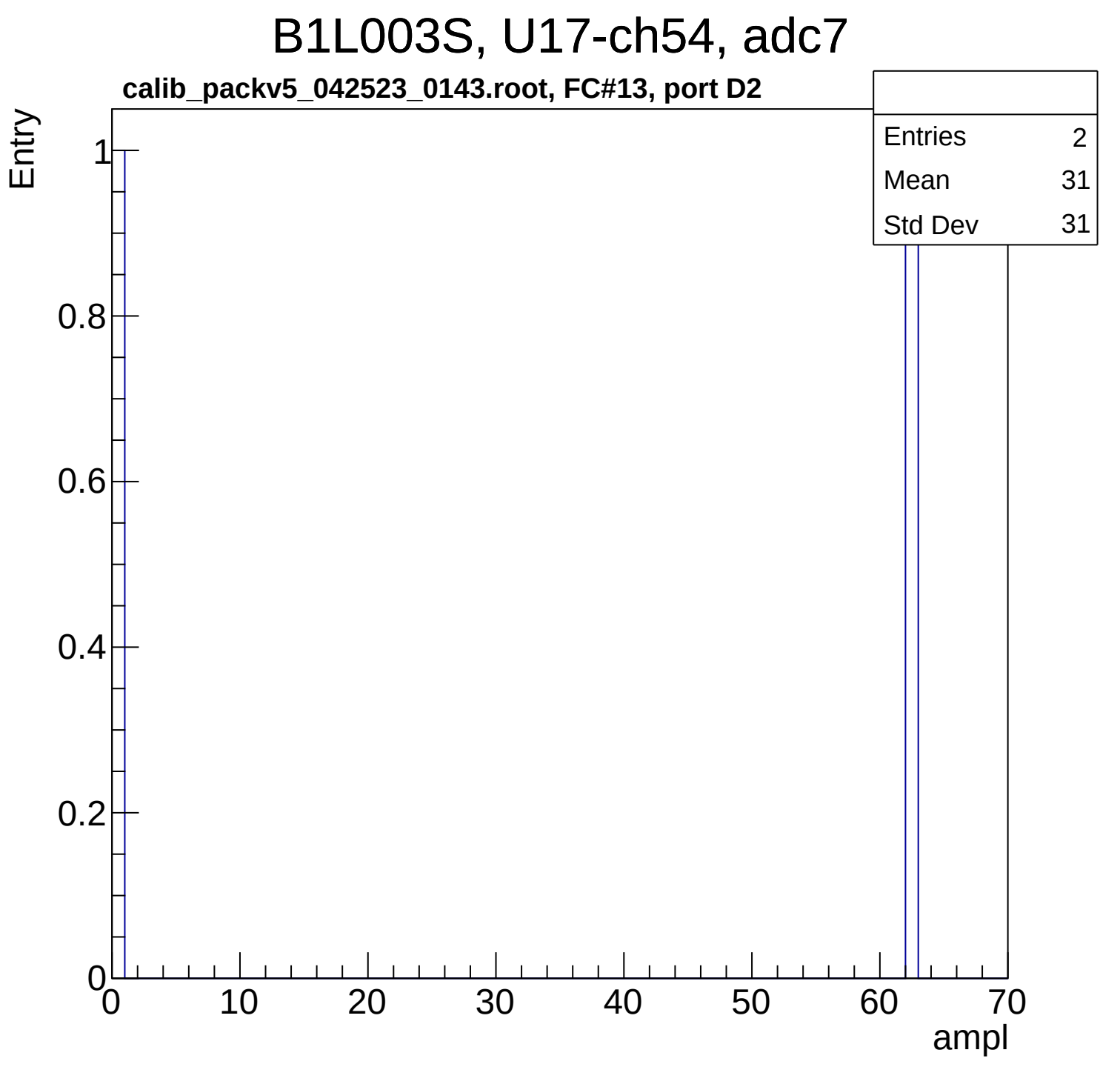
B1L003S, U17-ch54, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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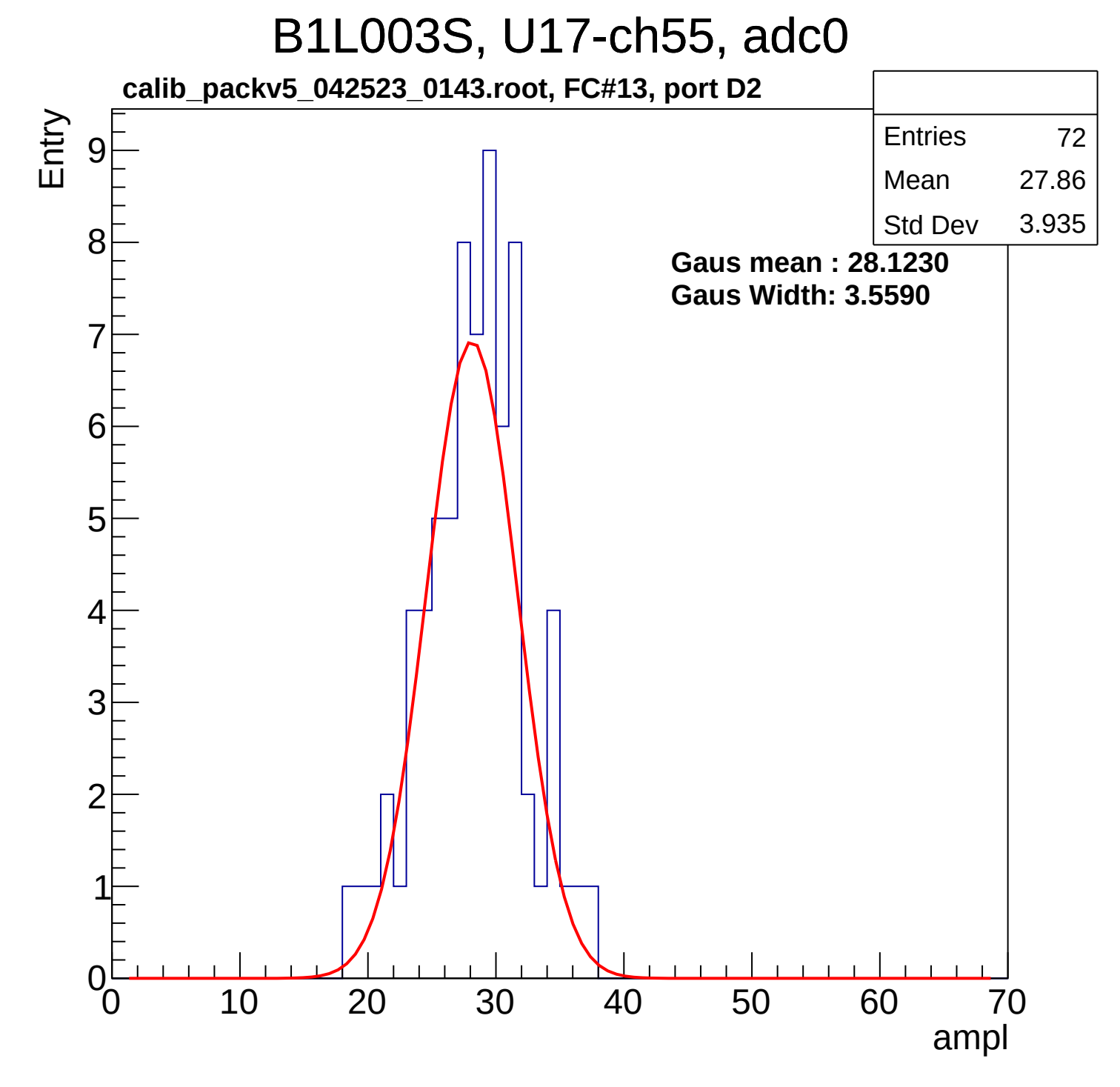
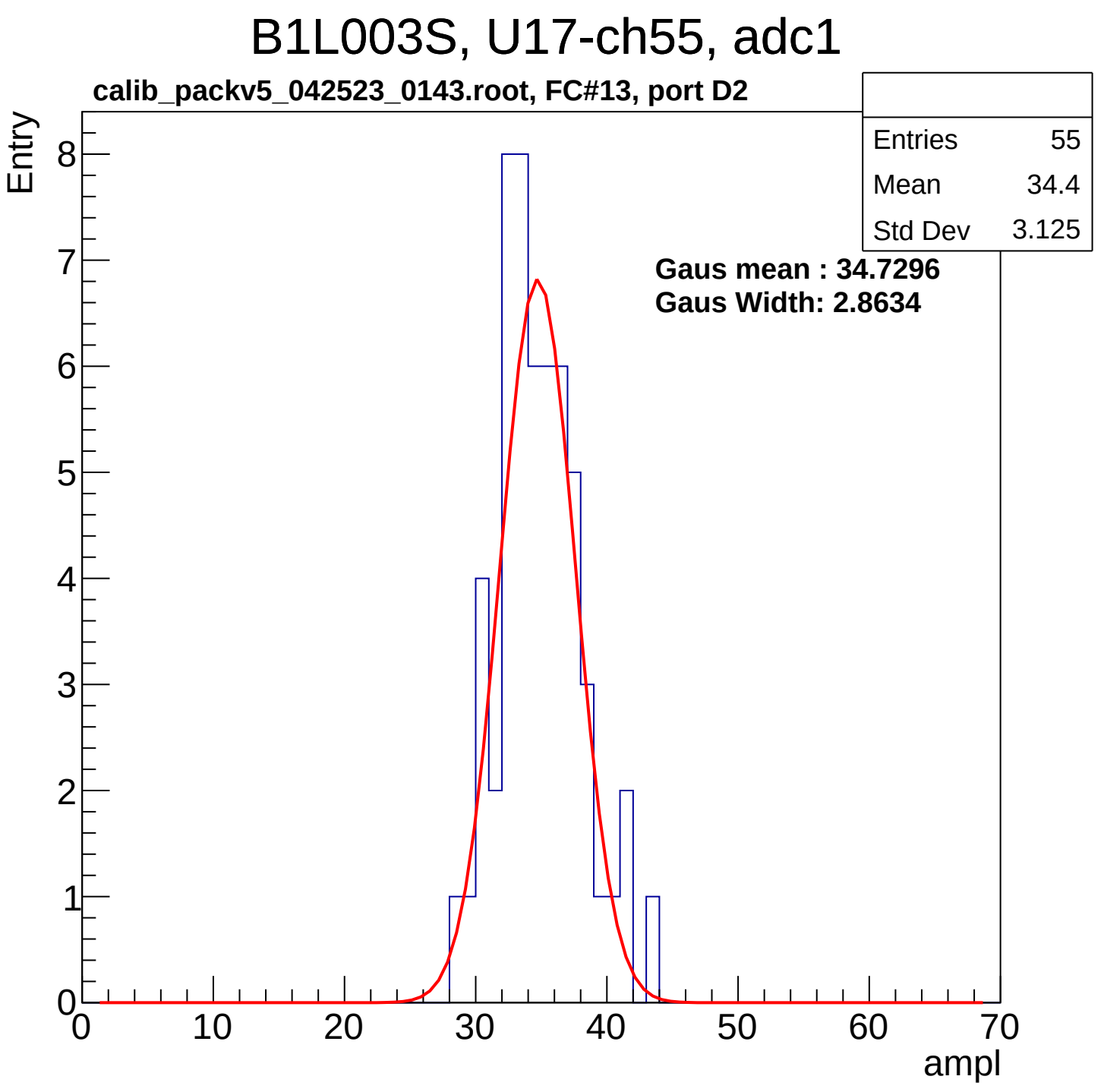
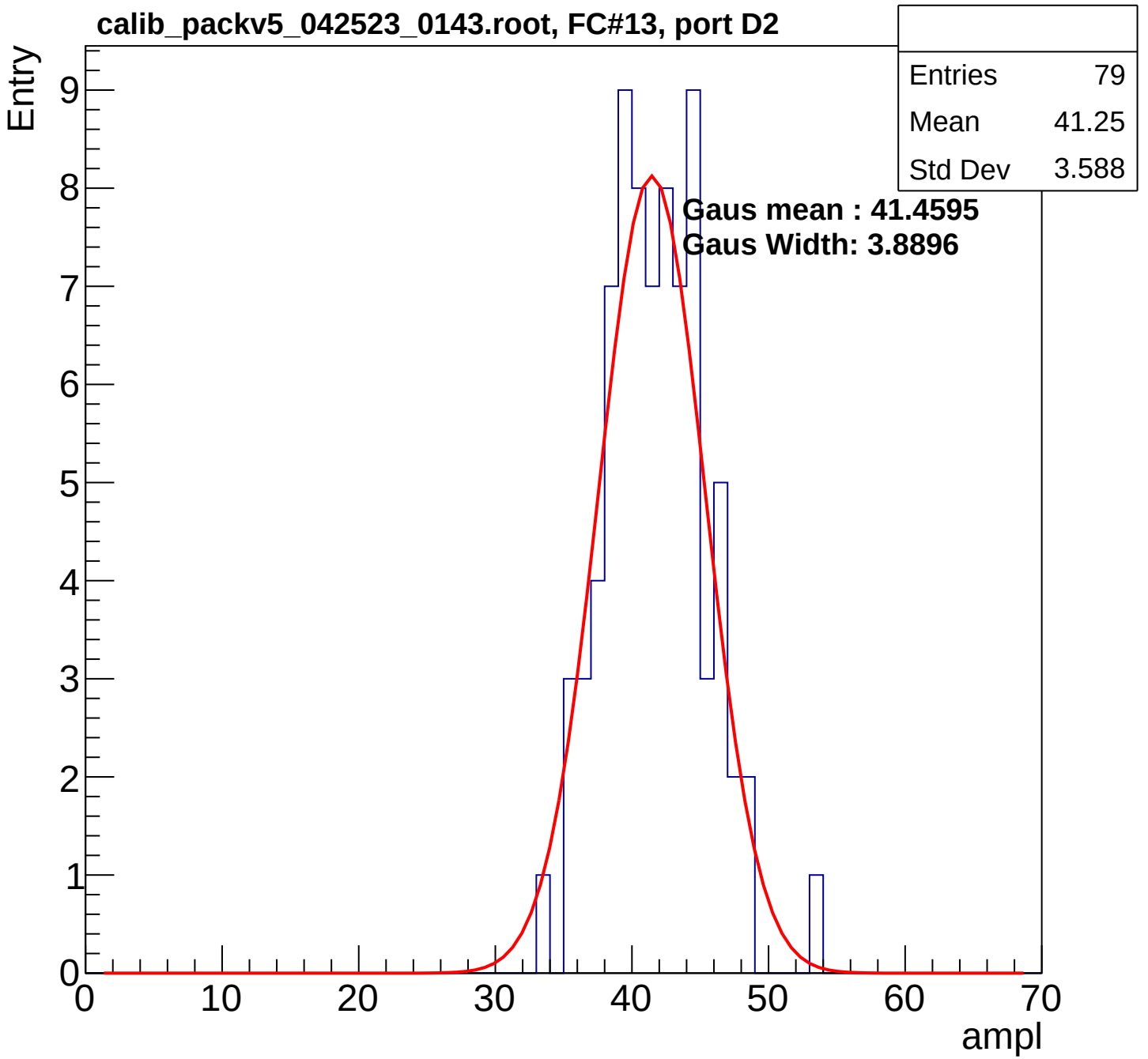
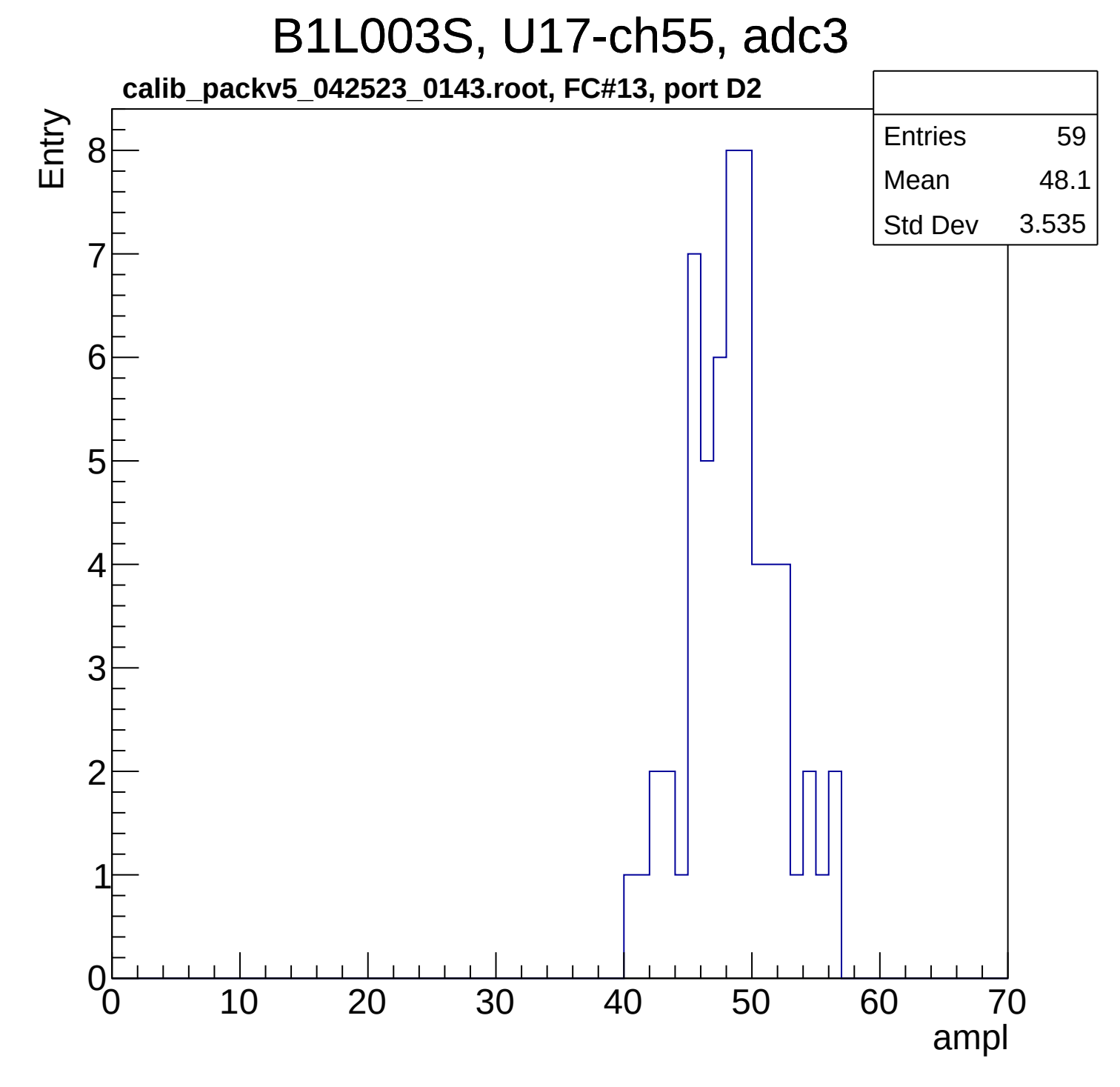
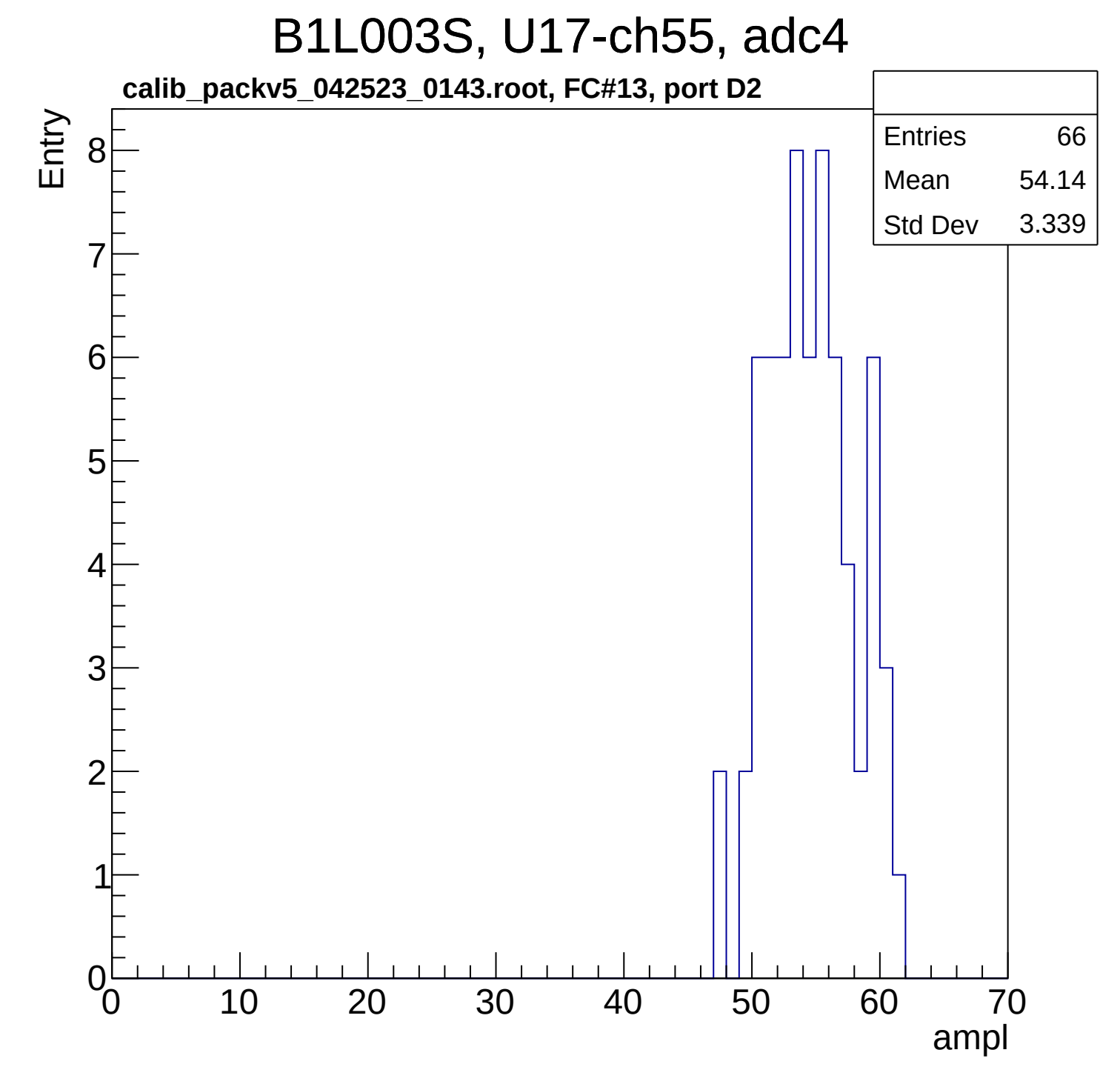
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

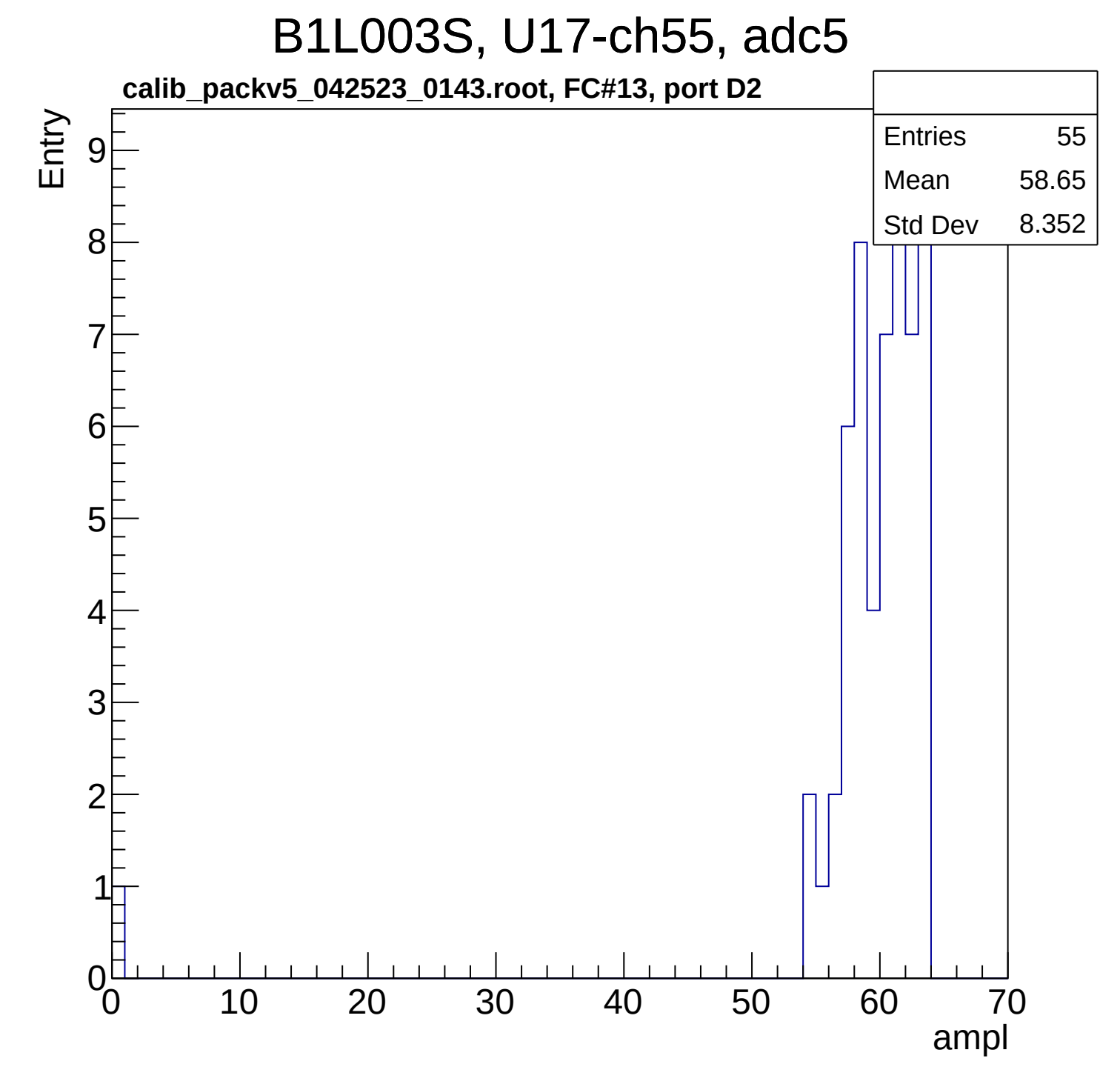

B1L003S, U17-ch55, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 27.86
Std Dev 3.935
Gaus mean : 28.1230
Gaus Width: 3.5590
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

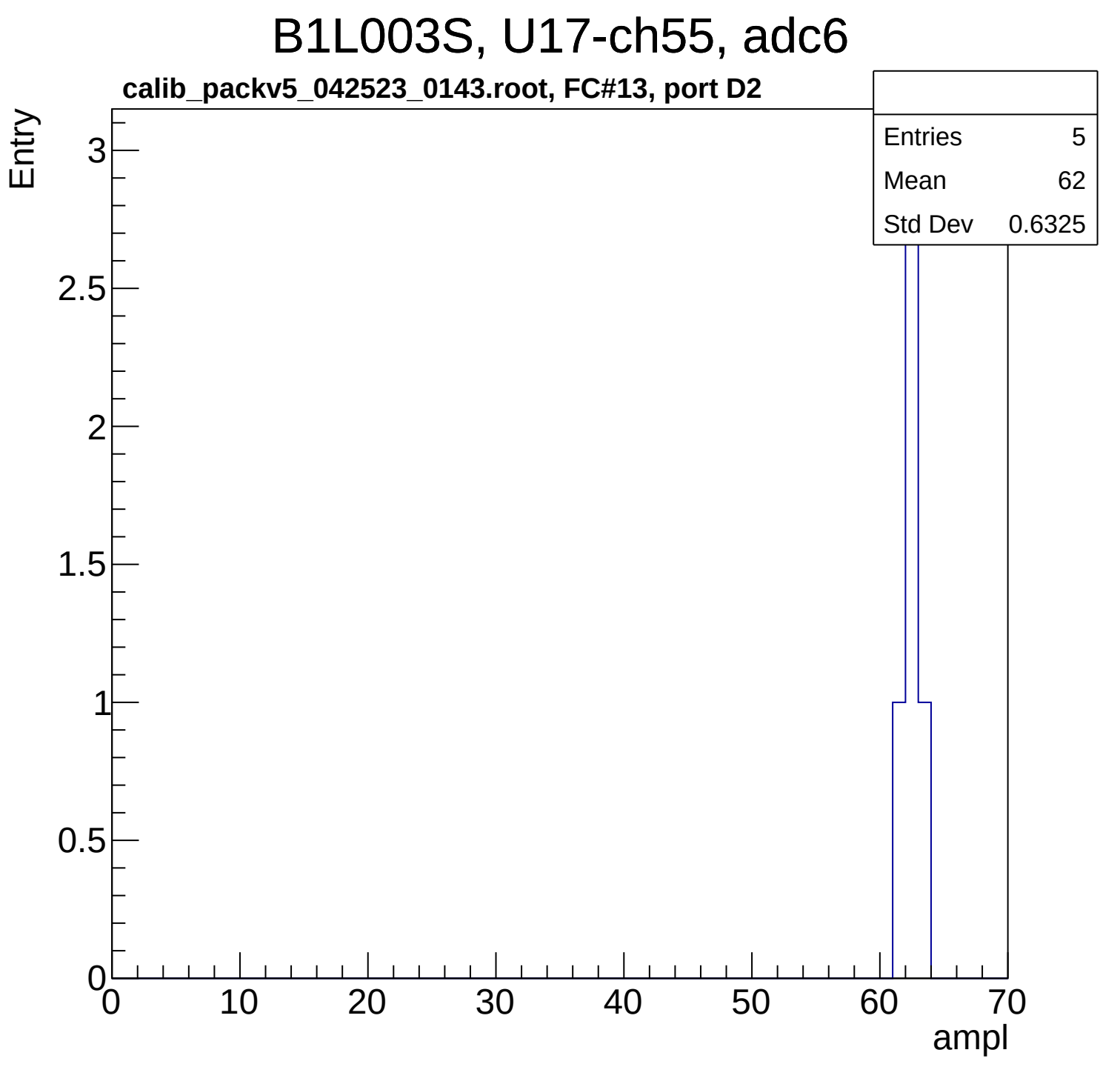

B1L003S, U17-ch55, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 34.4
Std Dev 3.125
Gaus mean : 34.7296
Gaus Width: 2.8634
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

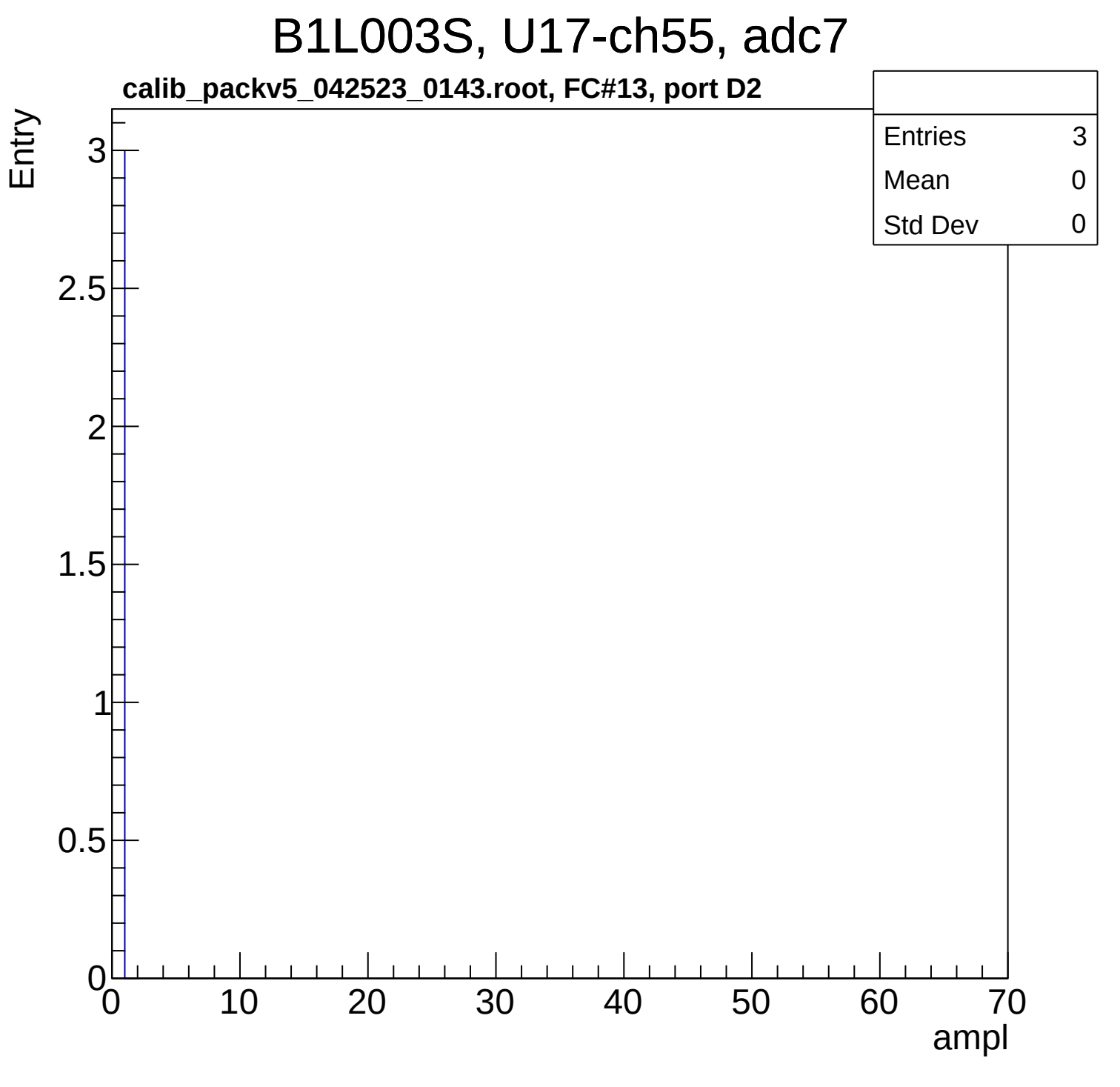

B1L003S, U17-ch55, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 41.25
Std Dev 3.588
Gaus mean : 41.4595
Gaus Width: 3.8896
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

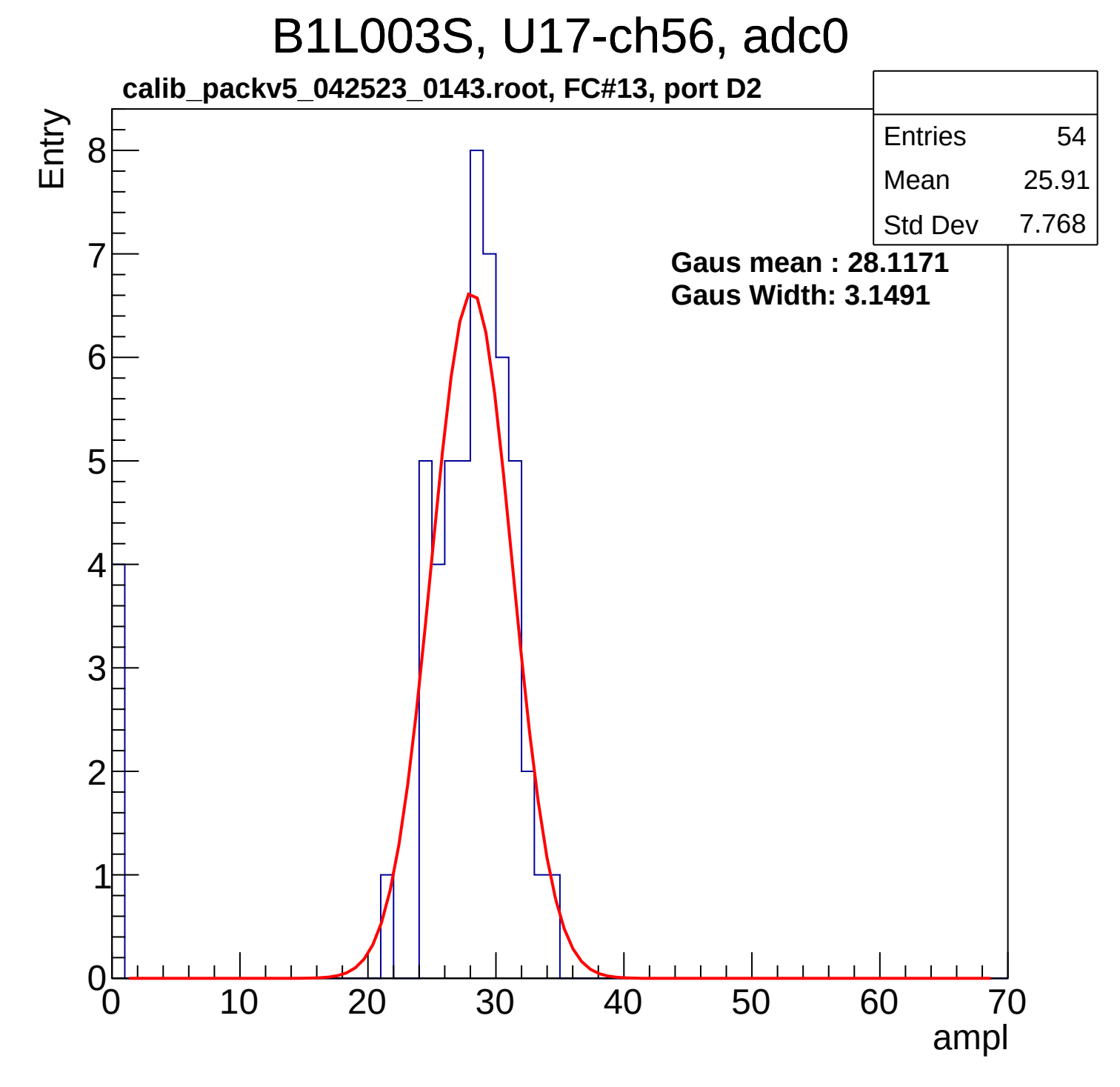

B1L003S, U17-ch55, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48.1
Std Dev 3.535
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

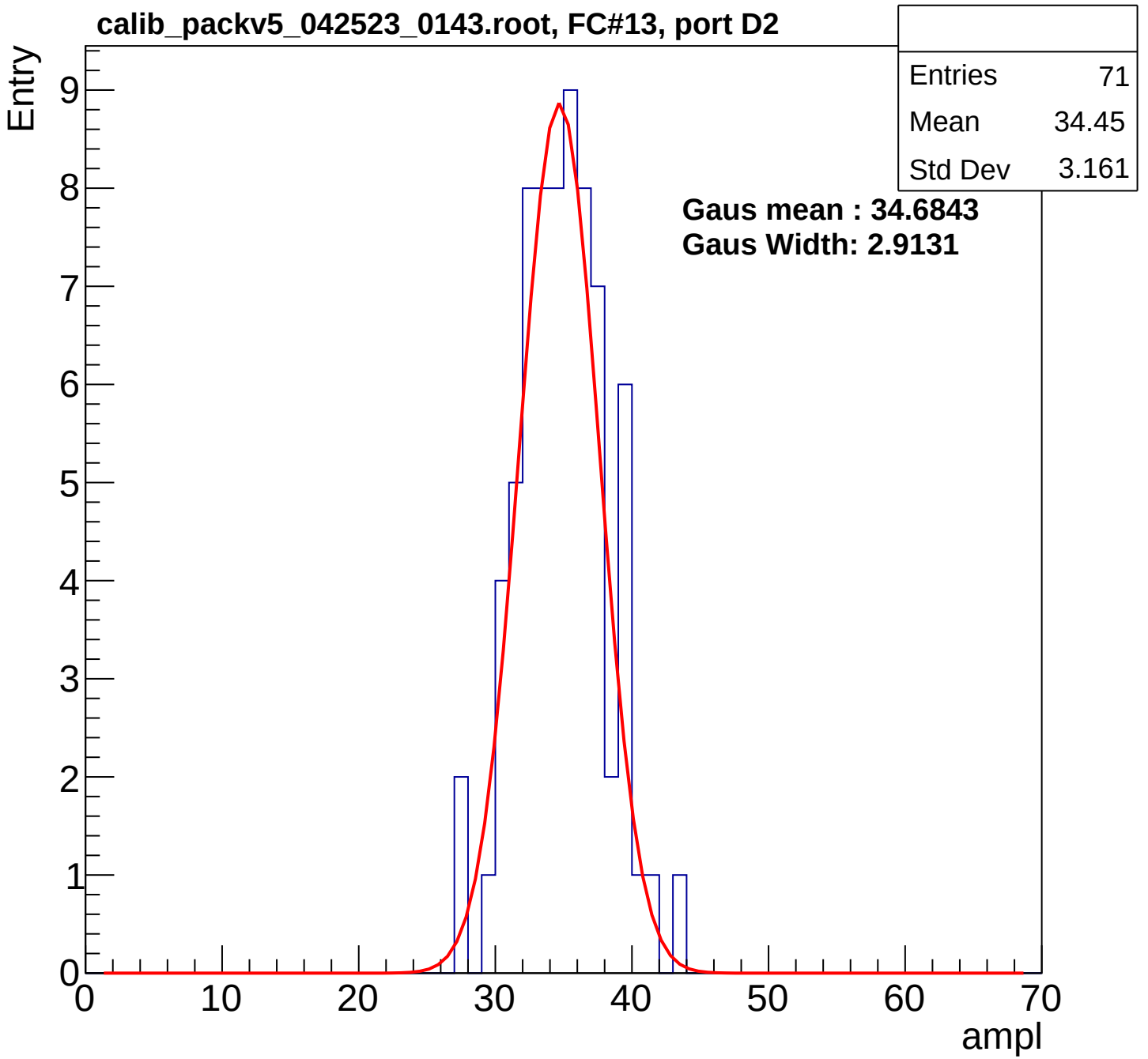

B1L003S, U17-ch55, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 54.14
Std Dev 3.339
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

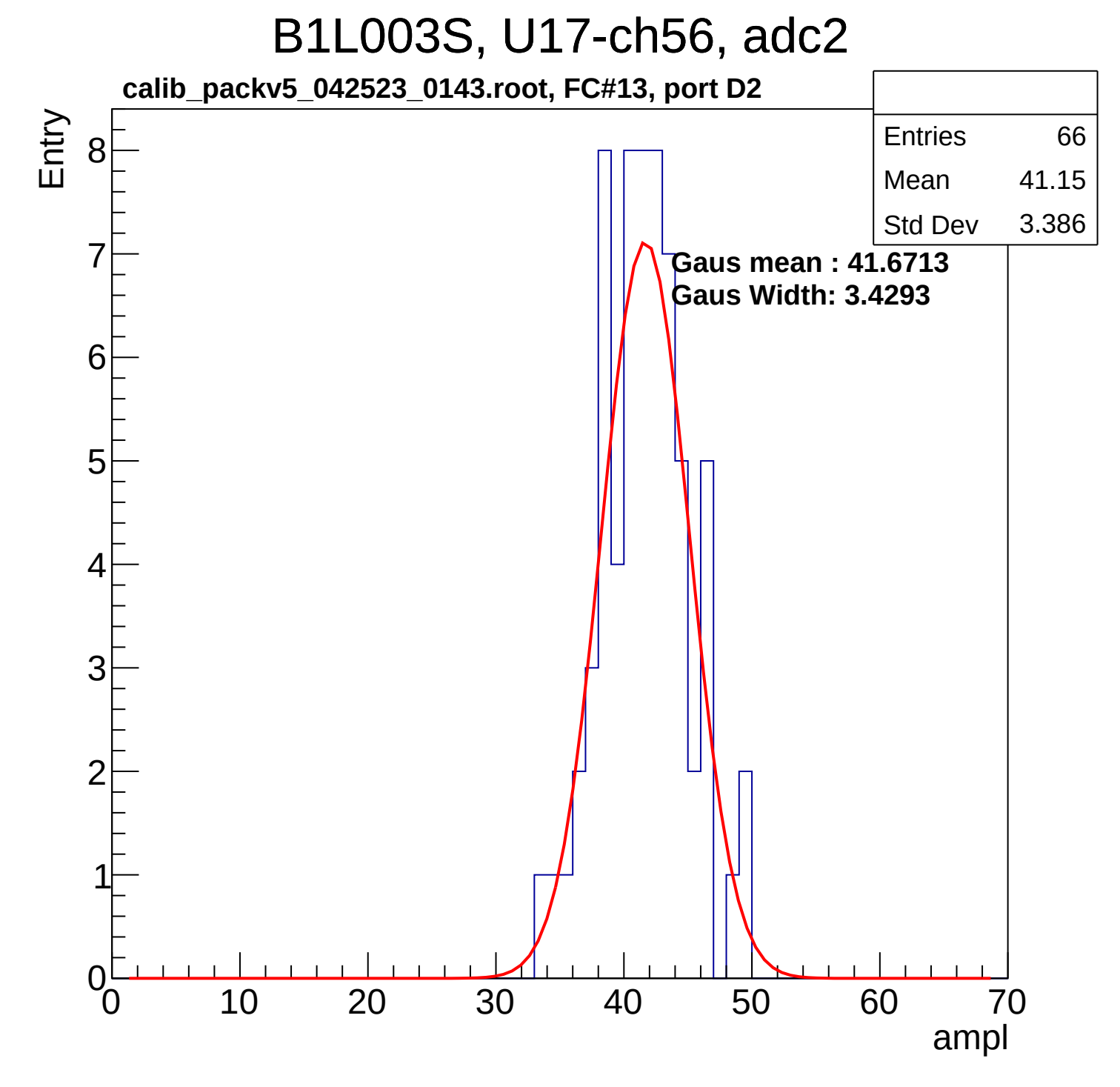

B1L003S, U17-ch55, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.65
Std Dev 8.352
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

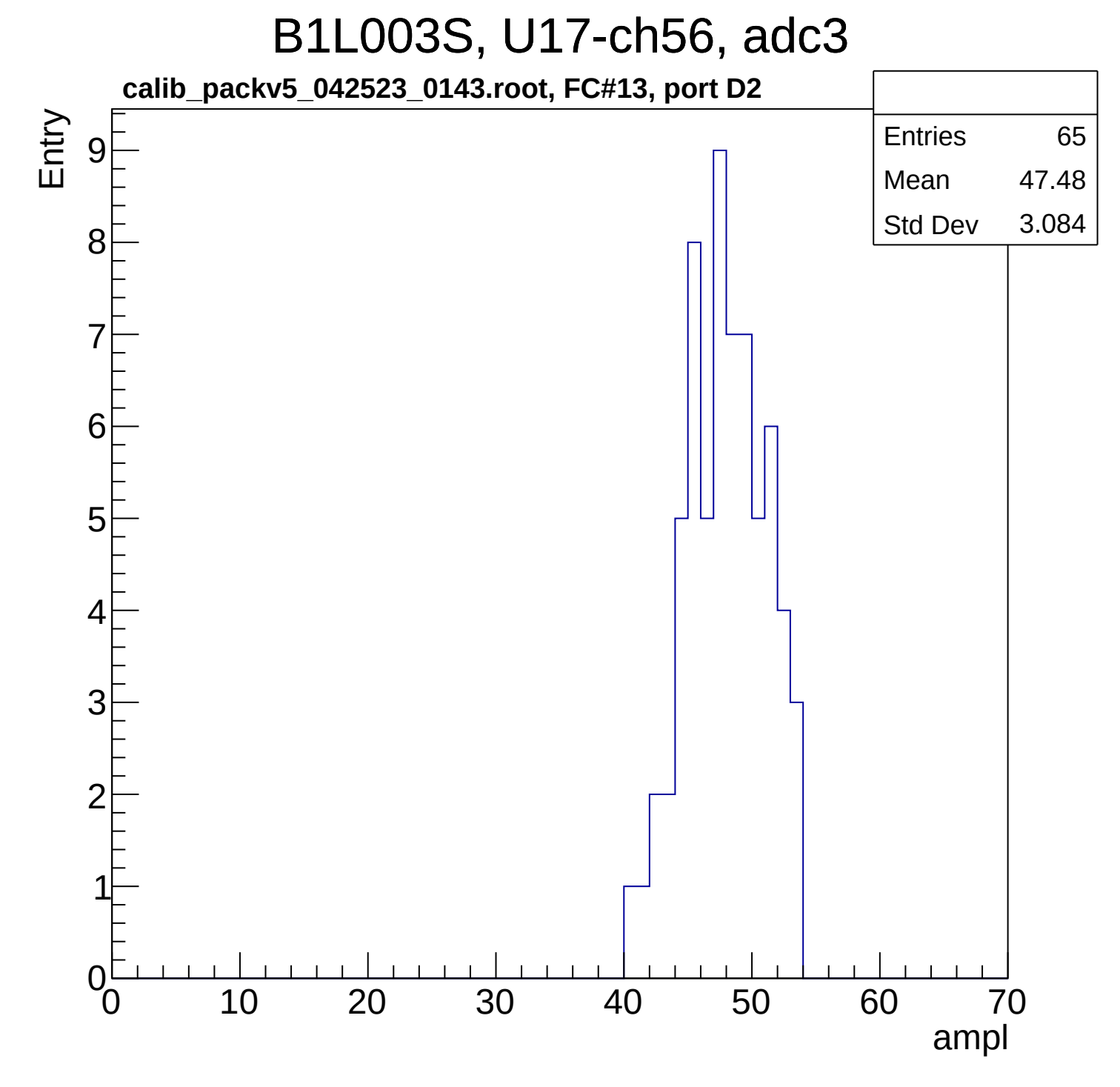

B1L003S, U17-ch55, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 0.6325
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

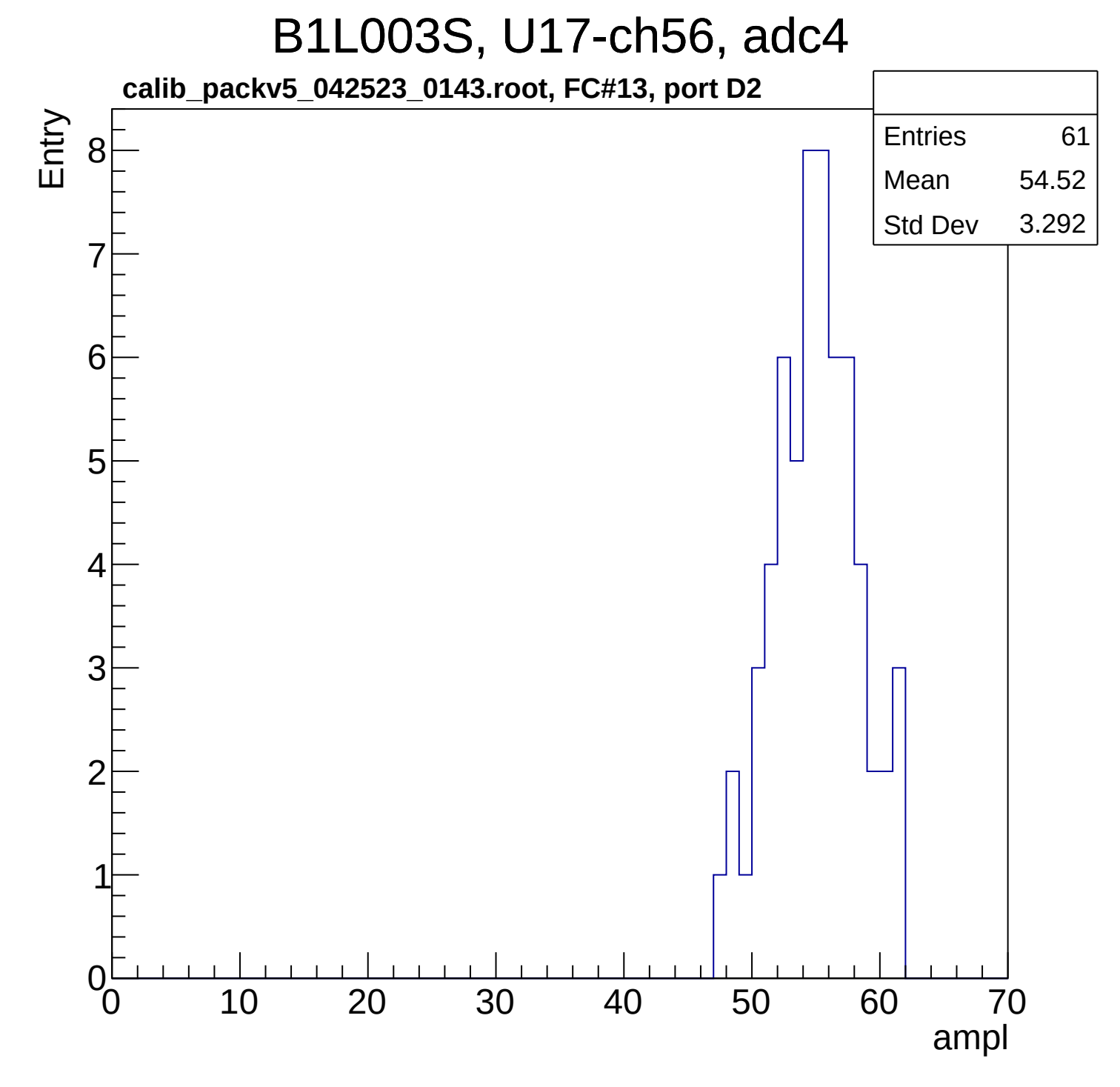

B1L003S, U17-ch55, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch56, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 25.91
Std Dev 7.768
Gaus mean : 28.1171
Gaus Width: 3.1491
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch56, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 34.45
Std Dev 3.161
Gaus mean : 34.6843
Gaus Width: 2.9131
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch56, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 41.15
Std Dev 3.386
Gaus mean : 41.6713
Gaus Width: 3.4293
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch56, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.48
Std Dev 3.084
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch56, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.52
Std Dev 3.292
Entry
ampl
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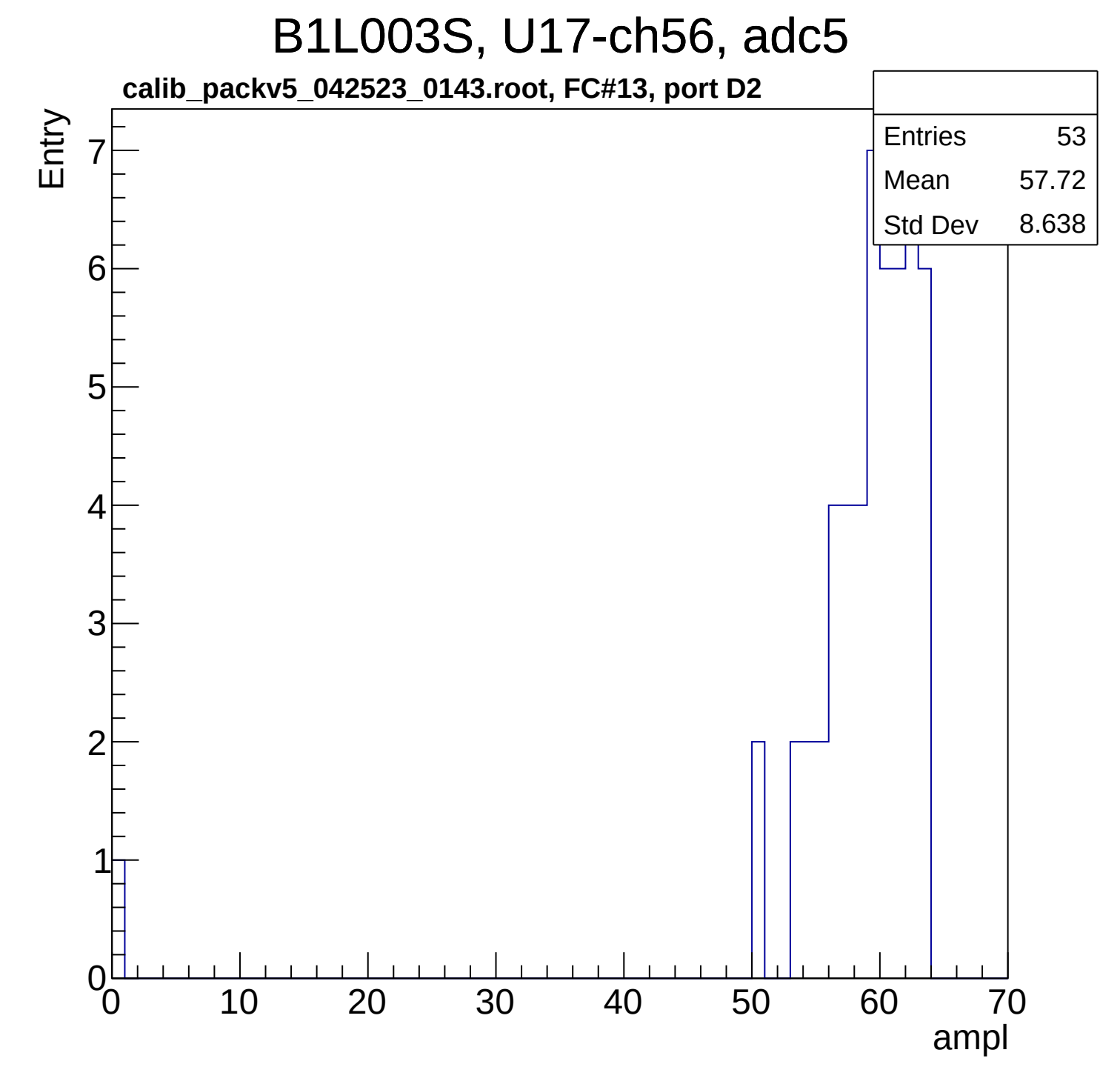
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch56, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 57.72
Std Dev 8.638
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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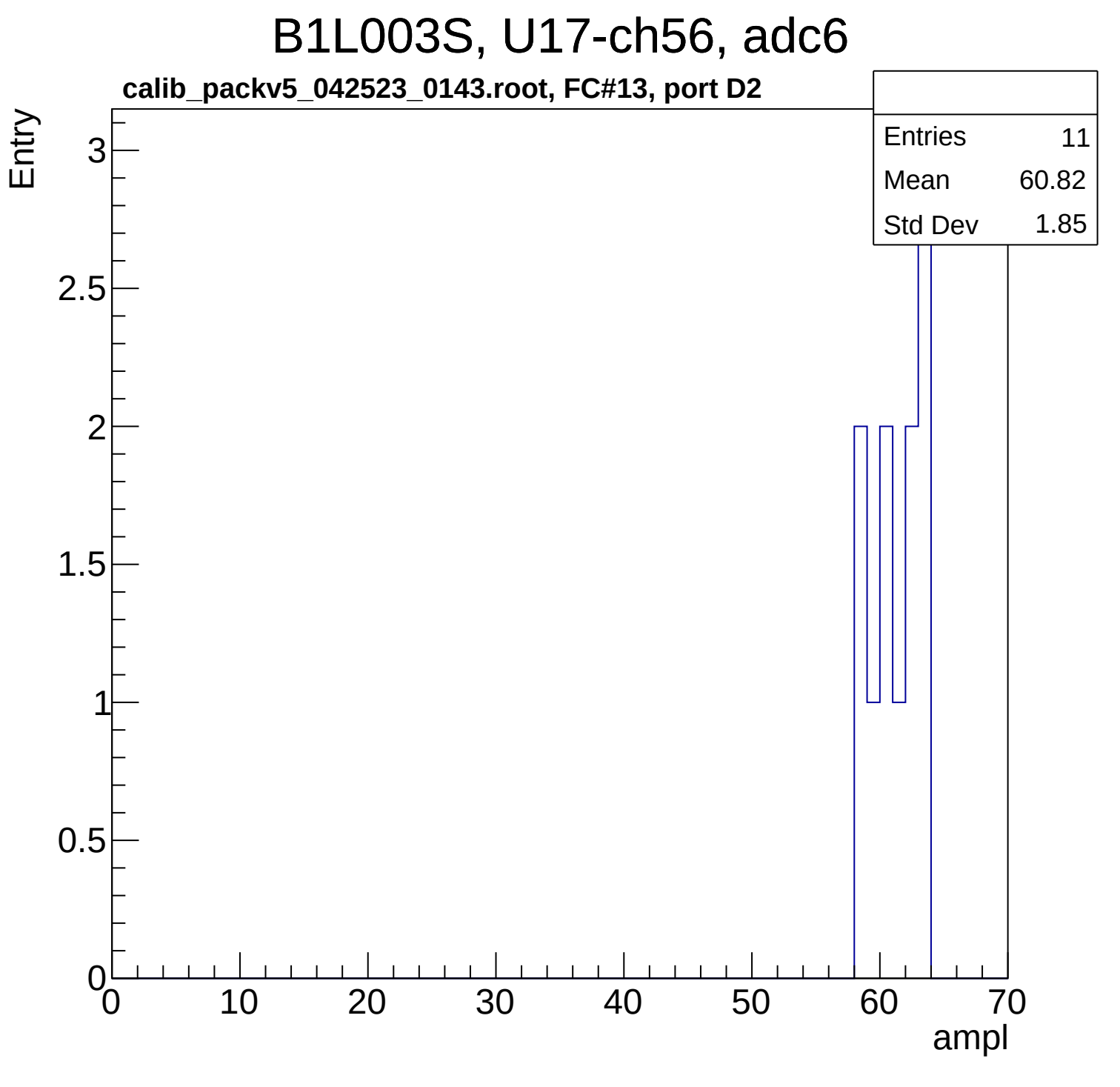
B1L003S, U17-ch56, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 60.82
Std Dev 1.85
Entry
ampl
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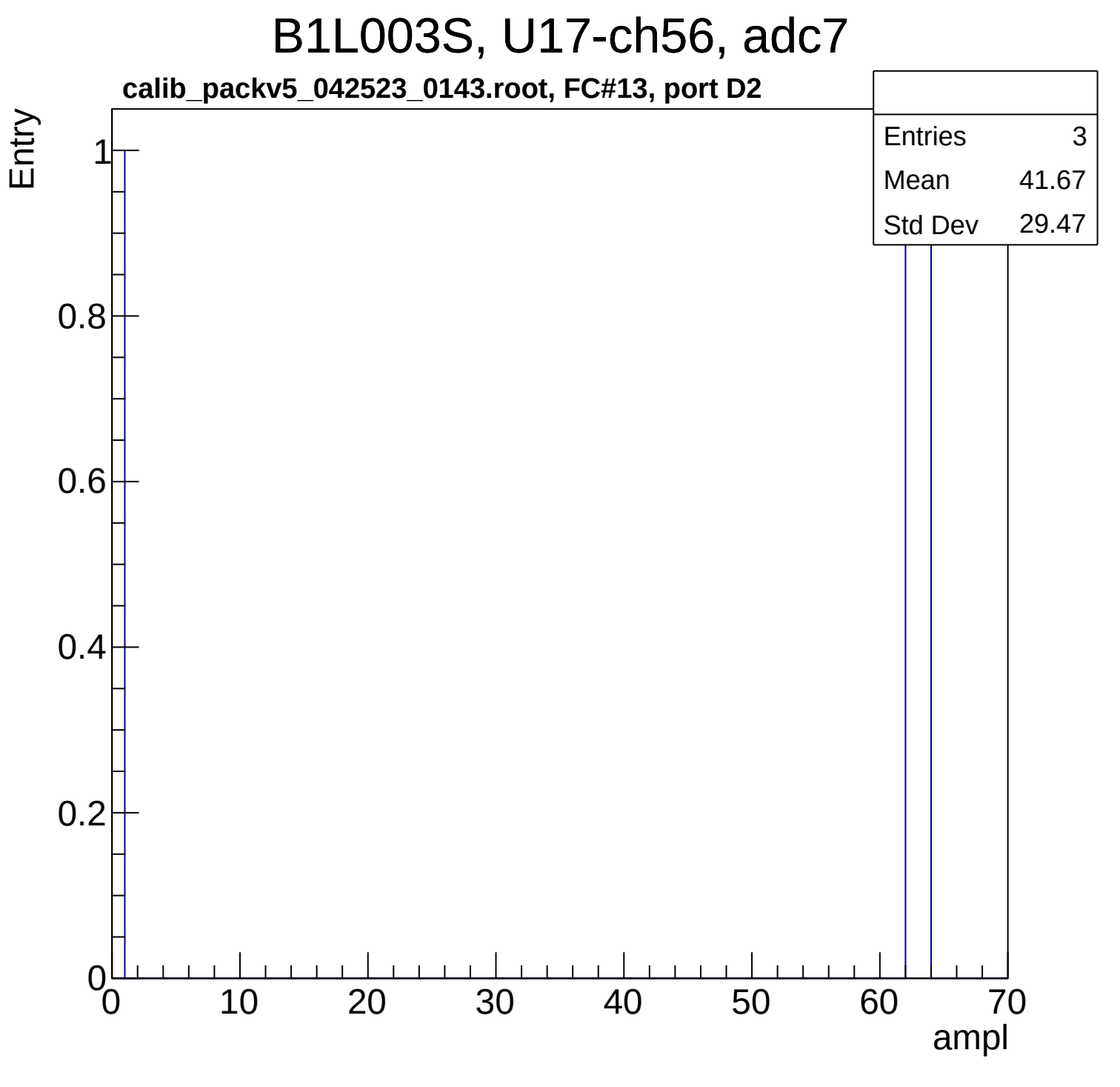
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch56, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
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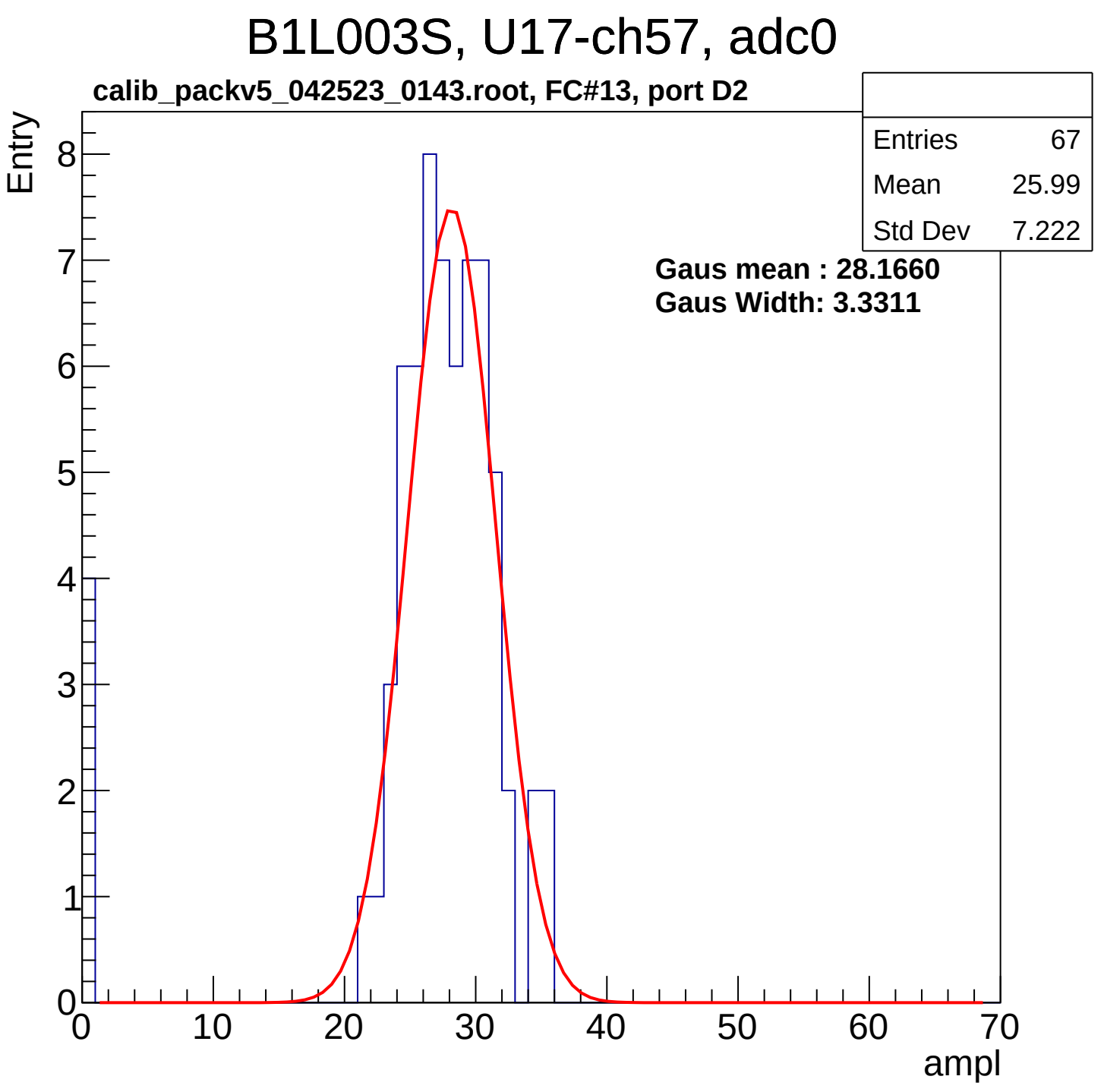
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch57, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 25.99
Std Dev 7.222
Gaus mean : 28.1660
Gaus Width: 3.3311
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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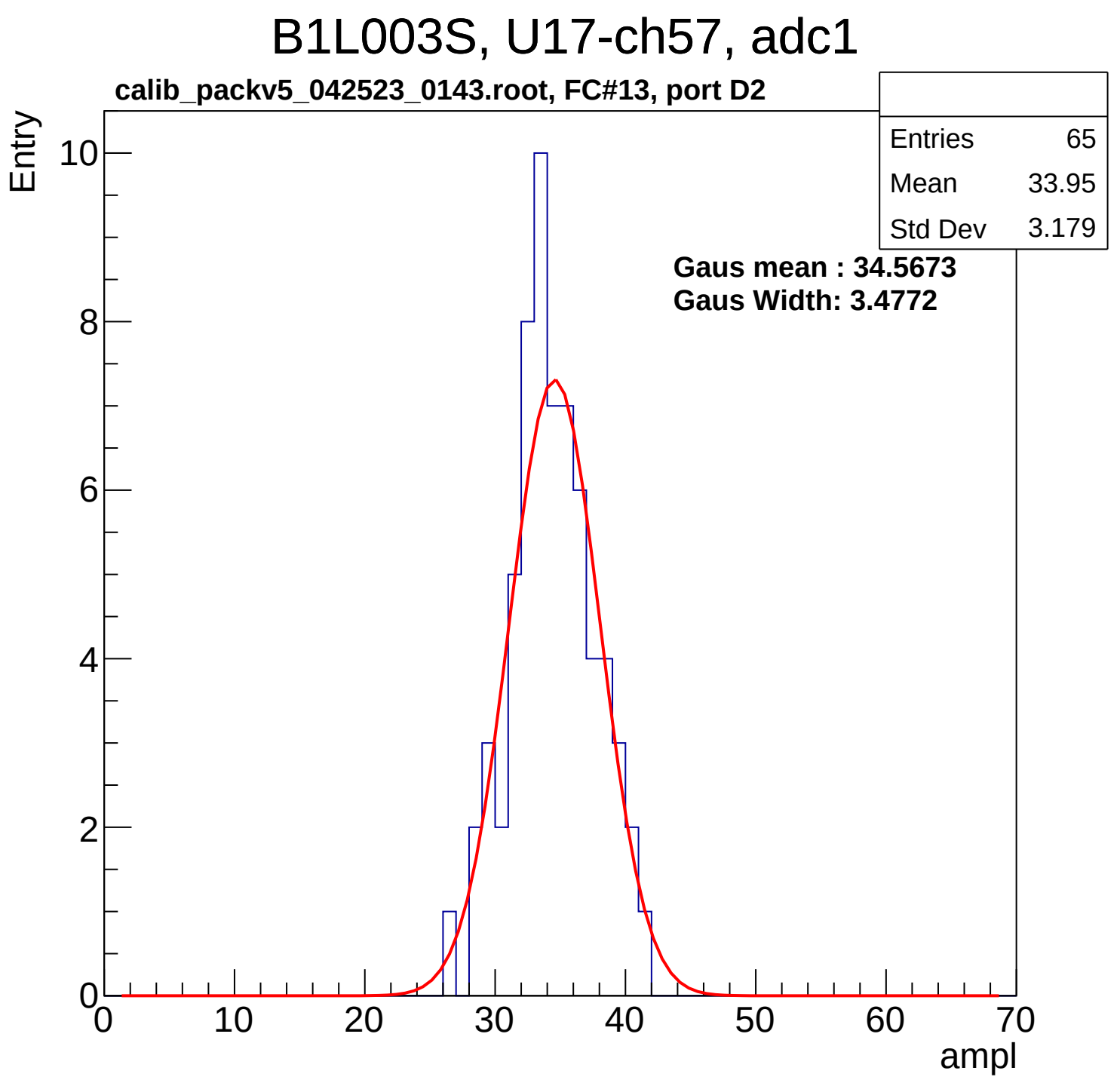
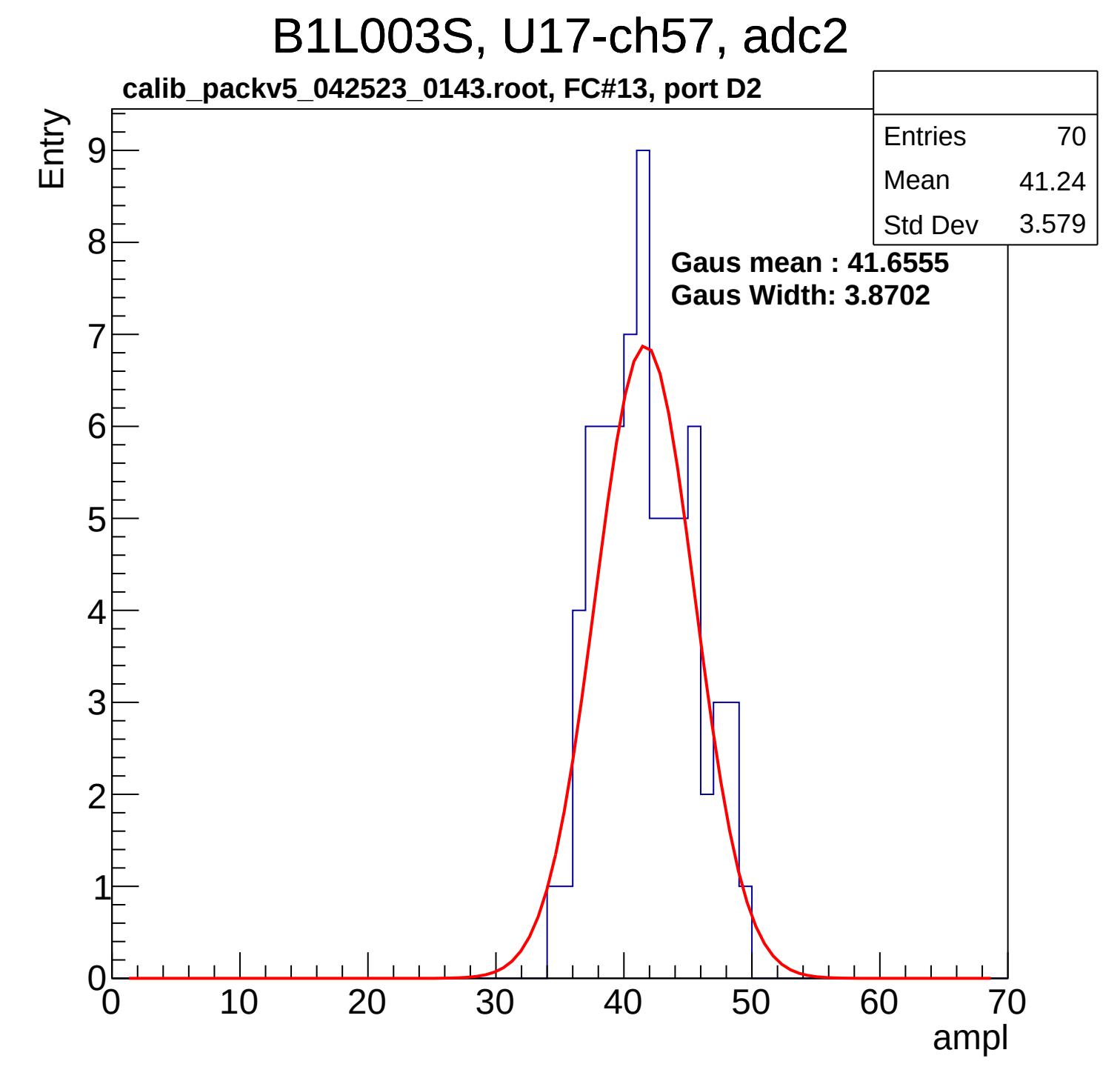
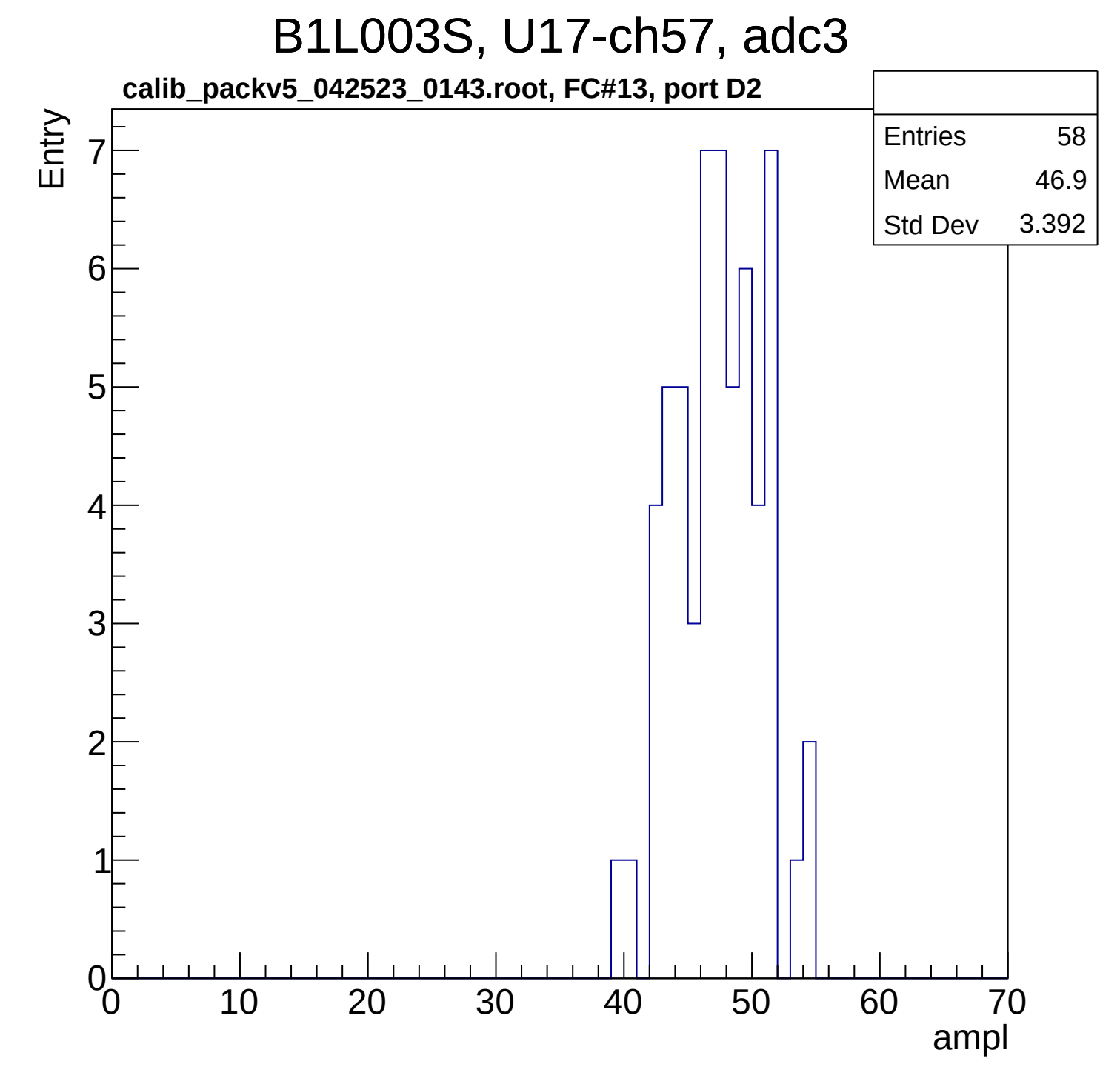
40
50
60
70


B1L003S, U17-ch57, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 33.95
Std Dev 3.179
Gaus mean : 34.5673
Gaus Width: 3.4772
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch57, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.24
Std Dev 3.579
Gaus mean : 41.6555
Gaus Width: 3.8702
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch57, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 46.9
Std Dev 3.392
Entry
ampl
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1
2
3
4
5
6
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0
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40
50
60
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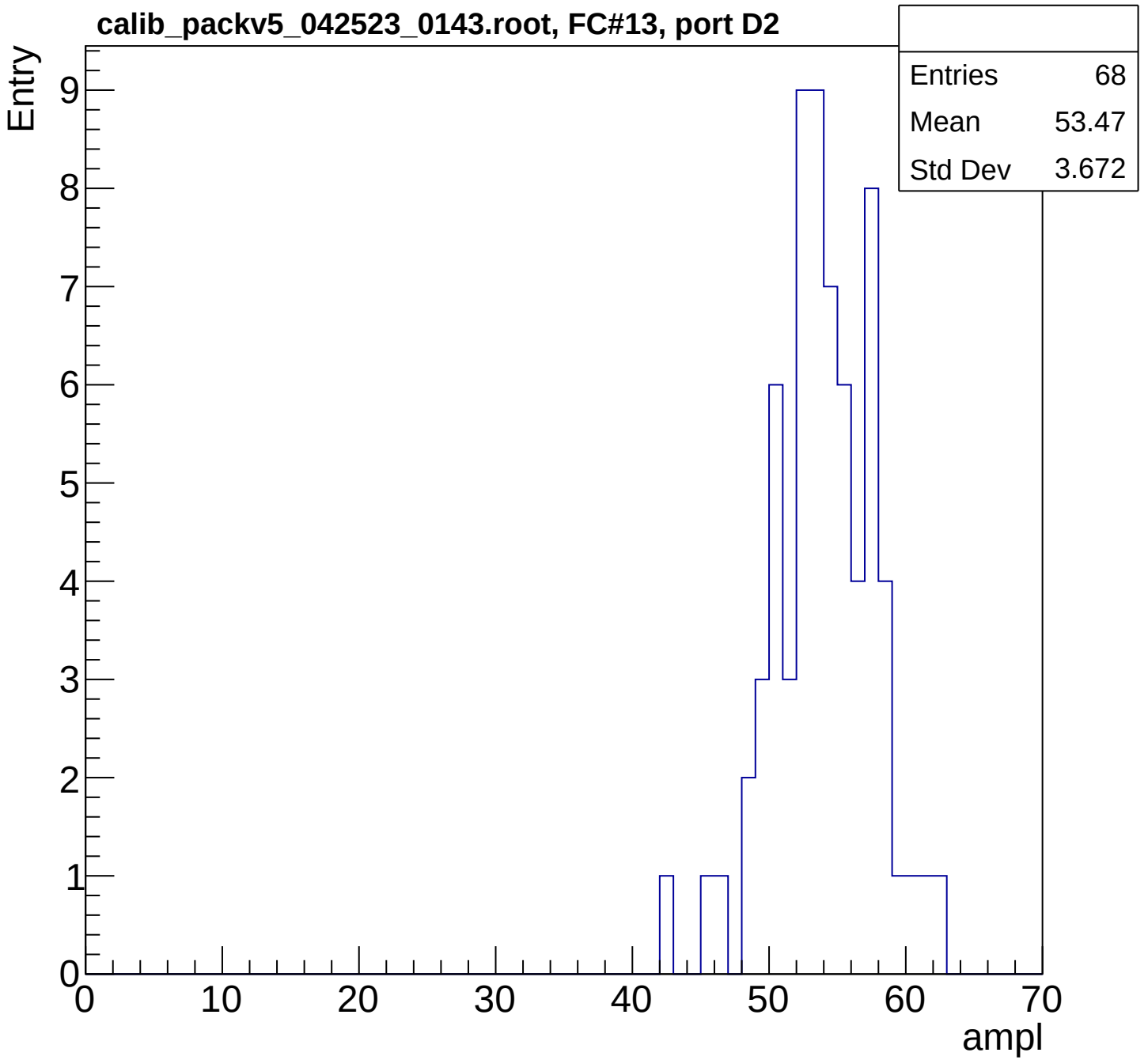
B1L003S, U17-ch57, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 53.47
Std Dev 3.672
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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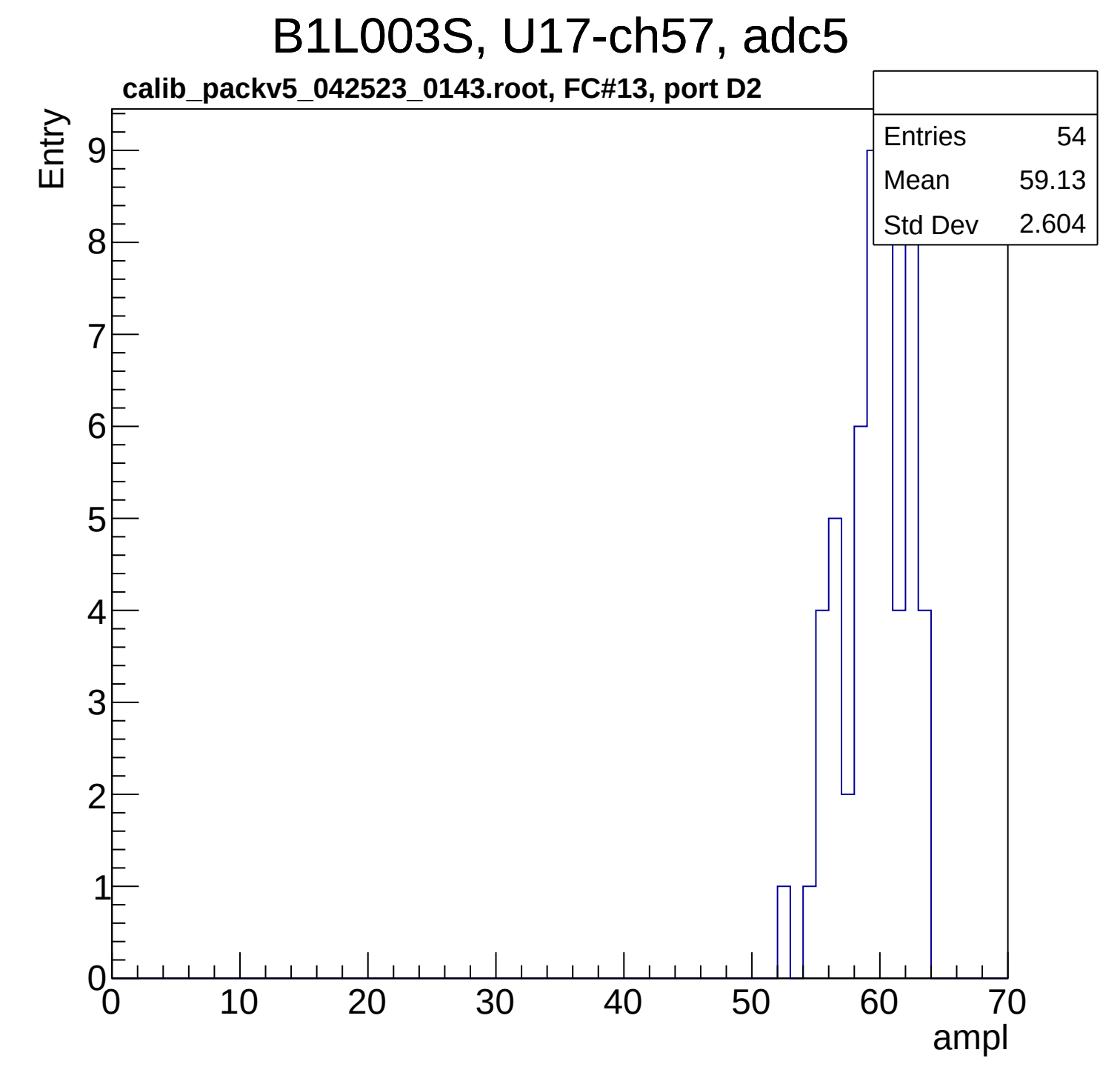
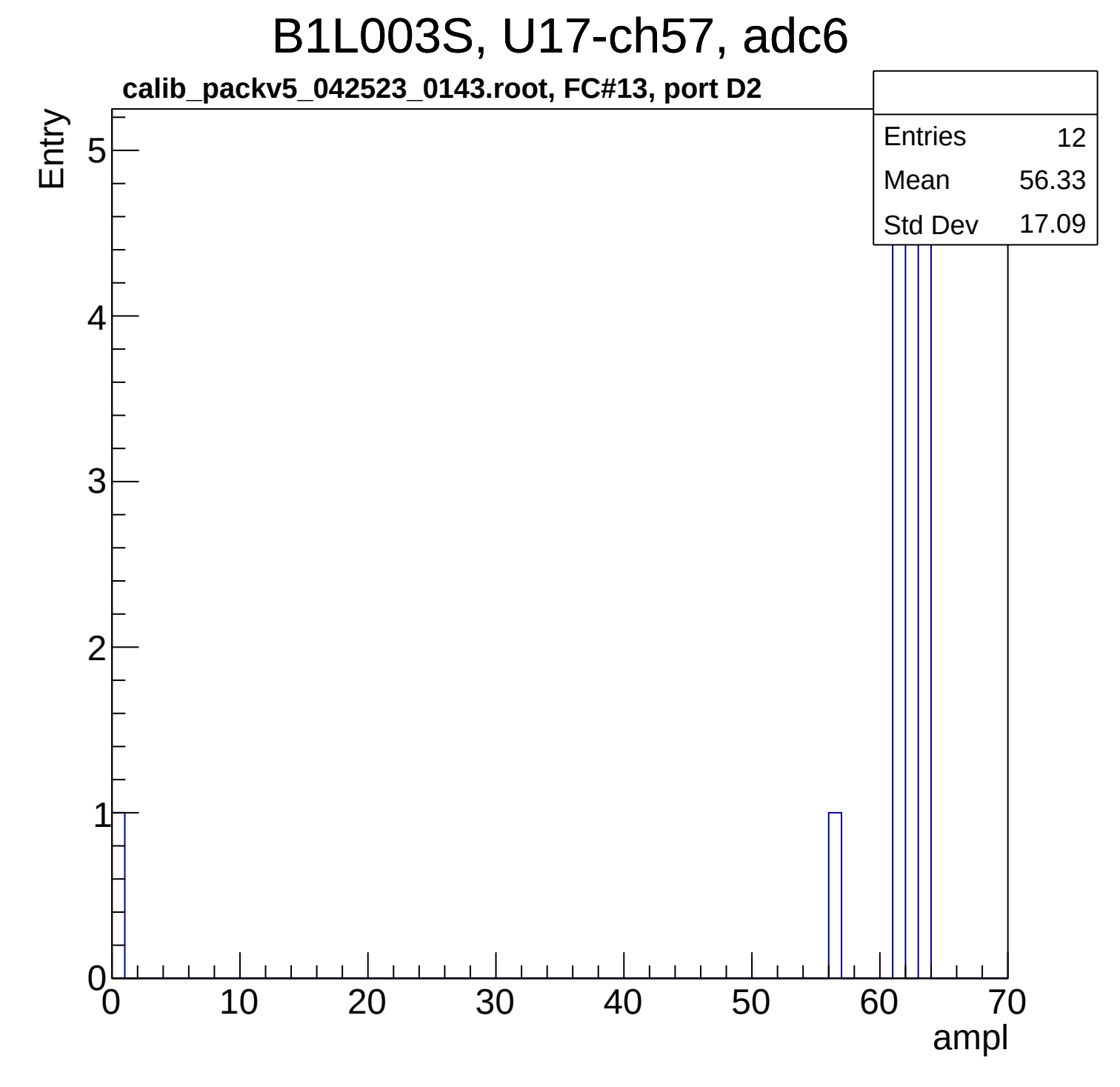
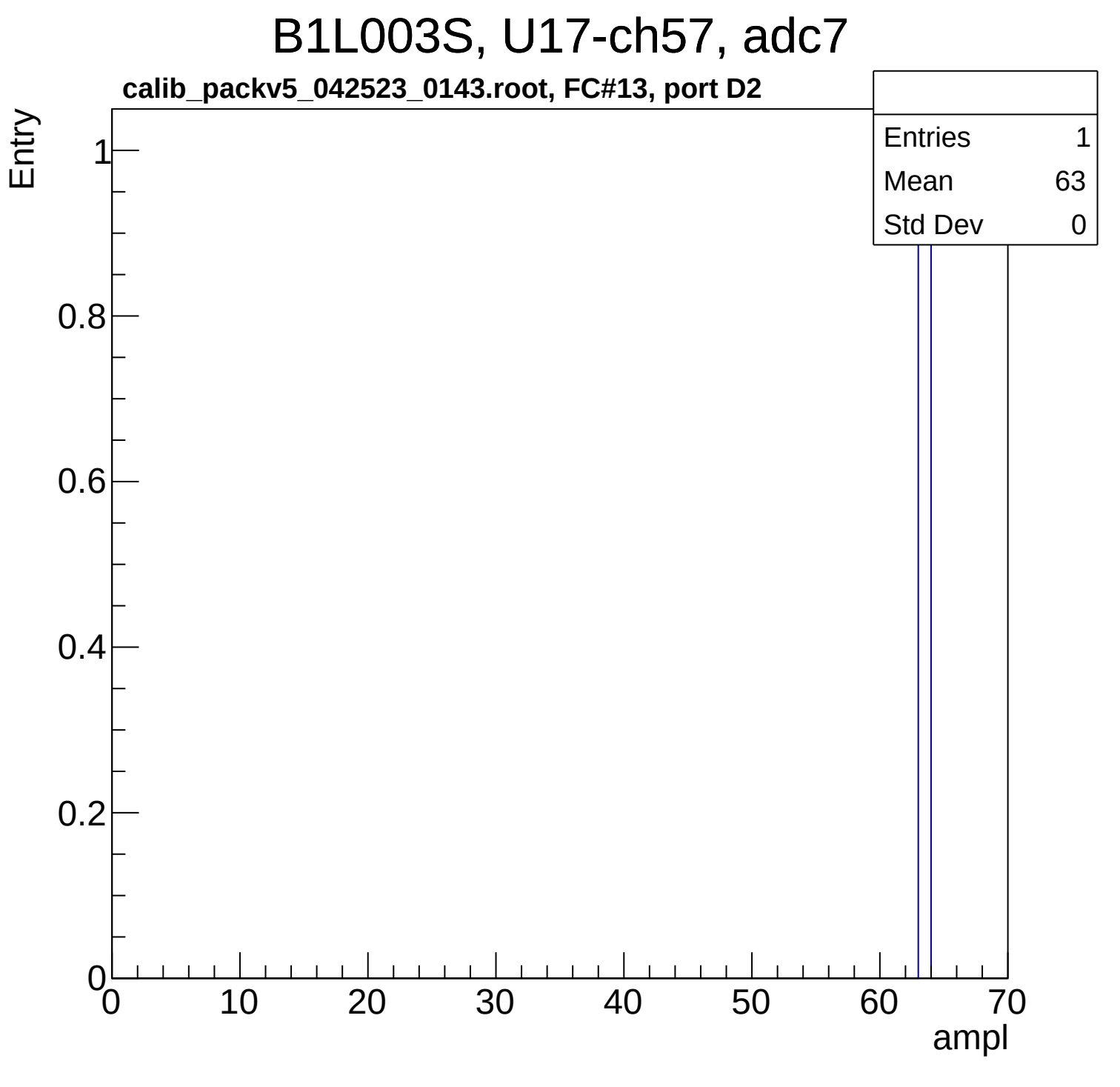
20
30
40
50
60
70


B1L003S, U17-ch57, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.13
Std Dev 2.604
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch57, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.33
Std Dev 17.09
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch57, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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0.4
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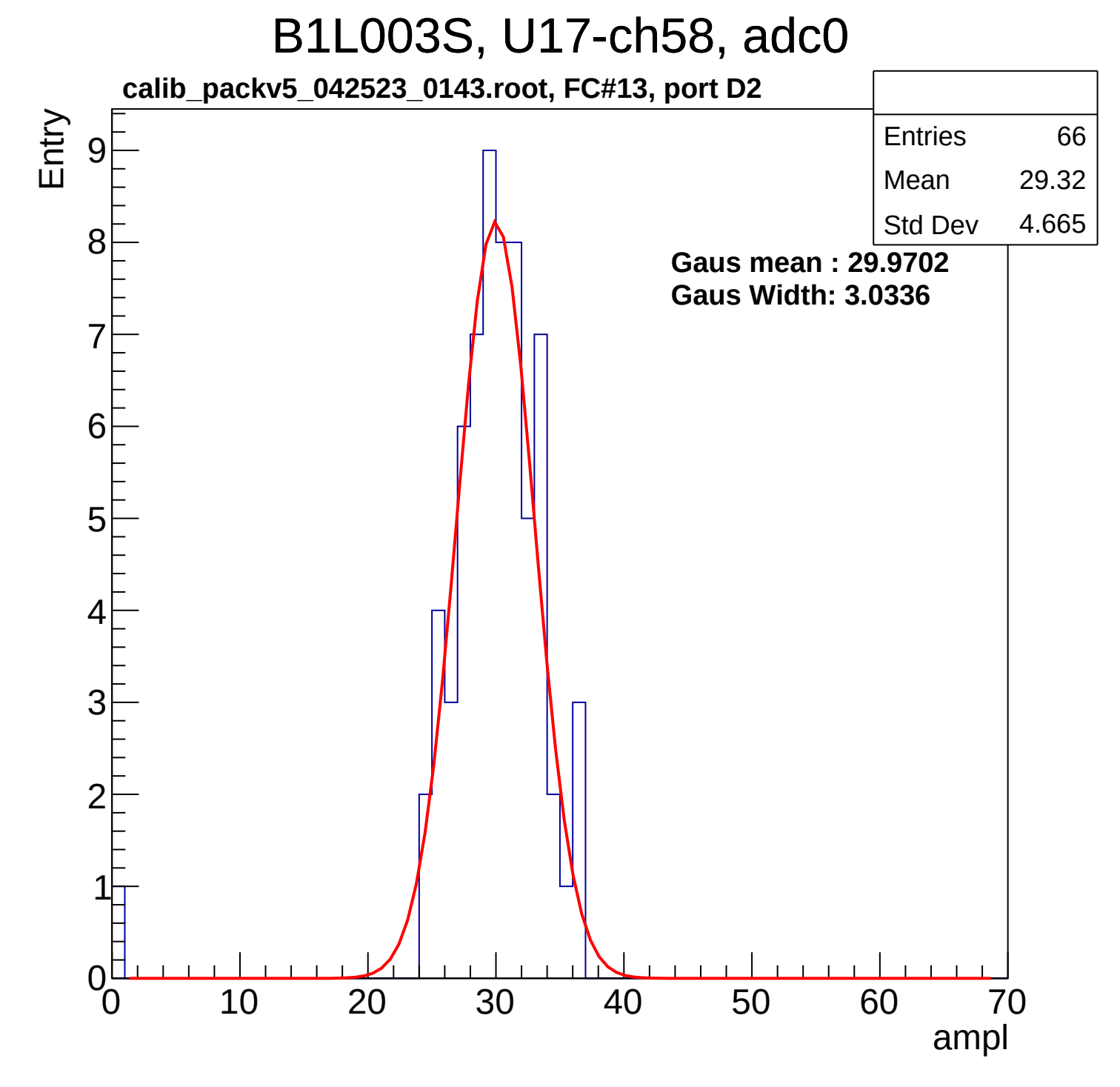
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch58, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 29.32
Std Dev 4.665
Gaus mean : 29.9702
Gaus Width: 3.0336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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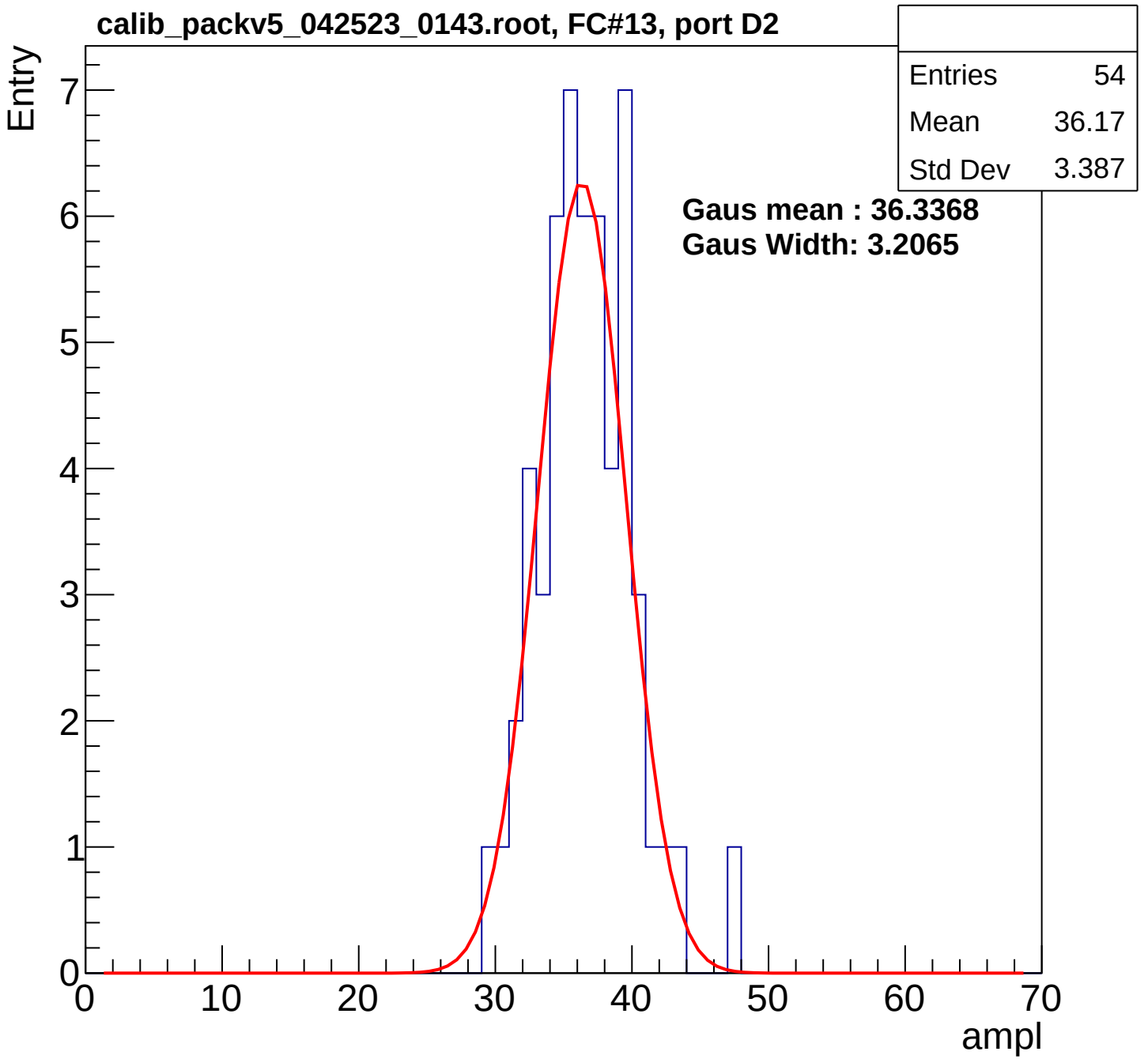
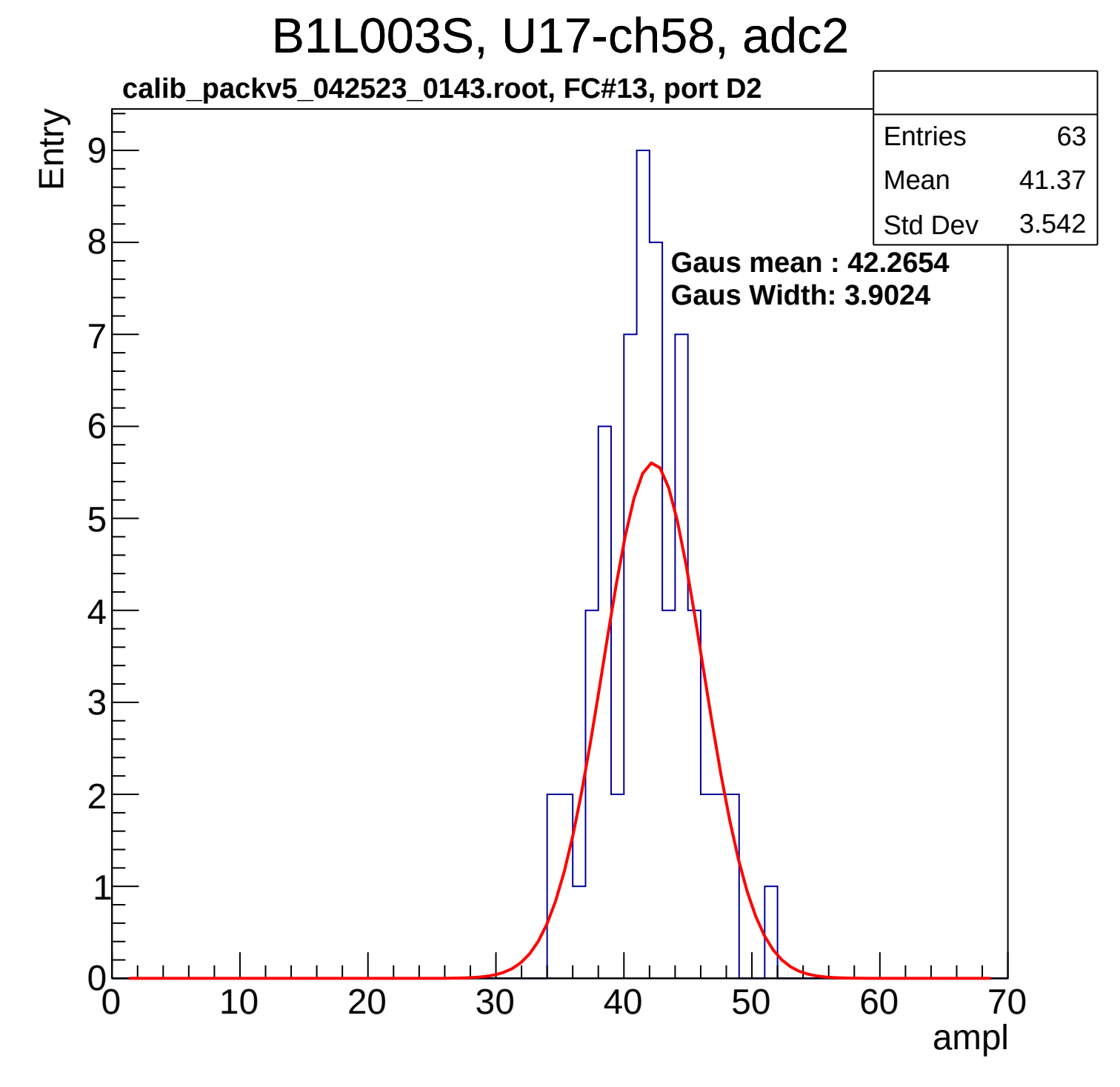
30
40
50
60
70


B1L003S, U17-ch58, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 36.17
Std Dev 3.387
Gaus mean : 36.3368
Gaus Width: 3.2065
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch58, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 41.37
Std Dev 3.542
Gaus mean : 42.2654
Gaus Width: 3.9024
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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40
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60
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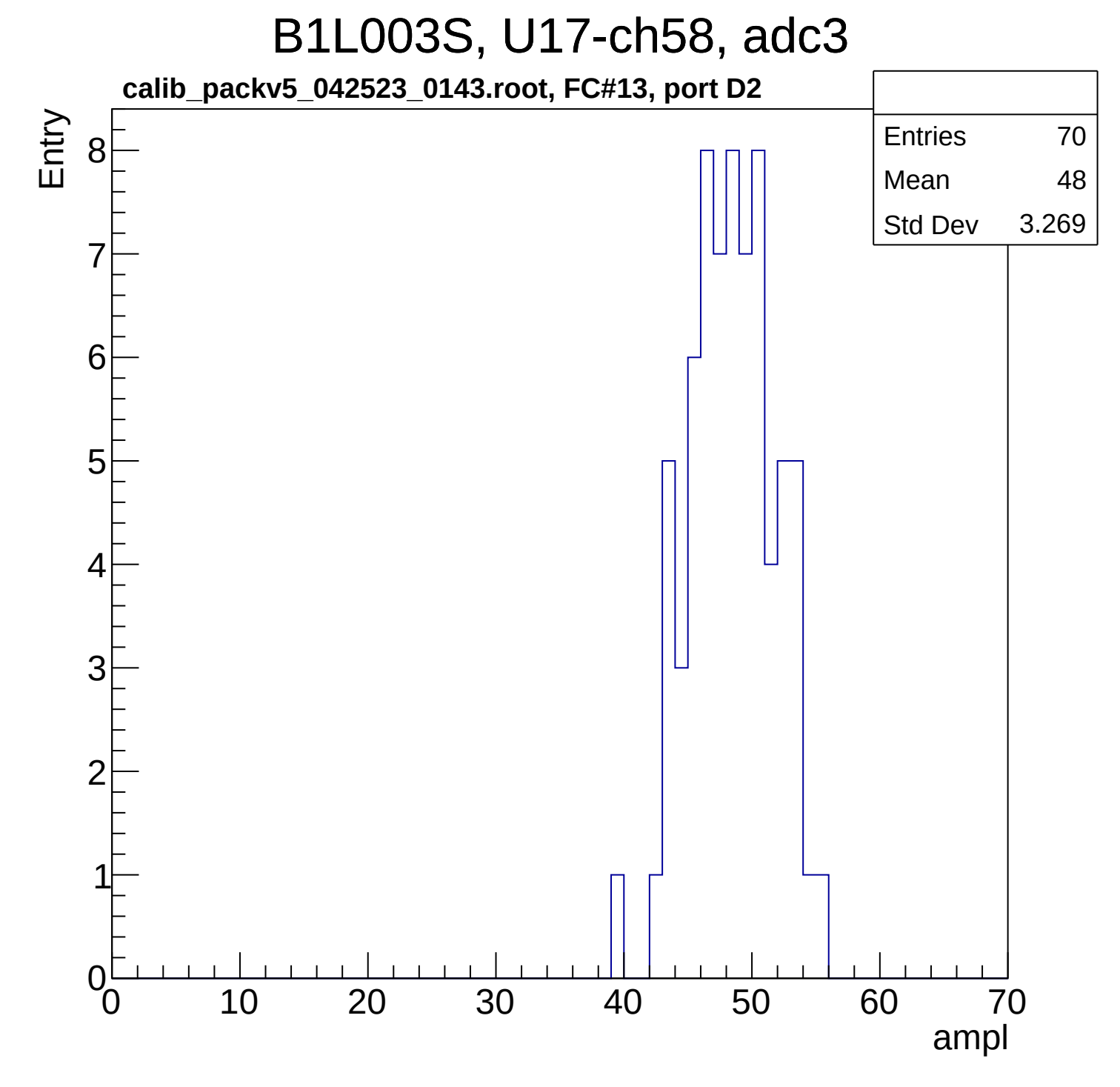
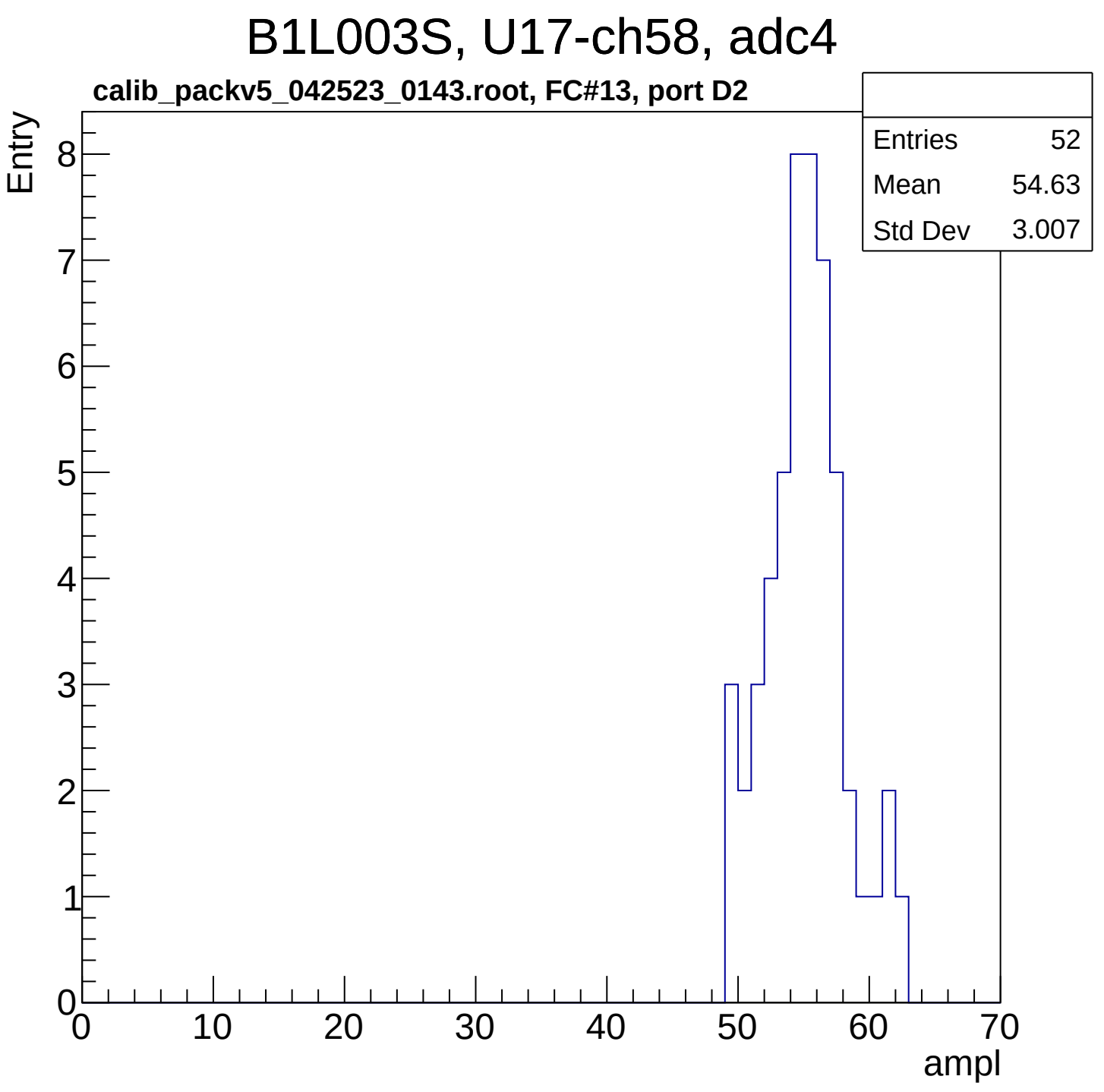

B1L003S, U17-ch58, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 48
Std Dev 3.269
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch58, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.63
Std Dev 3.007
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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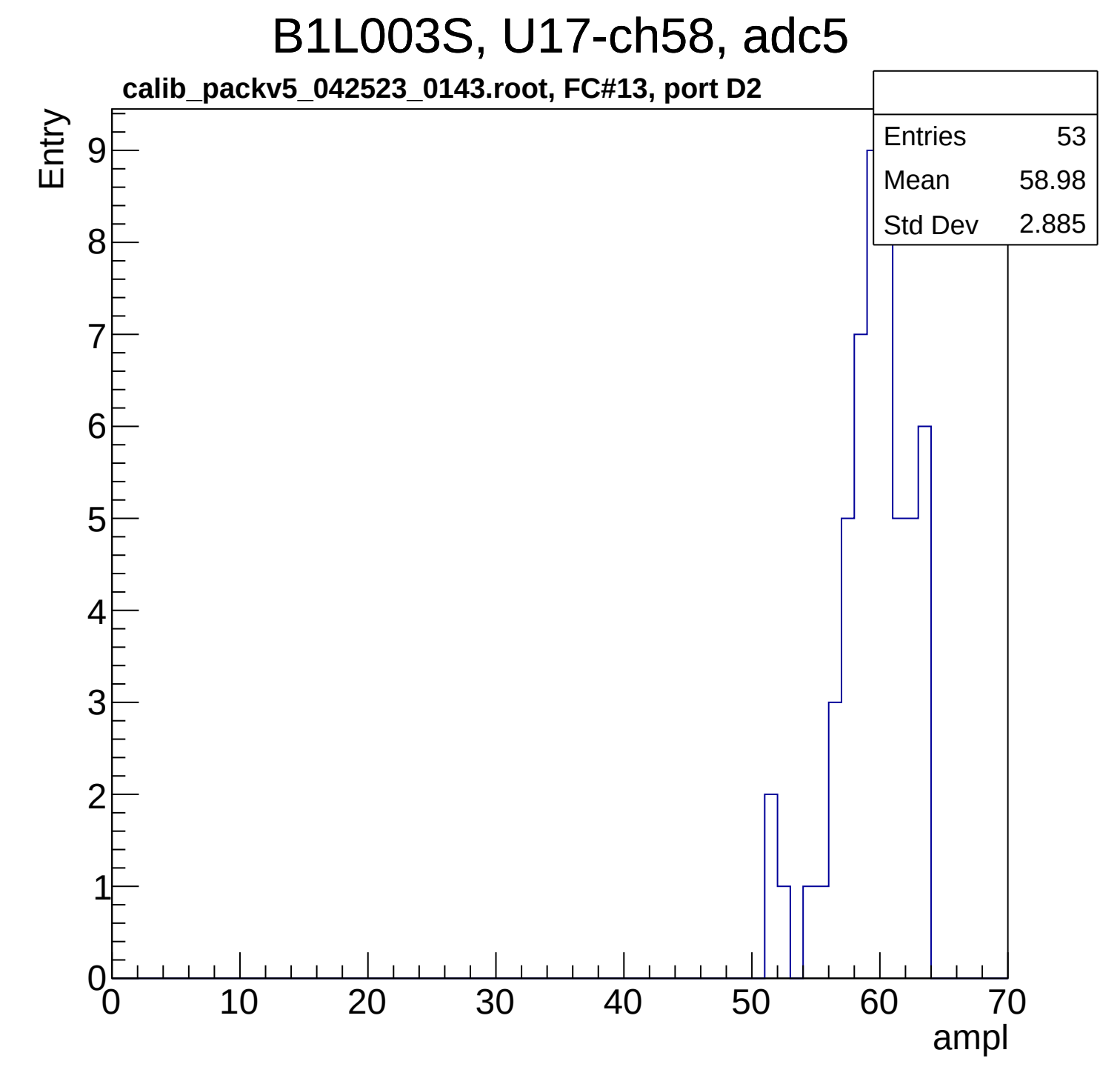
B1L003S, U17-ch58, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.98
Std Dev 2.885
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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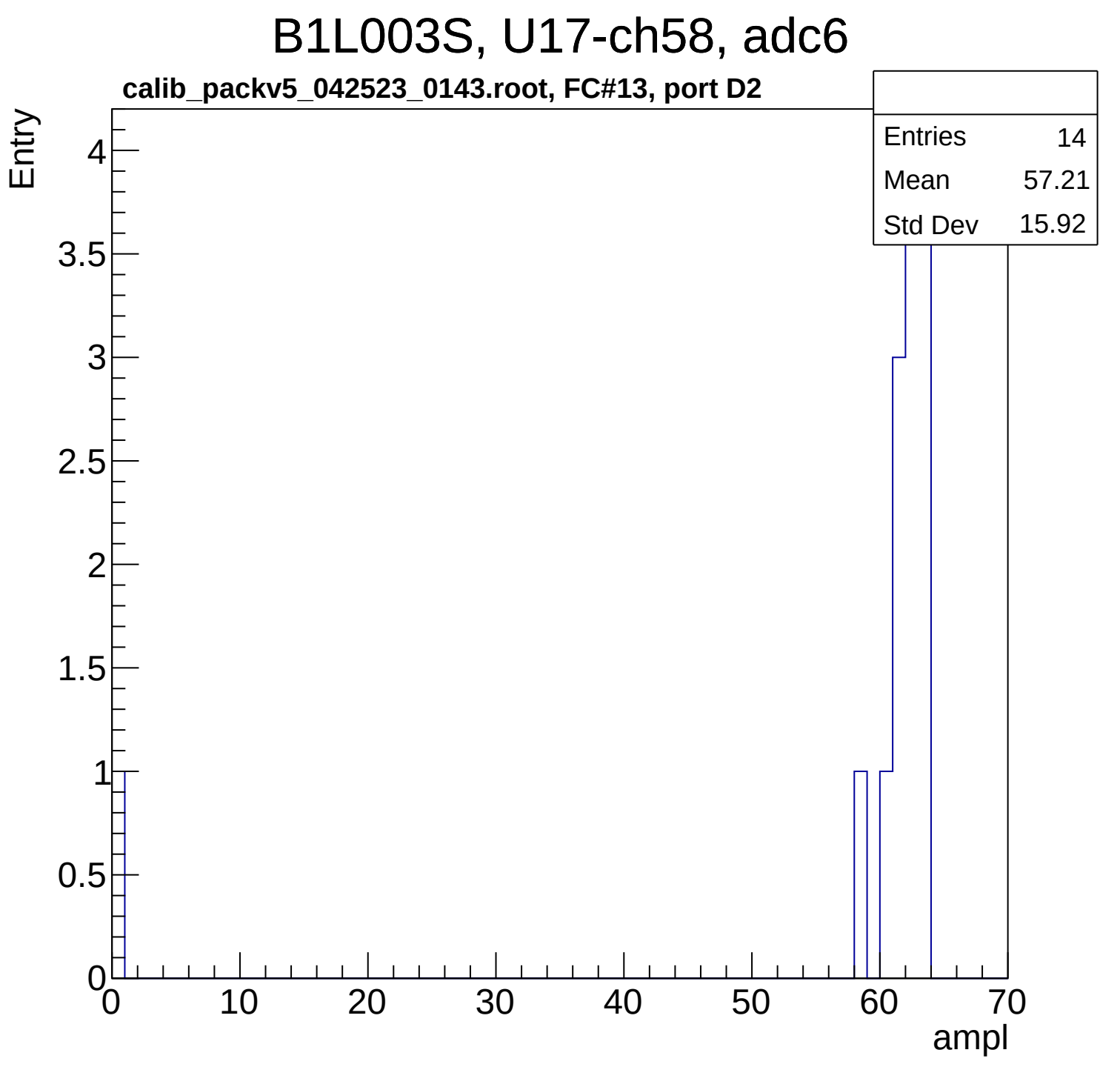
B1L003S, U17-ch58, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.21
Std Dev 15.92
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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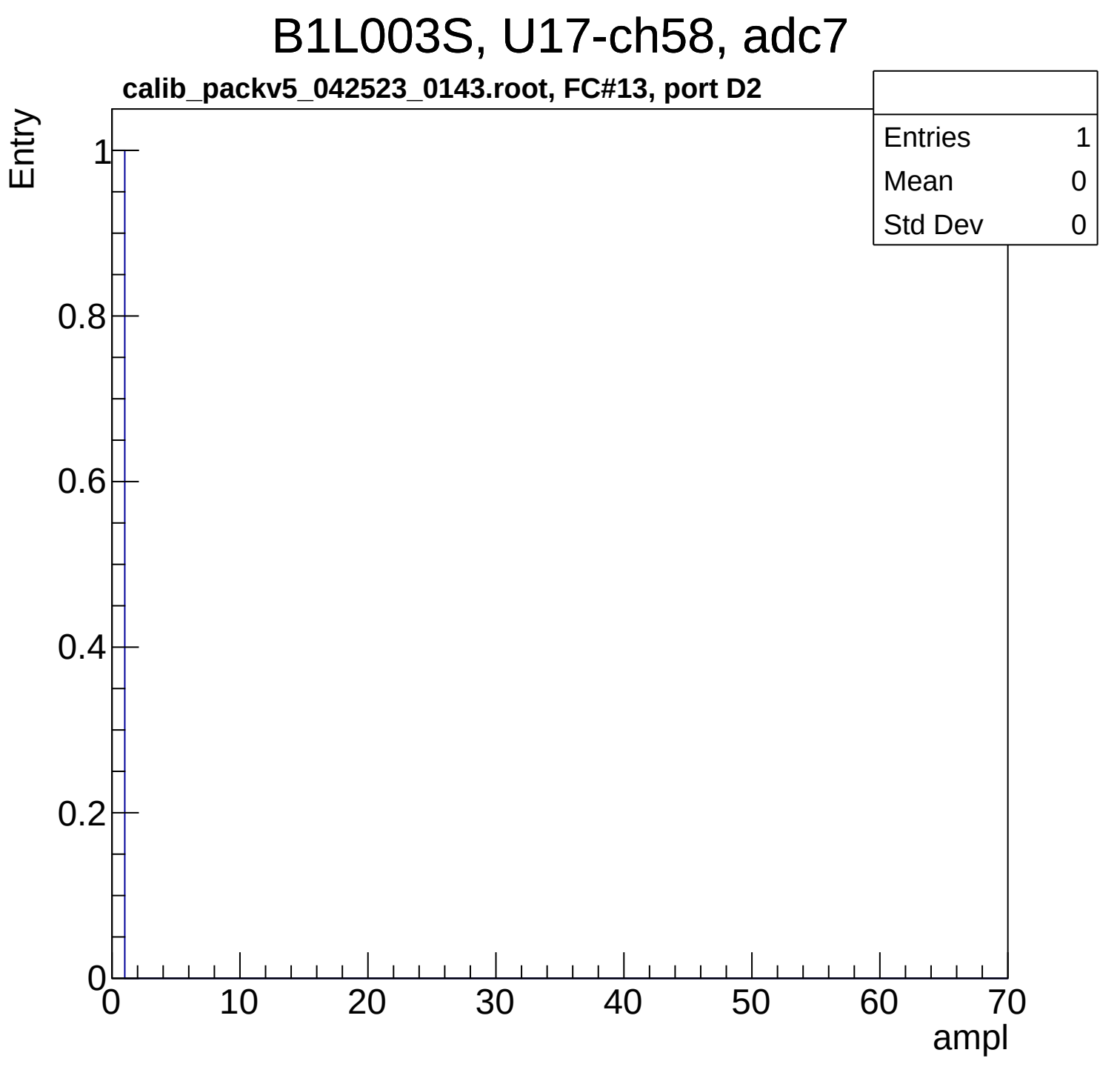
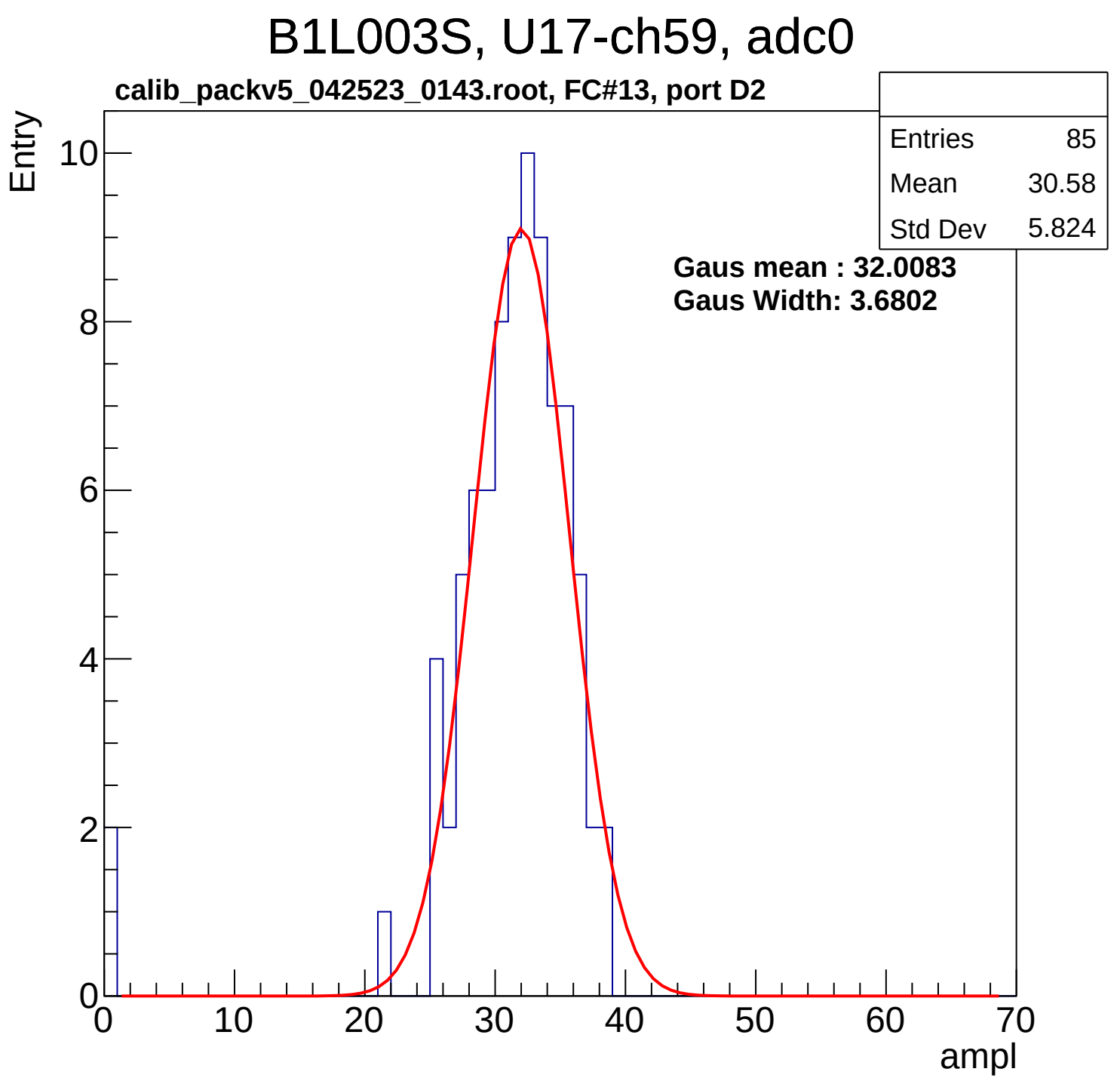
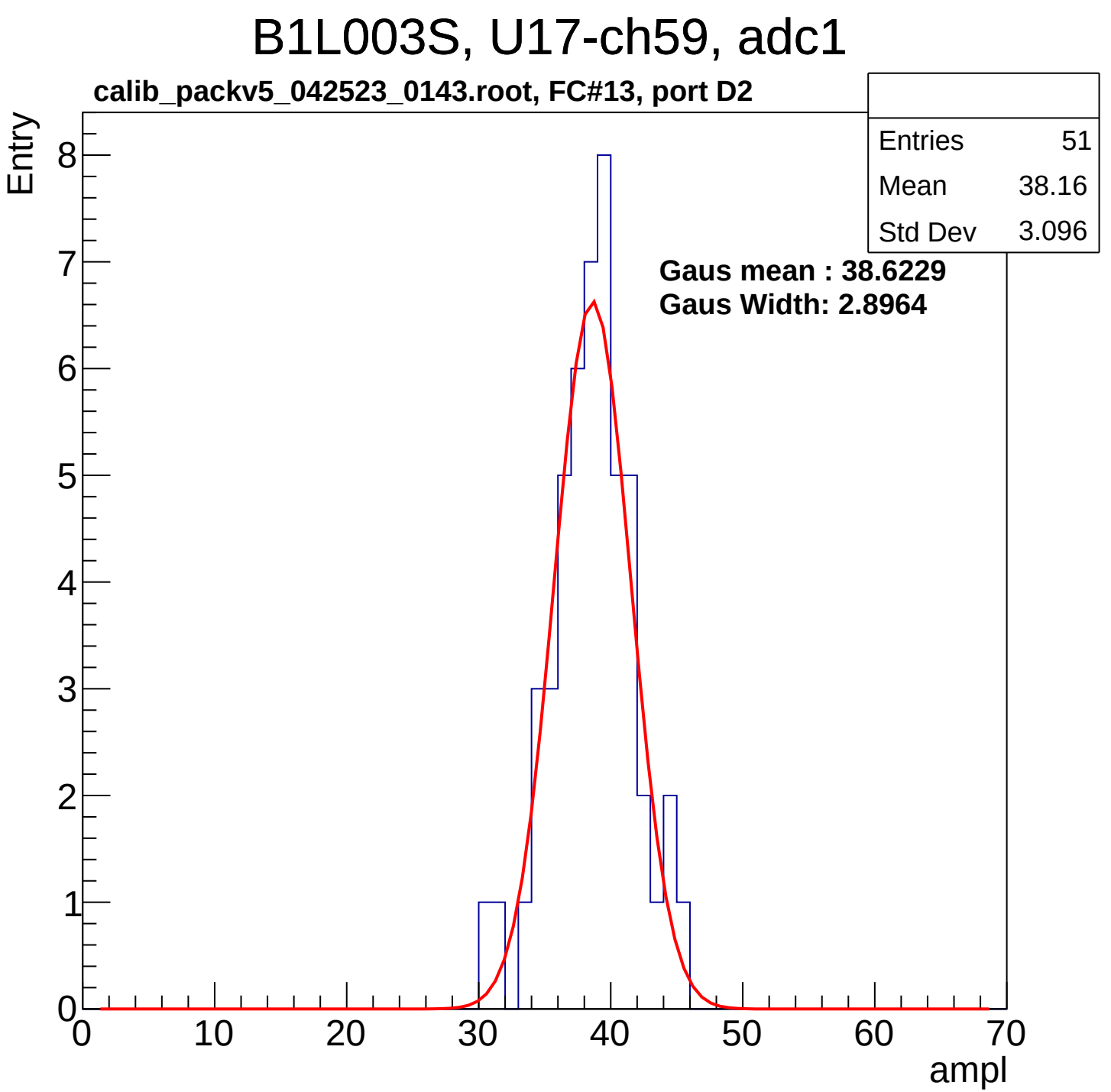

B1L003S, U17-ch58, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch59, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 30.58
Std Dev 5.824
Gaus mean : 32.0083
Gaus Width: 3.6802
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch59, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 38.16
Std Dev 3.096
Gaus mean : 38.6229
Gaus Width: 2.8964
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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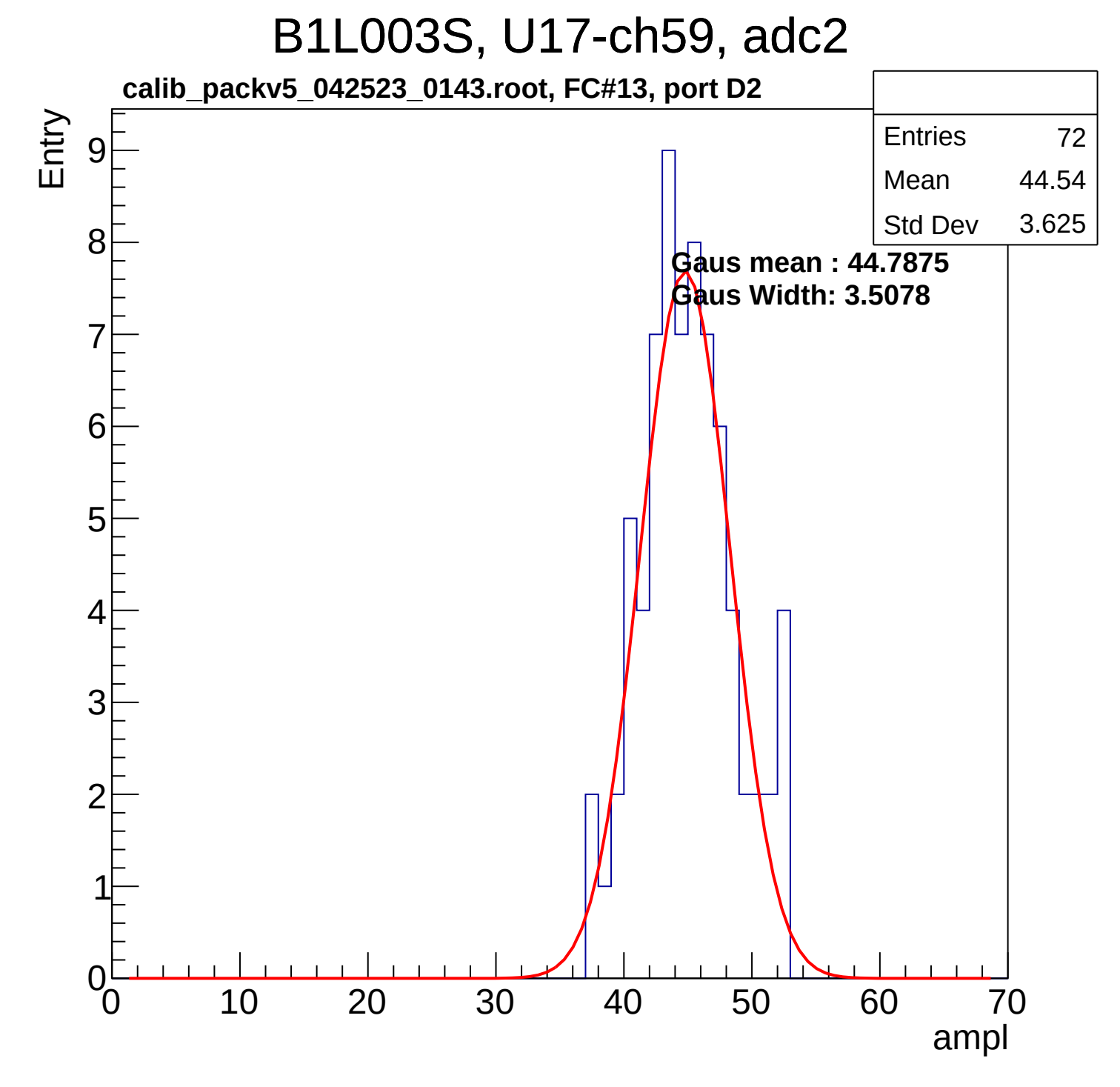
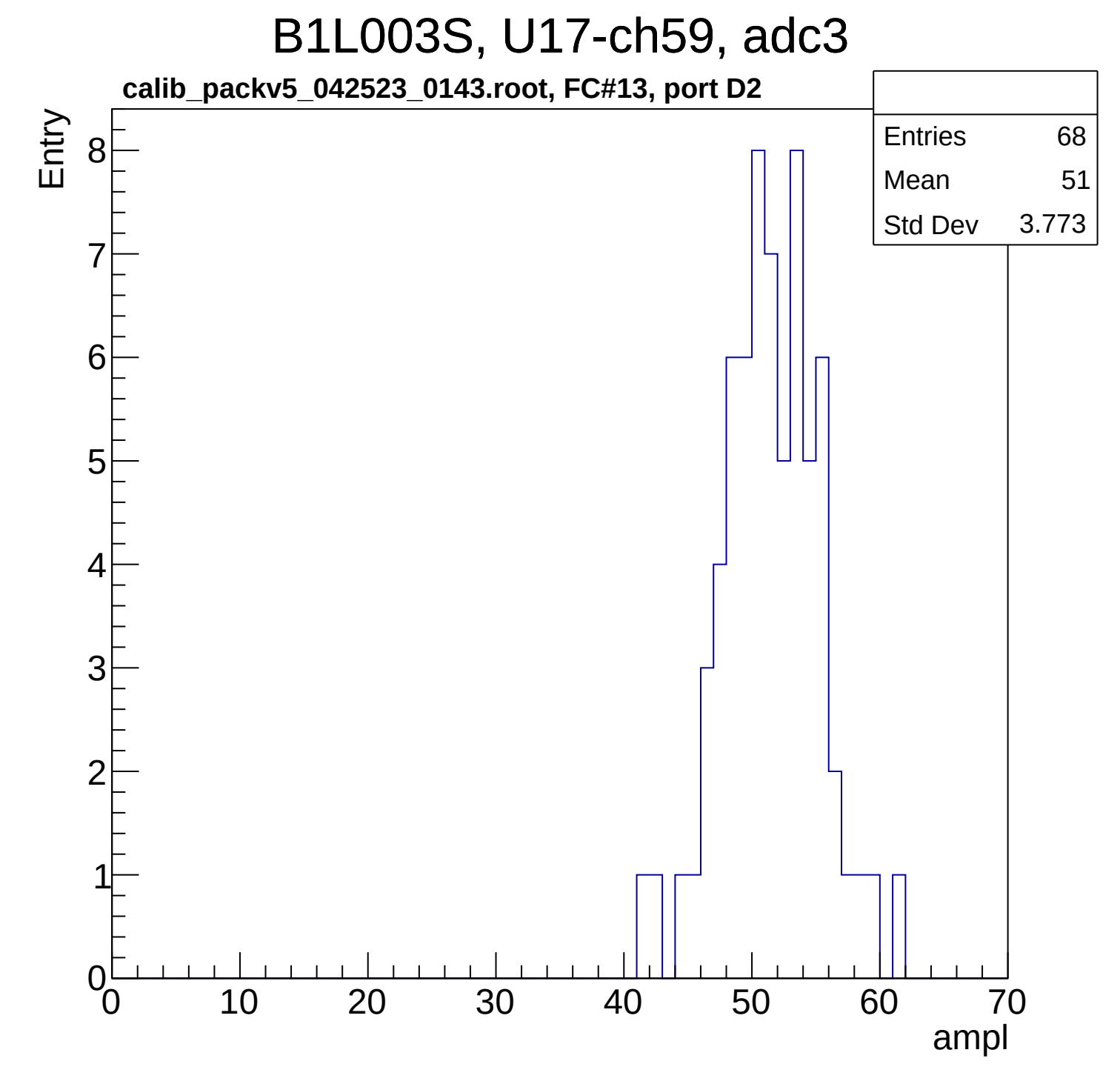
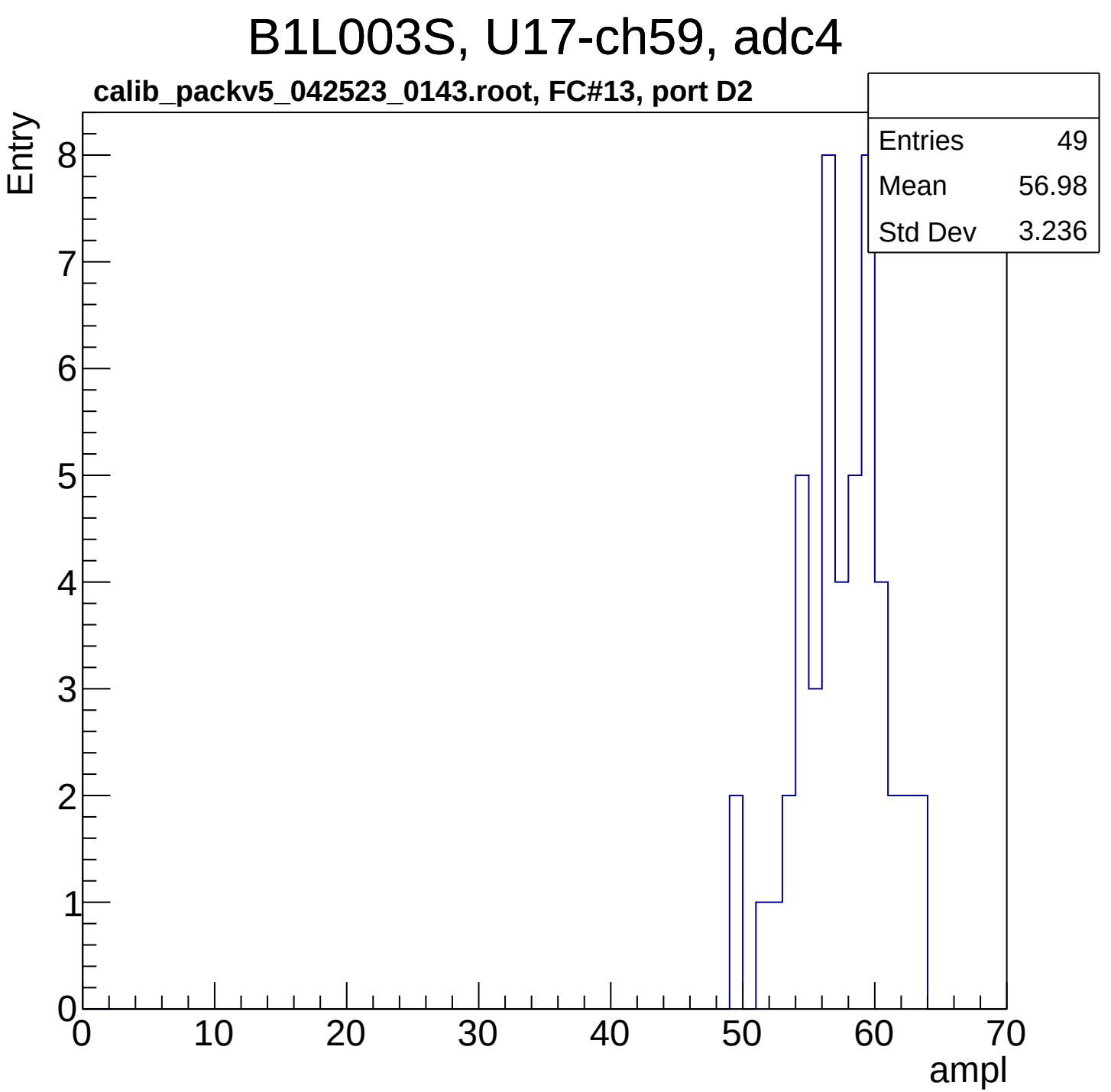

B1L003S, U17-ch59, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 44.54
Std Dev 3.625
Gaus mean : 44.7875
Gaus Width: 3.5078
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch59, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 51
Std Dev 3.773
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch59, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 56.98
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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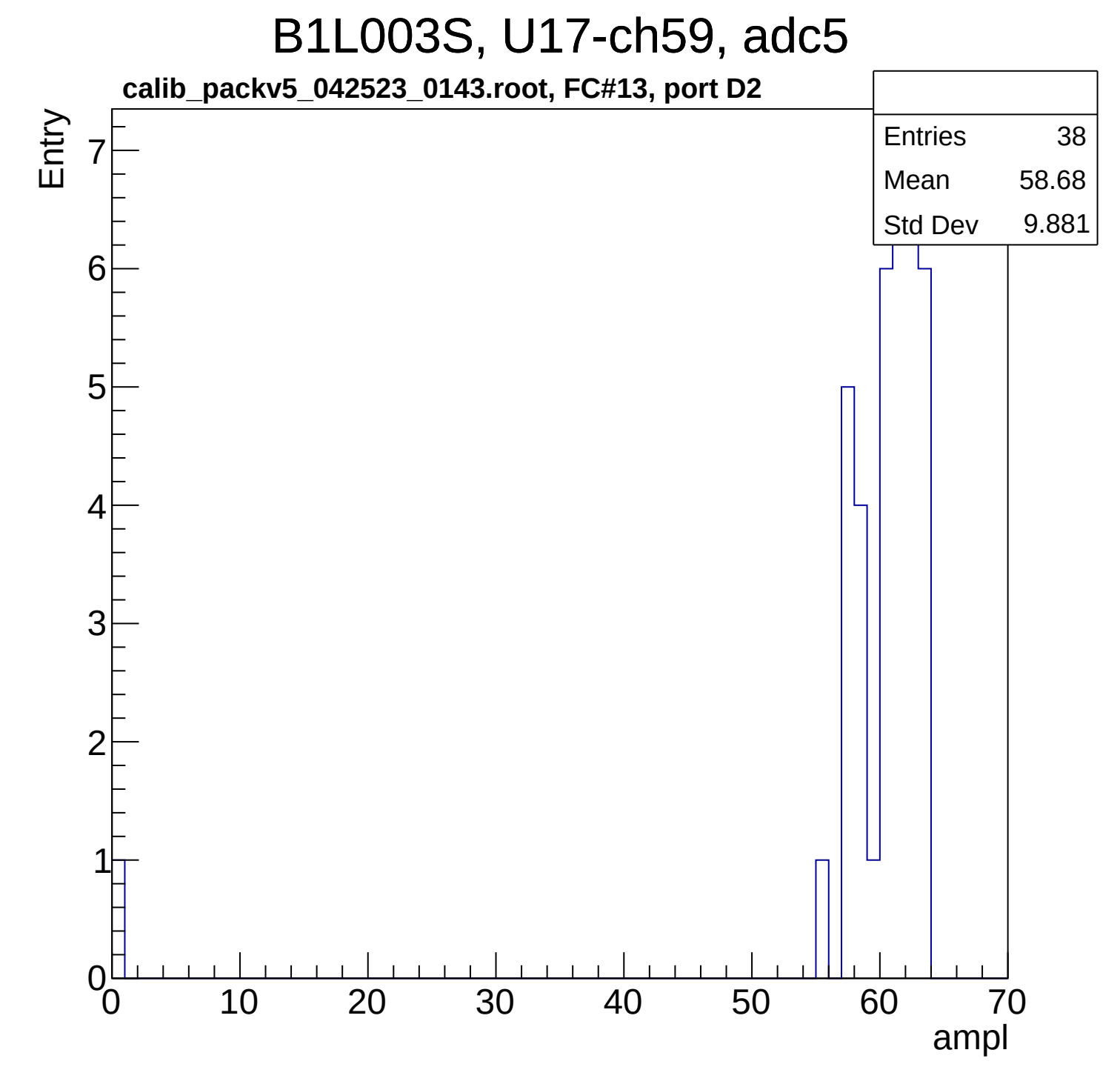
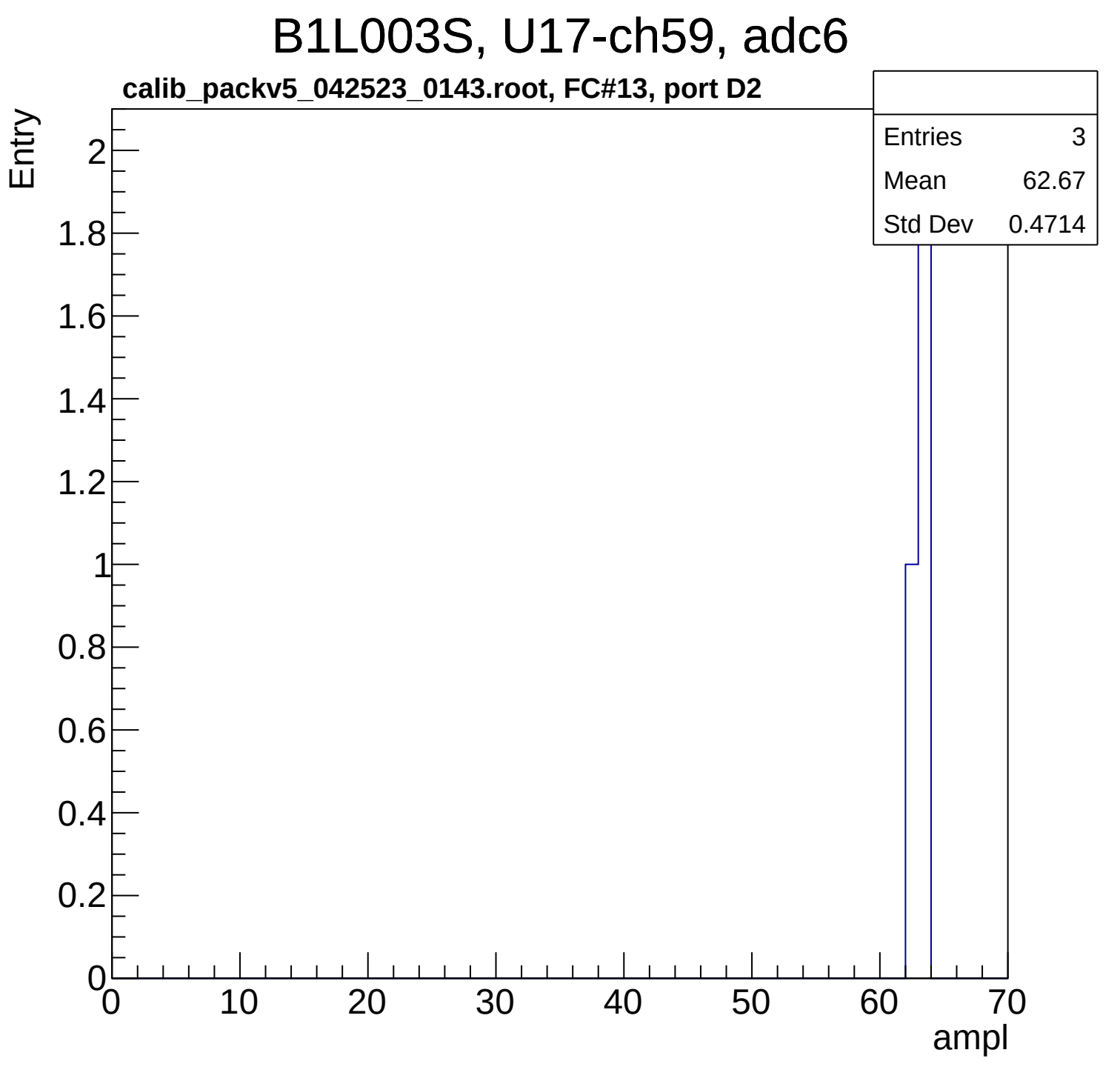
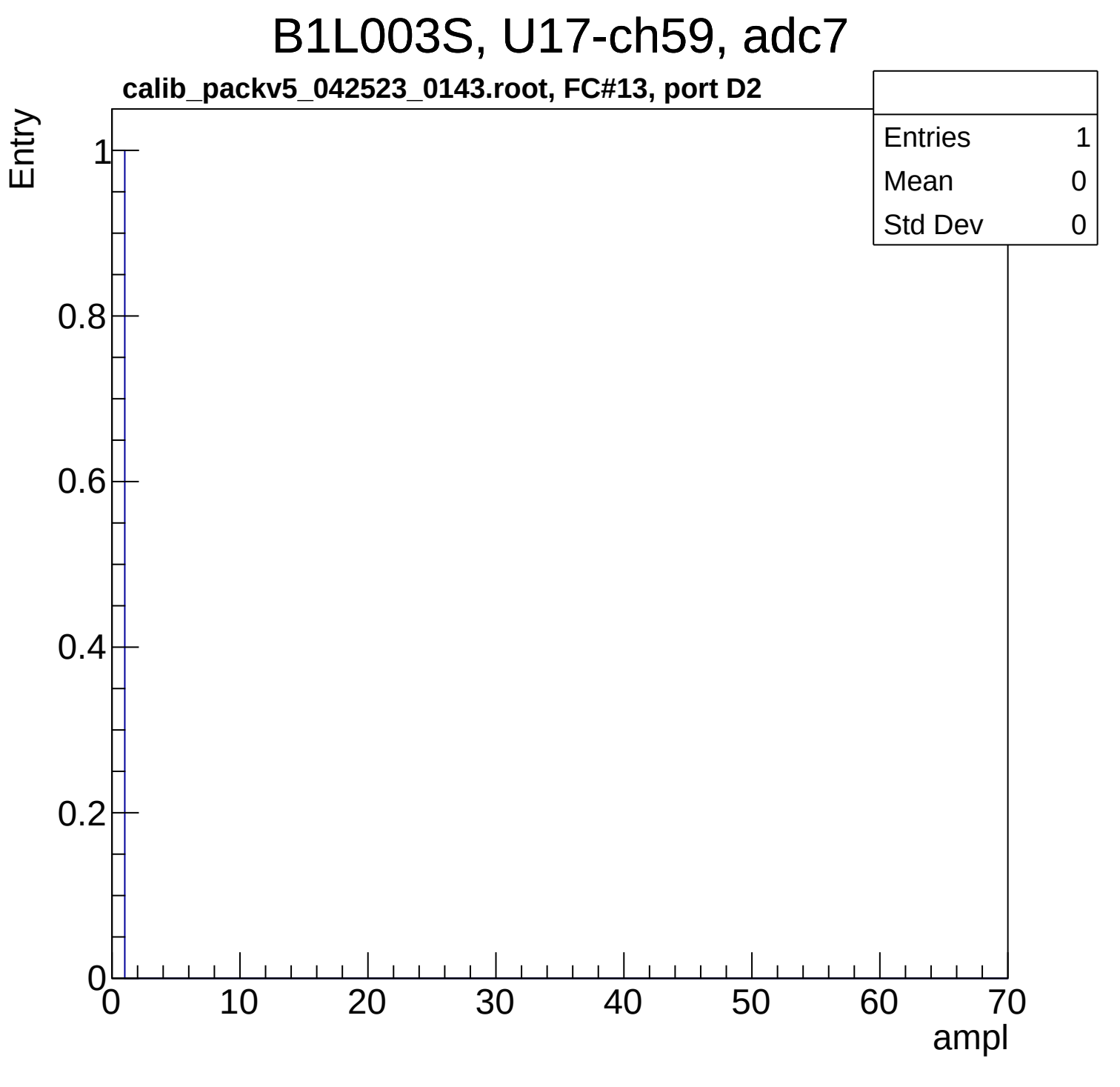
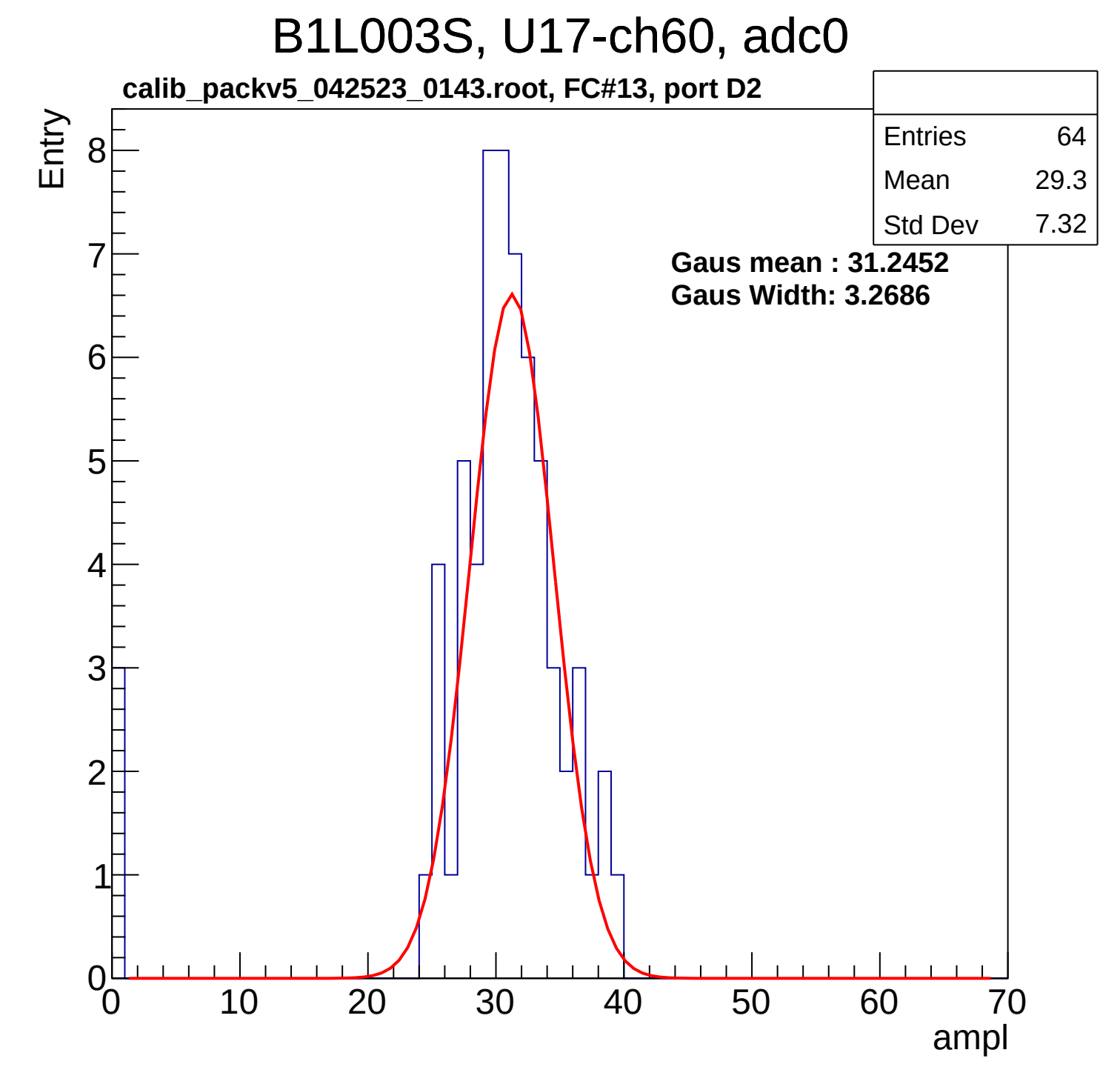
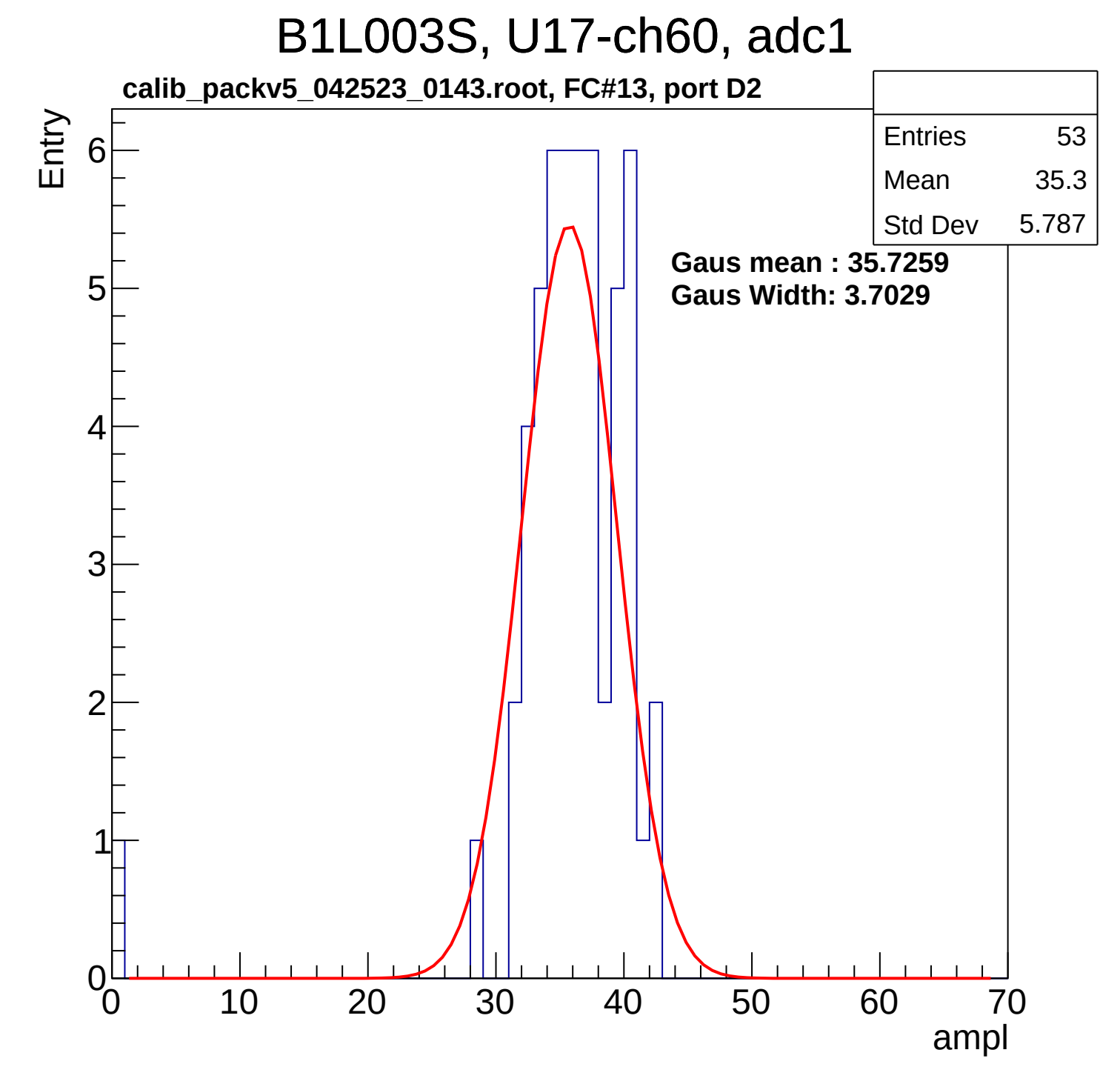
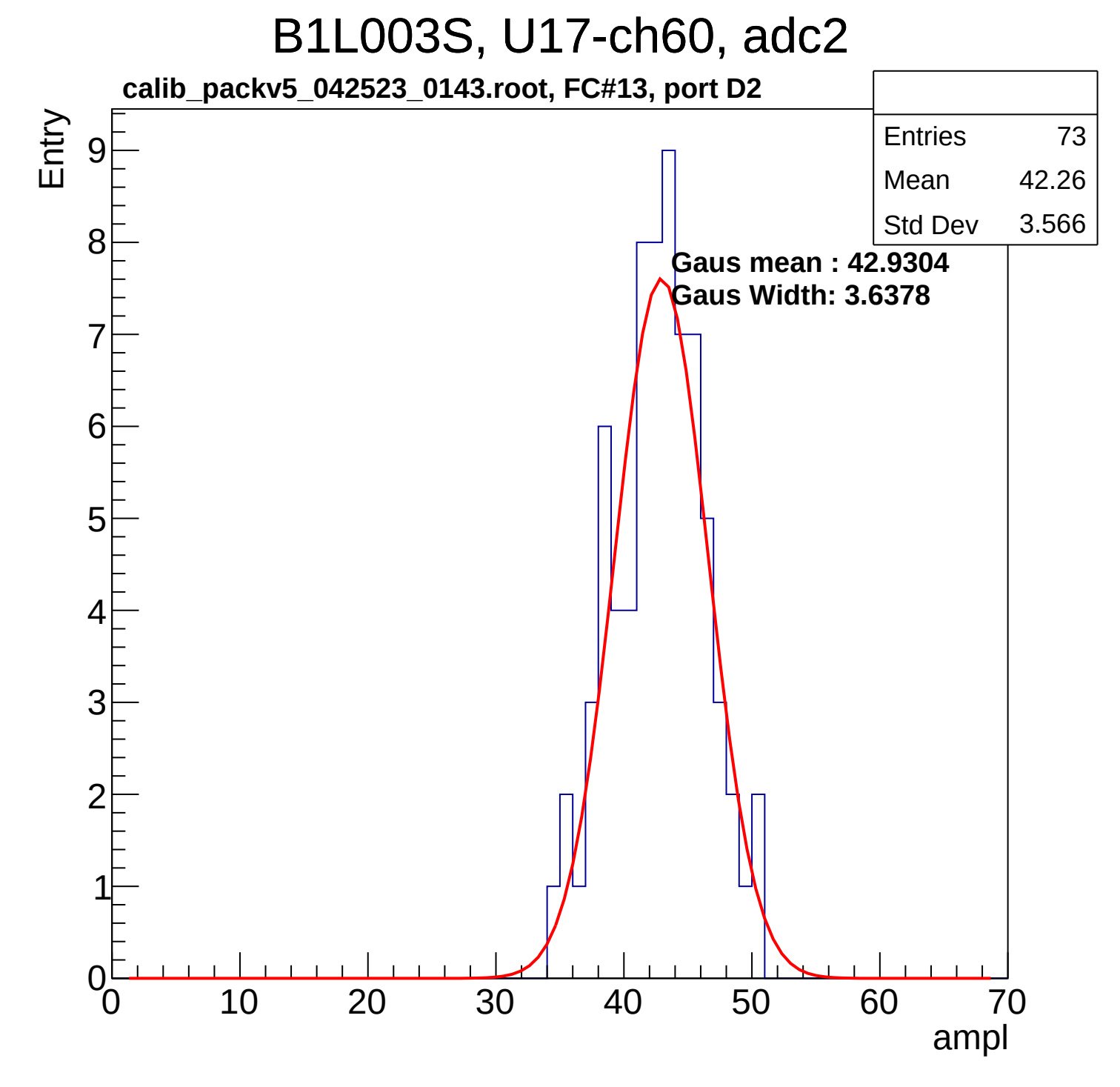

B1L003S, U17-ch59, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 58.68
Std Dev 9.881
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

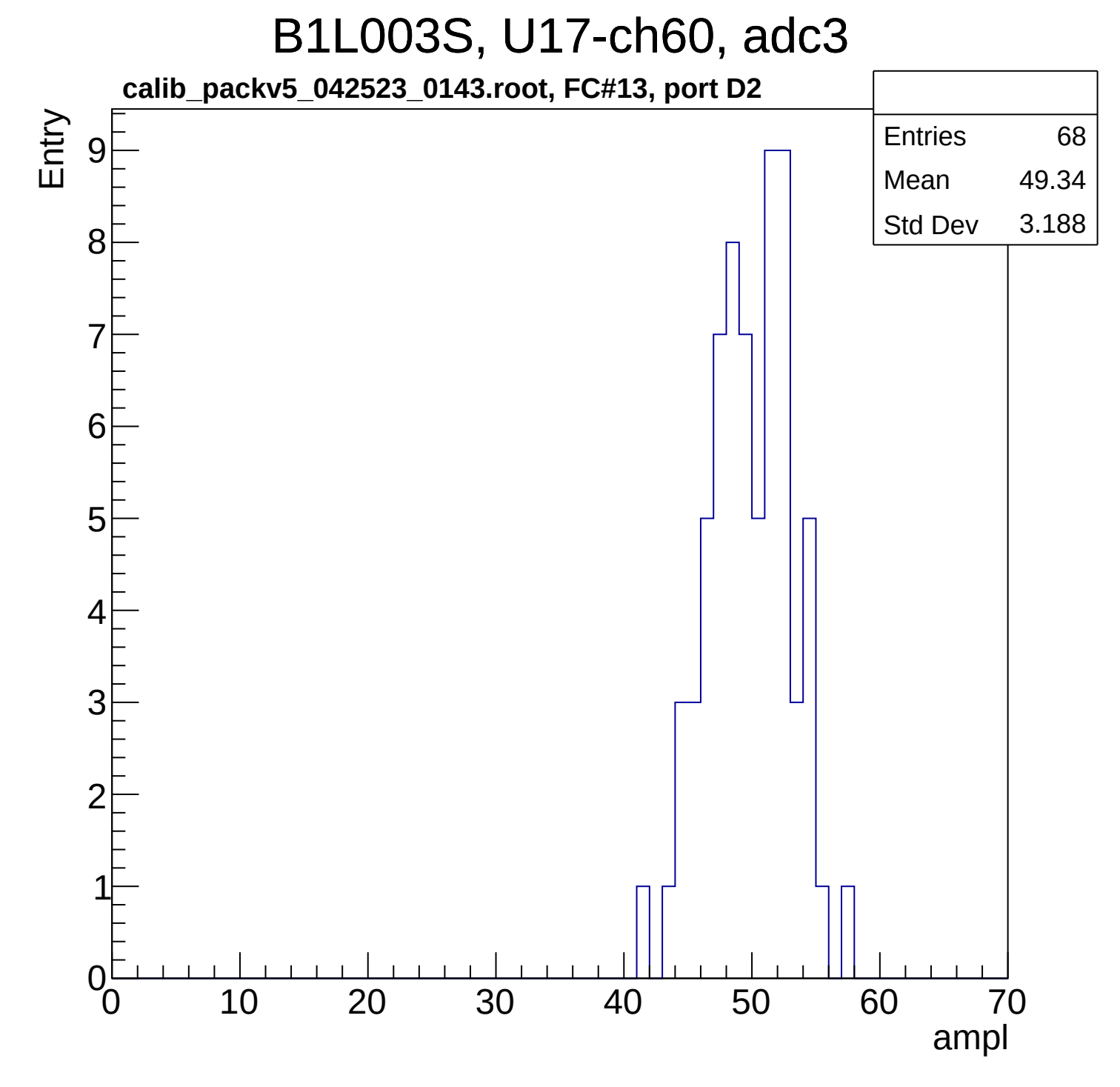

B1L003S, U17-ch59, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

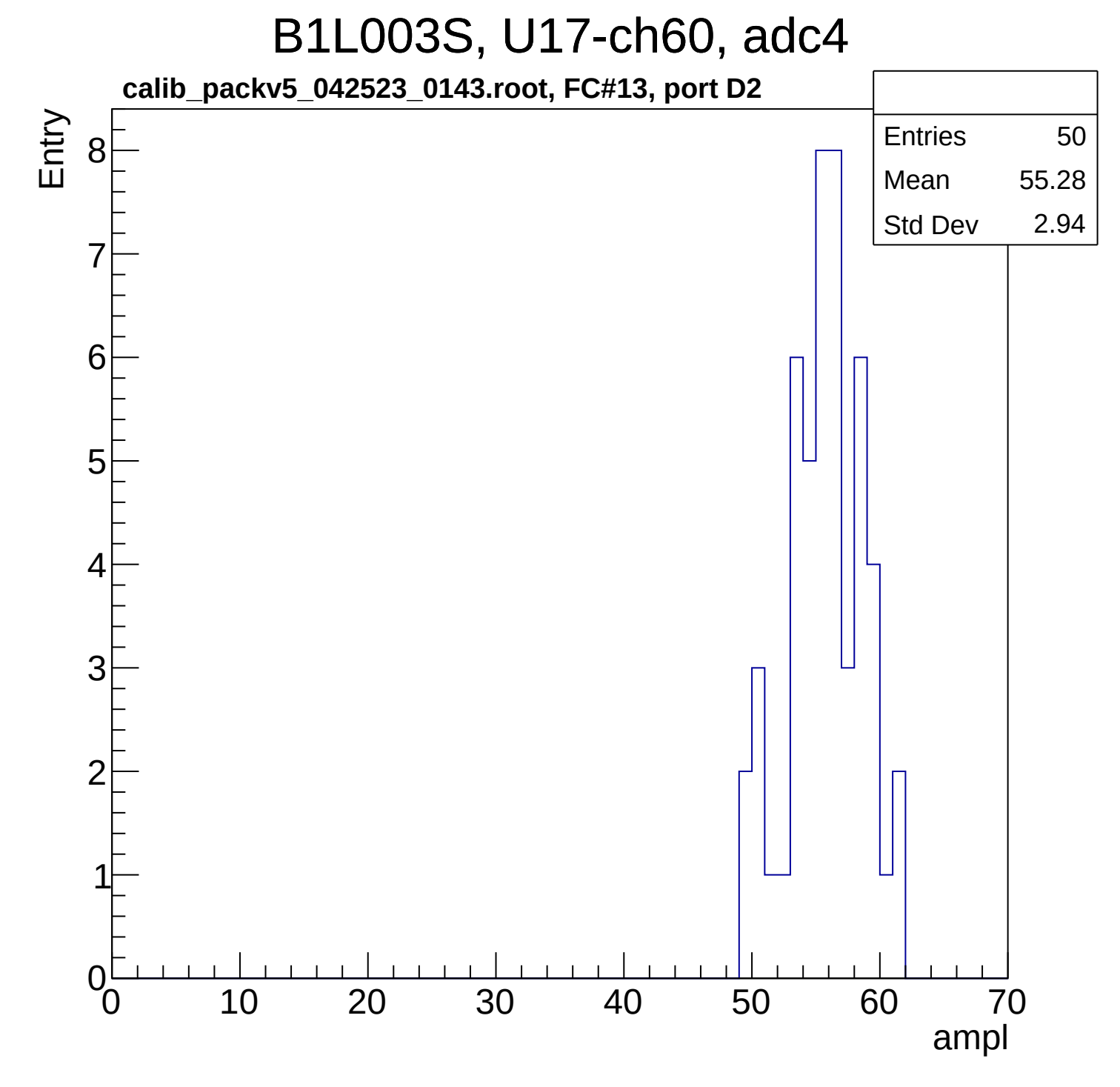

B1L003S, U17-ch59, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

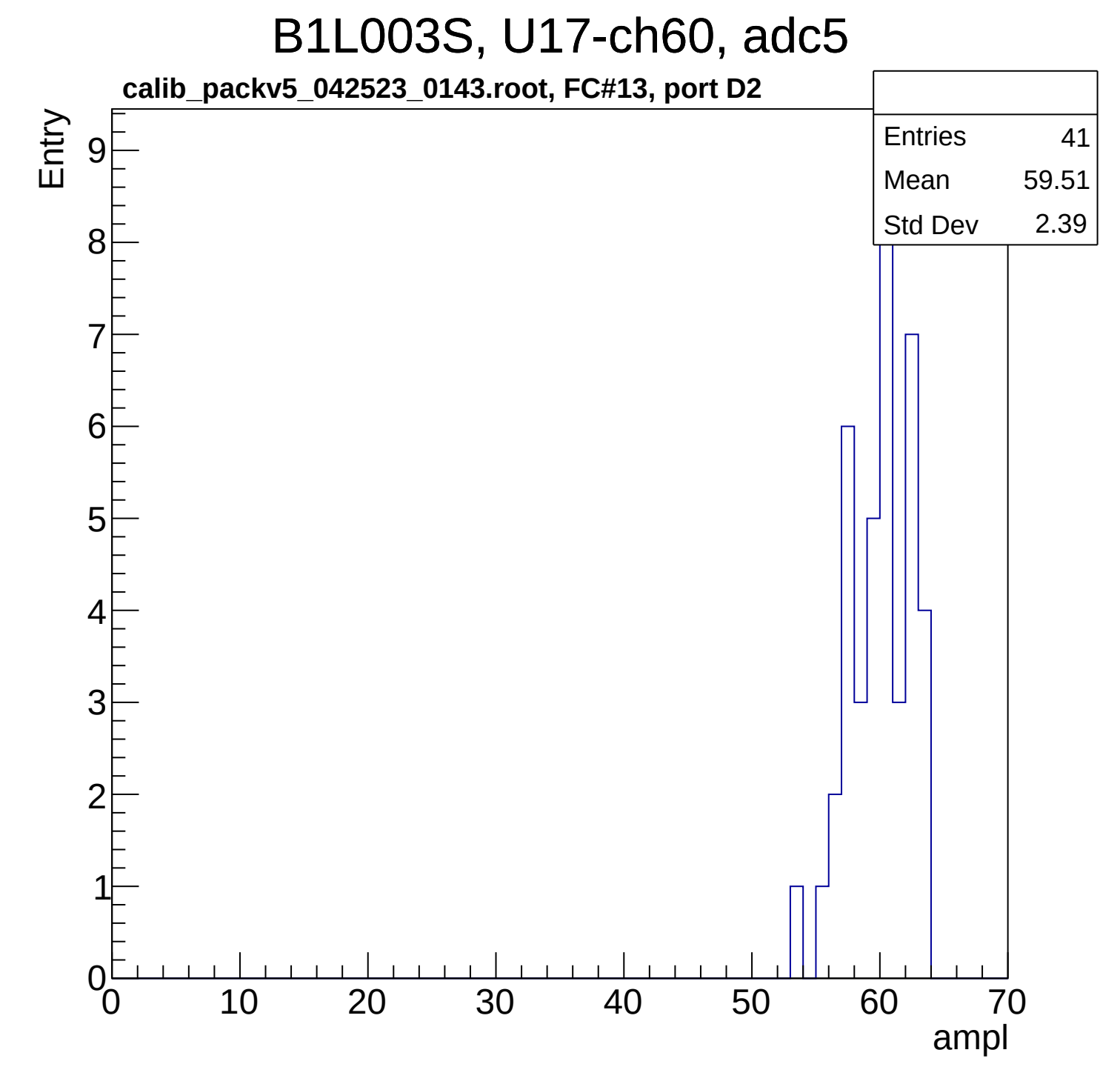

B1L003S, U17-ch60, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.3
Std Dev 7.32
Gaus mean : 31.2452
Gaus Width: 3.2686
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch60, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 35.3
Std Dev 5.787
Gaus mean : 35.7259
Gaus Width: 3.7029
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch60, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 42.26
Std Dev 3.566
Gaus mean : 42.9304
Gaus Width: 3.6378
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch60, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.34
Std Dev 3.188
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch60, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 55.28
Std Dev 2.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch60, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59.51
Std Dev 2.39
Entry
ampl
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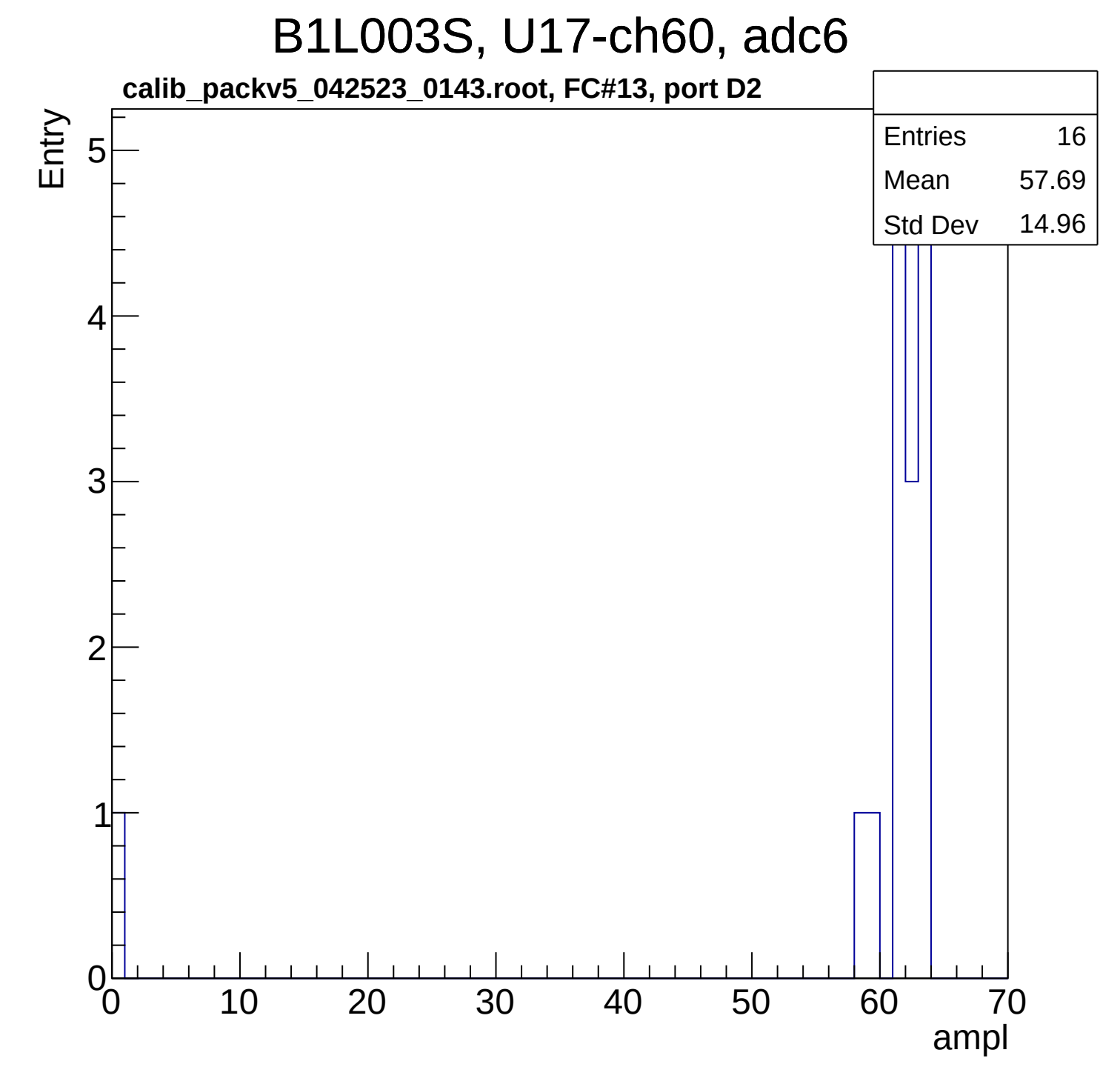
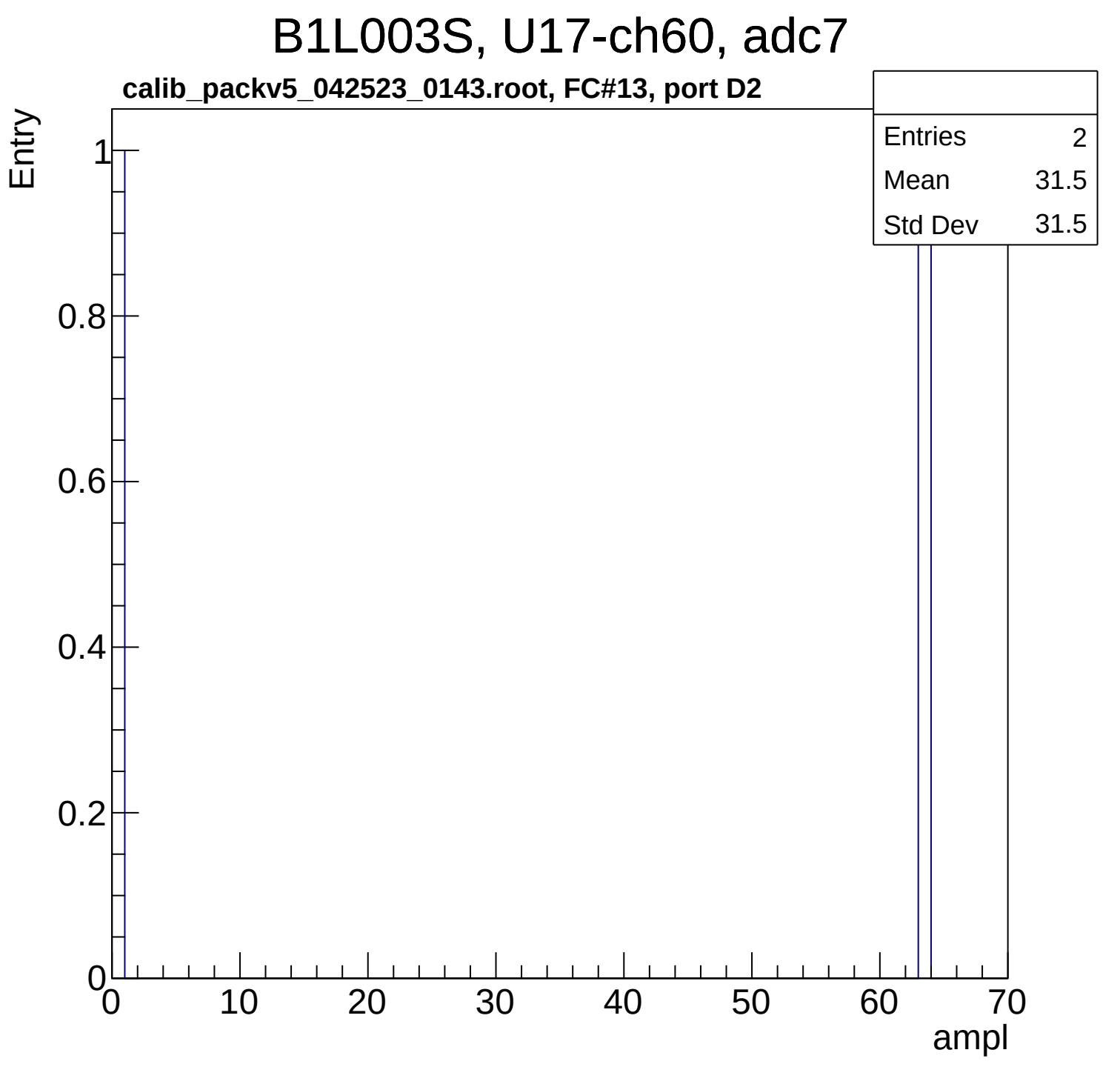
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch60, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 57.69
Std Dev 14.96
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch60, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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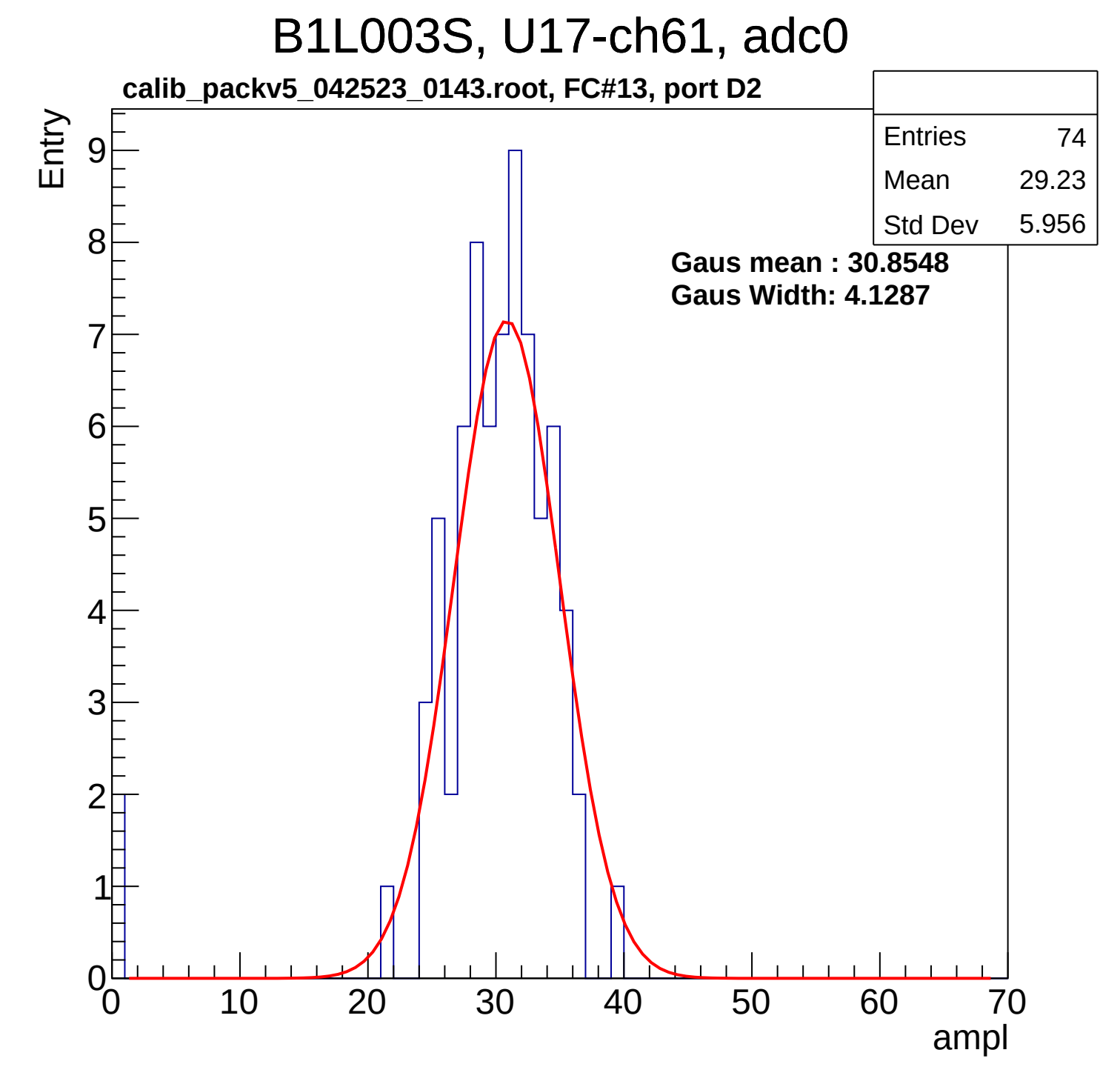
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch61, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.23
Std Dev 5.956
Gaus mean : 30.8548
Gaus Width: 4.1287
Entry
ampl
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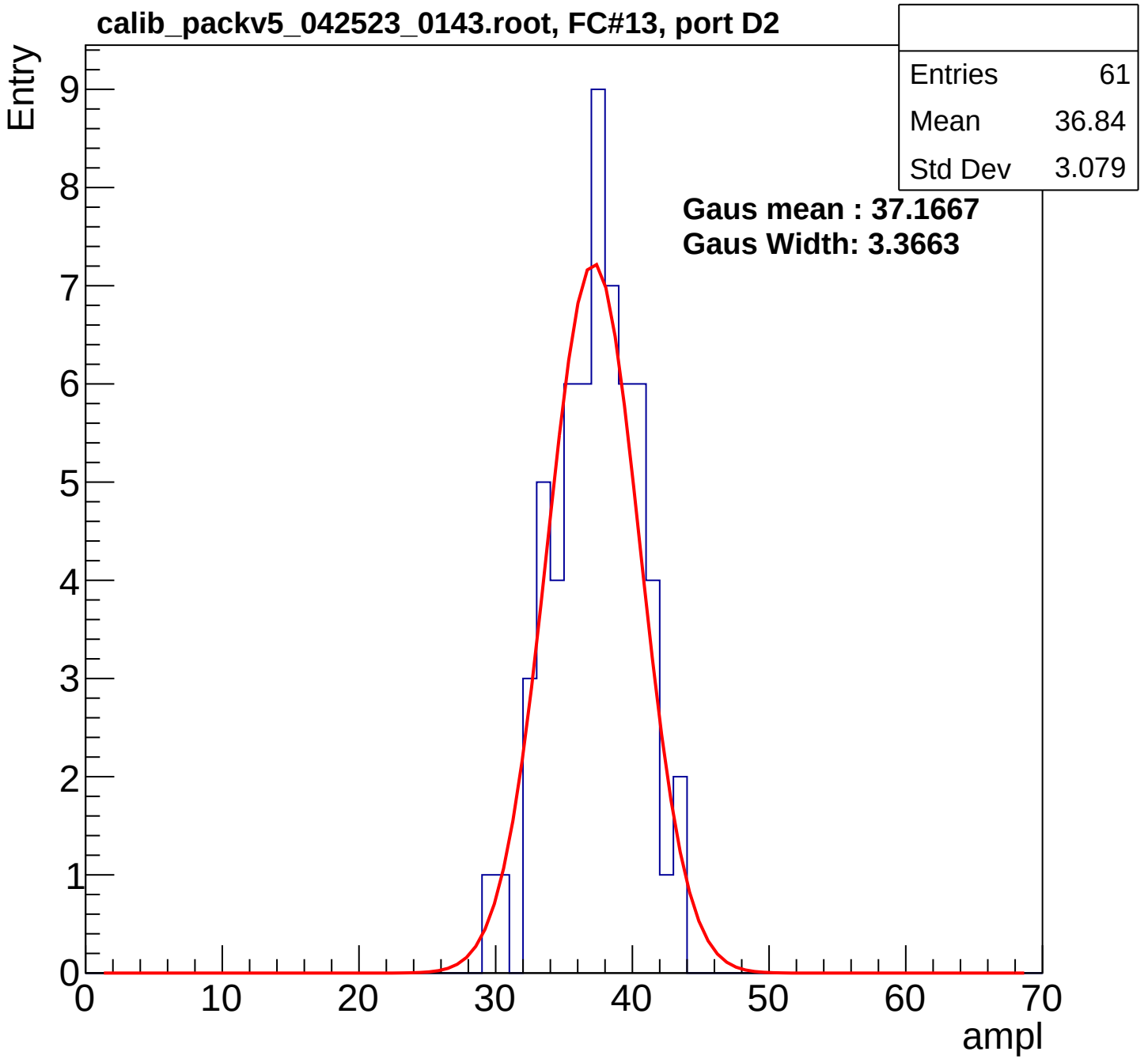
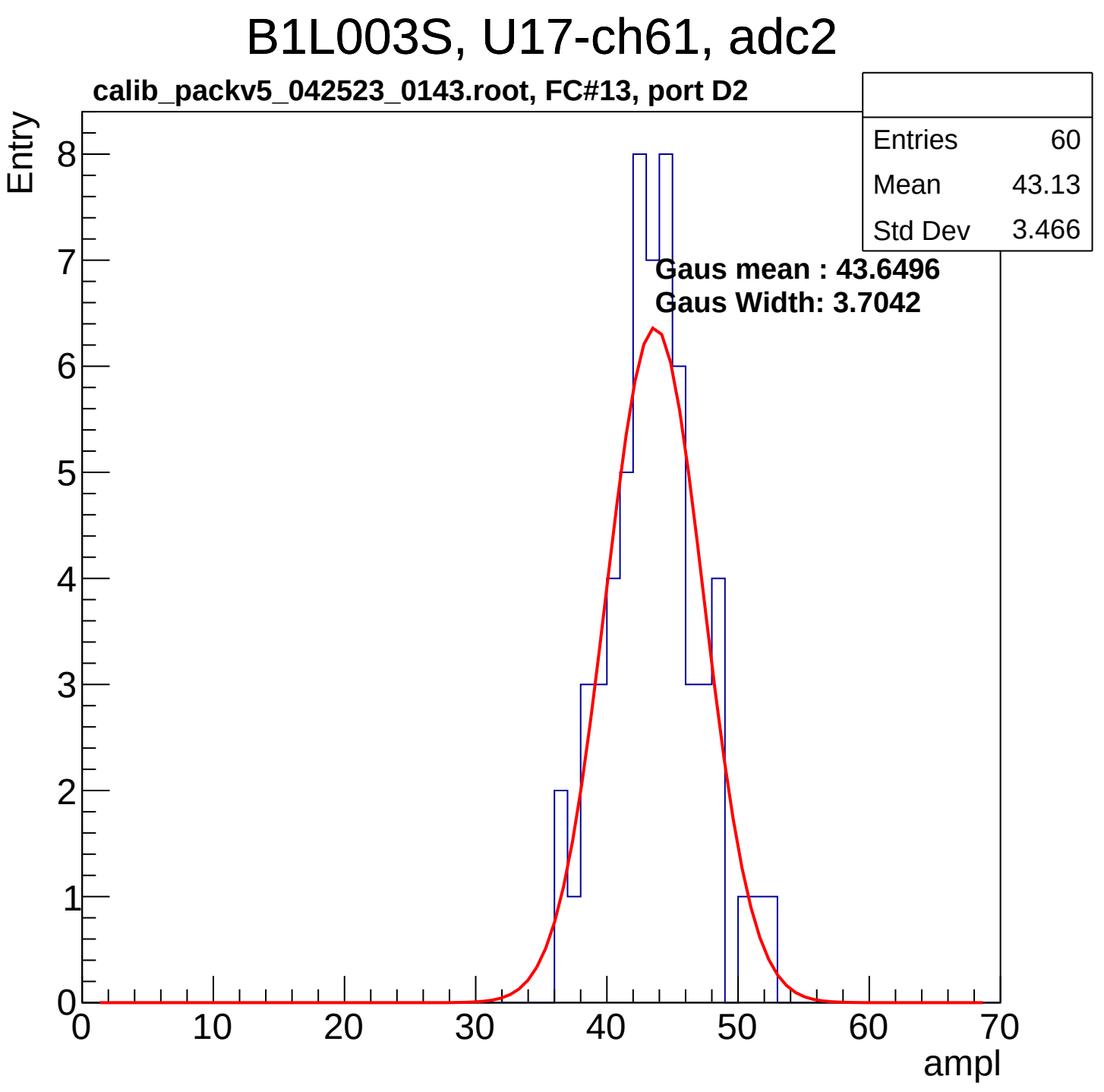
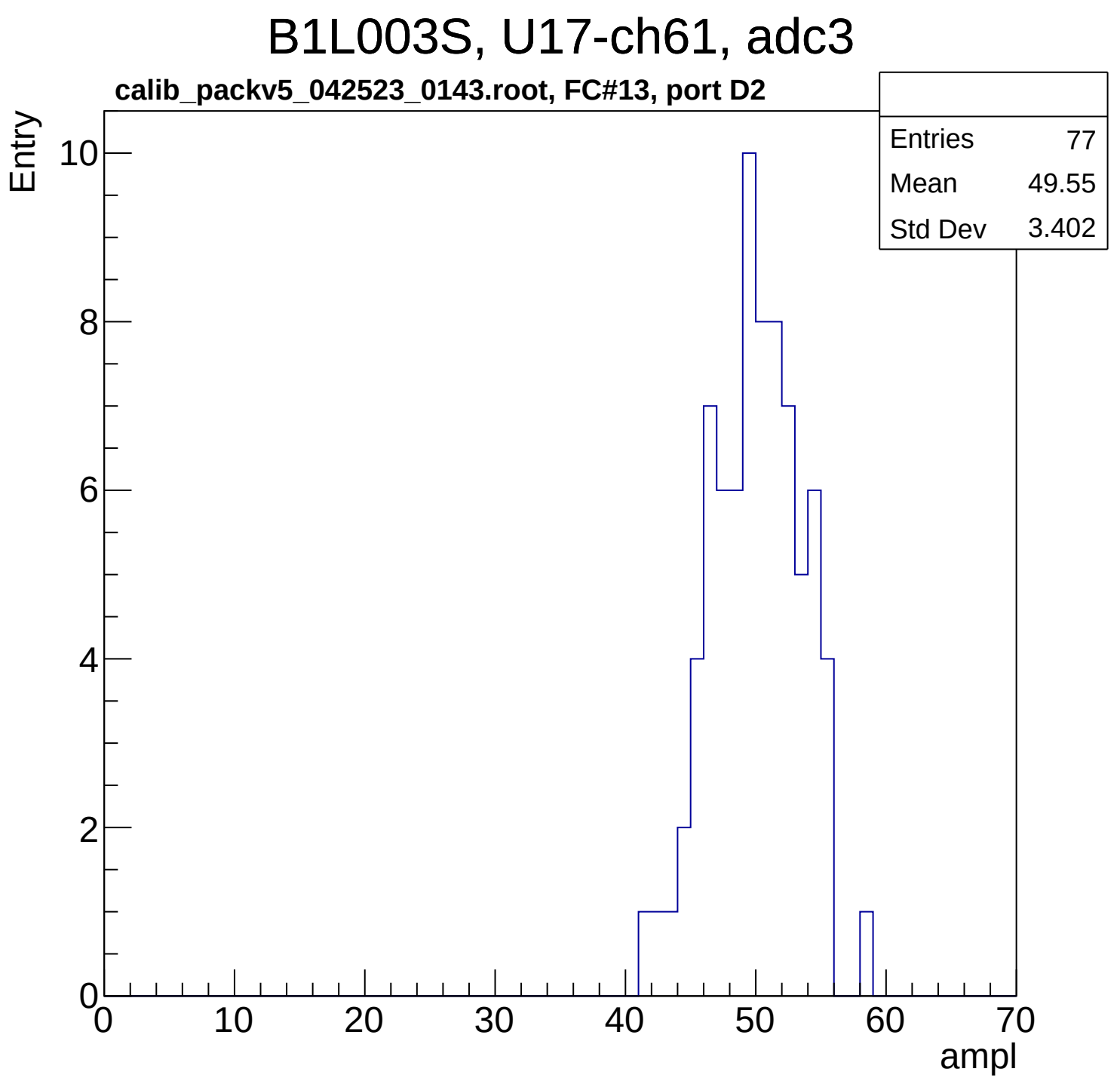
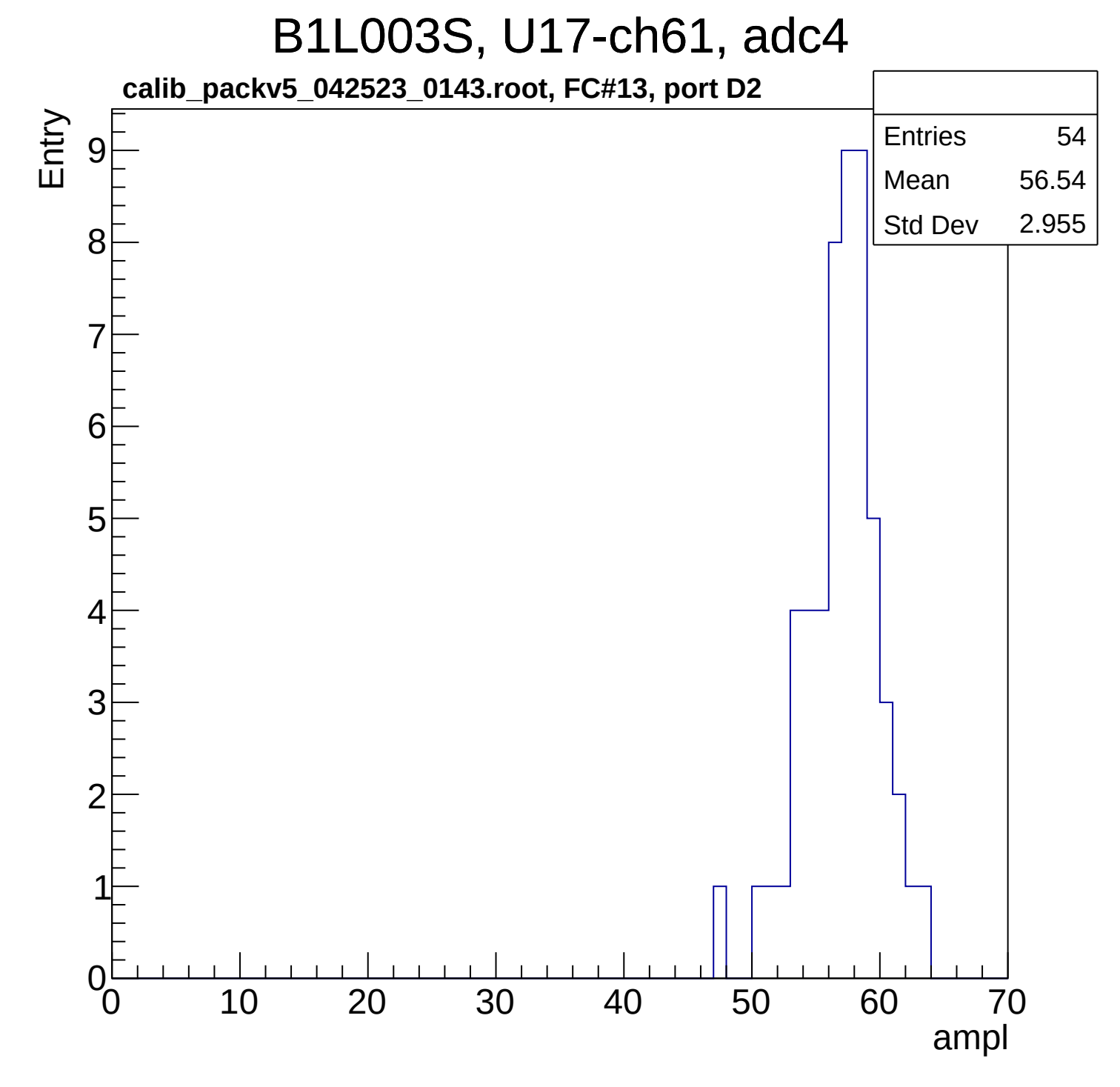
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch61, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.84
Std Dev 3.079
Gaus mean : 37.1667
Gaus Width: 3.3663
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch61, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.13
Std Dev 3.466
Gaus mean : 43.6496
Gaus Width: 3.7042
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch61, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 49.55
Std Dev 3.402
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch61, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 56.54
Std Dev 2.955
Entry
ampl
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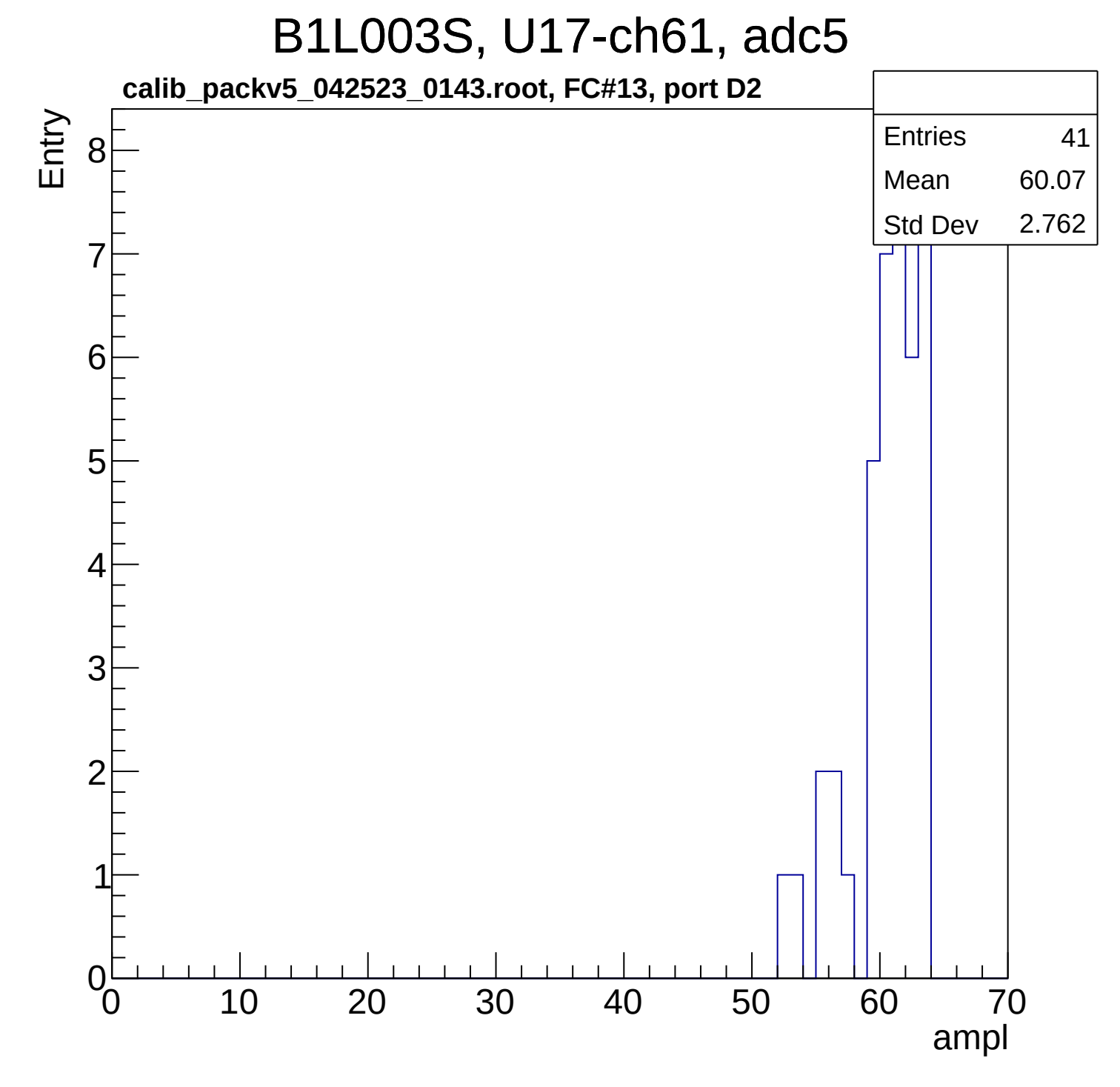
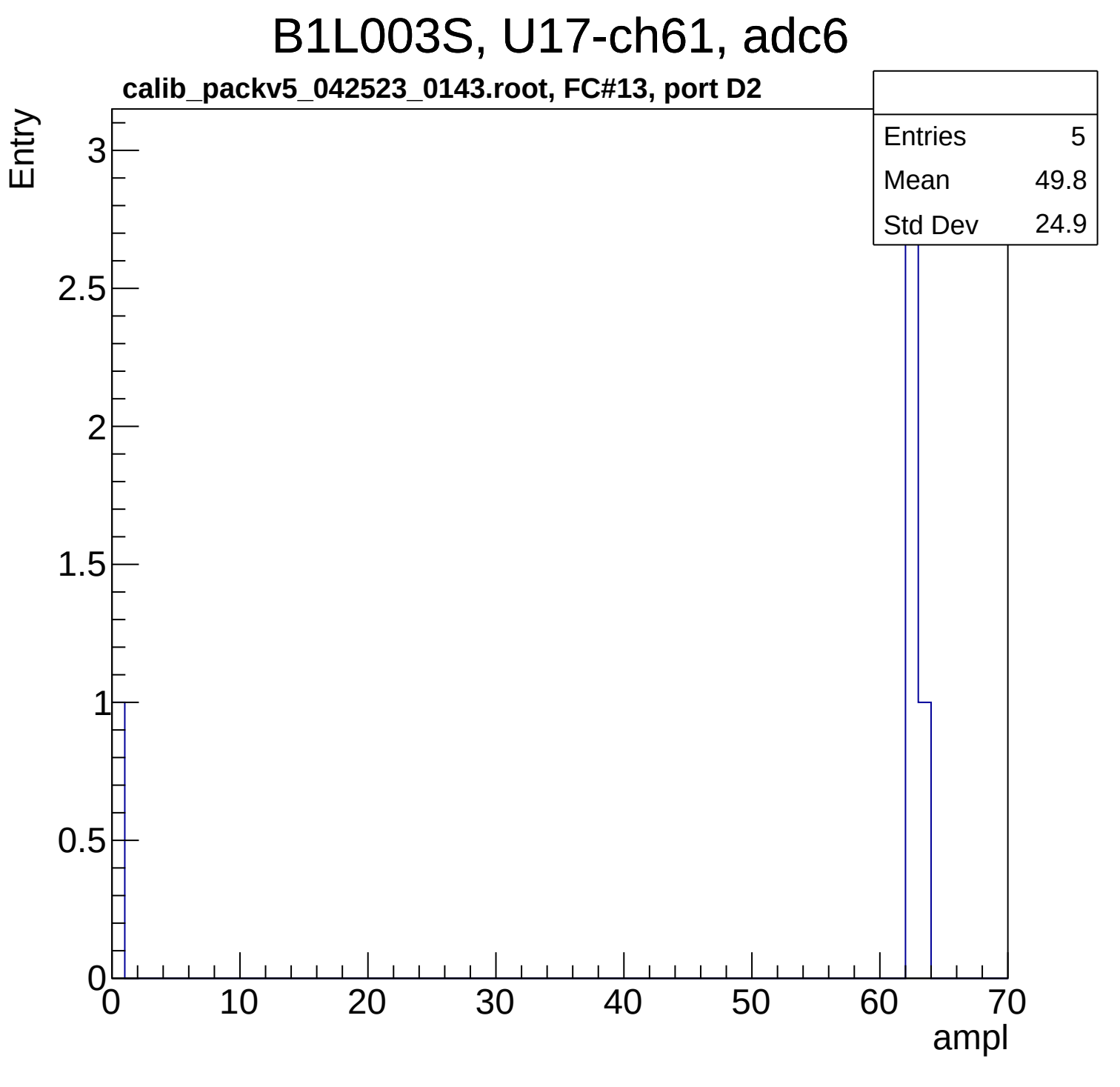
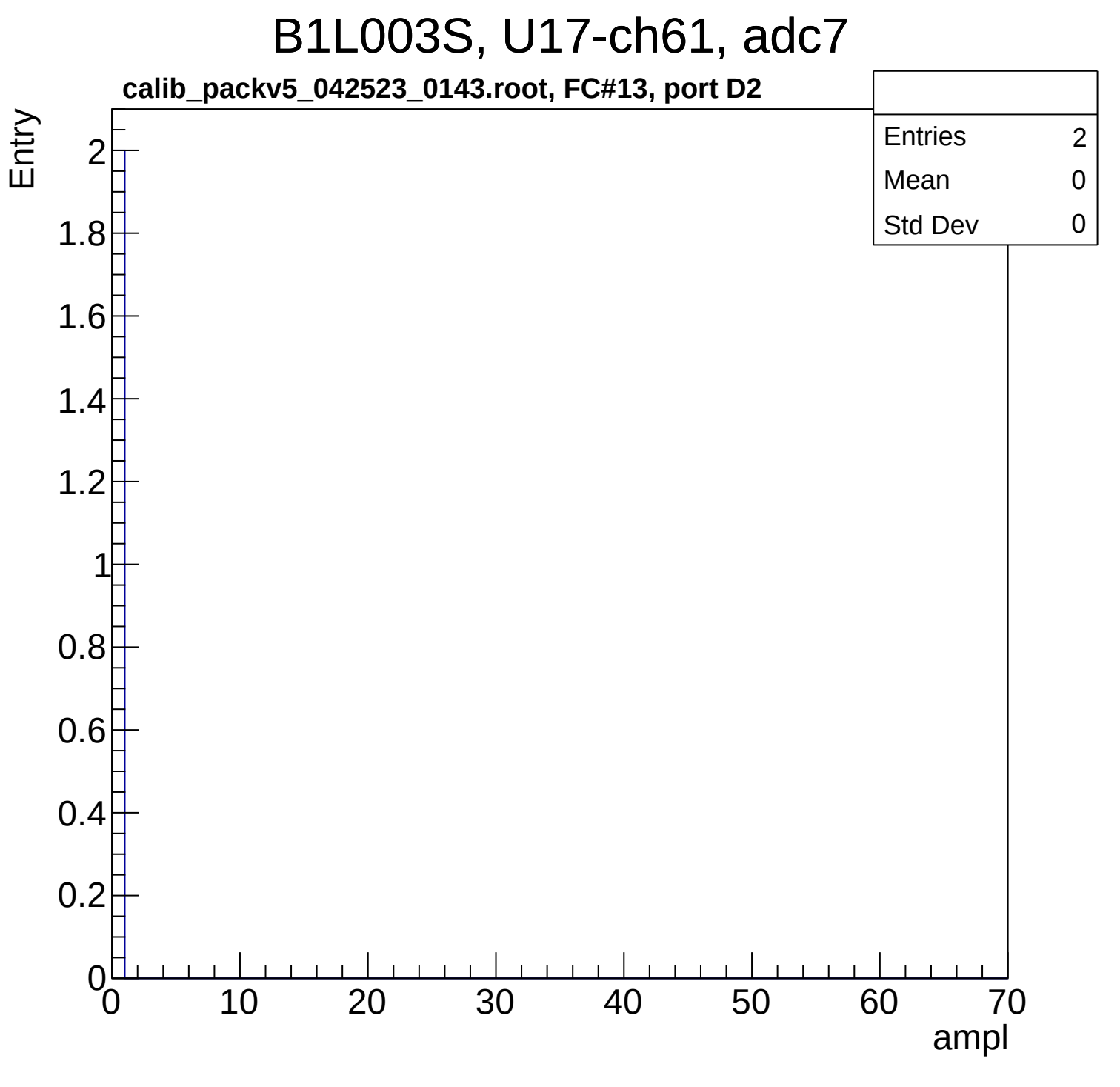
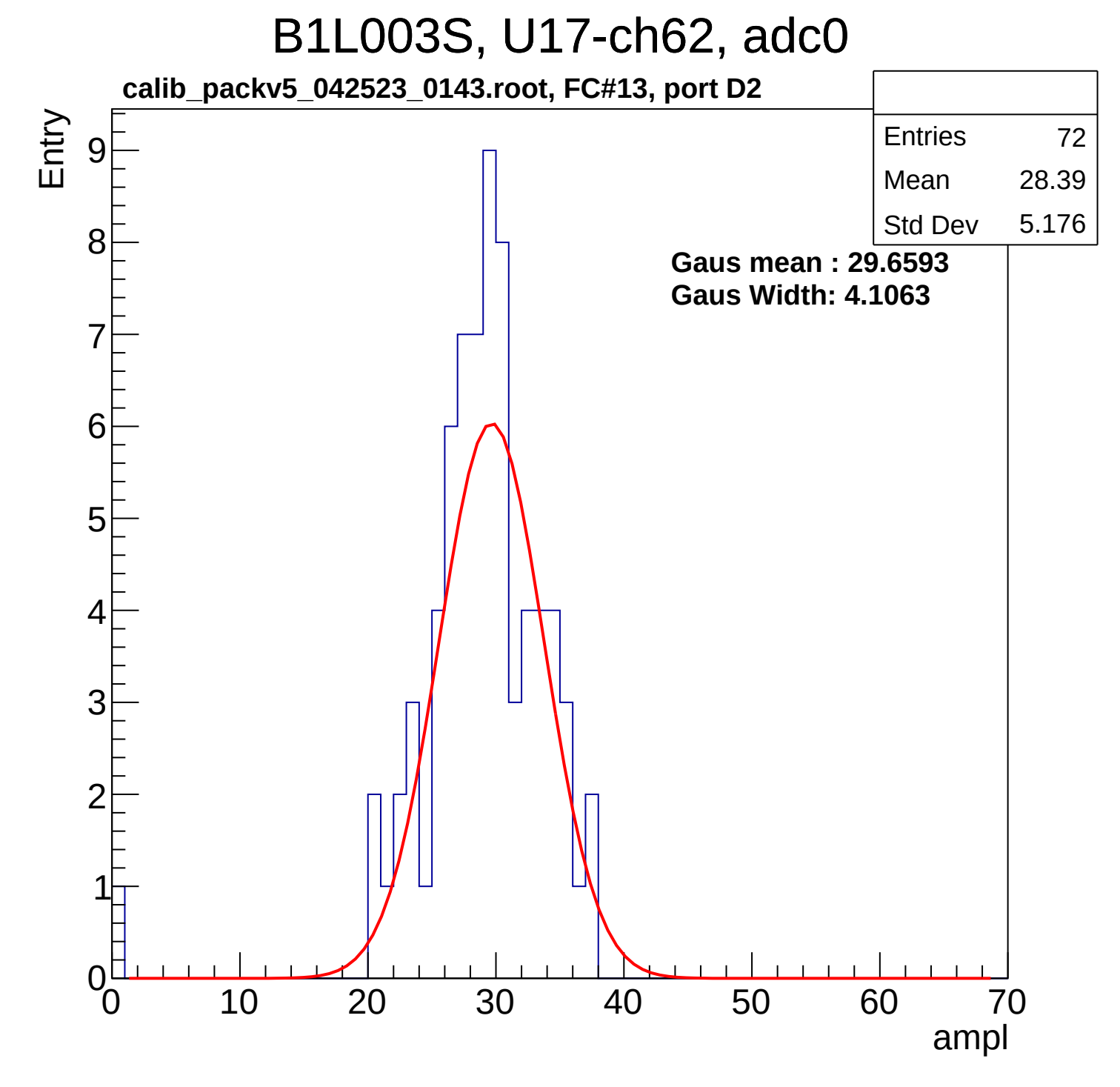
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch61, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 60.07
Std Dev 2.762
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch61, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 49.8
Std Dev 24.9
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch61, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch62, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 28.39
Std Dev 5.176
Gaus mean : 29.6593
Gaus Width: 4.1063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
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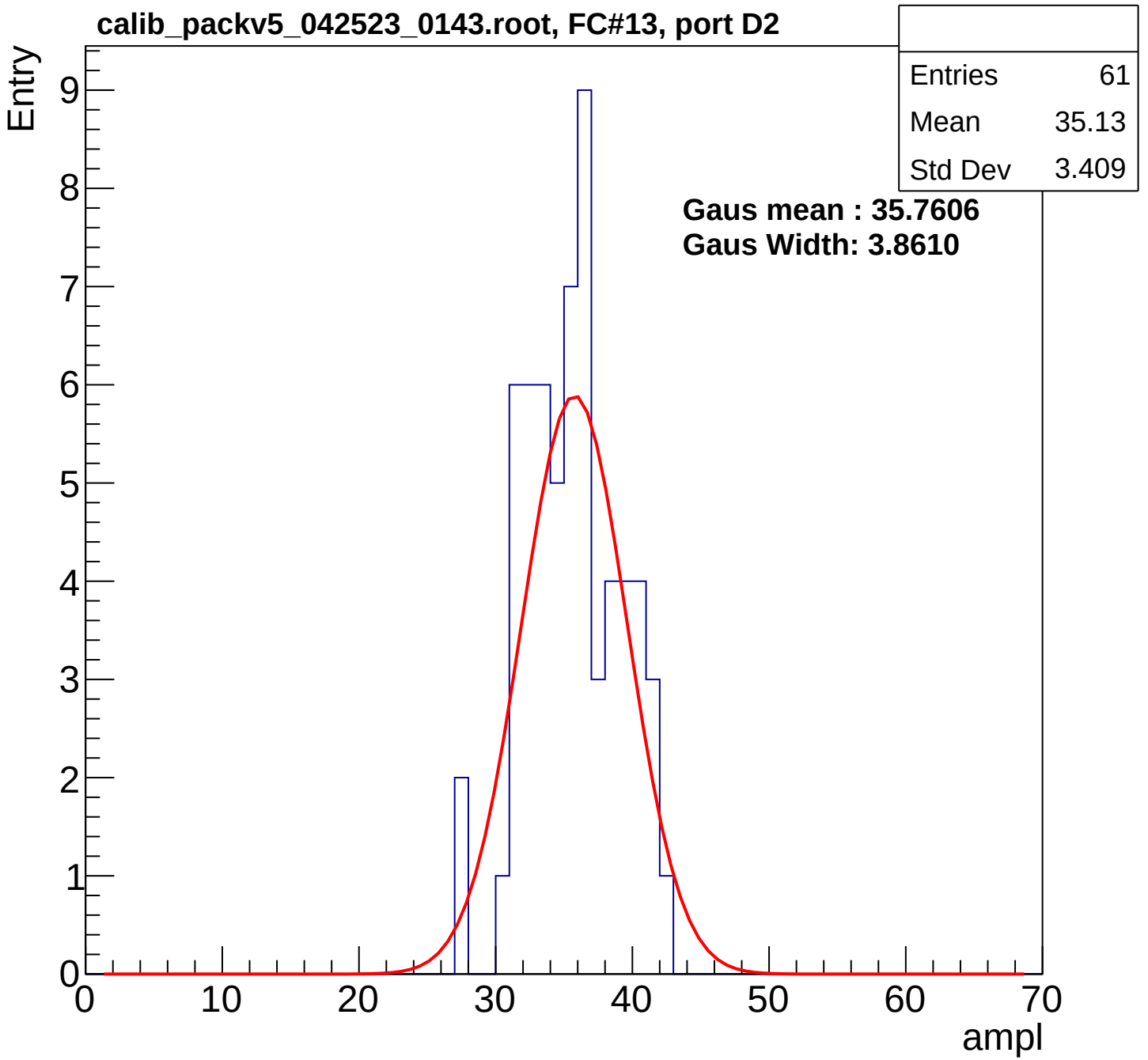
B1L003S, U17-ch62, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.13
Std Dev 3.409
Gaus mean : 35.7606
Gaus Width: 3.8610
Entry
ampl
0
10
20
30
40
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1
2
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6
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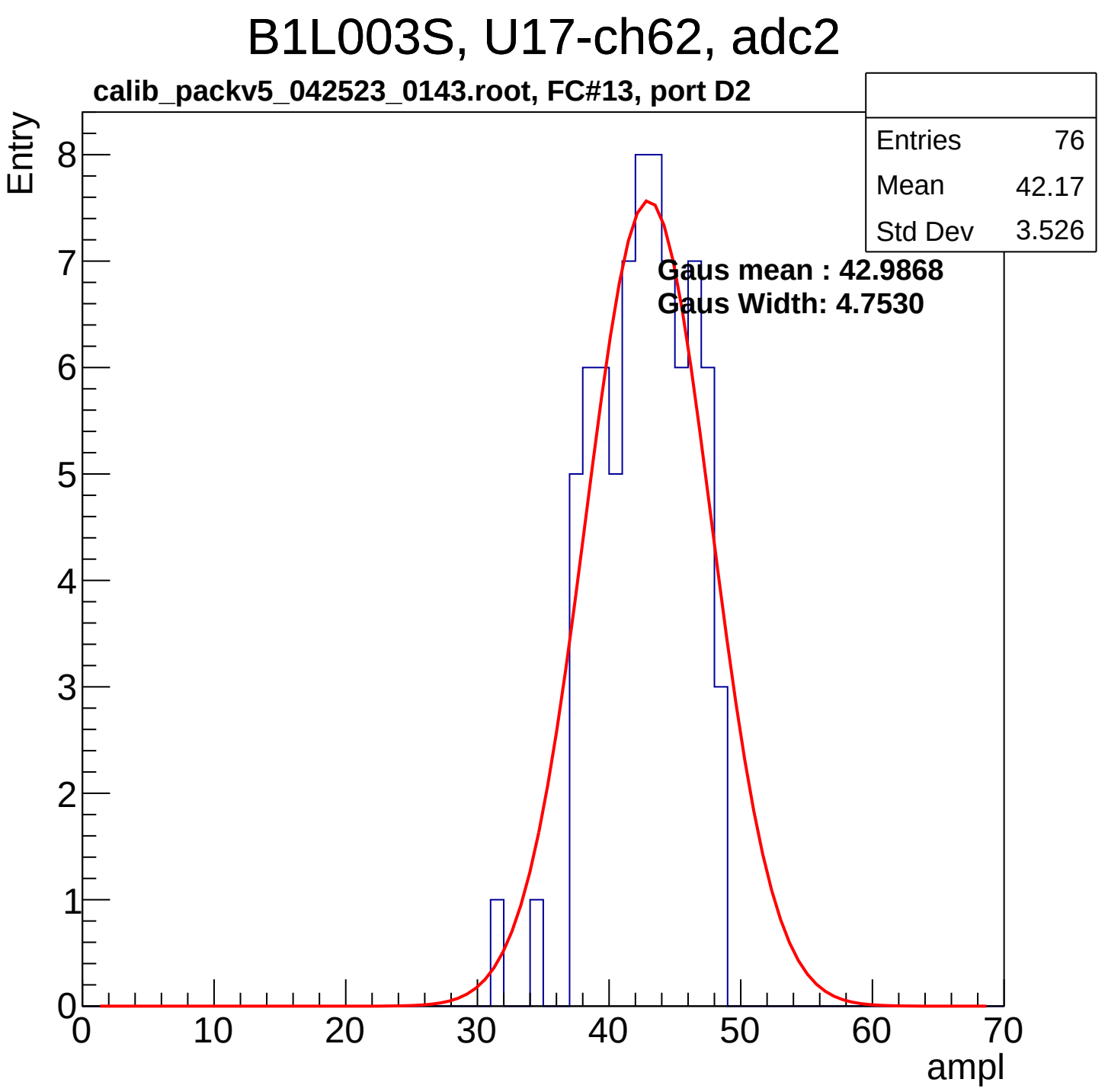
8
9


B1L003S, U17-ch62, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 42.17
Std Dev 3.526
Gaus mean : 42.9868
Gaus Width: 4.7530
Entry
ampl
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1
2
3
4
5
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7
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0
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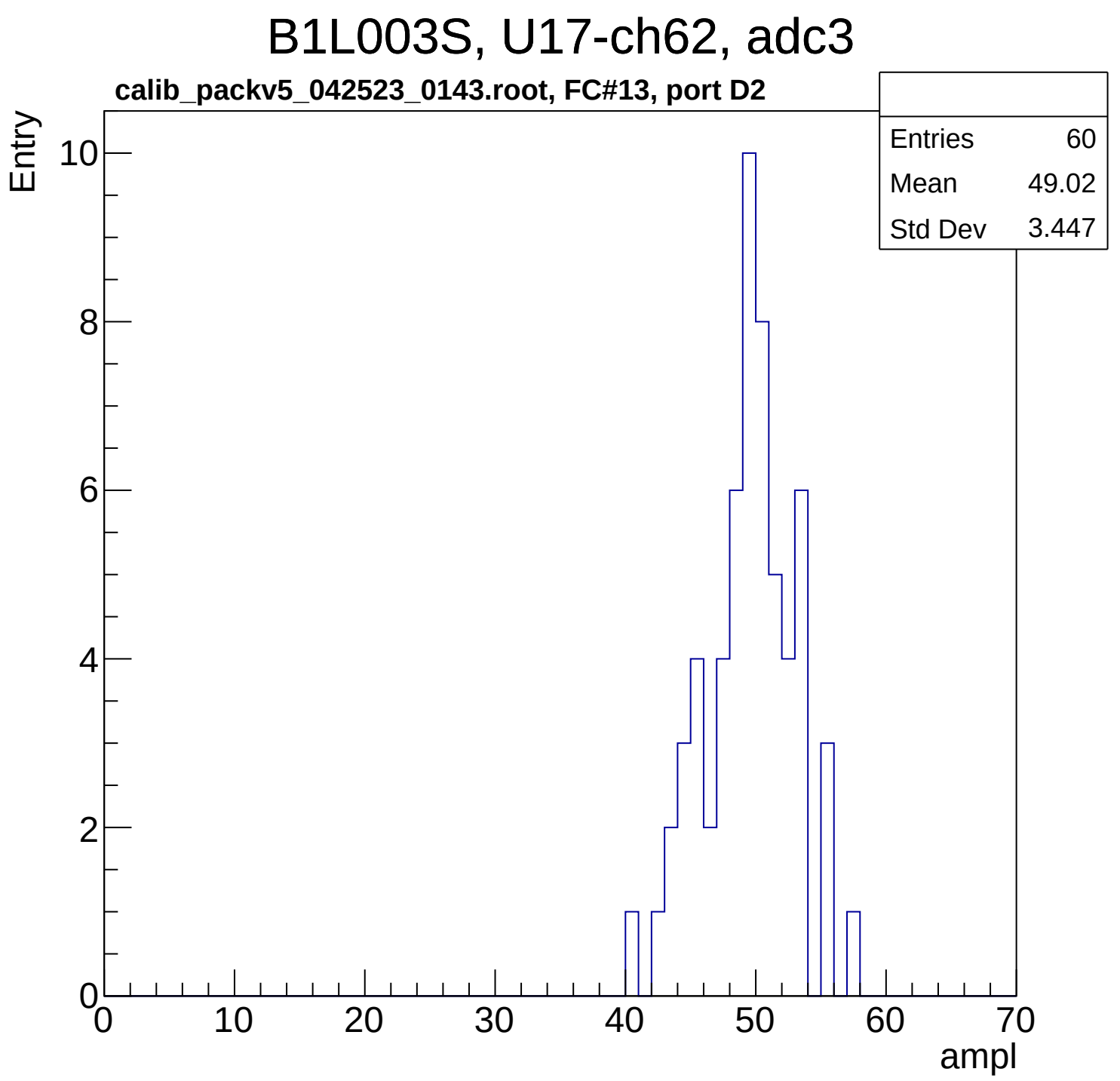
B1L003S, U17-ch62, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.02
Std Dev 3.447
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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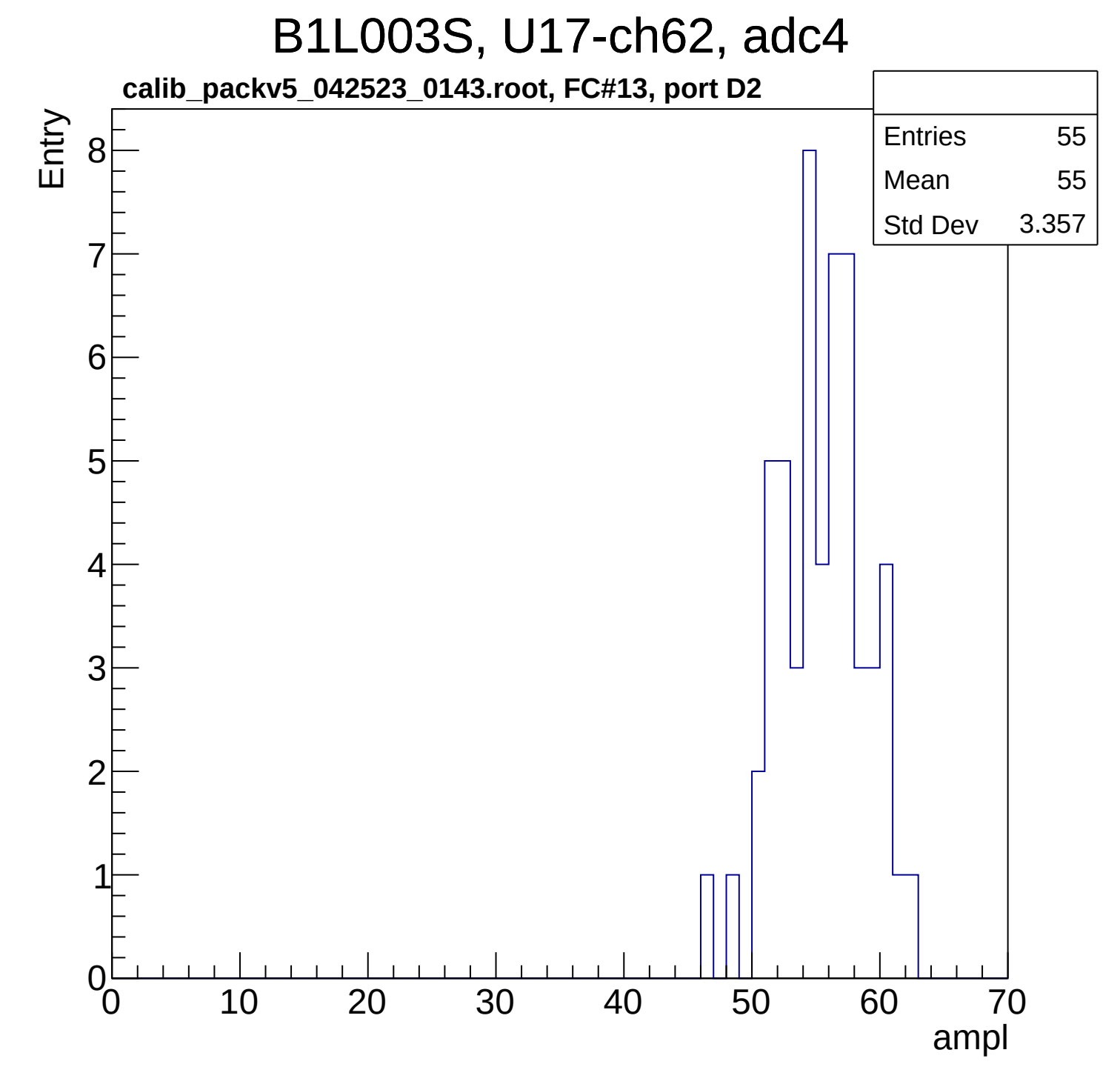
B1L003S, U17-ch62, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55
Std Dev 3.357
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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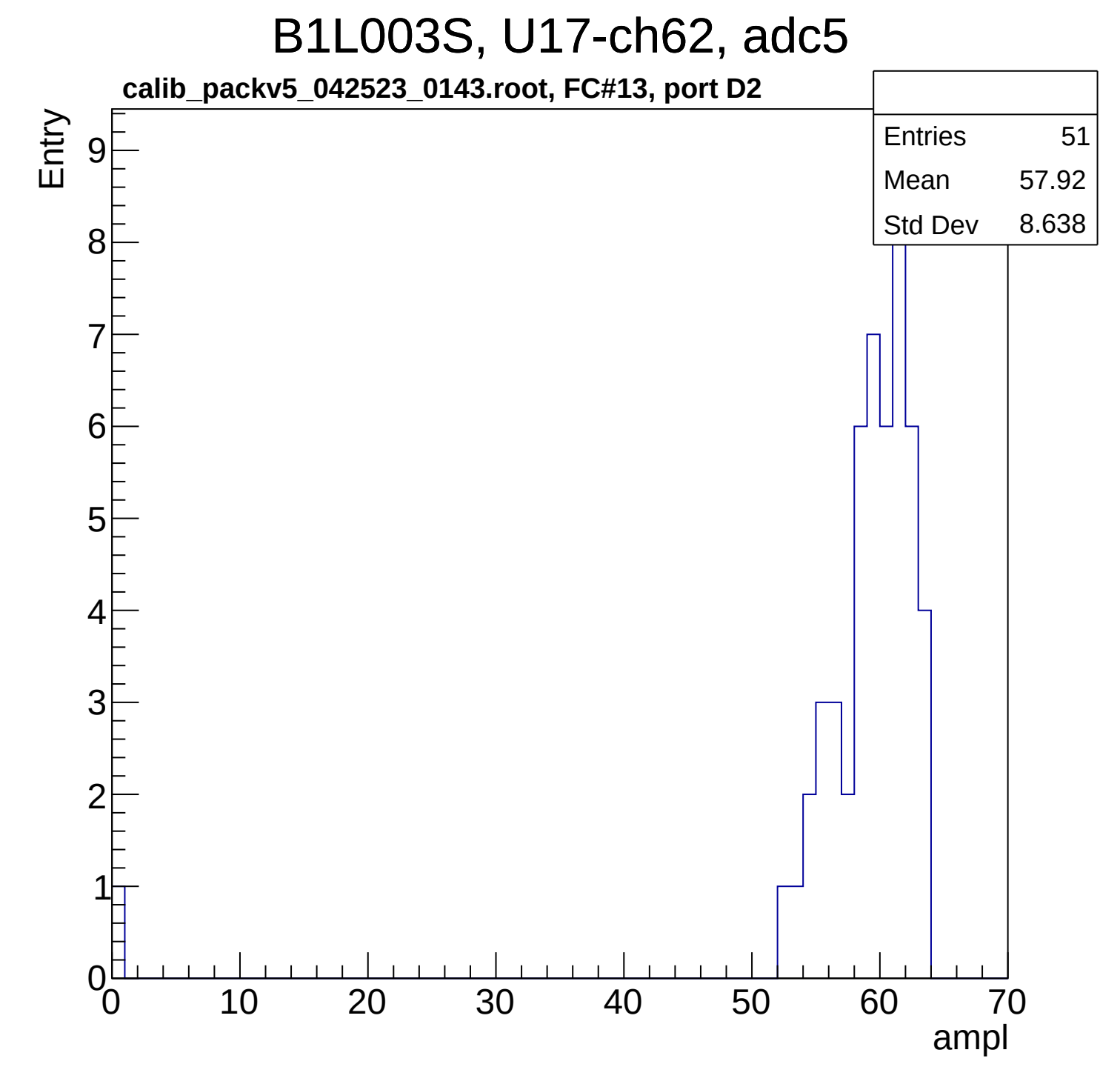
B1L003S, U17-ch62, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 57.92
Std Dev 8.638
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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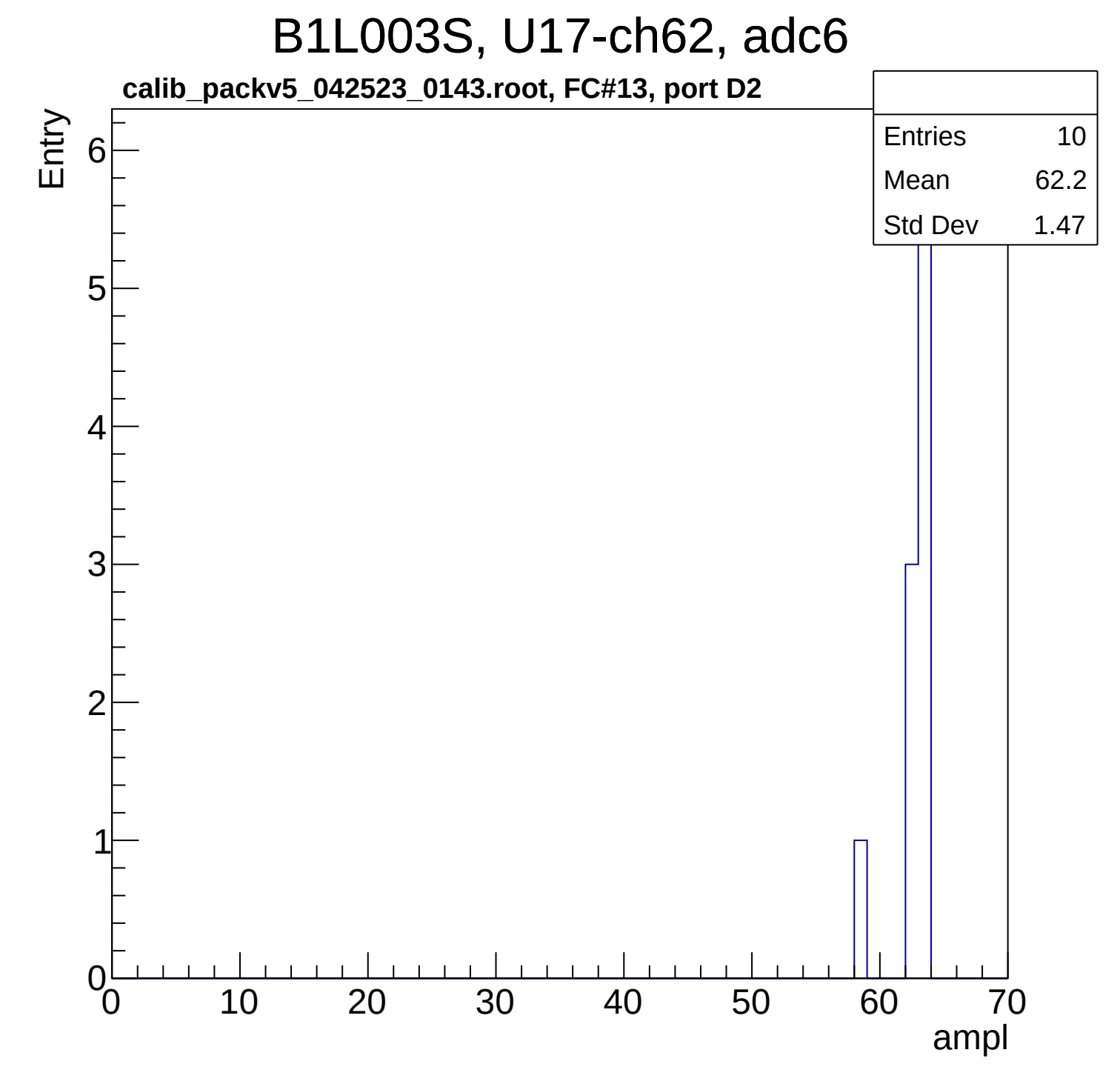
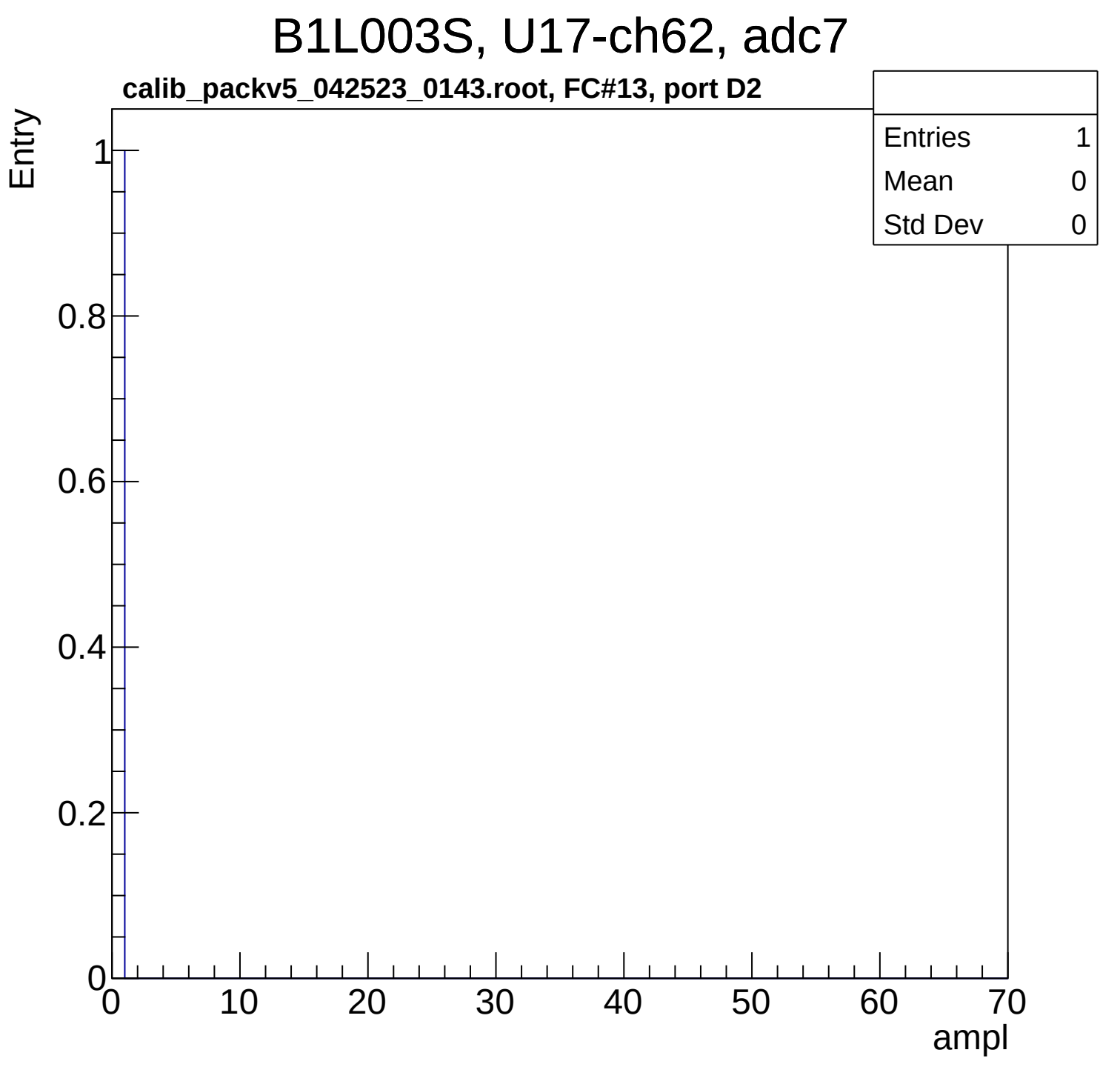
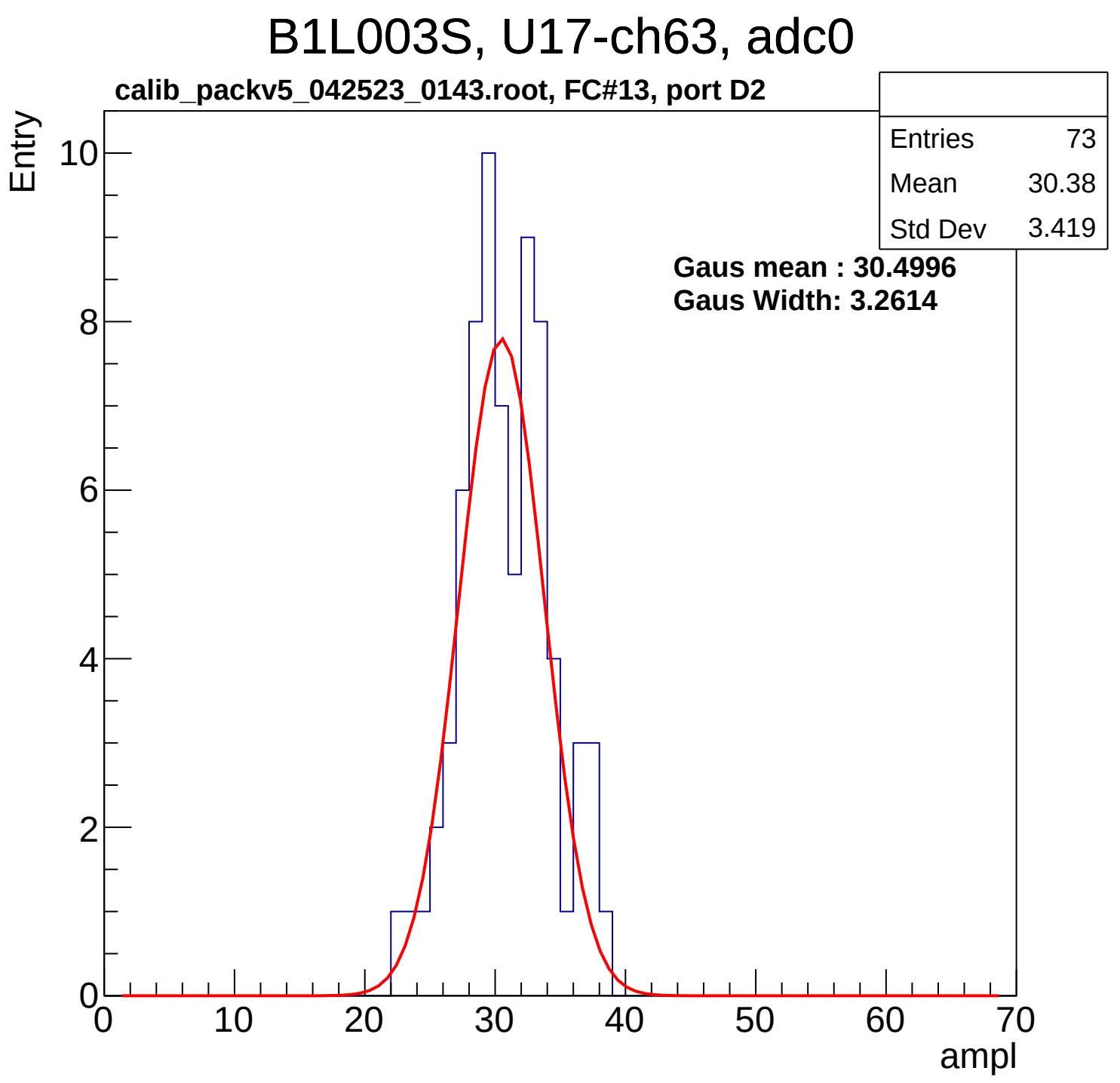

B1L003S, U17-ch62, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 62.2
Std Dev 1.47
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch62, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch63, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 30.38
Std Dev 3.419
Gaus mean : 30.4996
Gaus Width: 3.2614
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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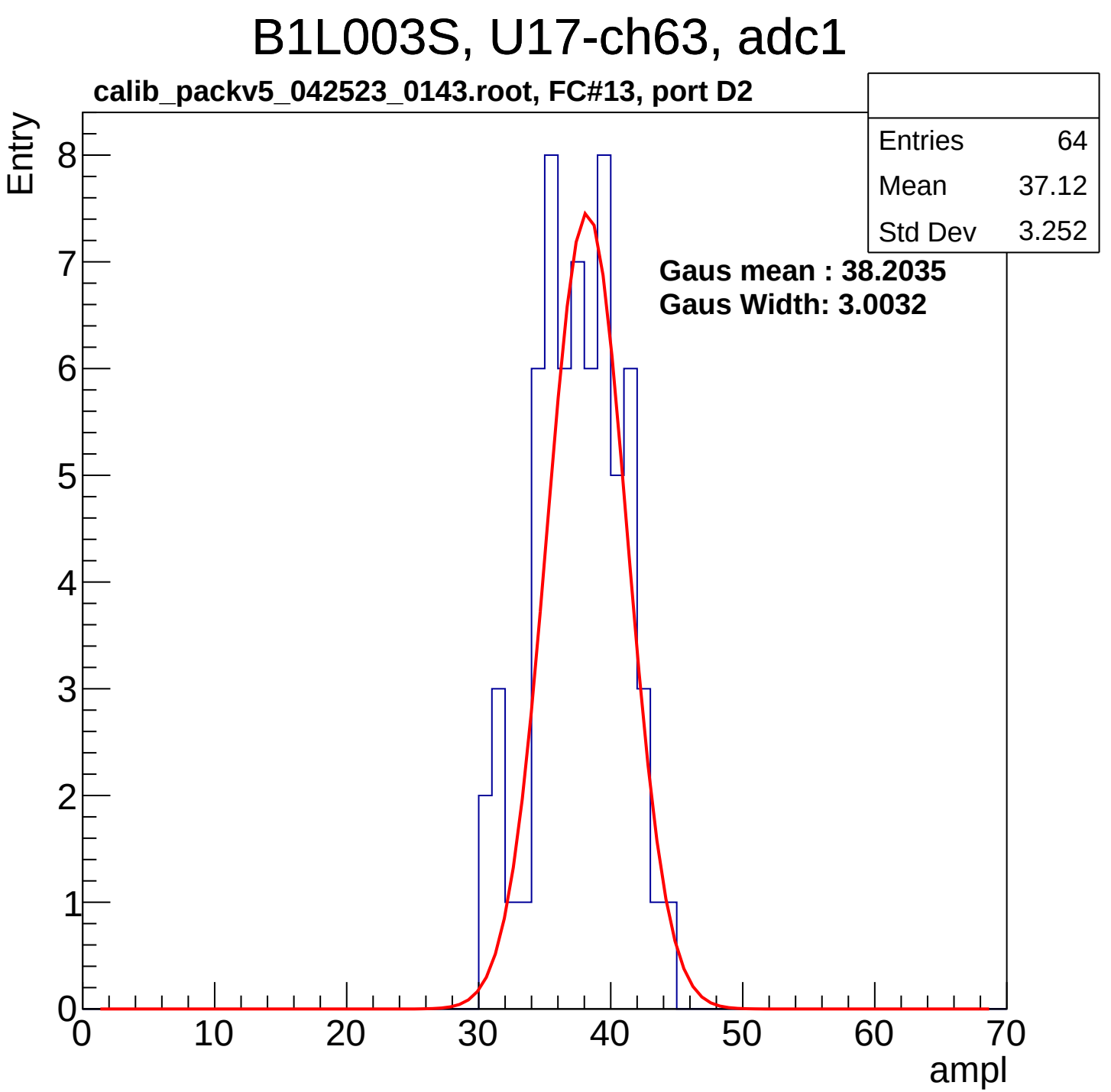
B1L003S, U17-ch63, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 37.12
Std Dev 3.252
Gaus mean : 38.2035
Gaus Width: 3.0032
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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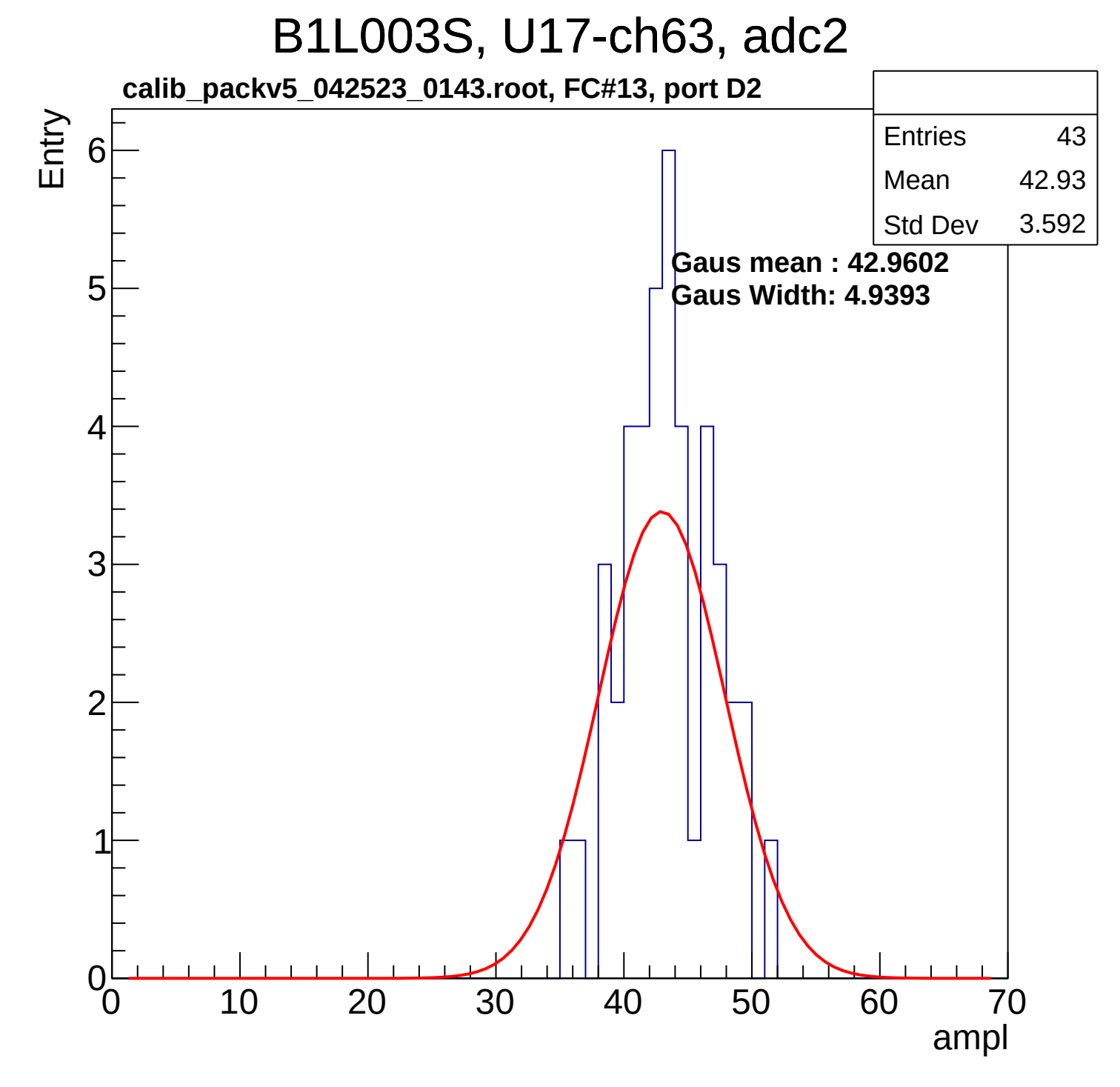
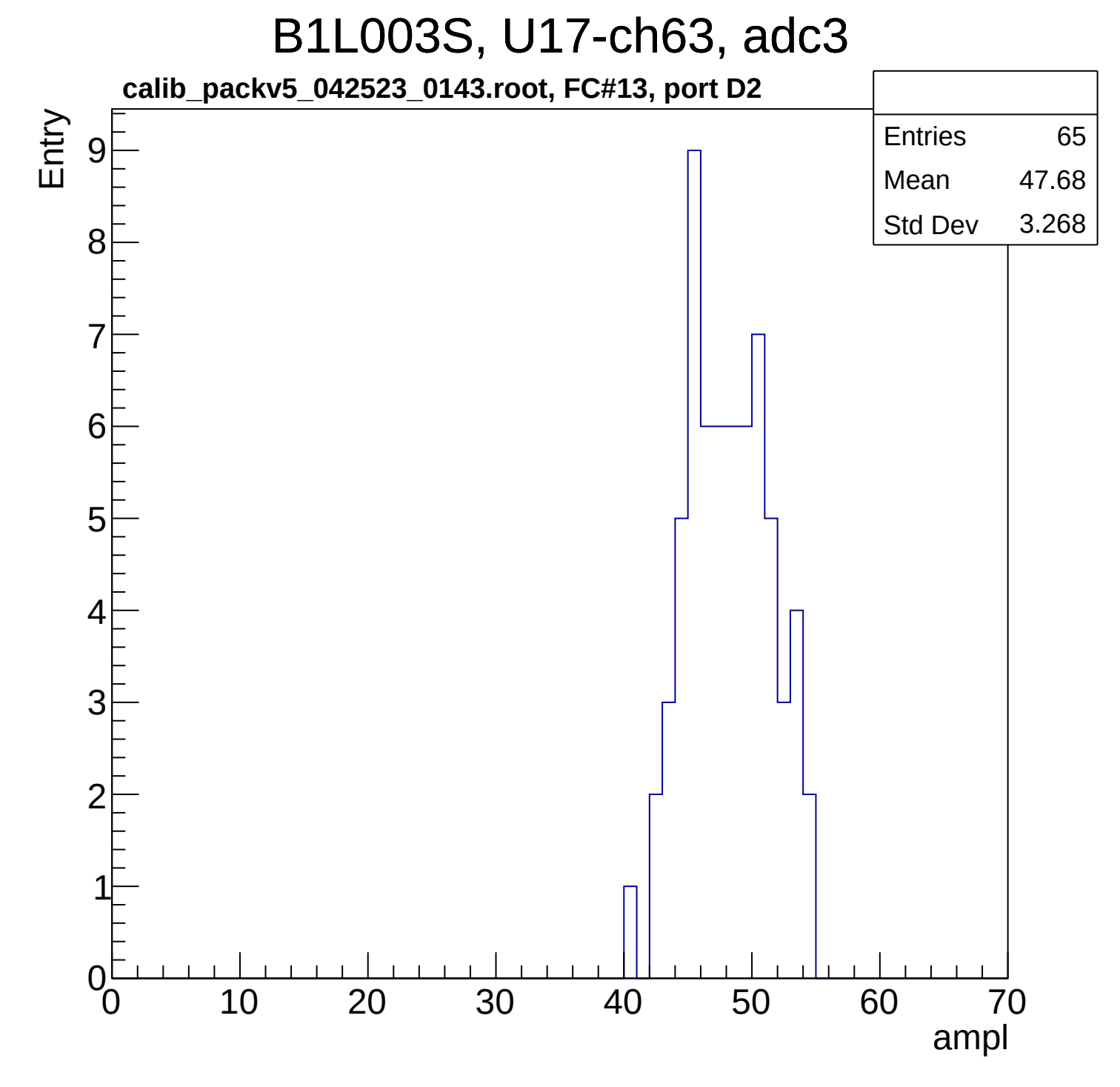

B1L003S, U17-ch63, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 42.93
Std Dev 3.592
Gaus mean : 42.9602
Gaus Width: 4.9393
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch63, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.68
Std Dev 3.268
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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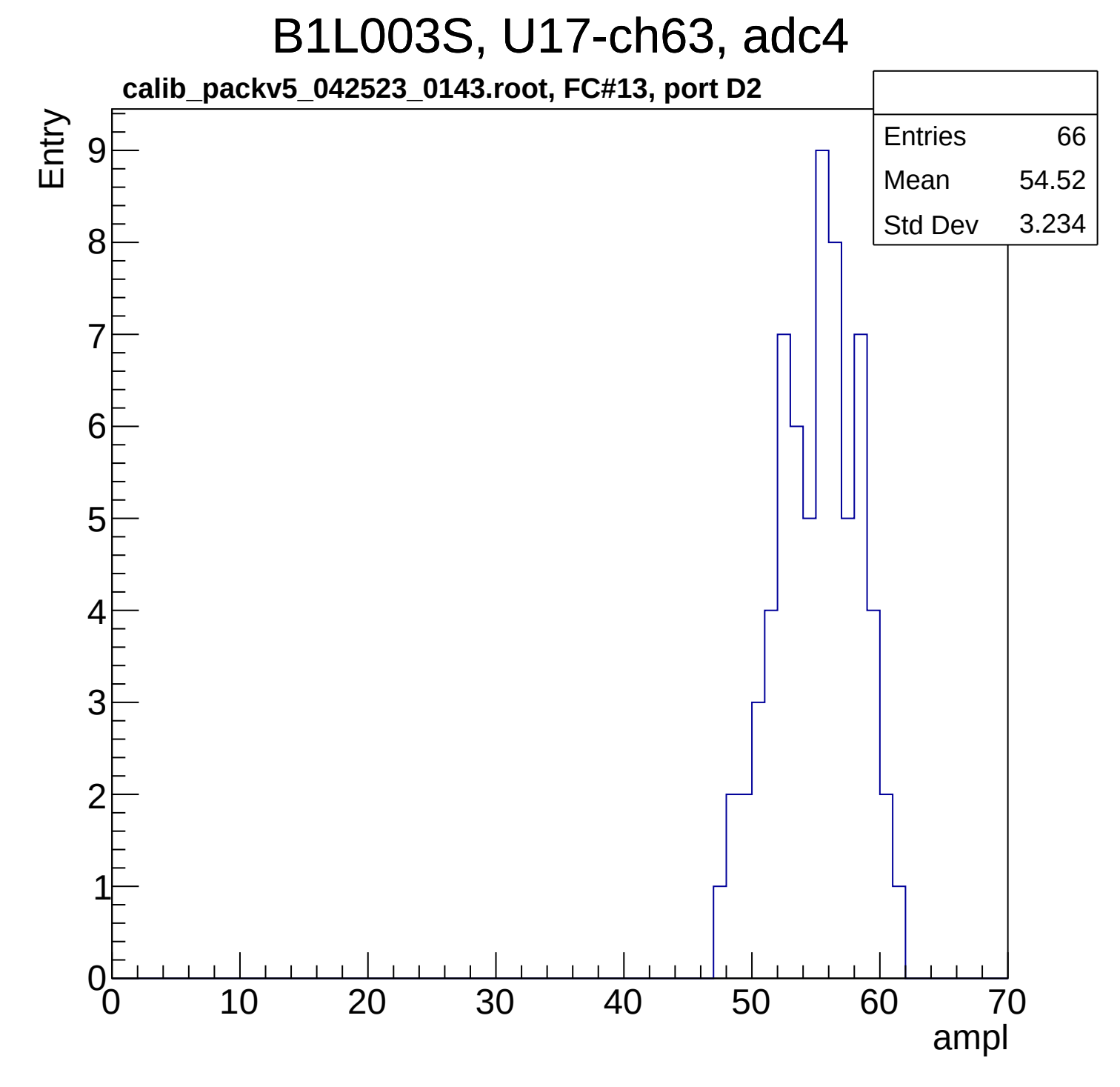
B1L003S, U17-ch63, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 54.52
Std Dev 3.234
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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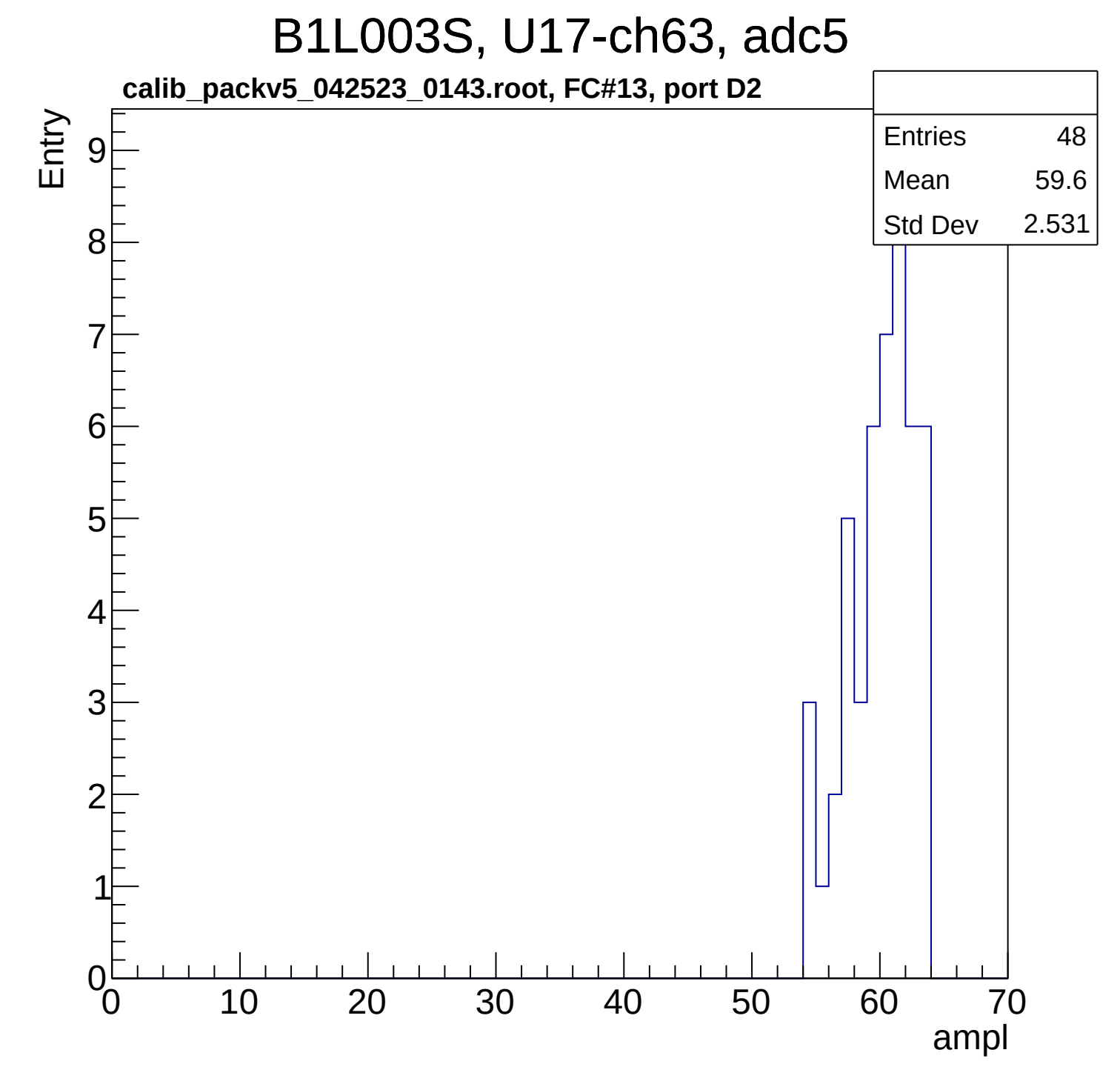
B1L003S, U17-ch63, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.6
Std Dev 2.531
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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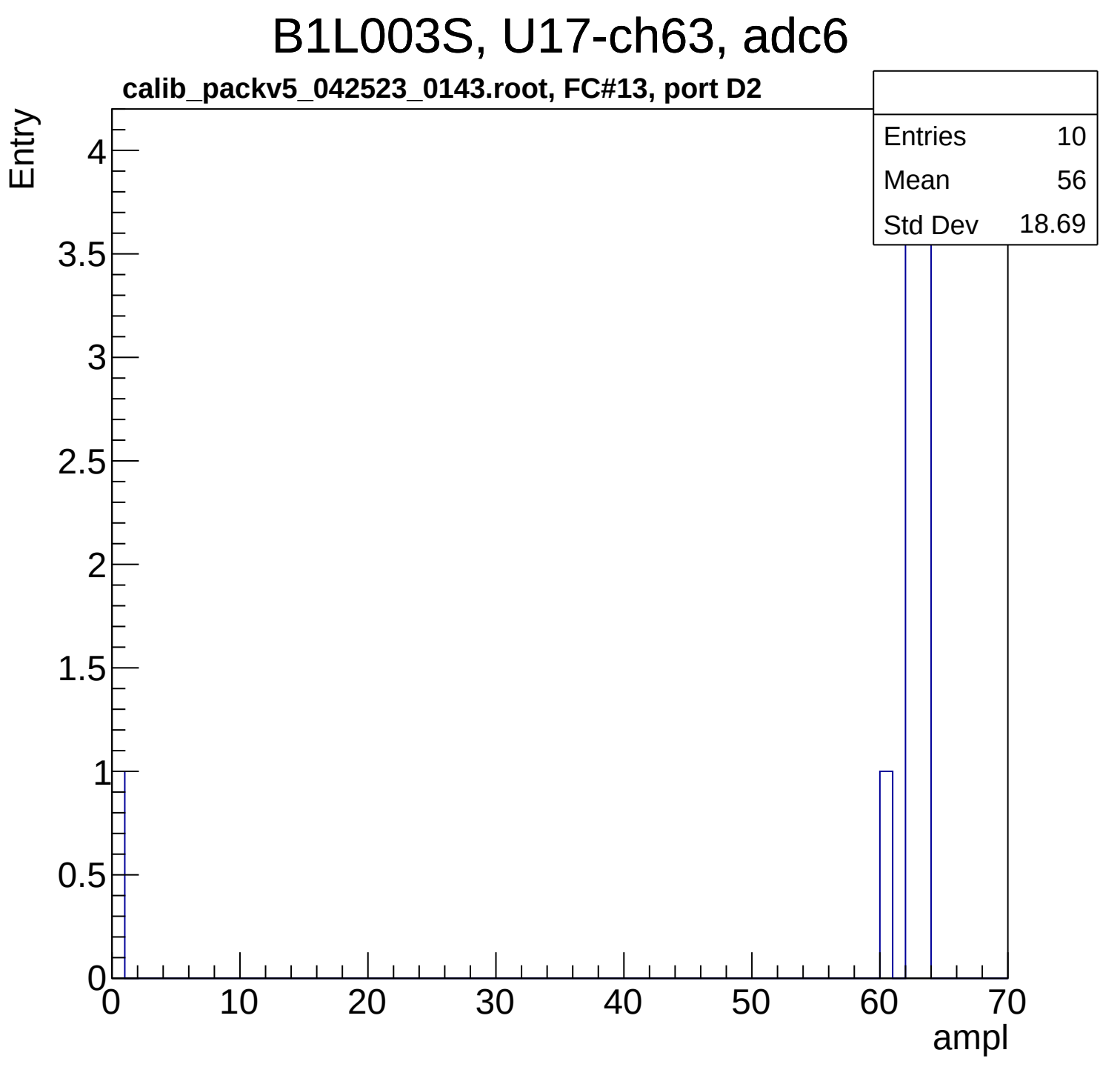
B1L003S, U17-ch63, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 56
Std Dev 18.69
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
30
40
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60
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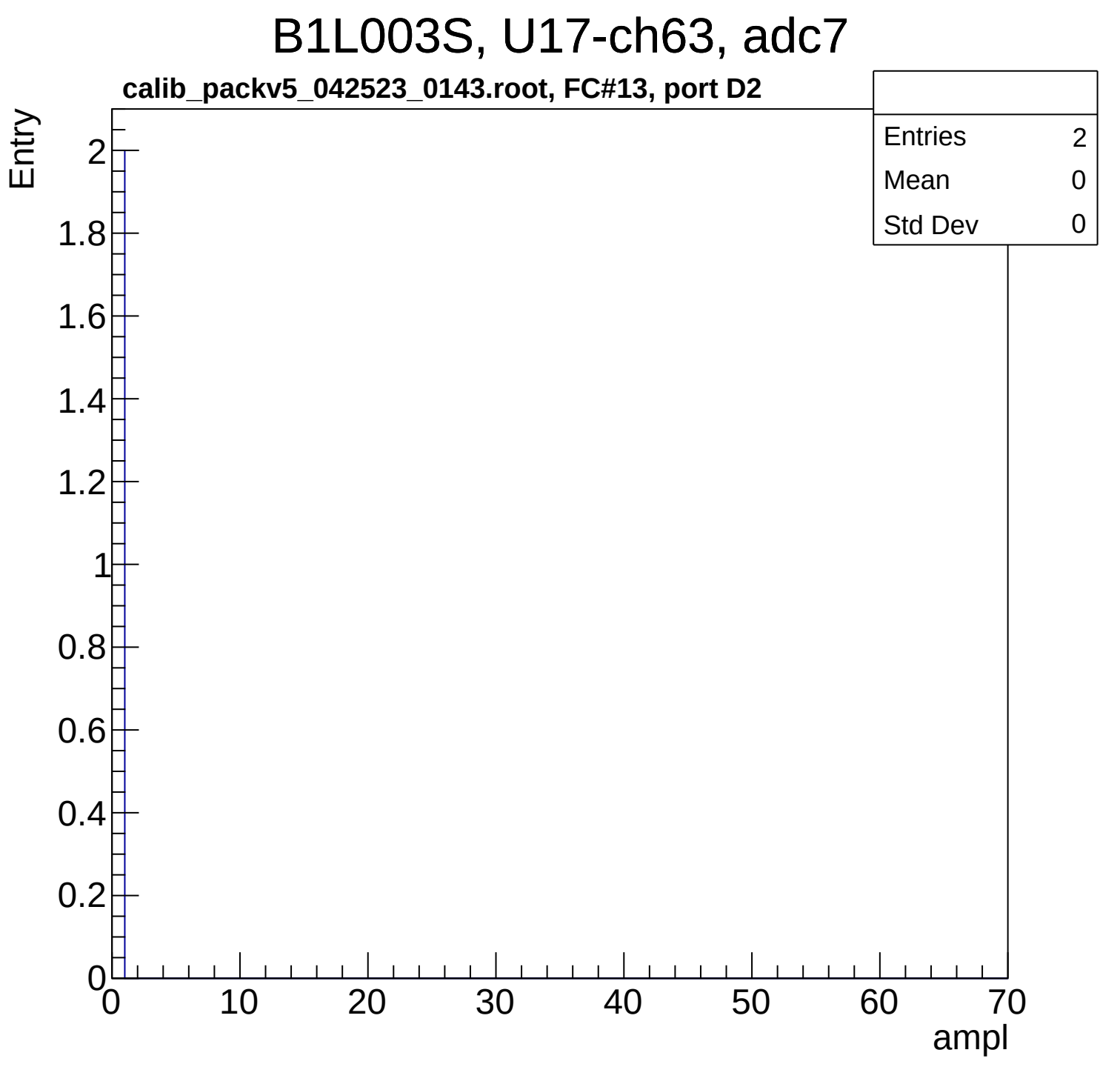
B1L003S, U17-ch63, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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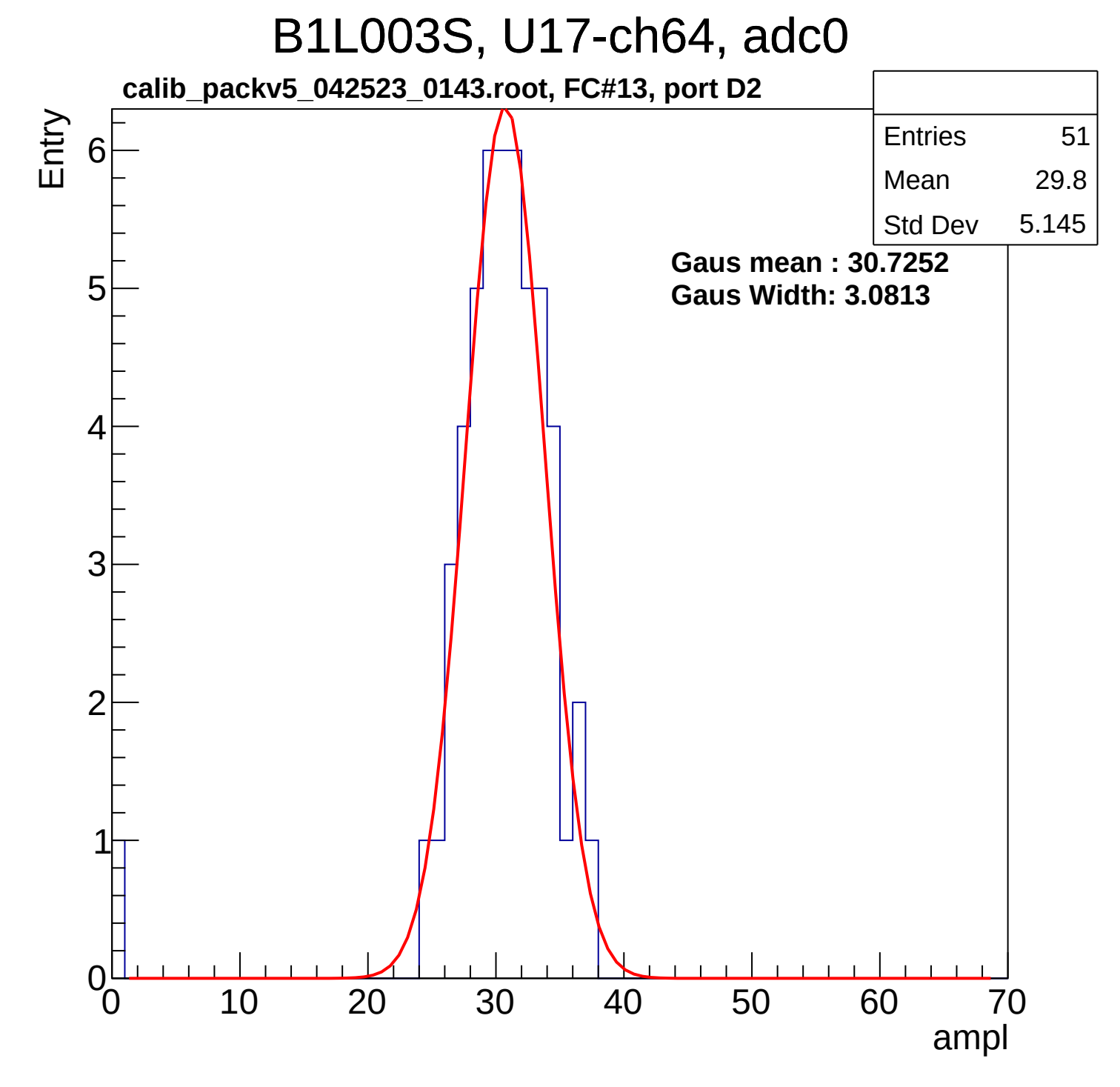
B1L003S, U17-ch64, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 29.8
Std Dev 5.145
Gaus mean : 30.7252
Gaus Width: 3.0813
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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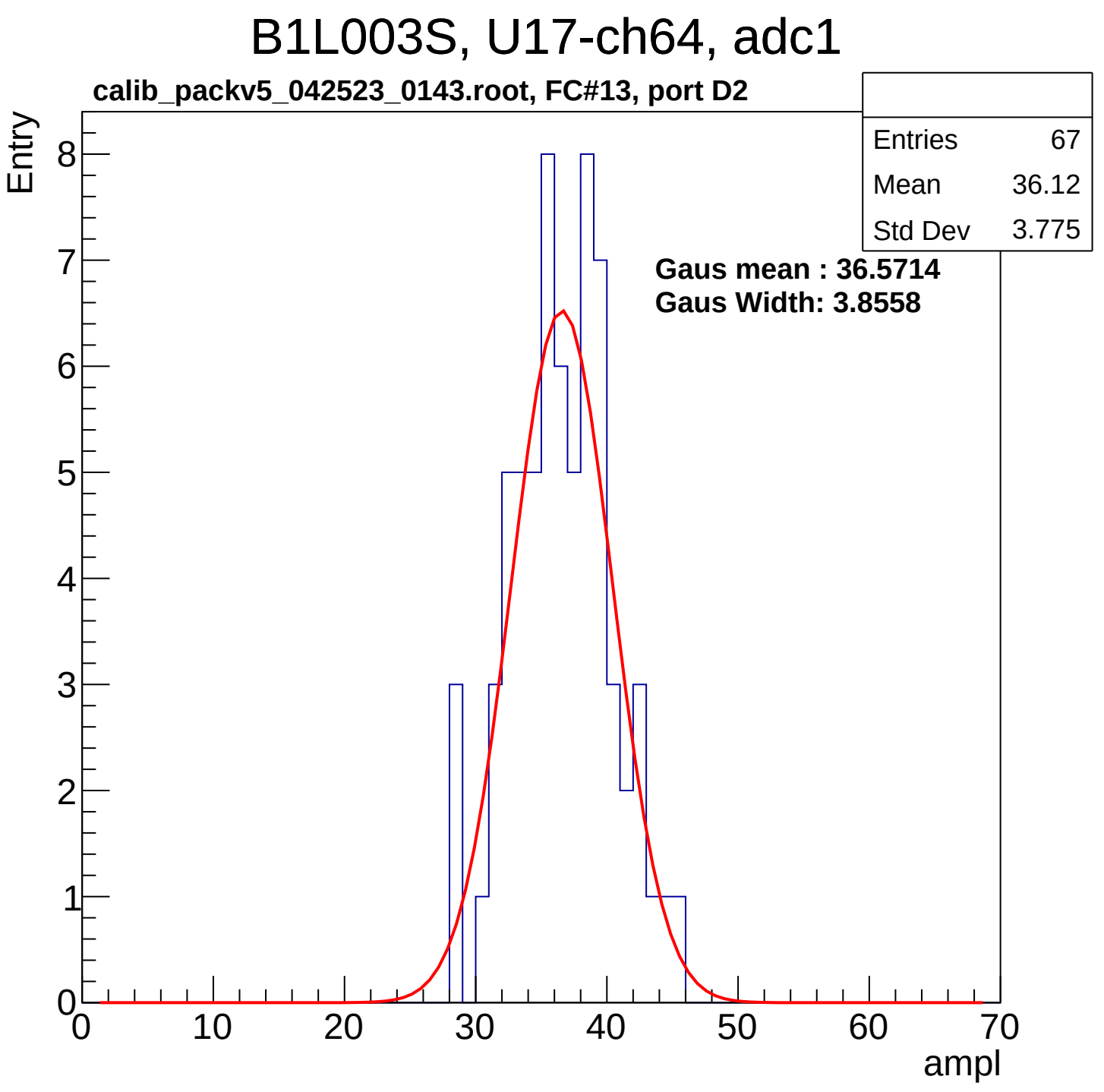
B1L003S, U17-ch64, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.12
Std Dev 3.775
Gaus mean : 36.5714
Gaus Width: 3.8558
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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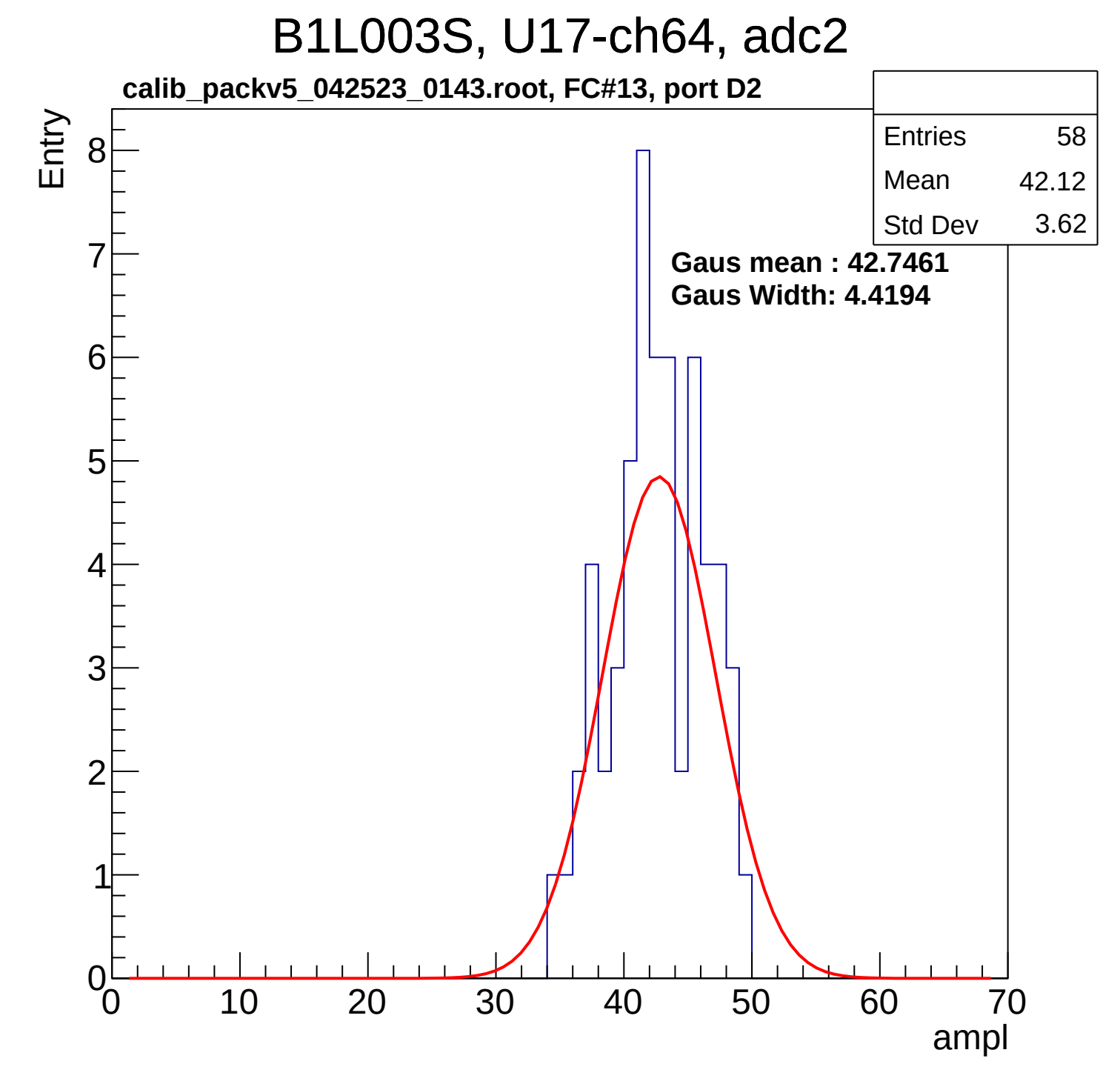
B1L003S, U17-ch64, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.12
Std Dev 3.62
Gaus mean : 42.7461
Gaus Width: 4.4194
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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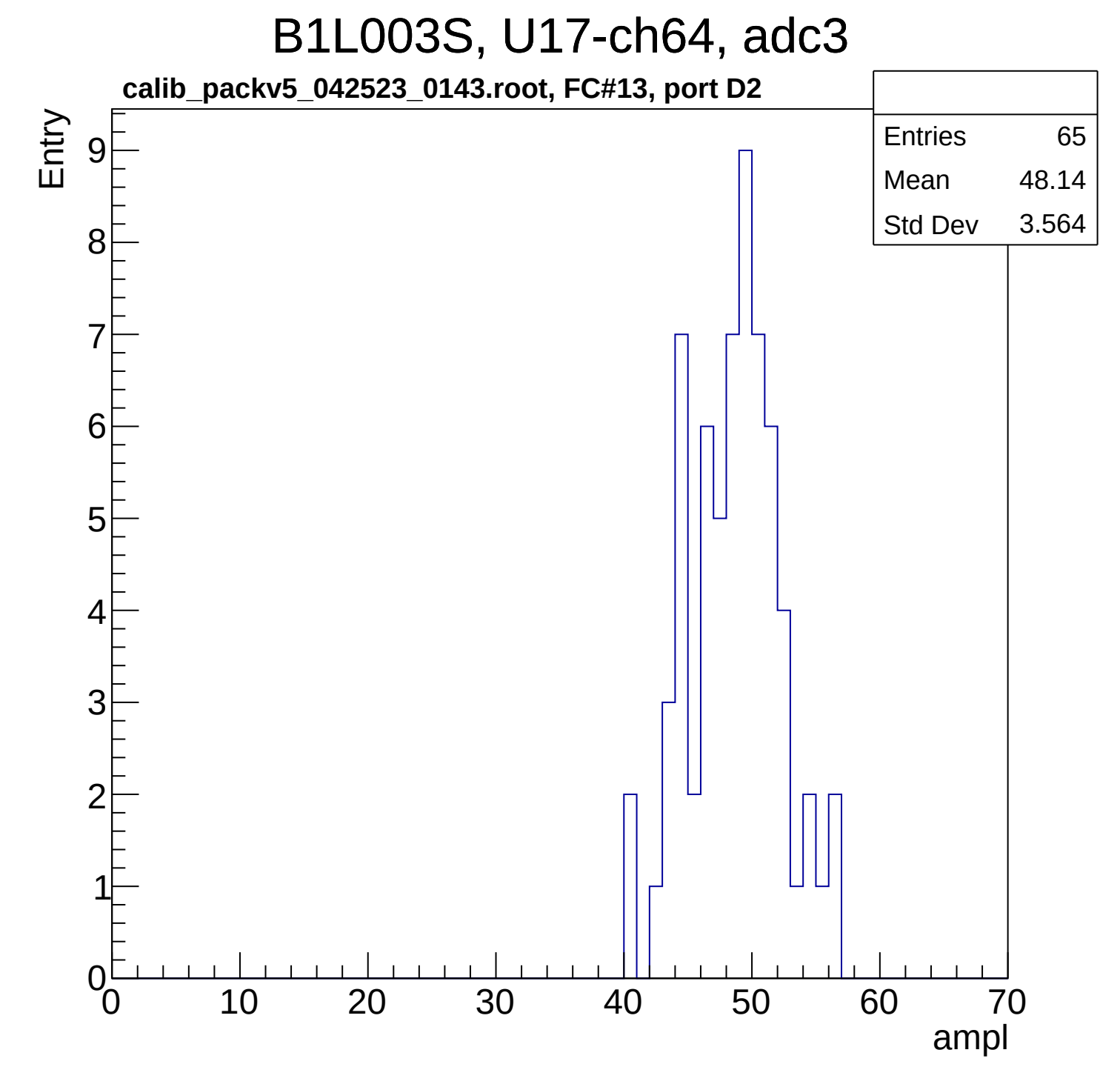
B1L003S, U17-ch64, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 48.14
Std Dev 3.564
Entry
ampl
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2
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0
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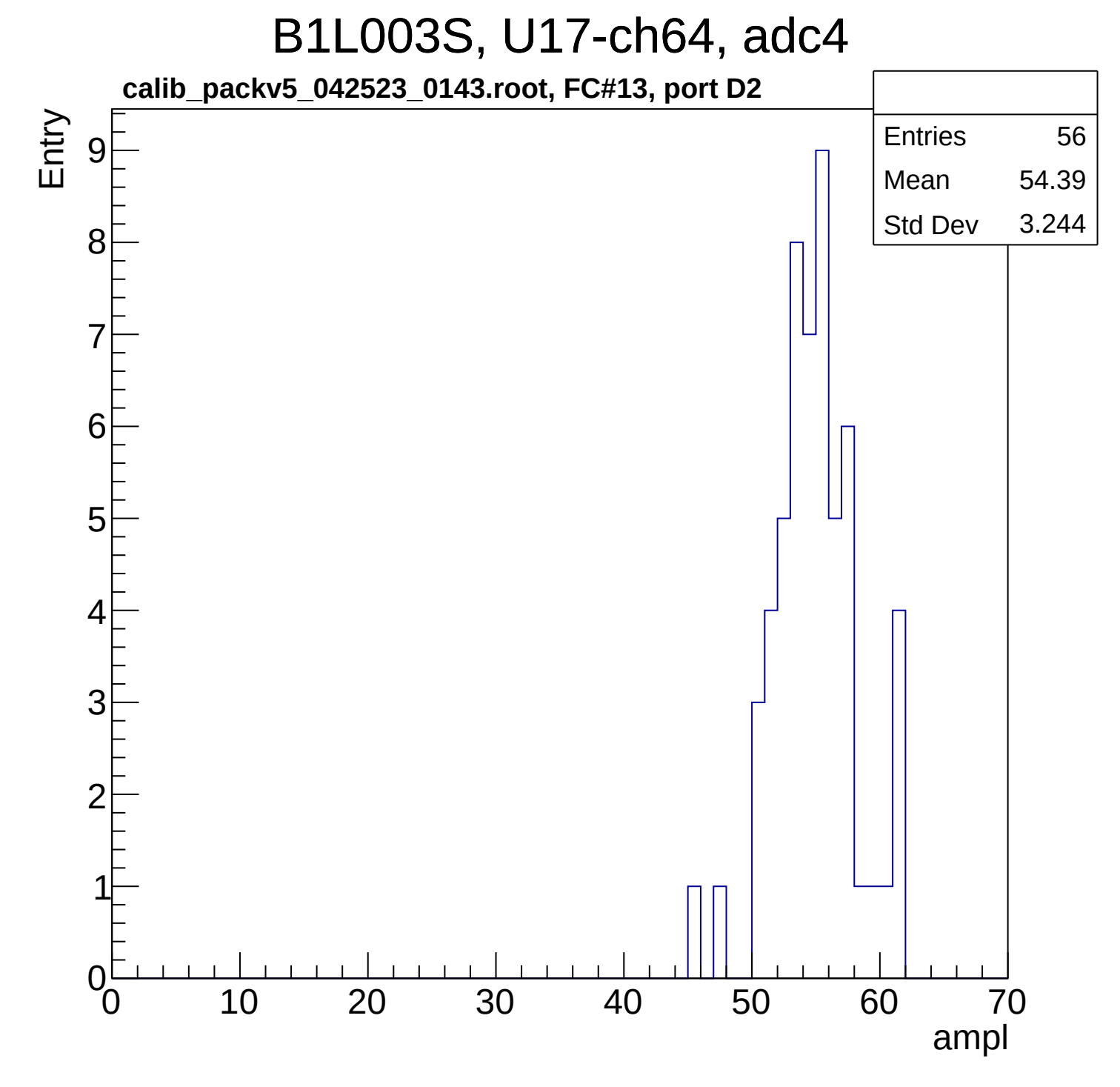
B1L003S, U17-ch64, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 54.39
Std Dev 3.244
Entry
ampl
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0
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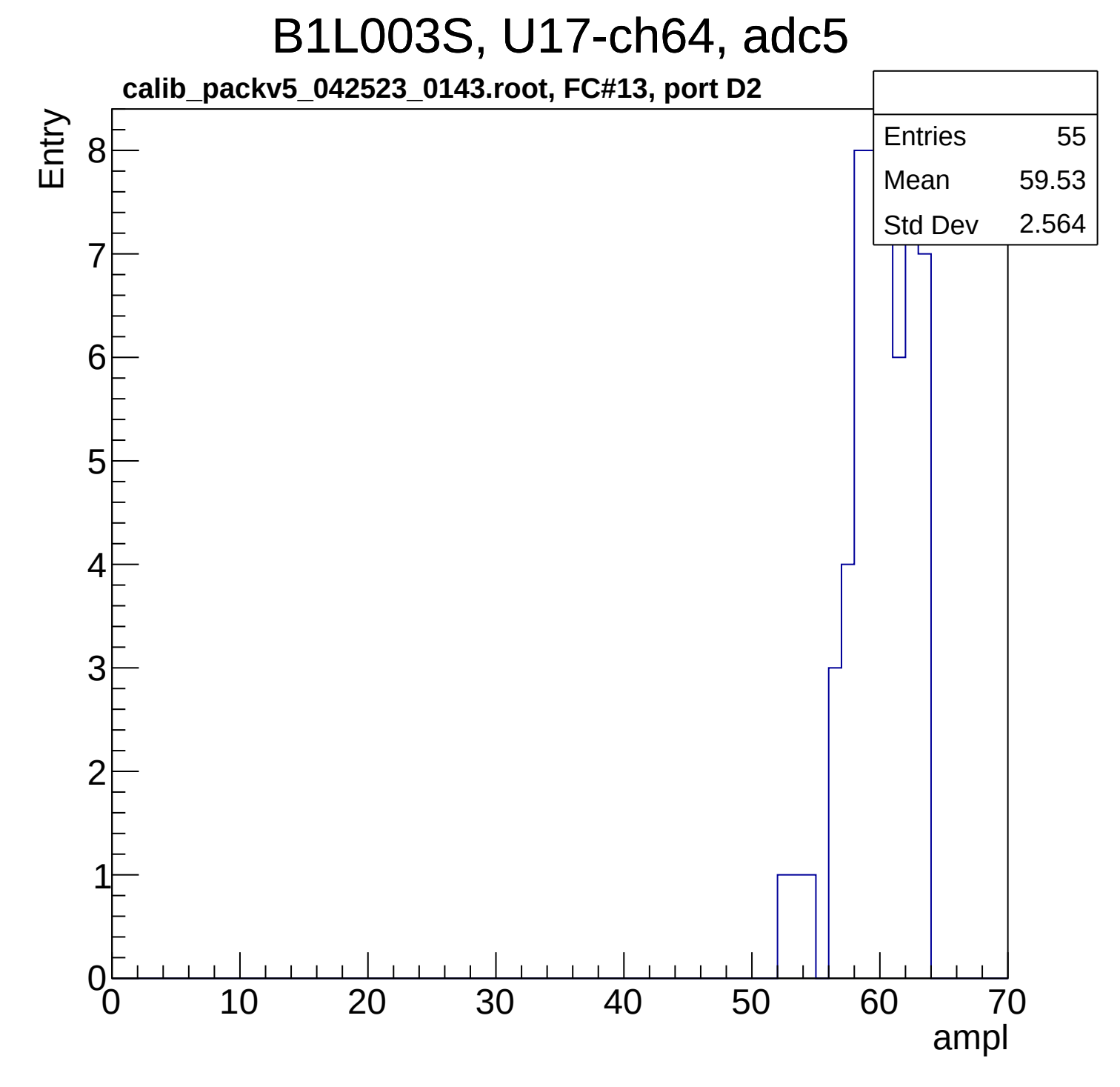
B1L003S, U17-ch64, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.53
Std Dev 2.564
Entry
ampl
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3
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5
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0
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60
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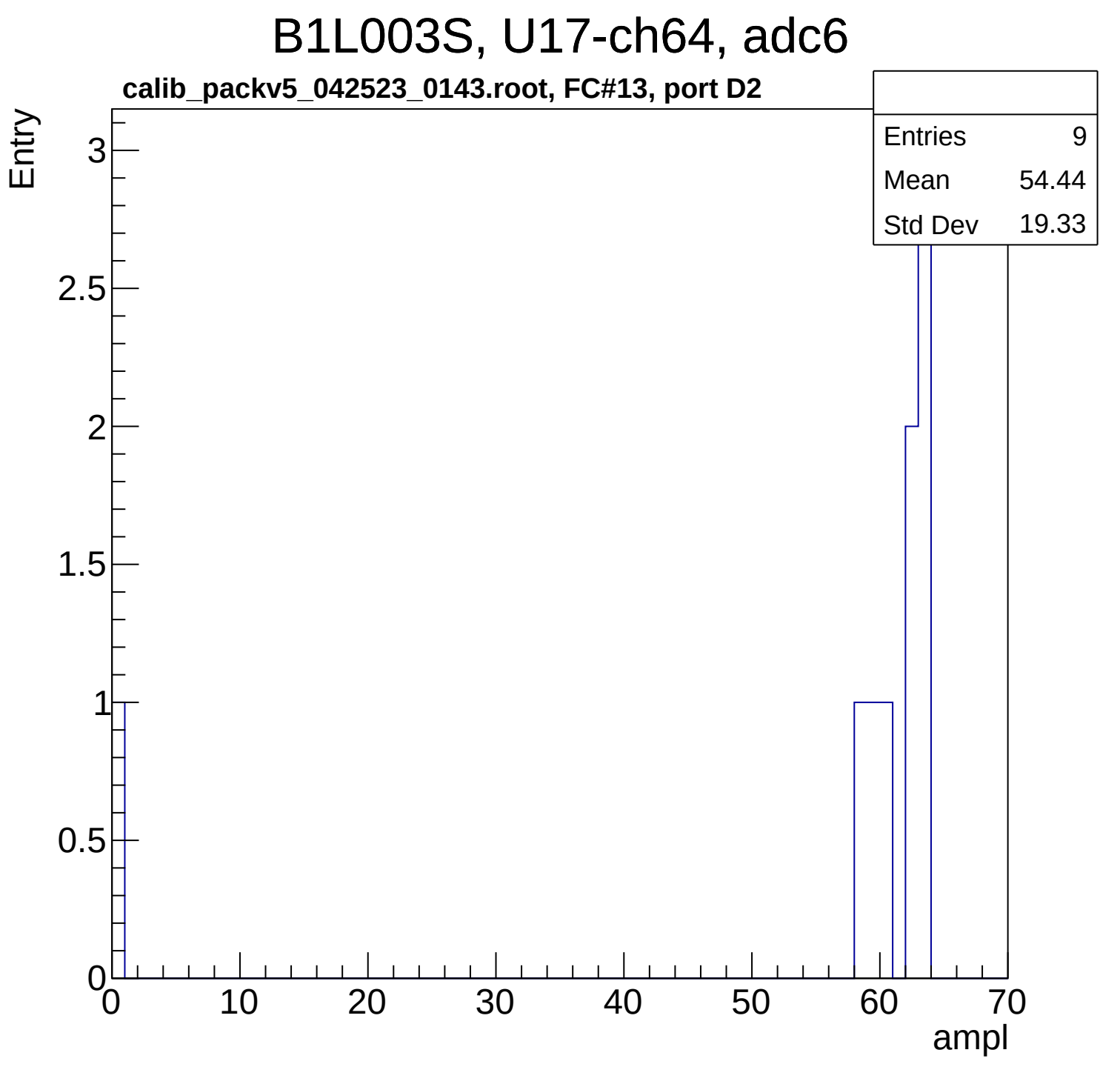
B1L003S, U17-ch64, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 54.44
Std Dev 19.33
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
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60
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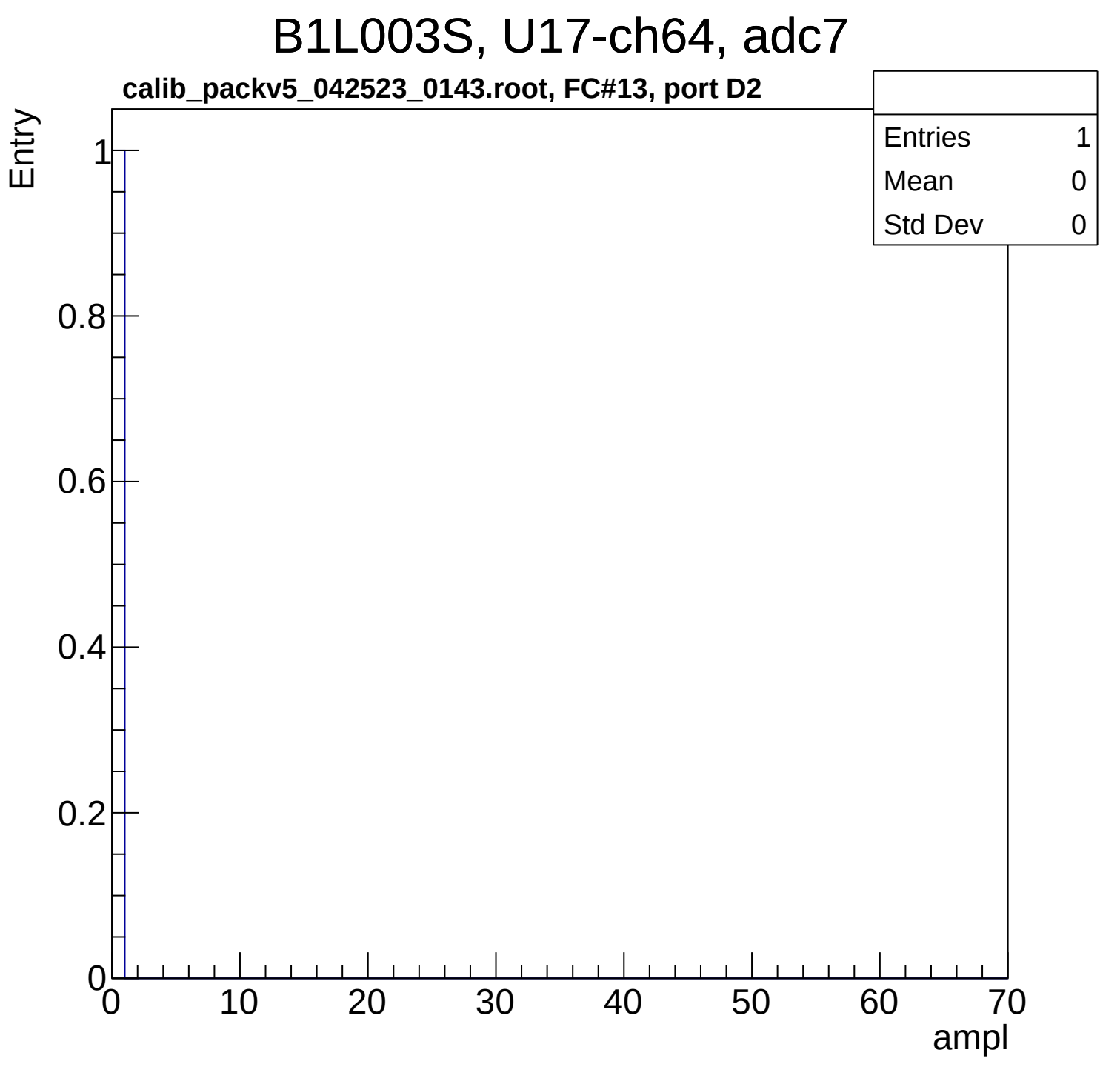
B1L003S, U17-ch64, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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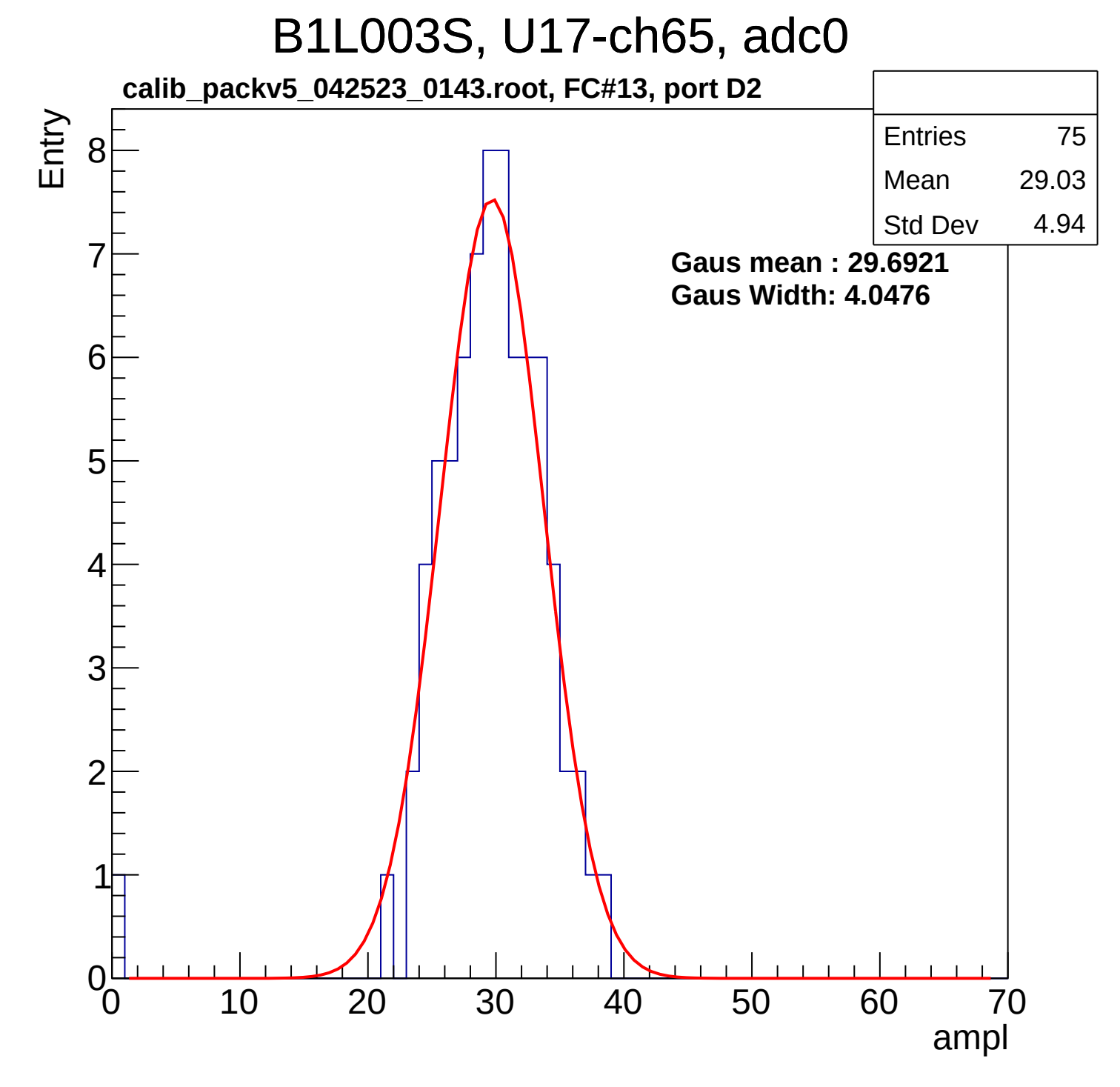
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch65, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 29.03
Std Dev 4.94
Gaus mean : 29.6921
Gaus Width: 4.0476
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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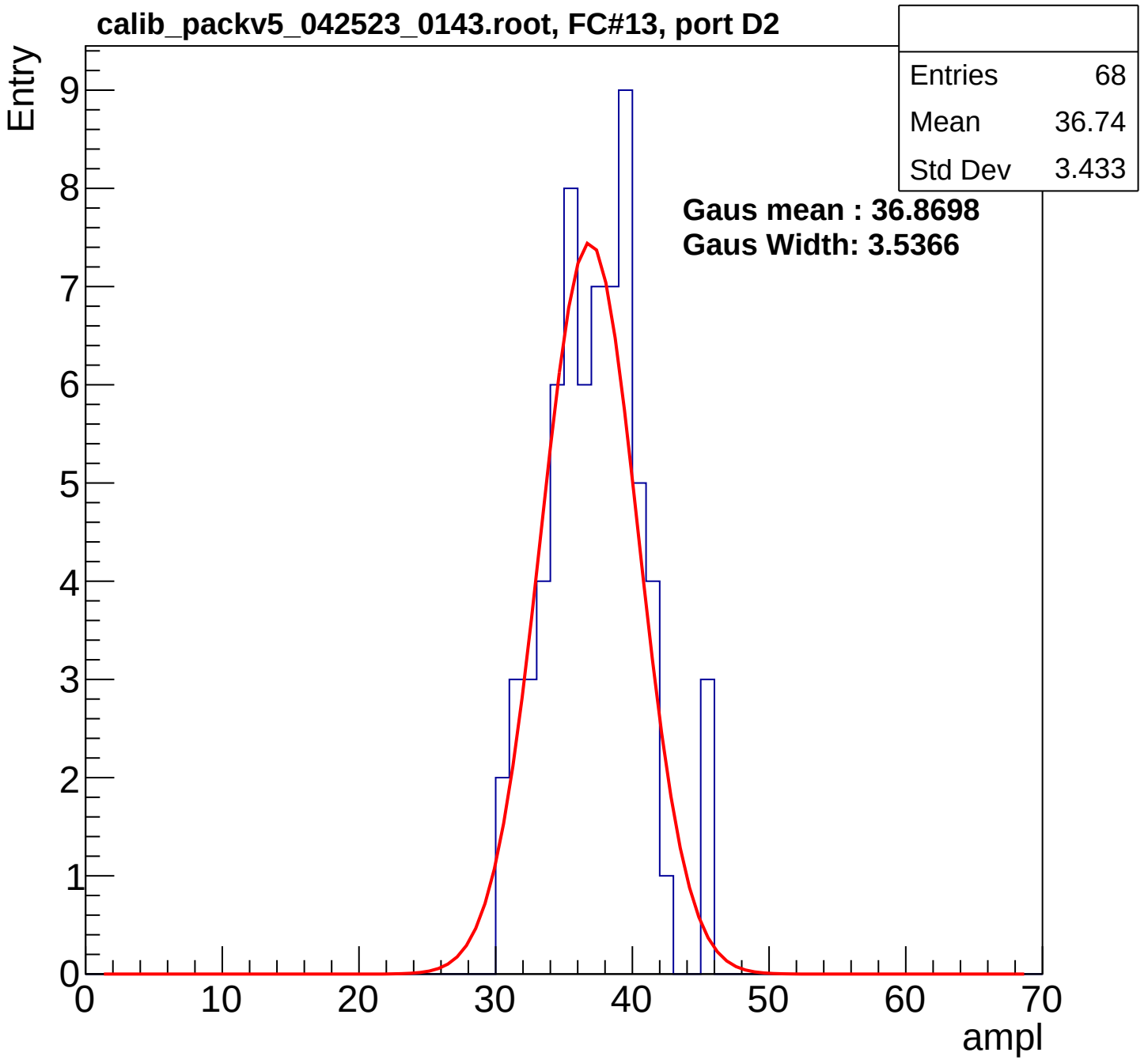
B1L003S, U17-ch65, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.74
Std Dev 3.433
Gaus mean : 36.8698
Gaus Width: 3.5366
Entry
ampl
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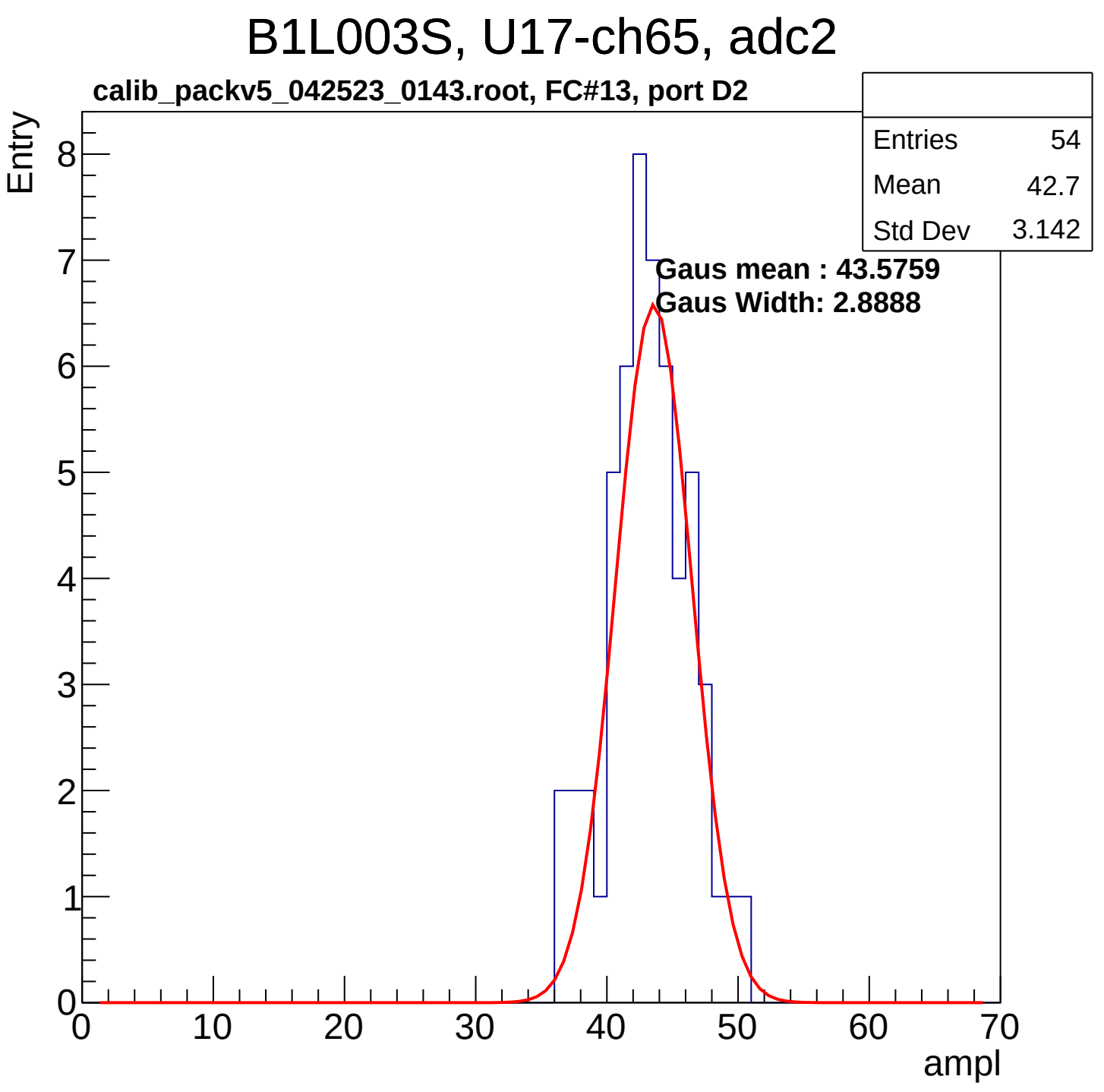
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch65, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.7
Std Dev 3.142
Gaus mean : 43.5759
Gaus Width: 2.8888
Entry
ampl
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1
2
3
4
5
6
7
8
0
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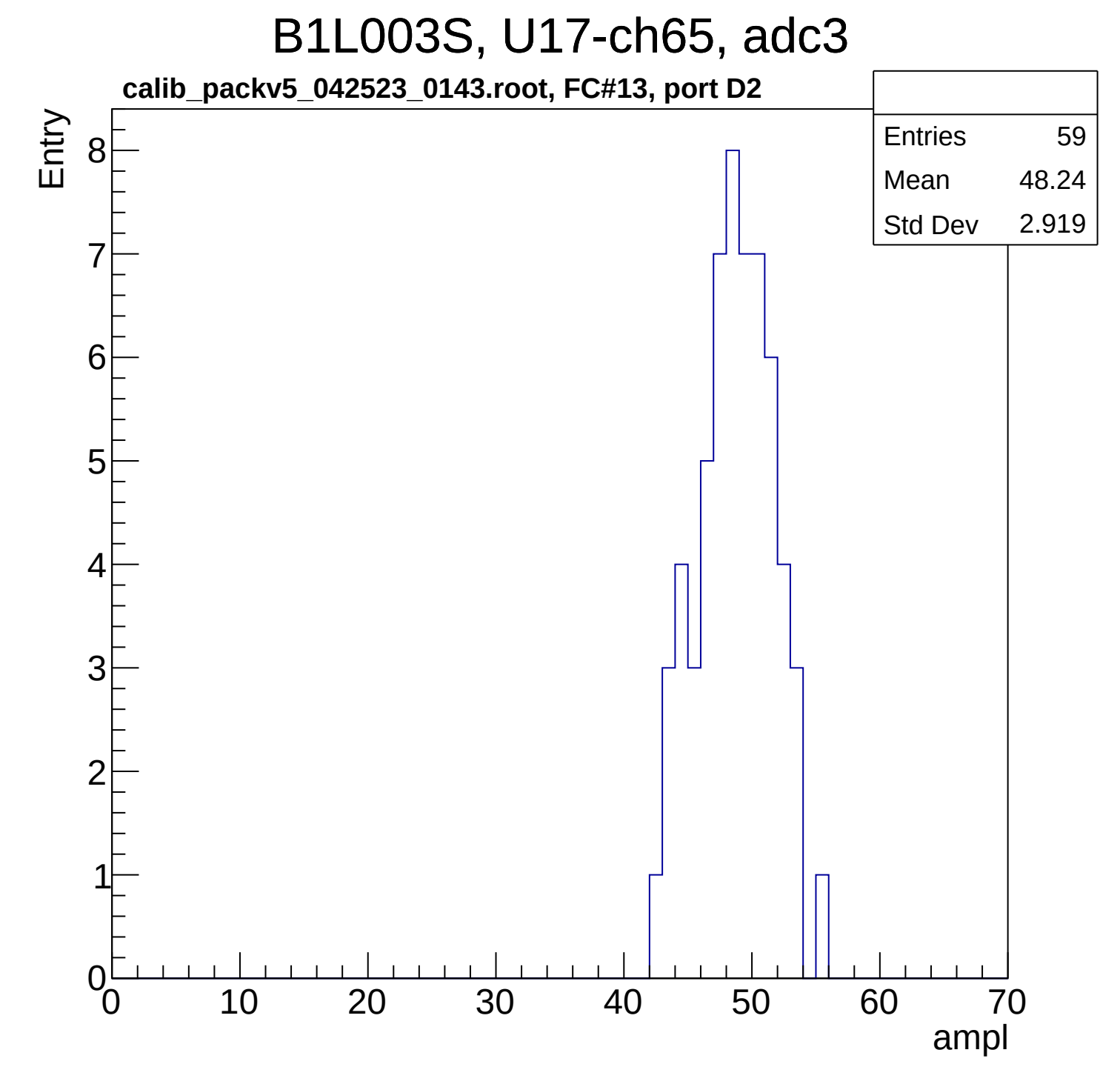
B1L003S, U17-ch65, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48.24
Std Dev 2.919
Entry
ampl
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1
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3
4
5
6
7
8
0
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20
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40
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60
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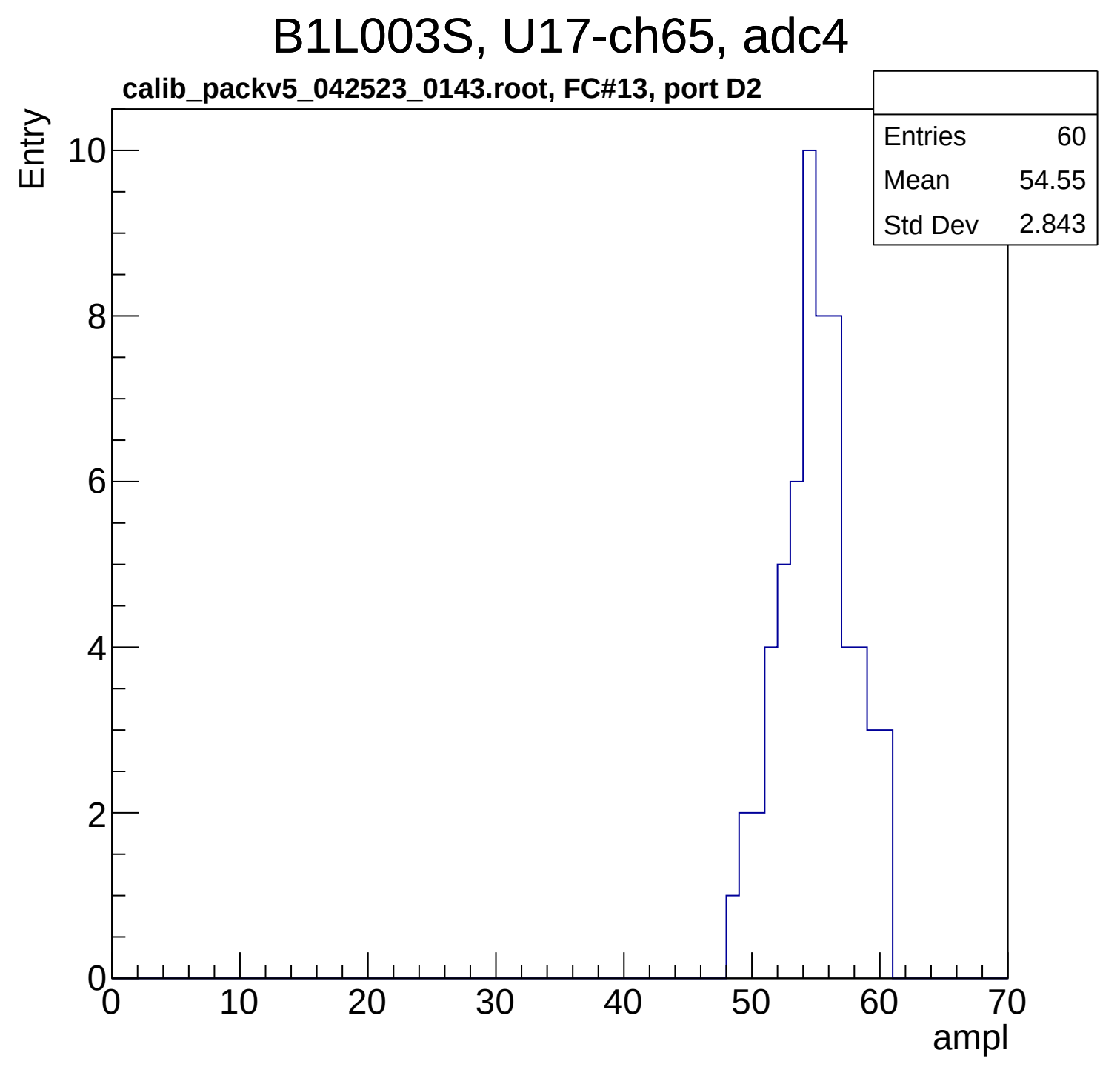
B1L003S, U17-ch65, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.55
Std Dev 2.843
Entry
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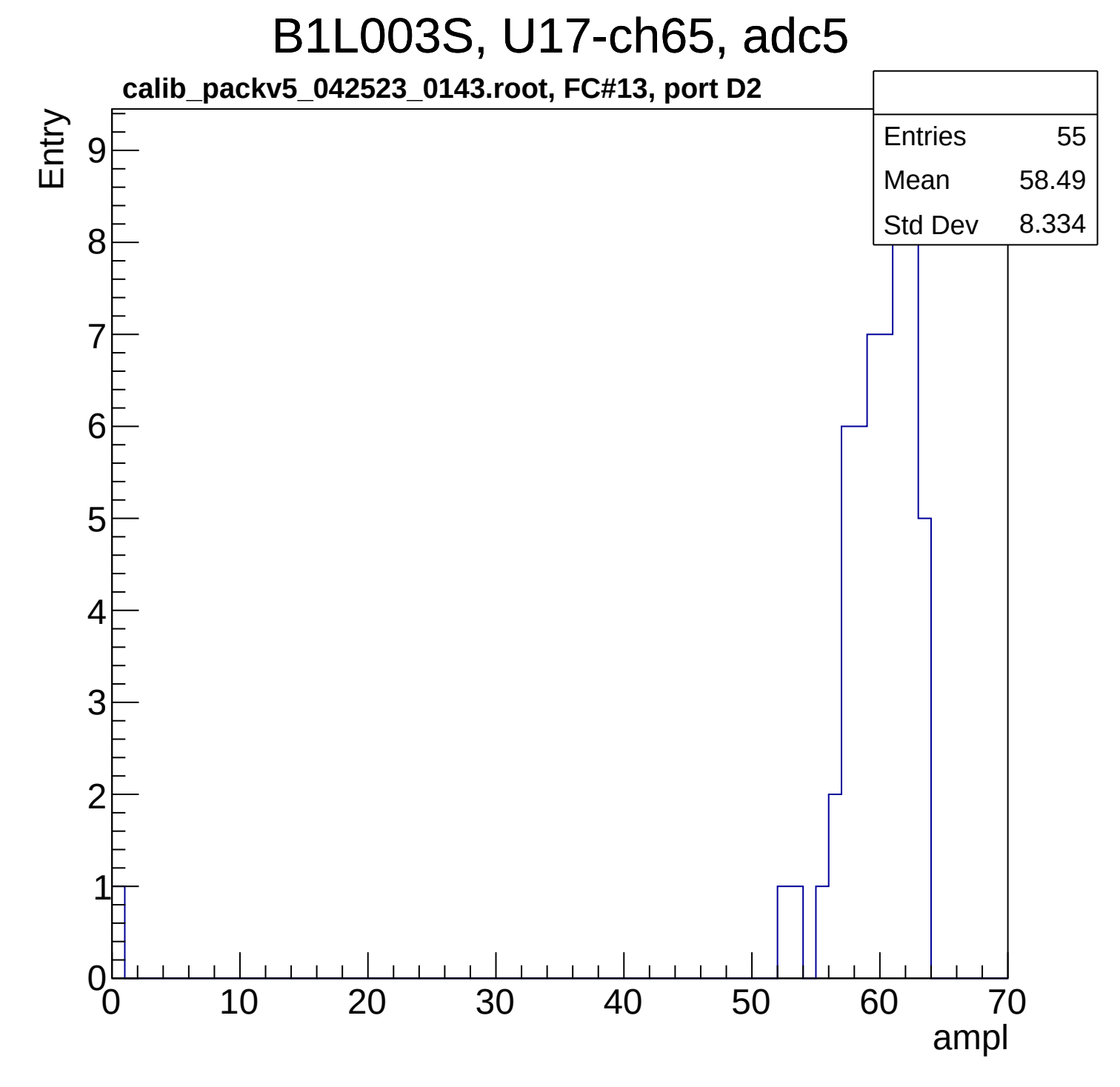
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch65, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.49
Std Dev 8.334
Entry
ampl
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3
4
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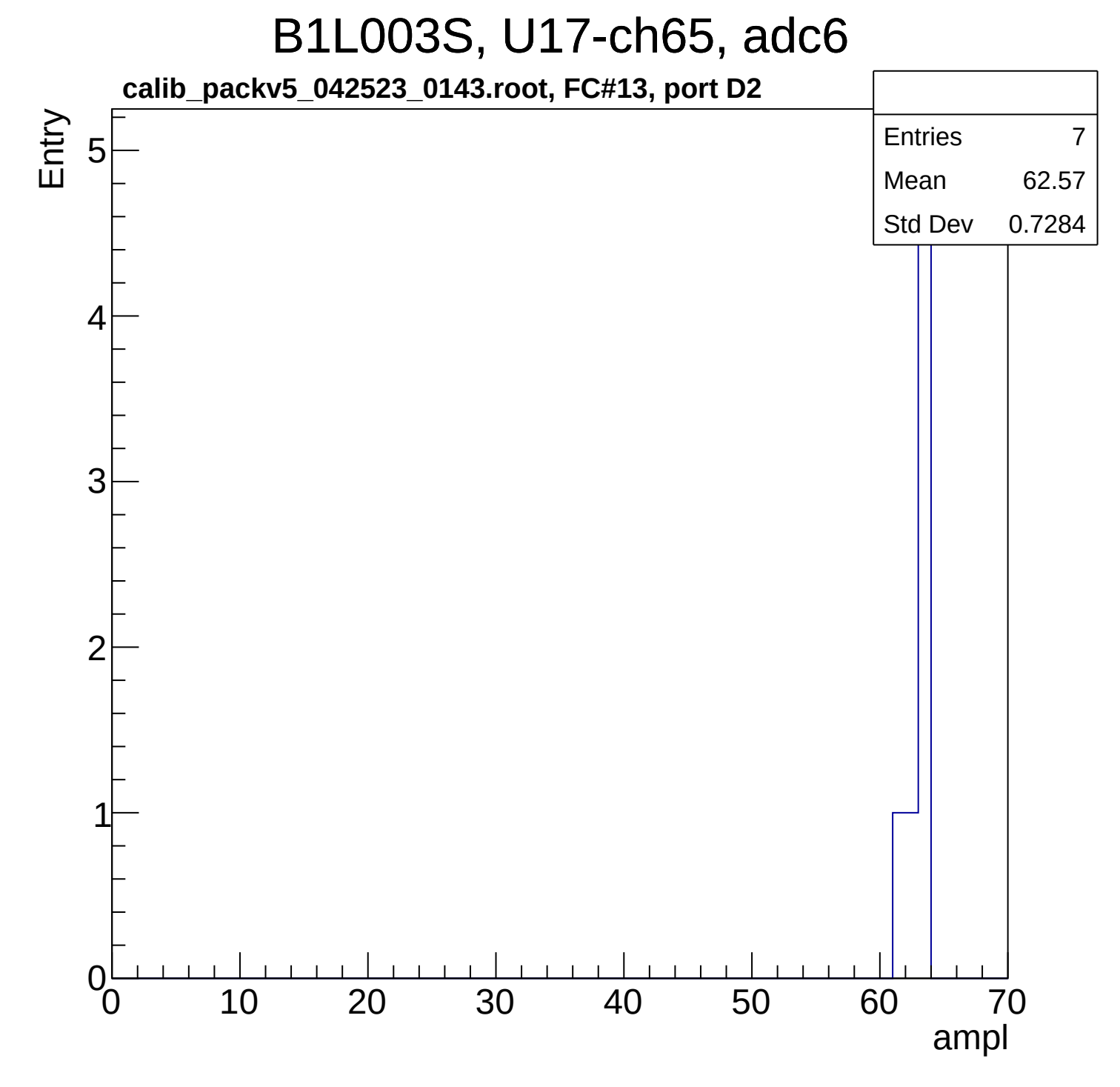
B1L003S, U17-ch65, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.57
Std Dev 0.7284
Entry
ampl
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3
4
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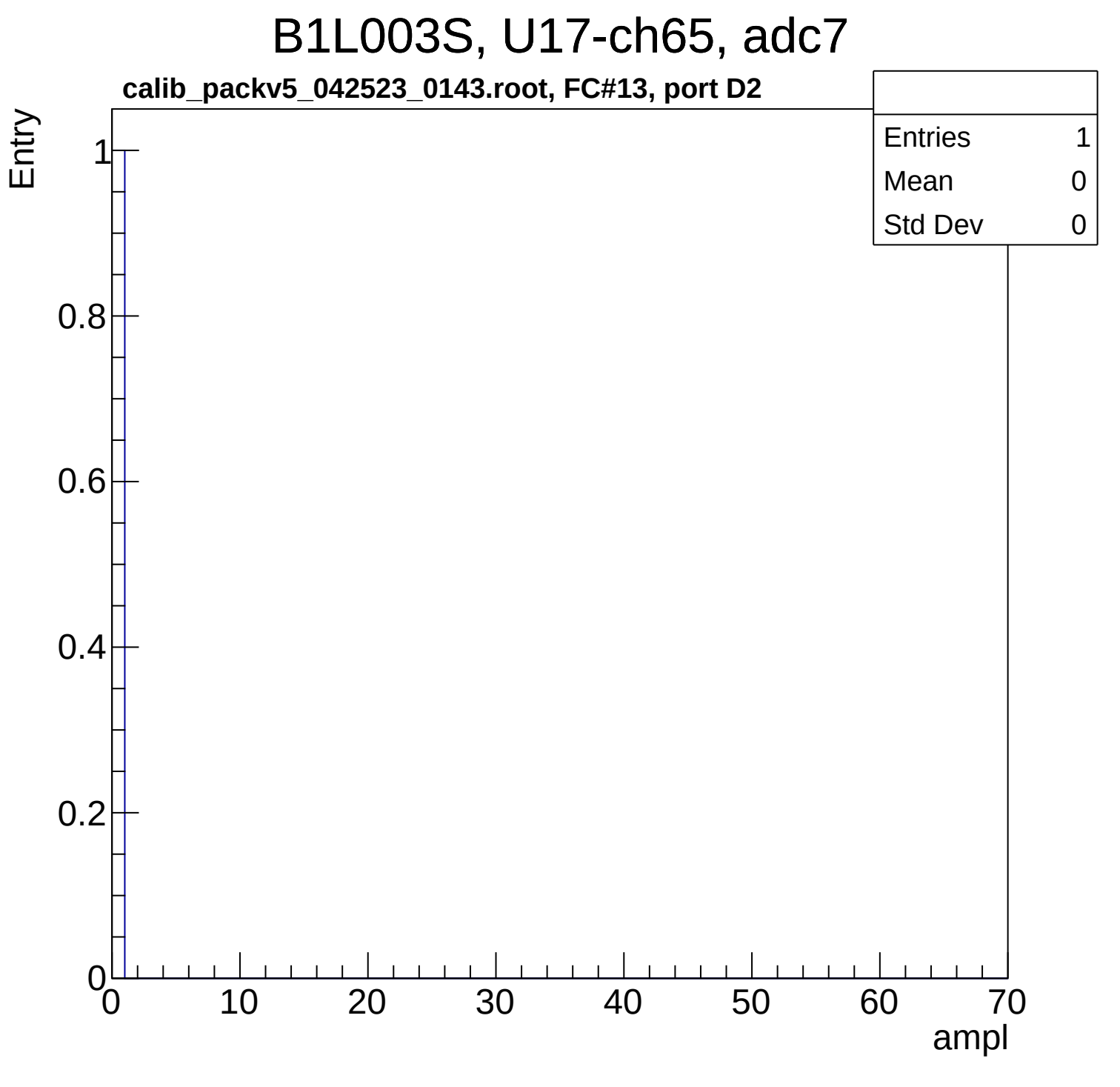
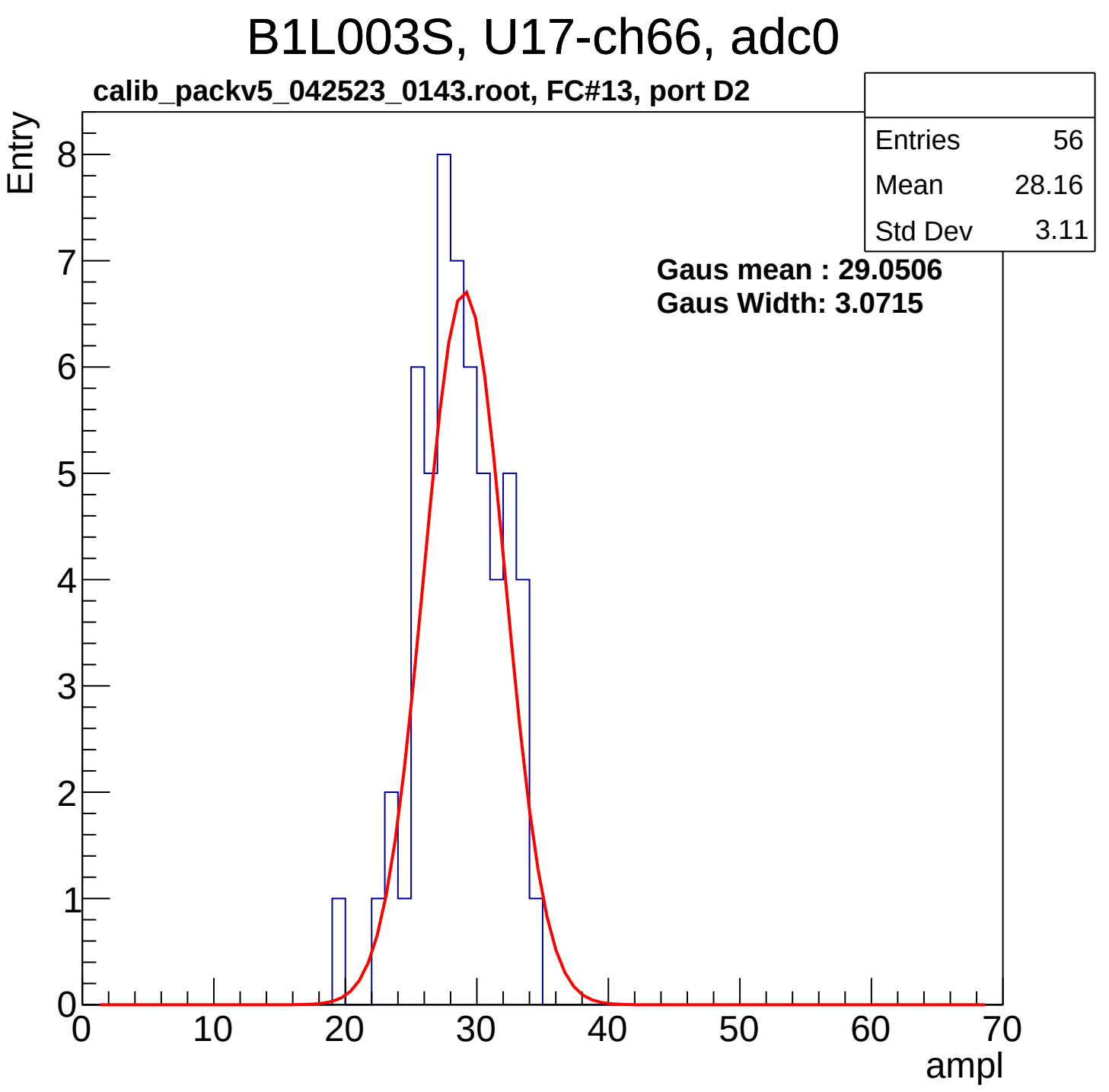
0
10
20
30
40
50
60
70


B1L003S, U17-ch65, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch66, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 28.16
Std Dev 3.11
Gaus mean : 29.0506
Gaus Width: 3.0715
Entry
ampl
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1
2
3
4
5
6
7
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0
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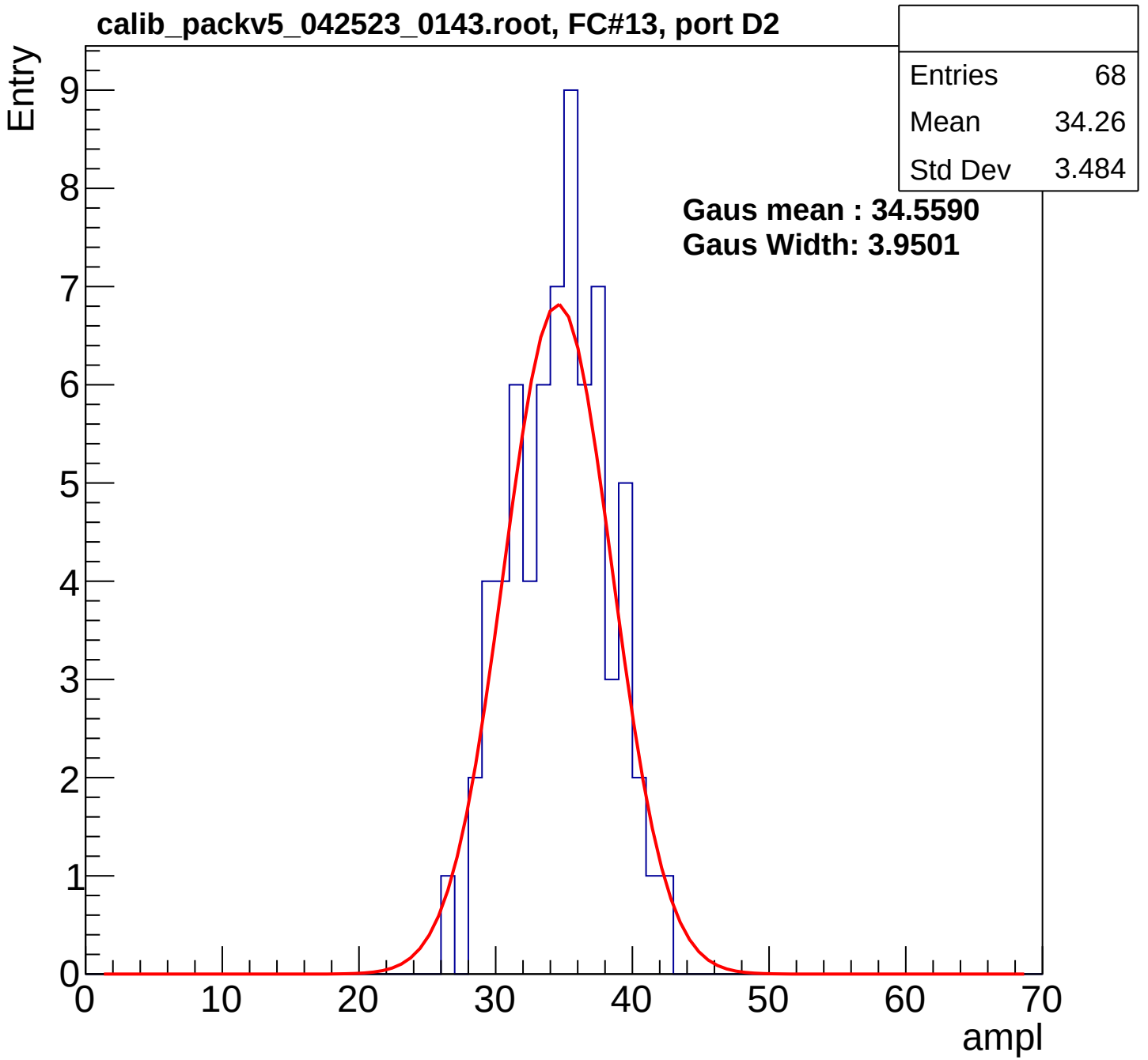
B1L003S, U17-ch66, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 34.26
Std Dev 3.484
Gaus mean : 34.5590
Gaus Width: 3.9501
Entry
ampl
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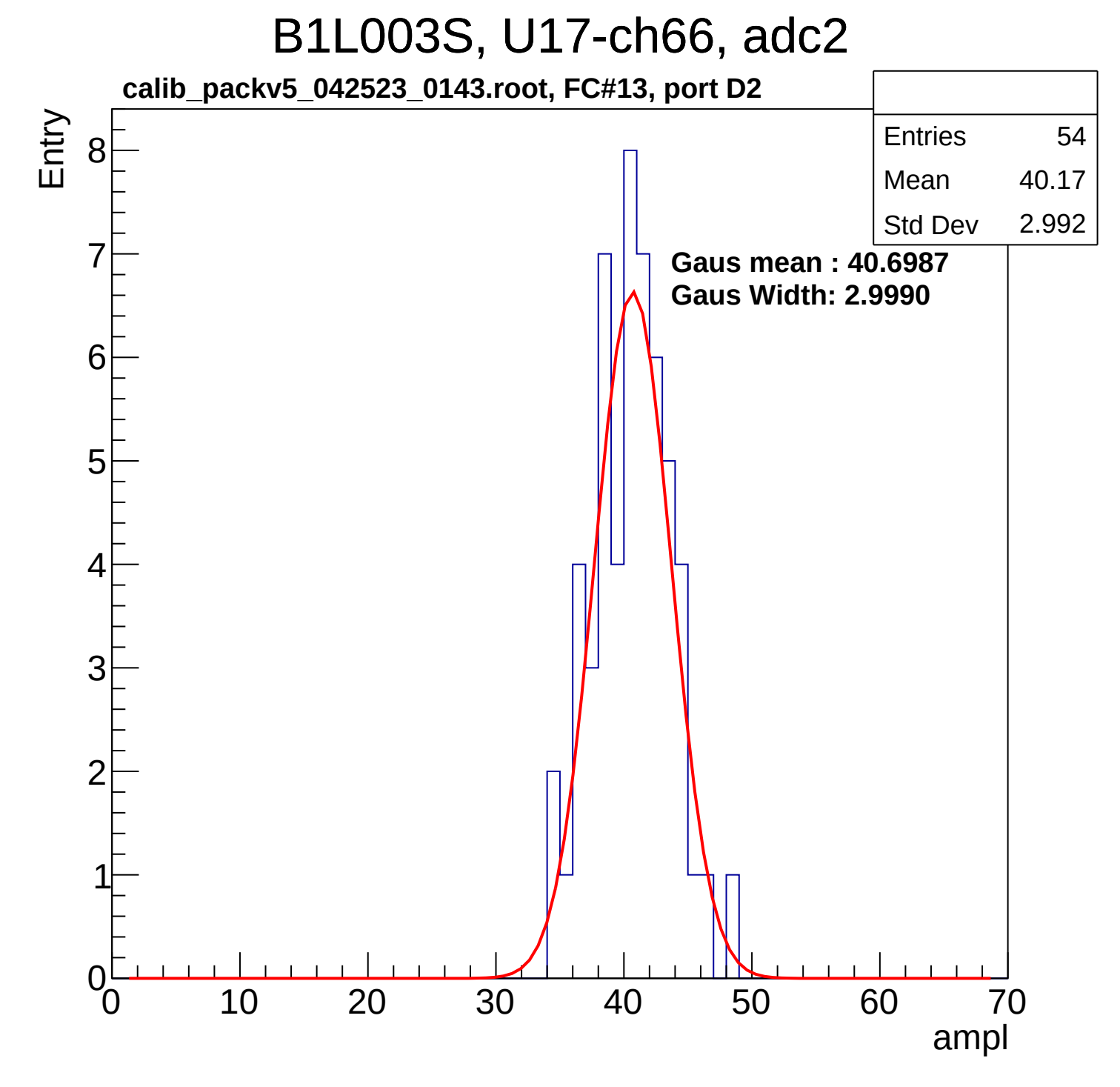
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch66, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 40.17
Std Dev 2.992
Gaus mean : 40.6987
Gaus Width: 2.9990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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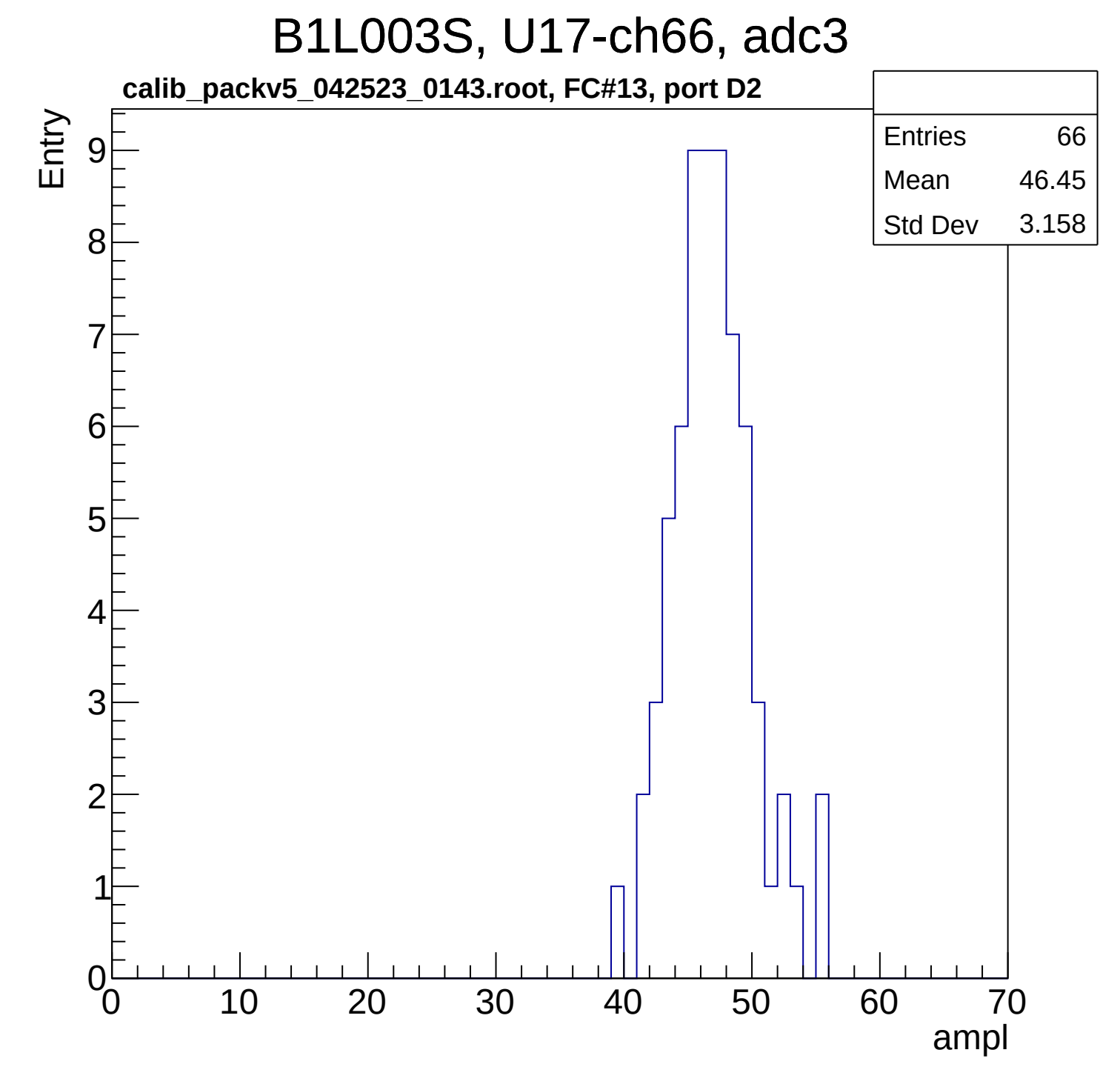
B1L003S, U17-ch66, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 46.45
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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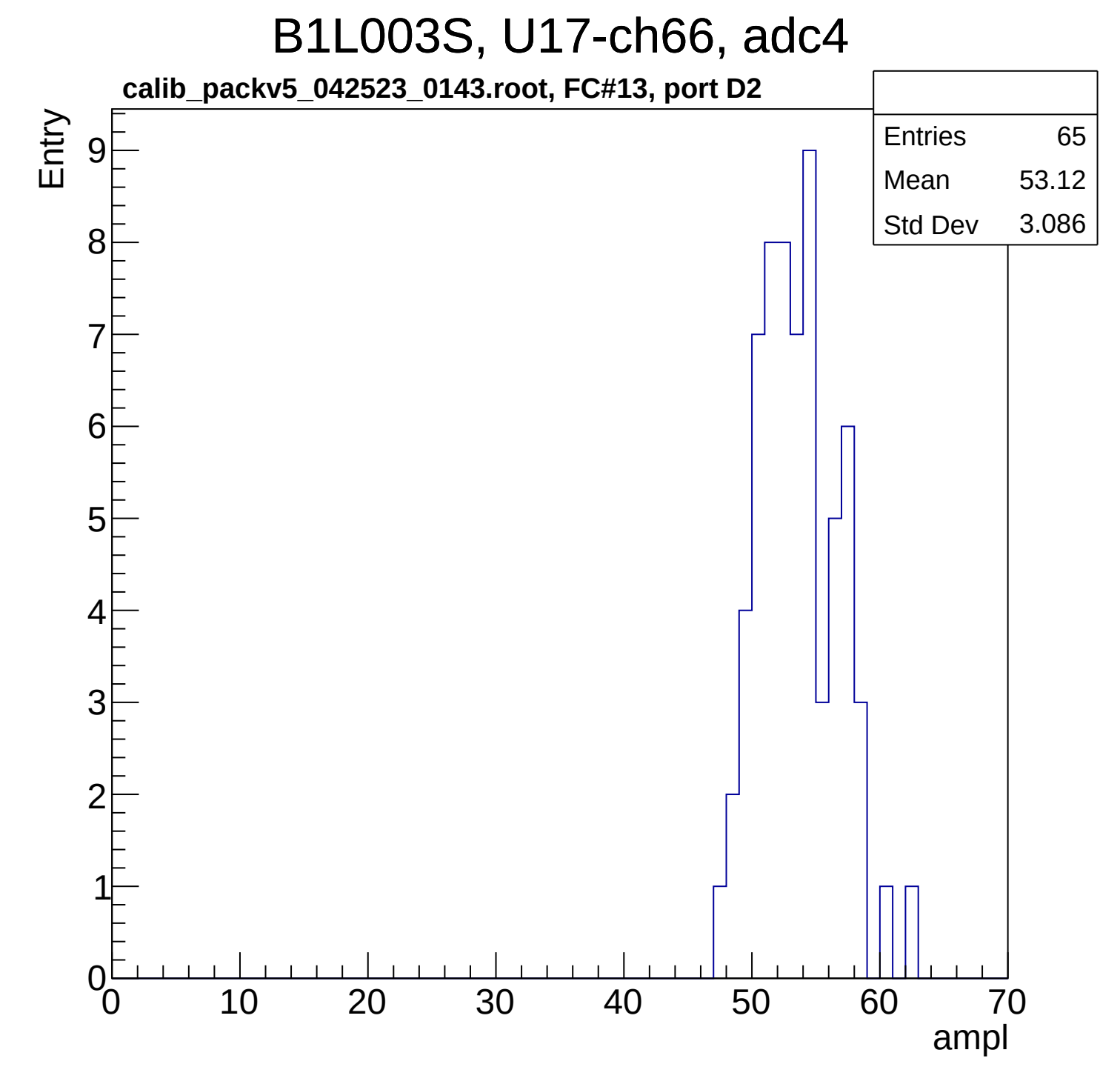
B1L003S, U17-ch66, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 53.12
Std Dev 3.086
Entry
ampl
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2
3
4
5
6
7
8
9
0
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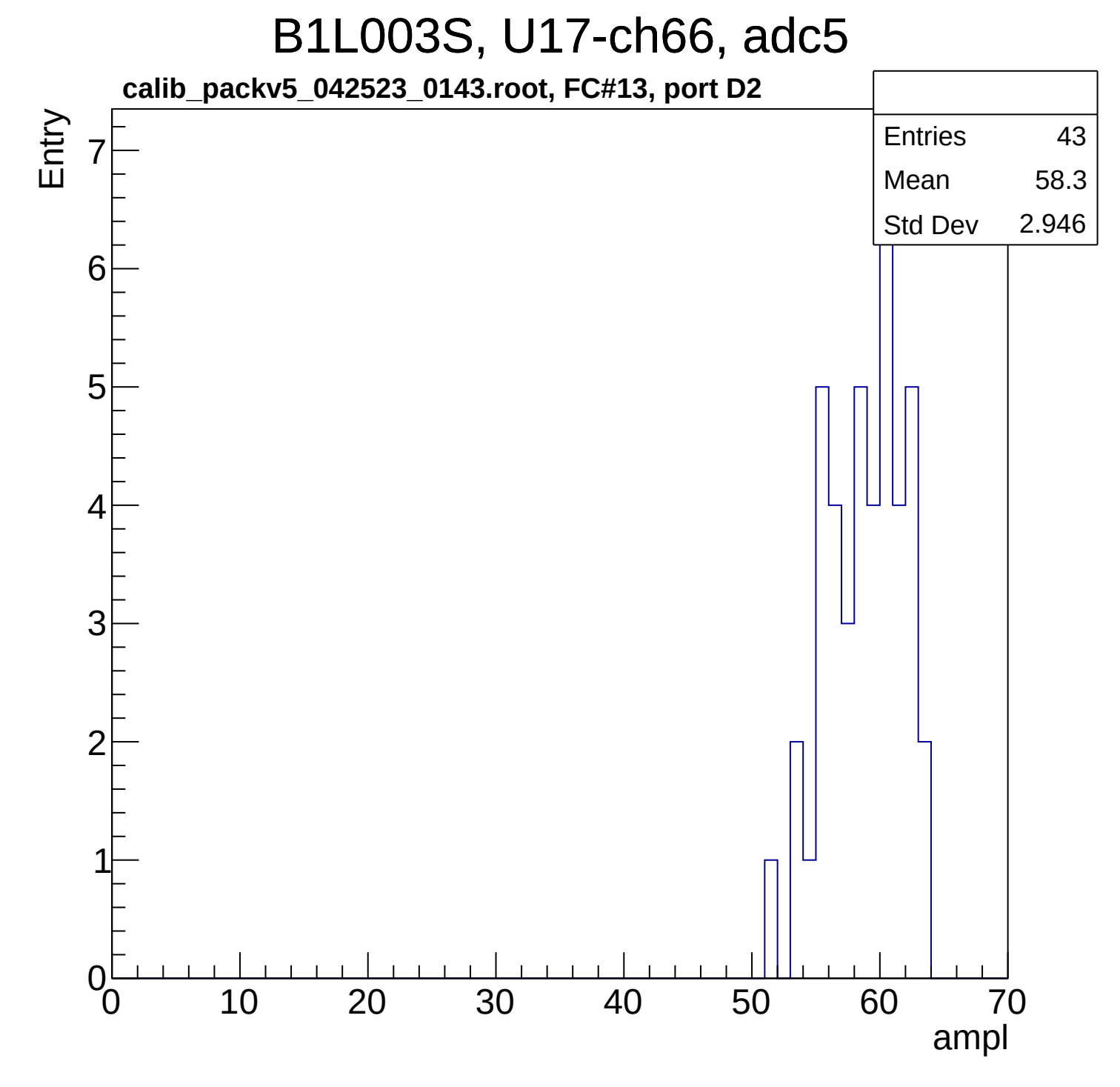
B1L003S, U17-ch66, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 58.3
Std Dev 2.946
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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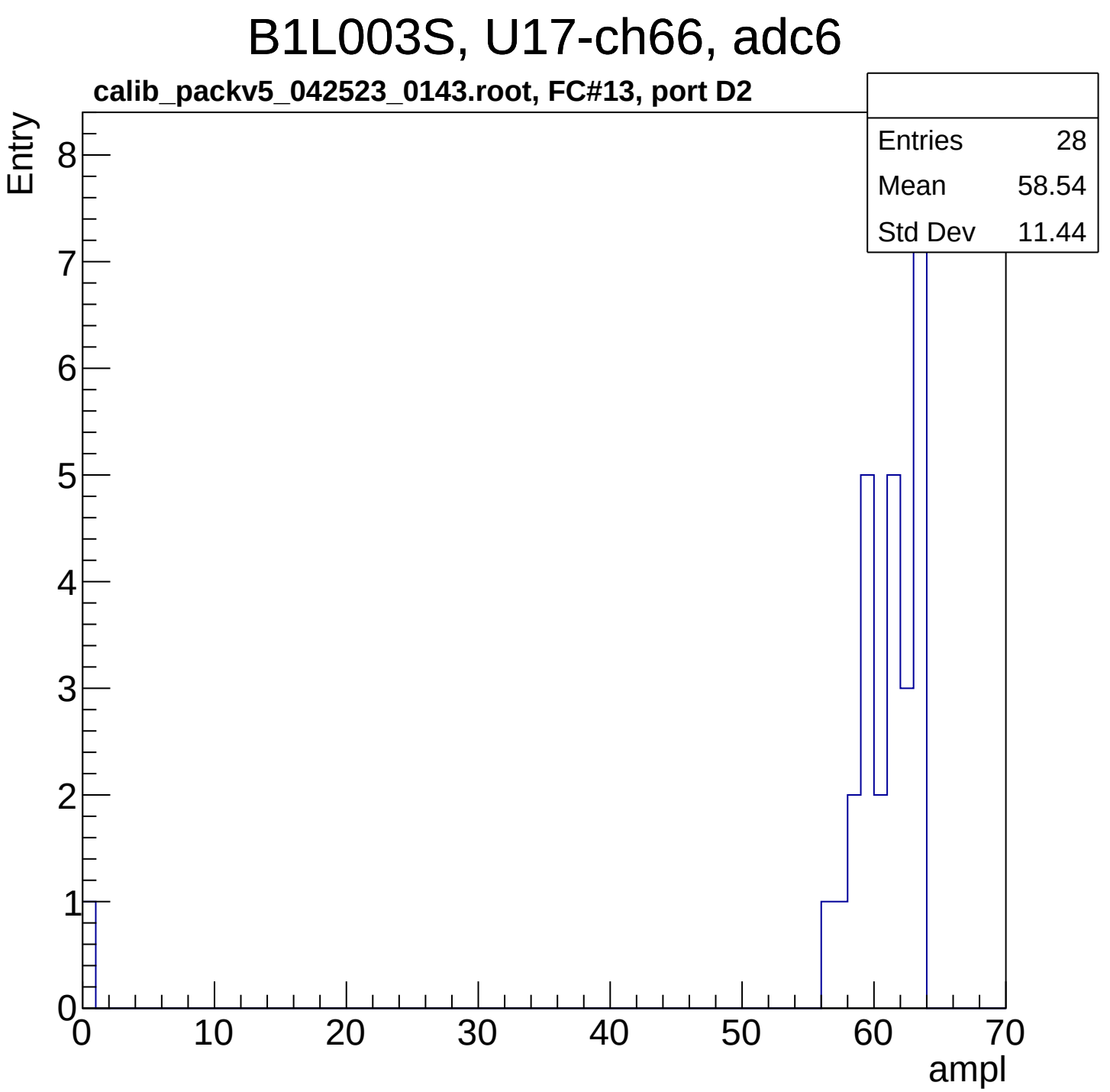
B1L003S, U17-ch66, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 28
Mean 58.54
Std Dev 11.44
Entry
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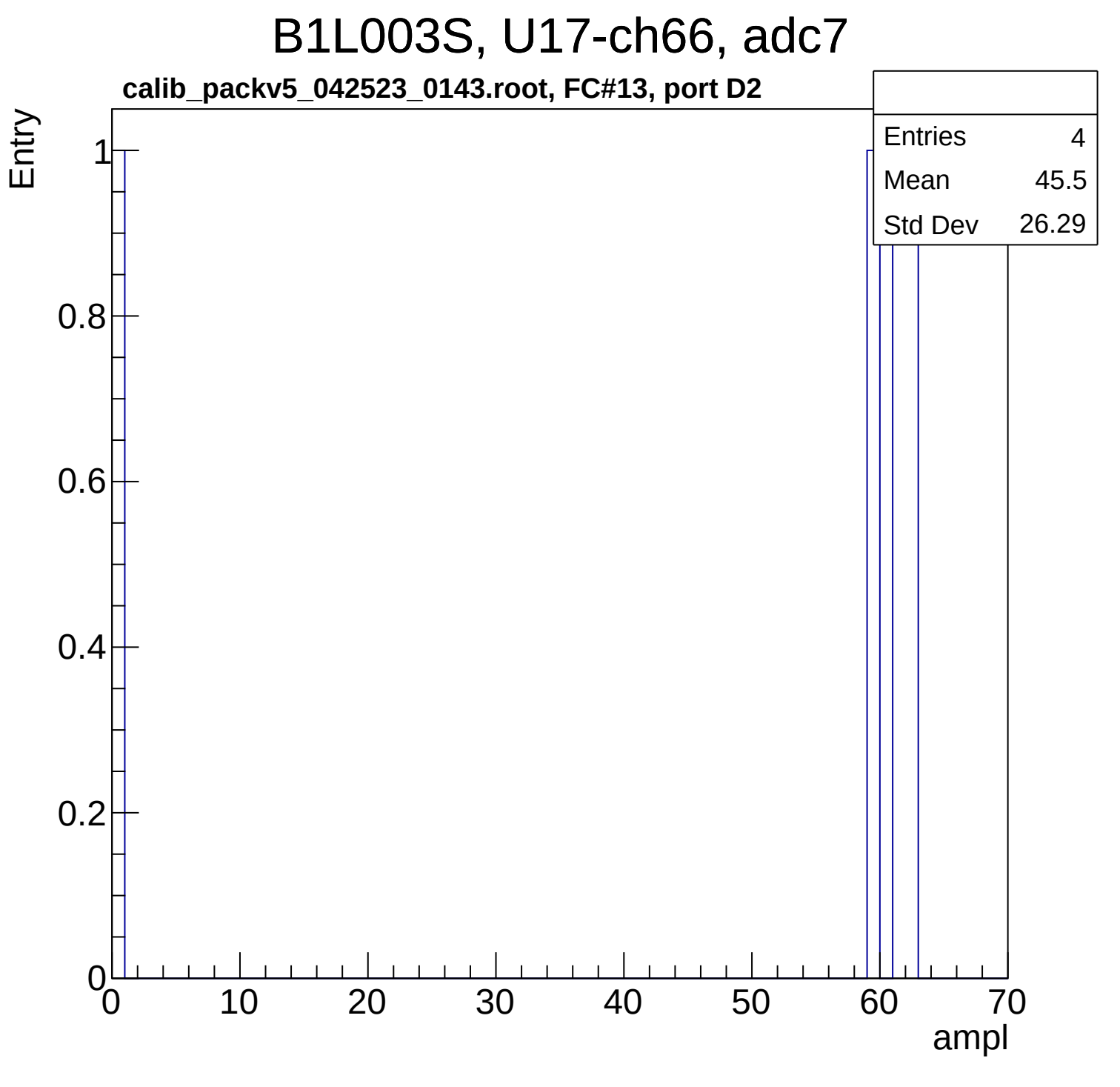
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch66, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 45.5
Std Dev 26.29
Entry
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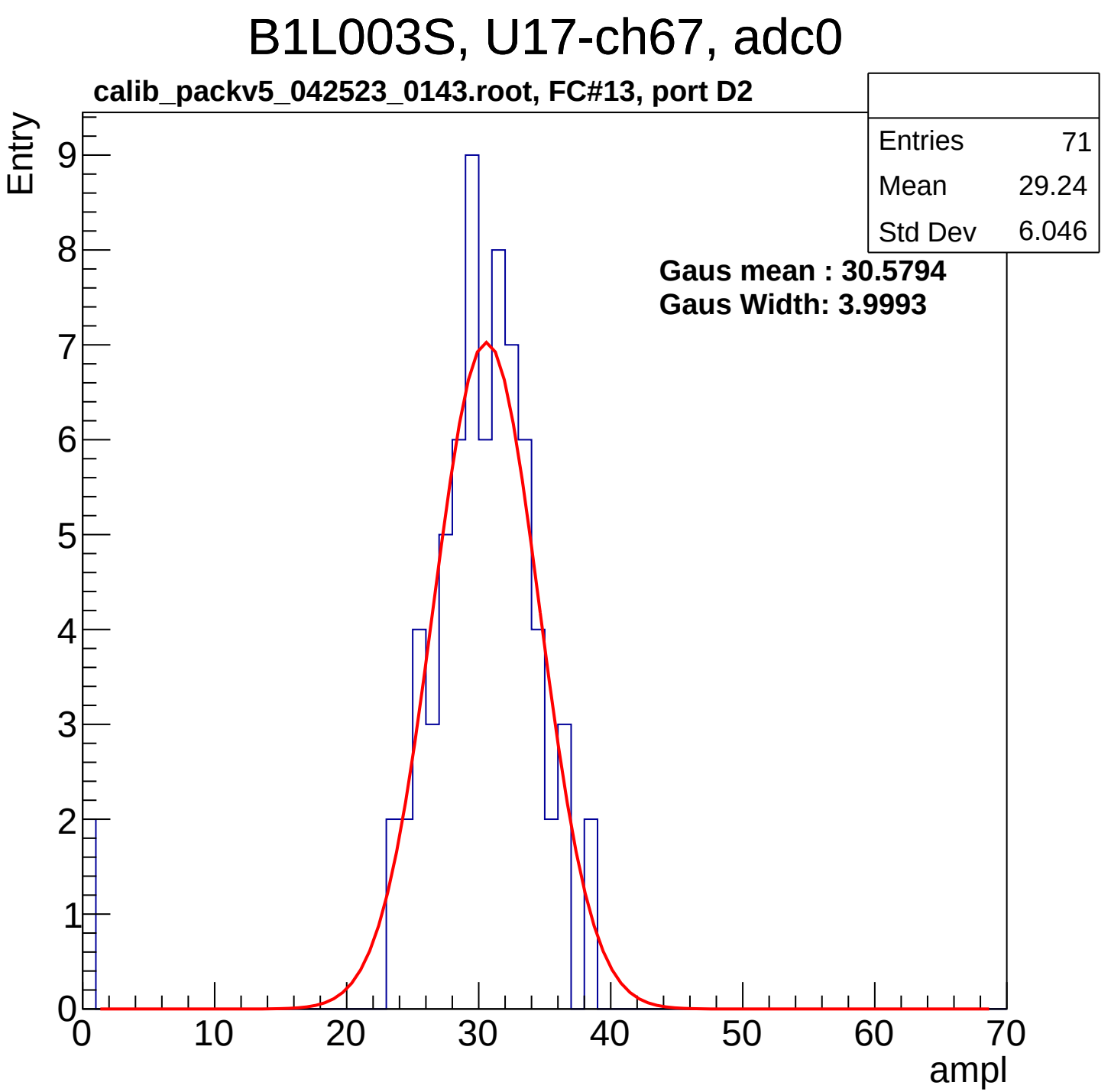
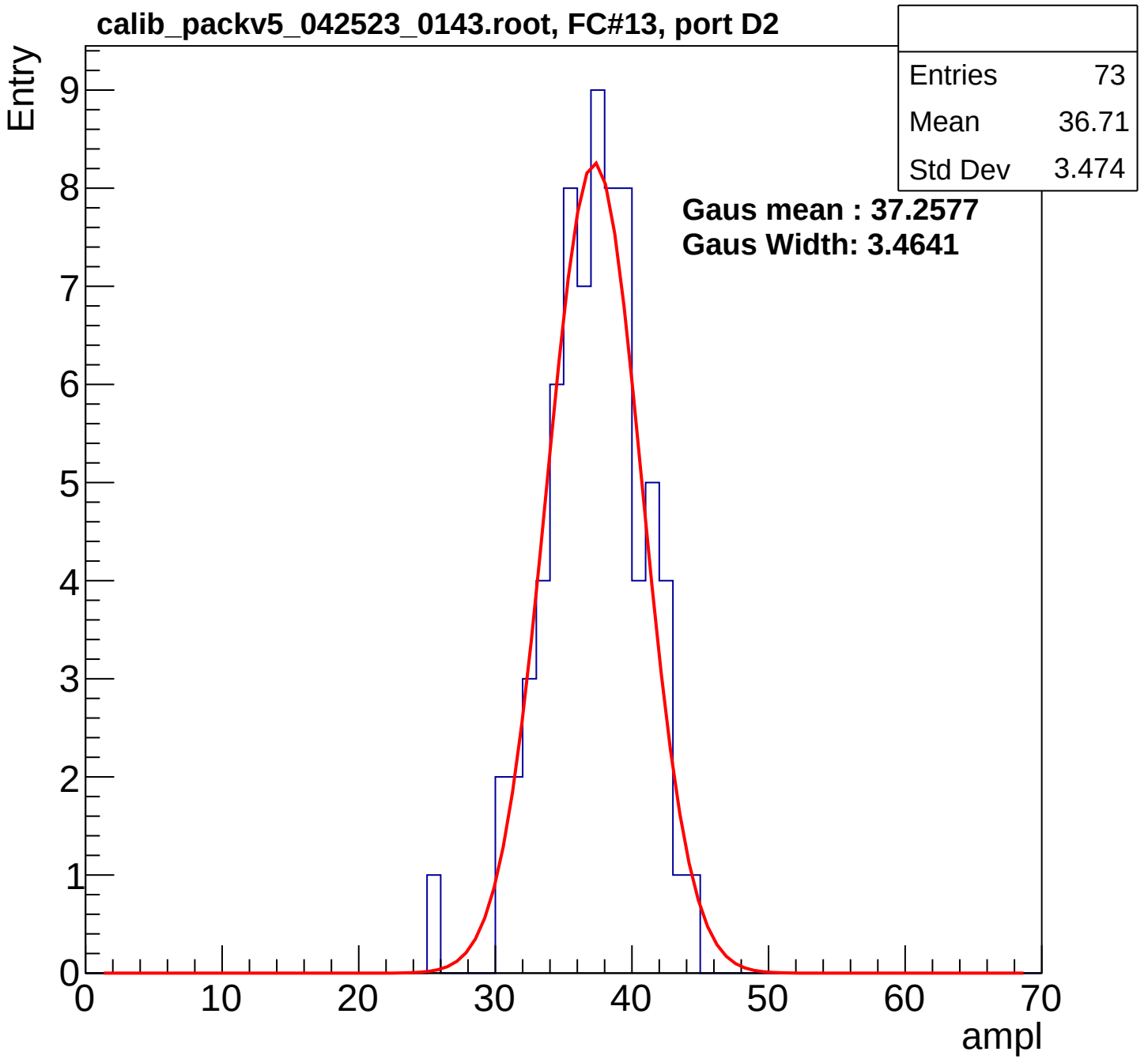
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch67, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.24
Std Dev 6.046
Gaus mean : 30.5794
Gaus Width: 3.9993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch67, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.71
Std Dev 3.474
Gaus mean : 37.2577
Gaus Width: 3.4641
Entry
ampl
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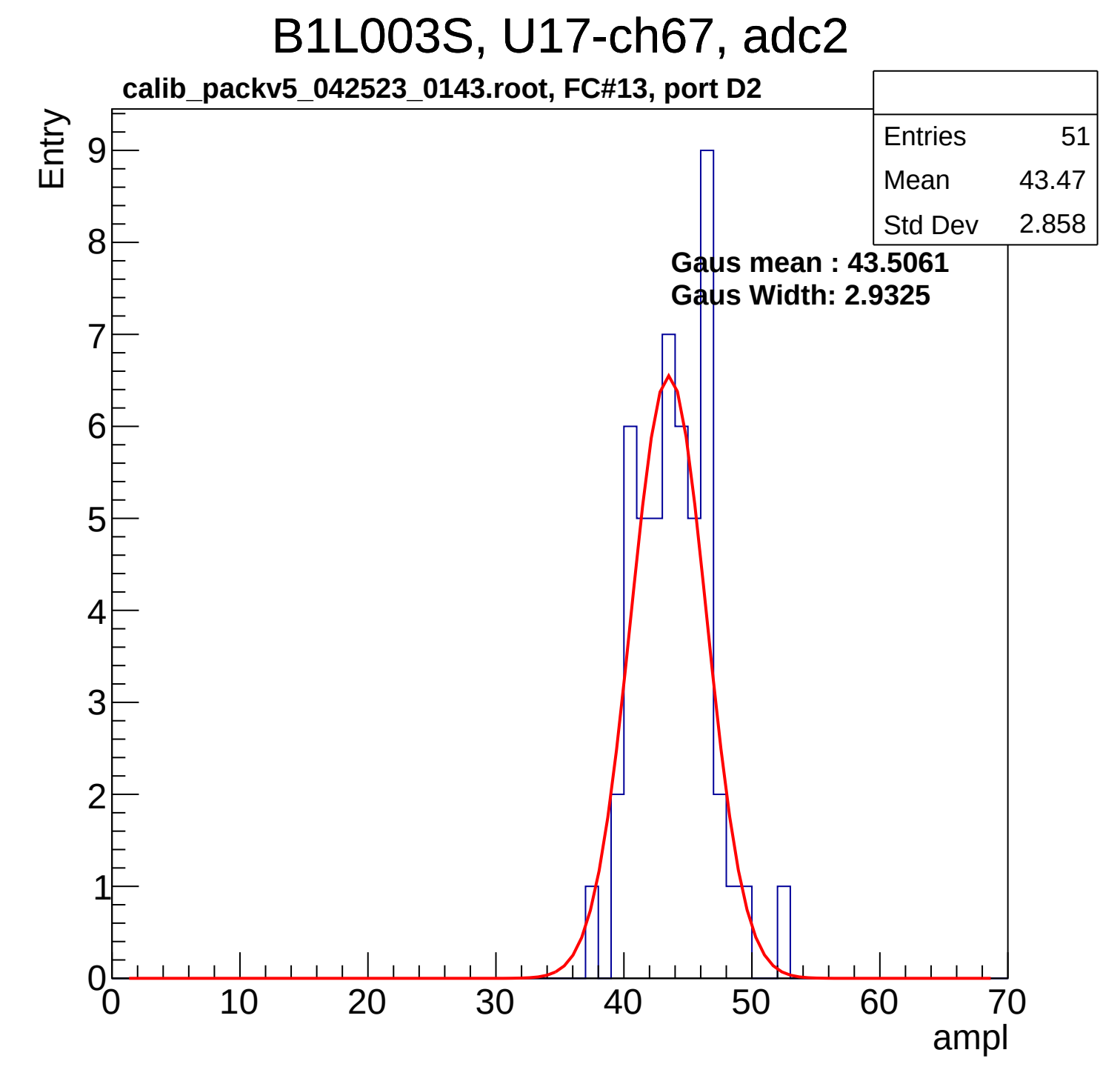
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch67, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 43.47
Std Dev 2.858
Gaus mean : 43.5061
Gaus Width: 2.9325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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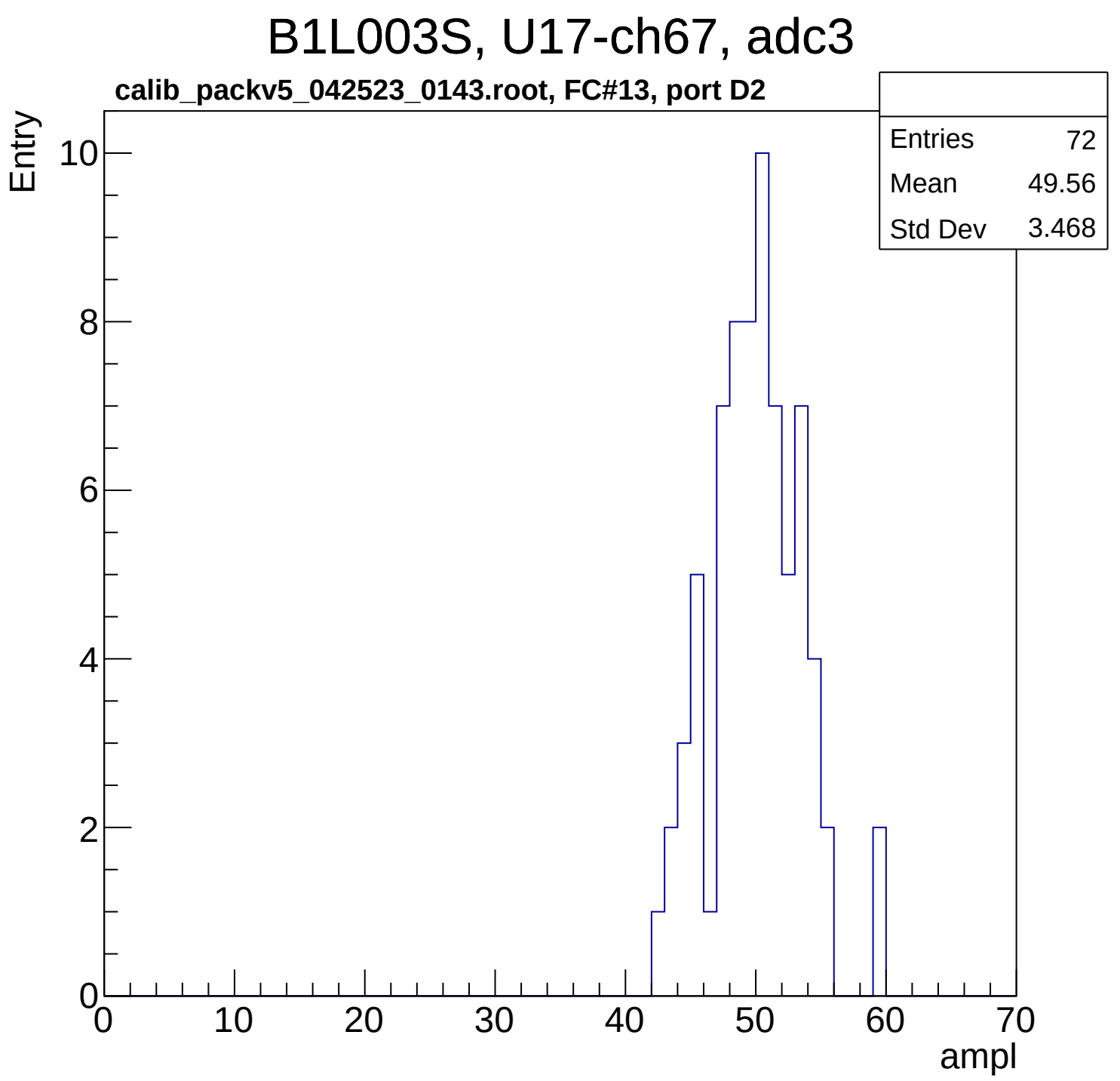
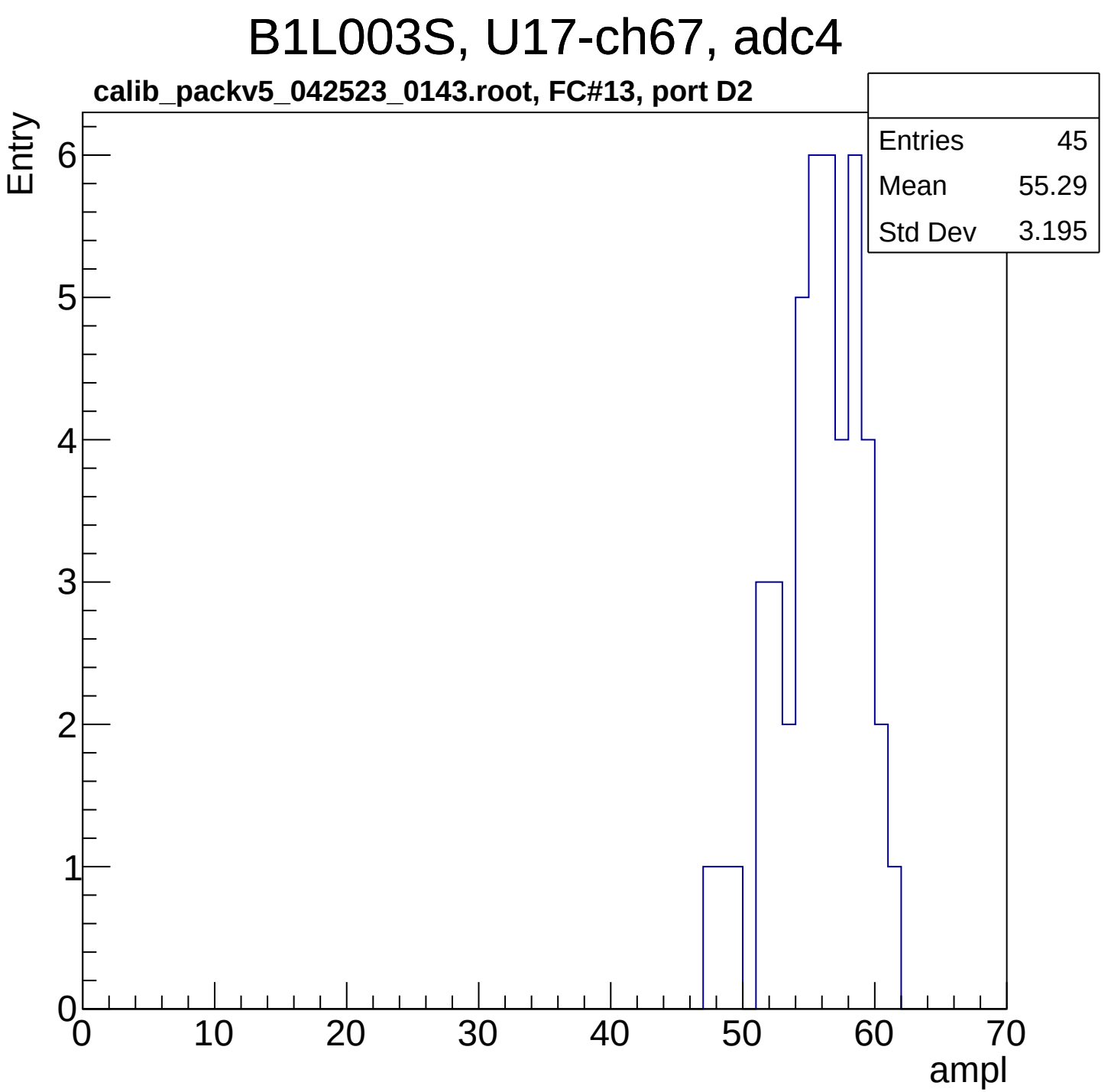
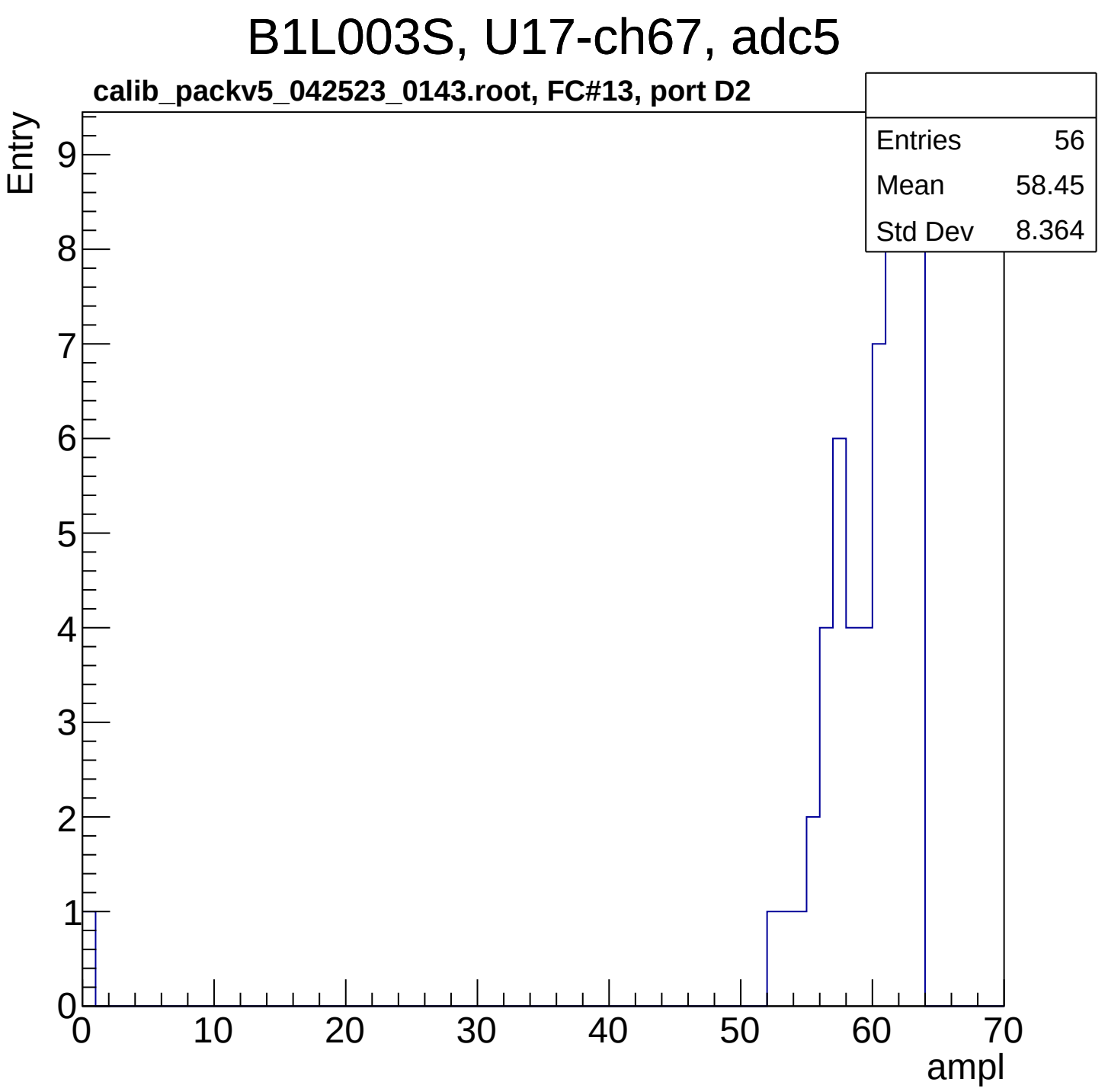
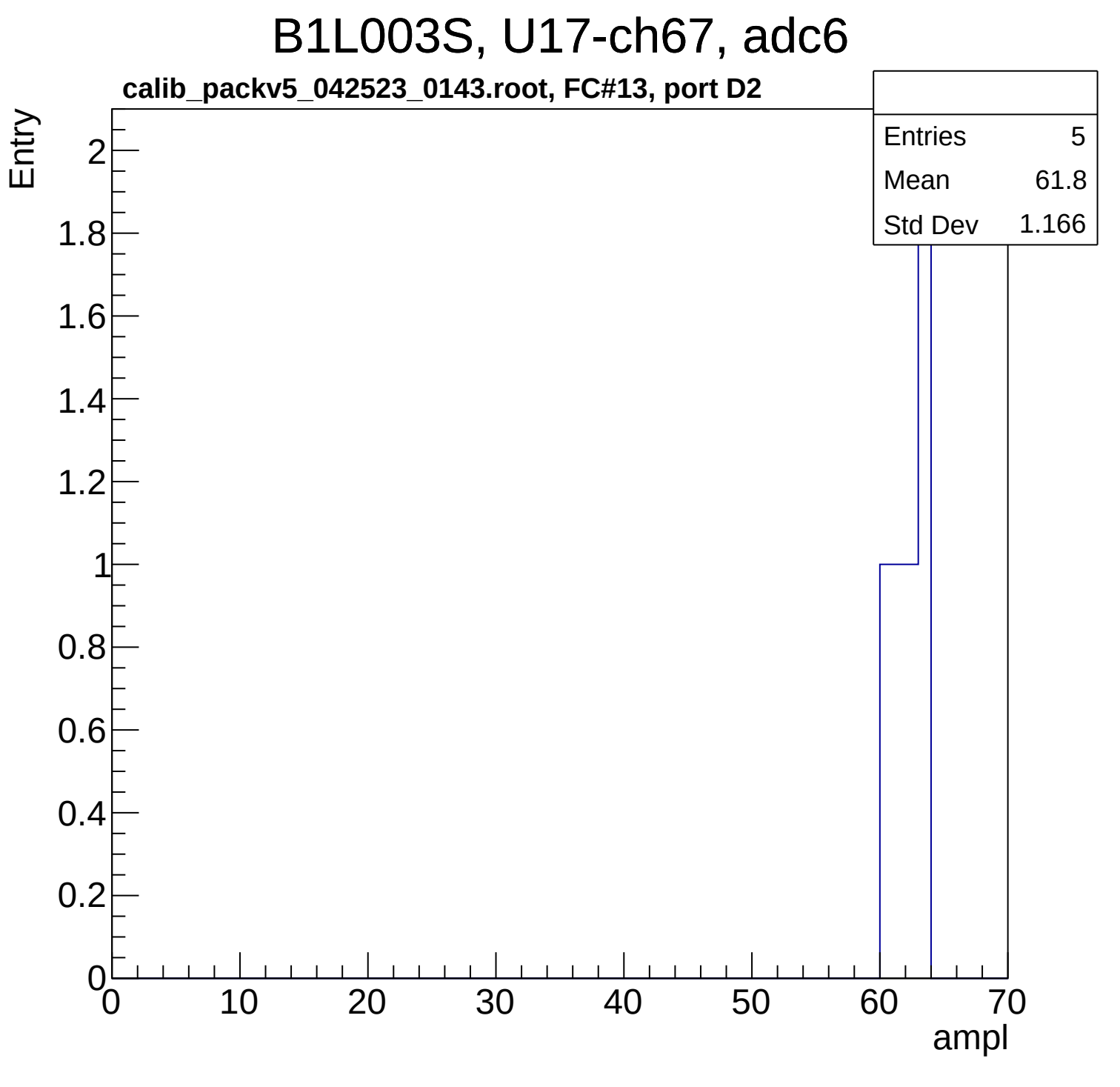
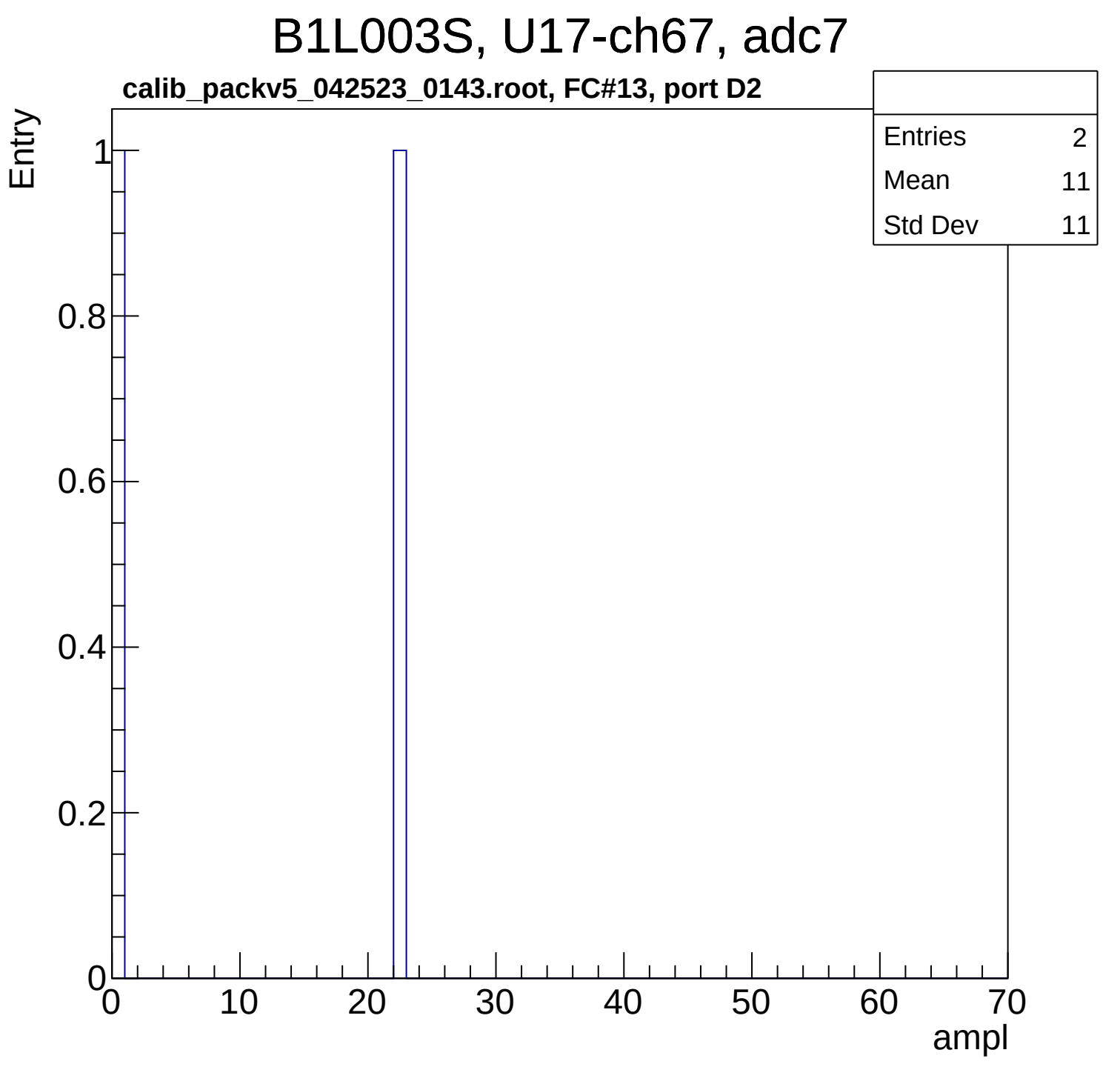

B1L003S, U17-ch67, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.56
Std Dev 3.468
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch67, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 55.29
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch67, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.45
Std Dev 8.364
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch67, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U17-ch67, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
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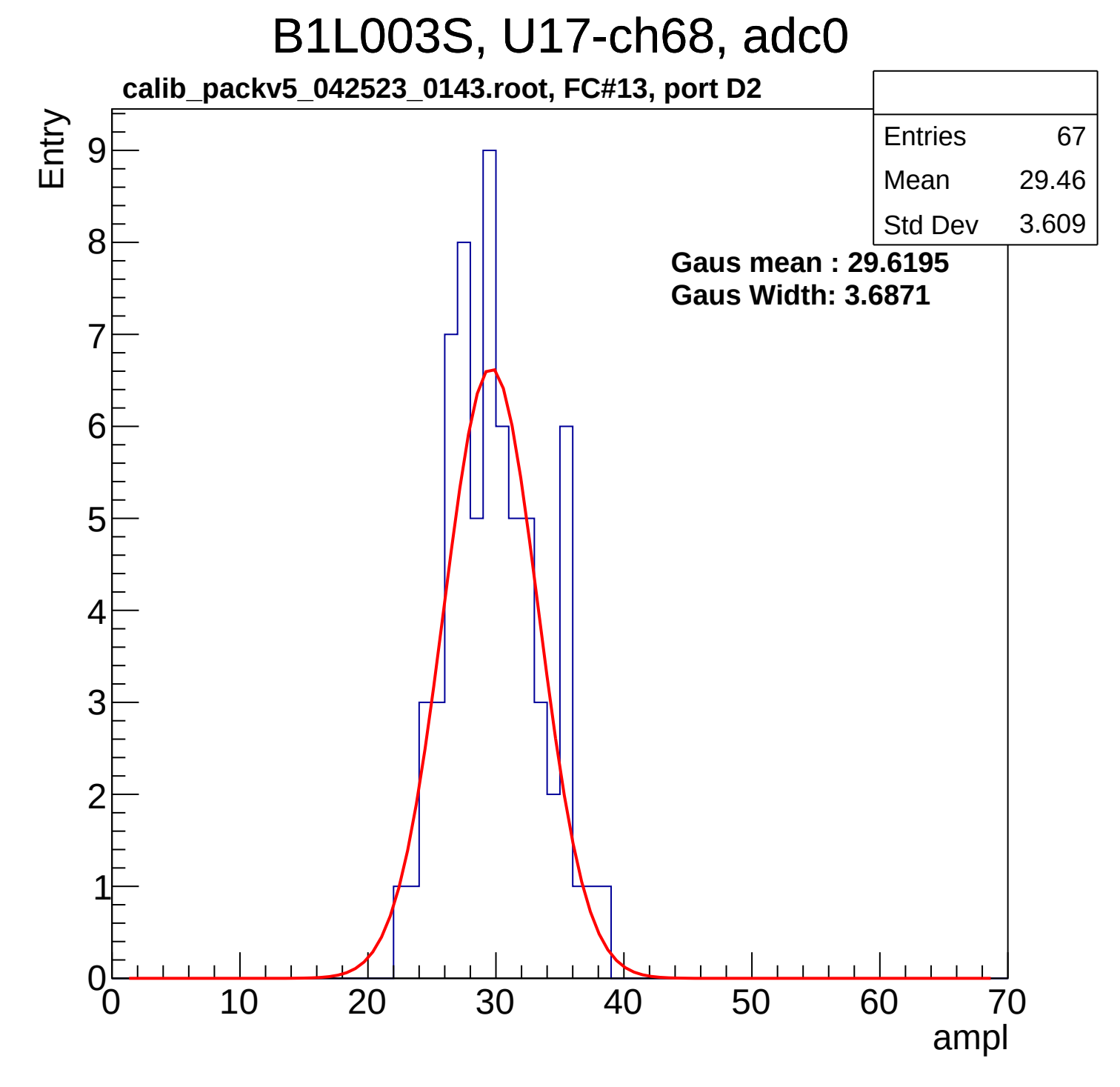
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch68, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.46
Std Dev 3.609
Gaus mean : 29.6195
Gaus Width: 3.6871
Entry
ampl
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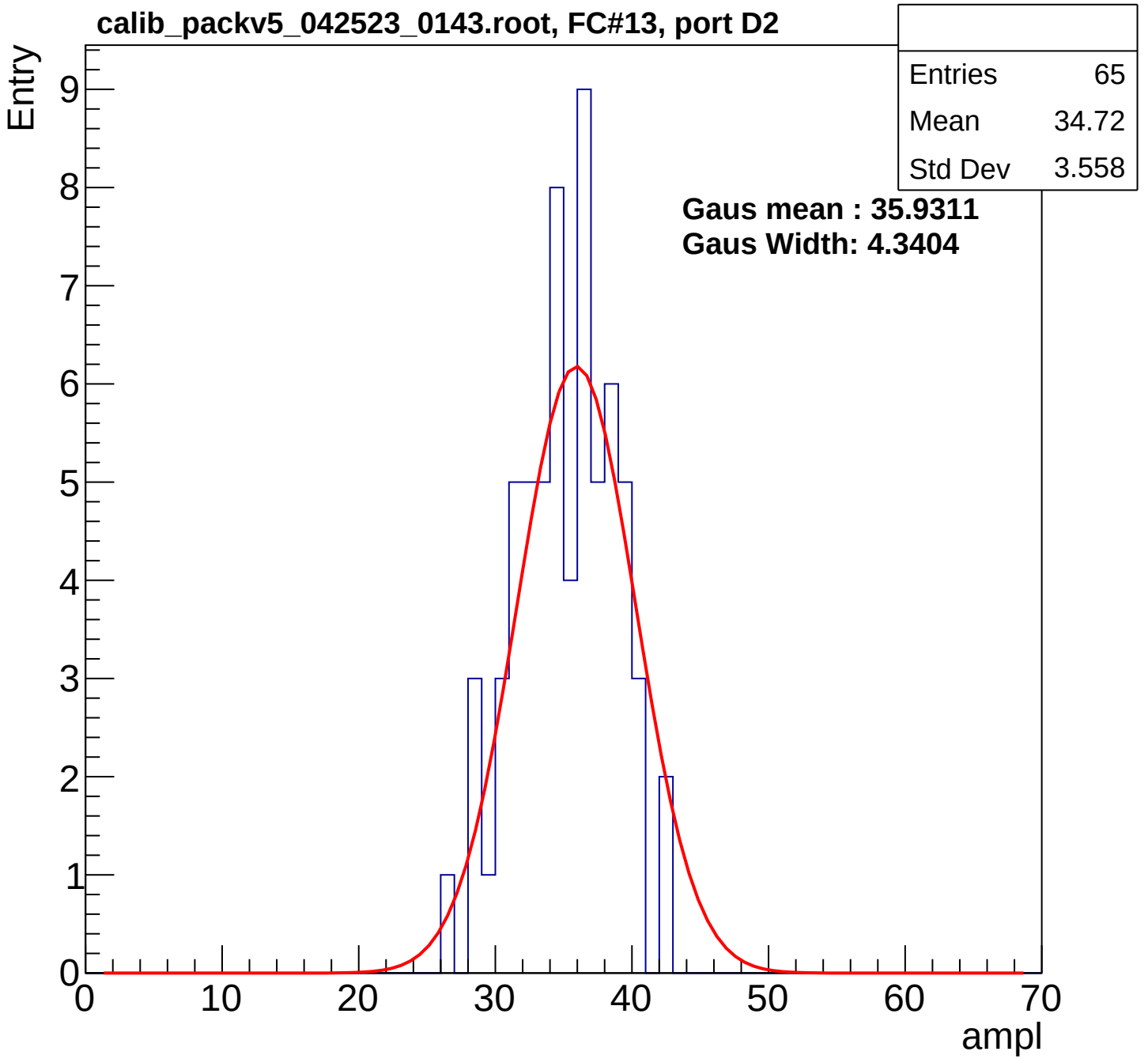
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch68, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 34.72
Std Dev 3.558
Gaus mean : 35.9311
Gaus Width: 4.3404
Entry
ampl
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2
3
4
5
6
7
8
9
0
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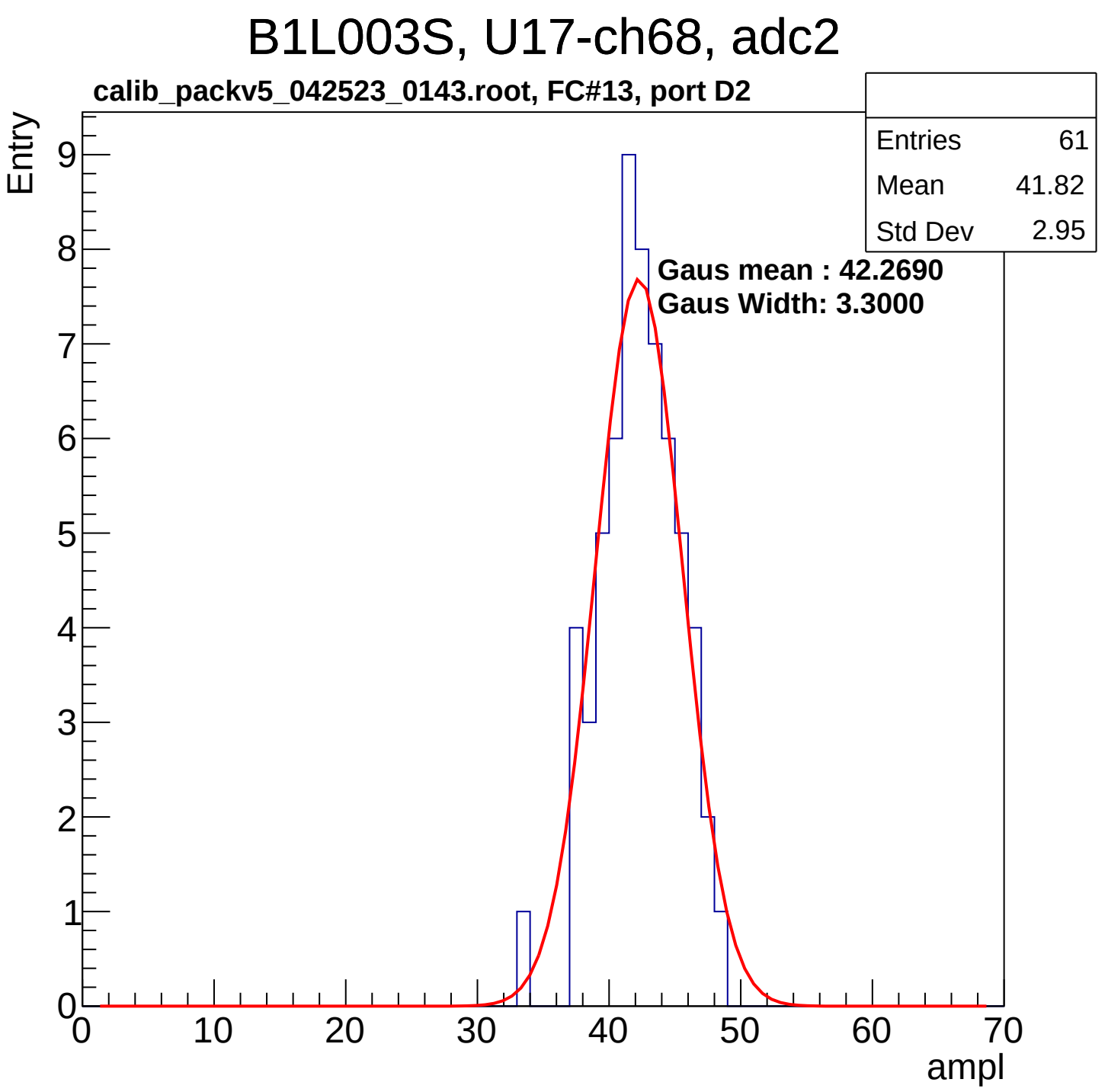
20
30
40
50
60
70


B1L003S, U17-ch68, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 41.82
Std Dev 2.95
Gaus mean : 42.2690
Gaus Width: 3.3000
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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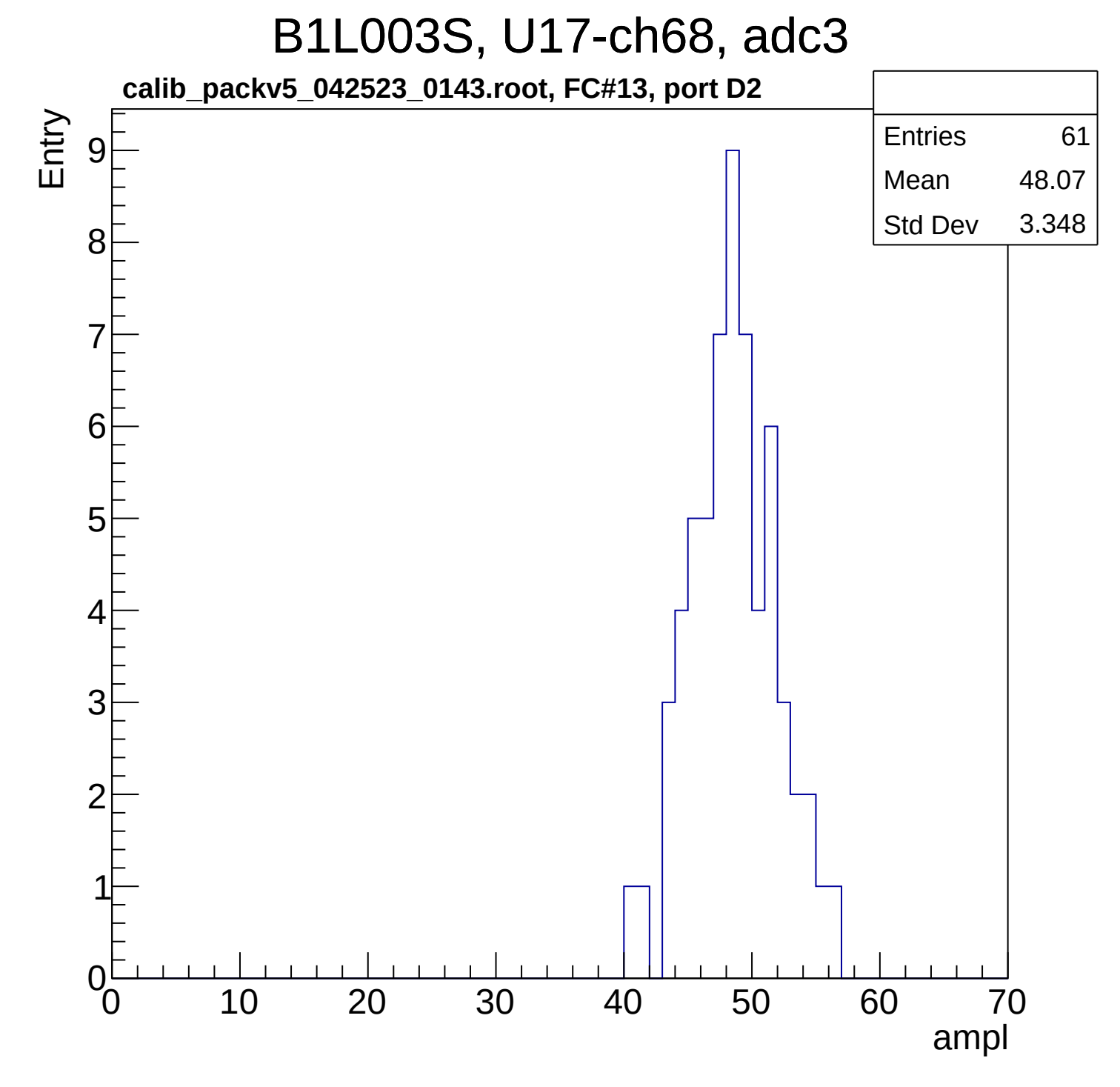
B1L003S, U17-ch68, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.07
Std Dev 3.348
Entry
ampl
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3
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7
8
9
0
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60
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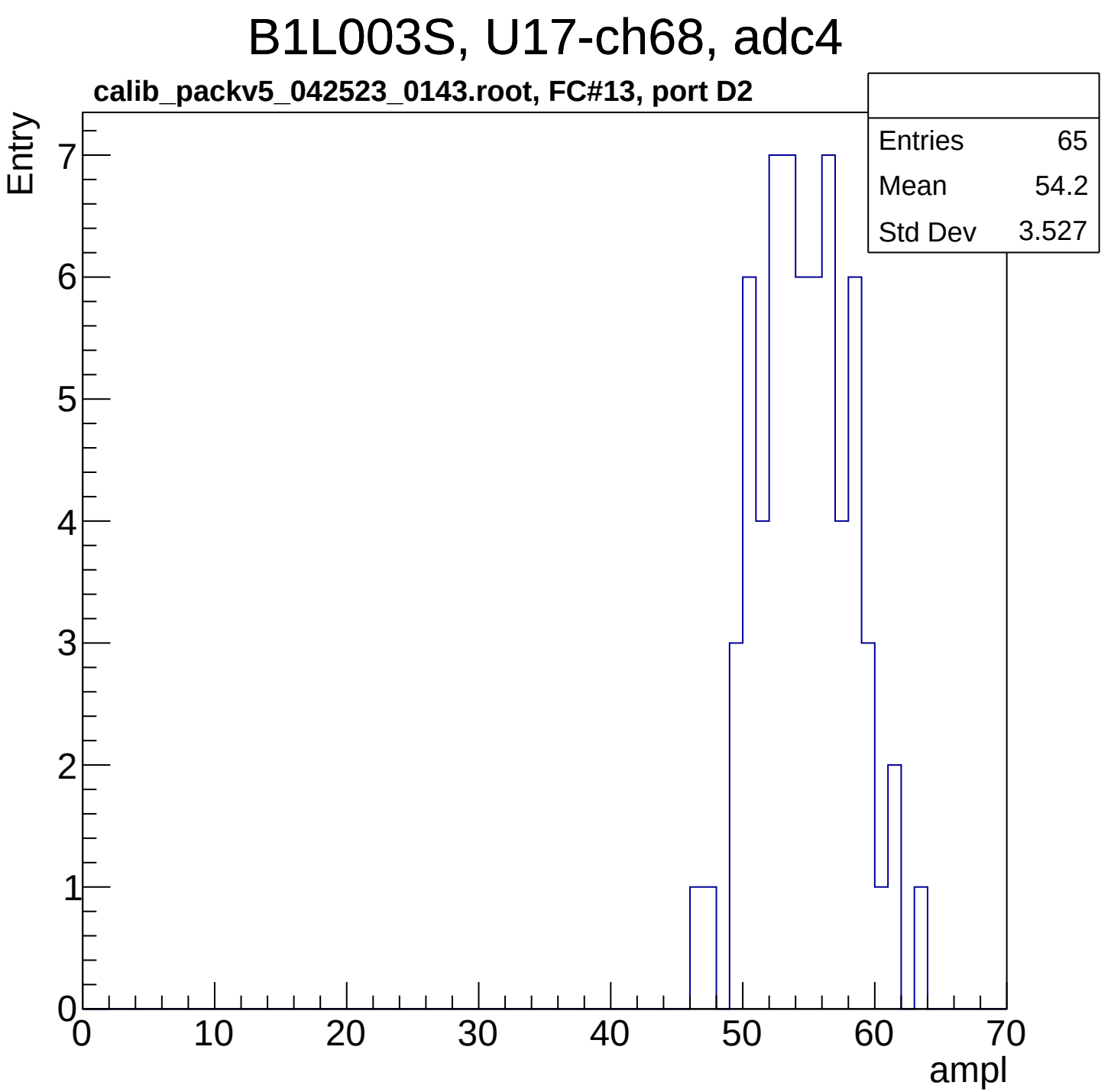
B1L003S, U17-ch68, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 54.2
Std Dev 3.527
Entry
ampl
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1
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3
4
5
6
7
0
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20
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40
50
60
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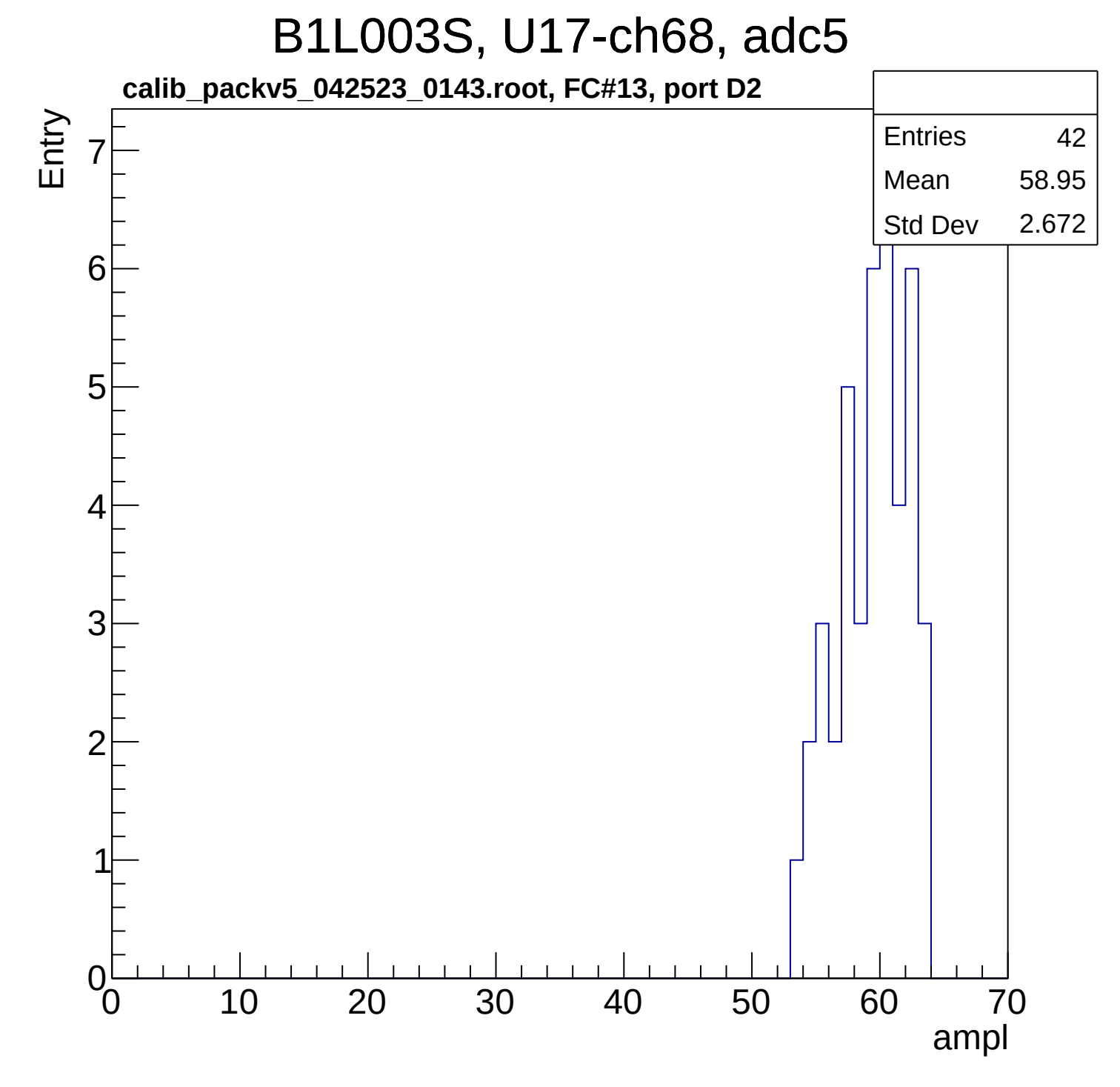
B1L003S, U17-ch68, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 58.95
Std Dev 2.672
Entry
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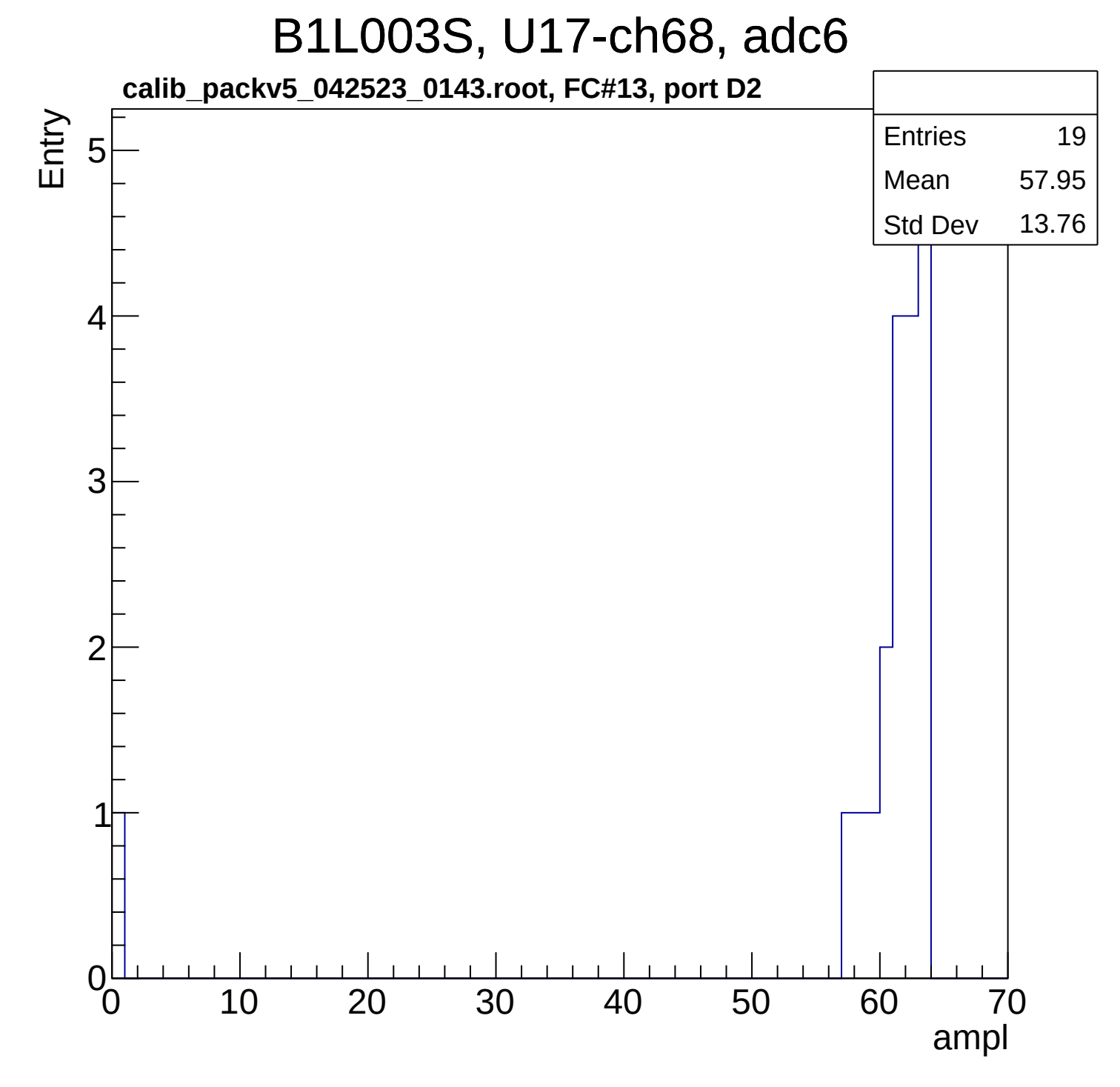
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch68, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 57.95
Std Dev 13.76
Entry
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3
4
5
0
10
20
30
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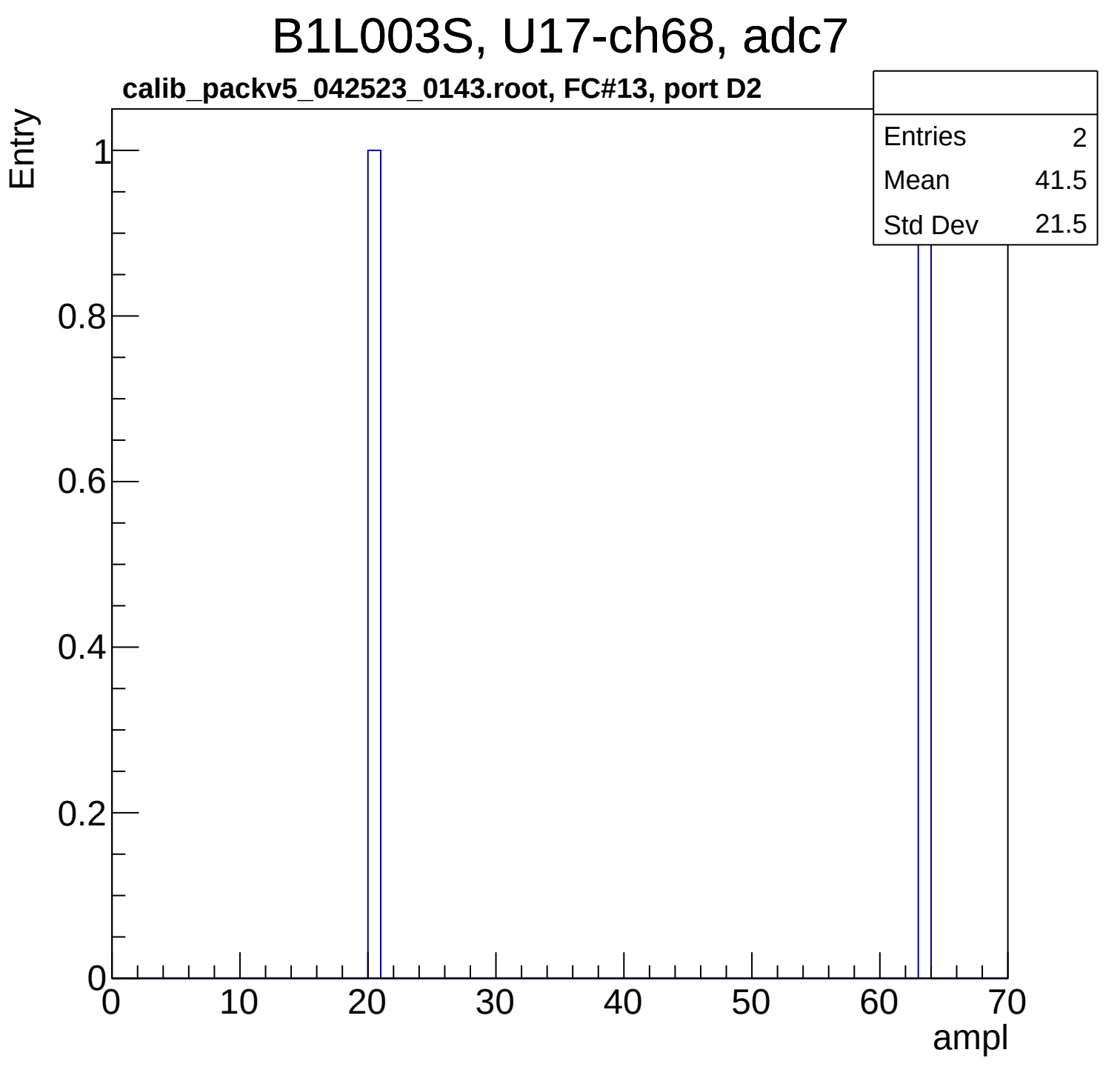
50
60
70
ampl


B1L003S, U17-ch68, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 41.5
Std Dev 21.5
Entry
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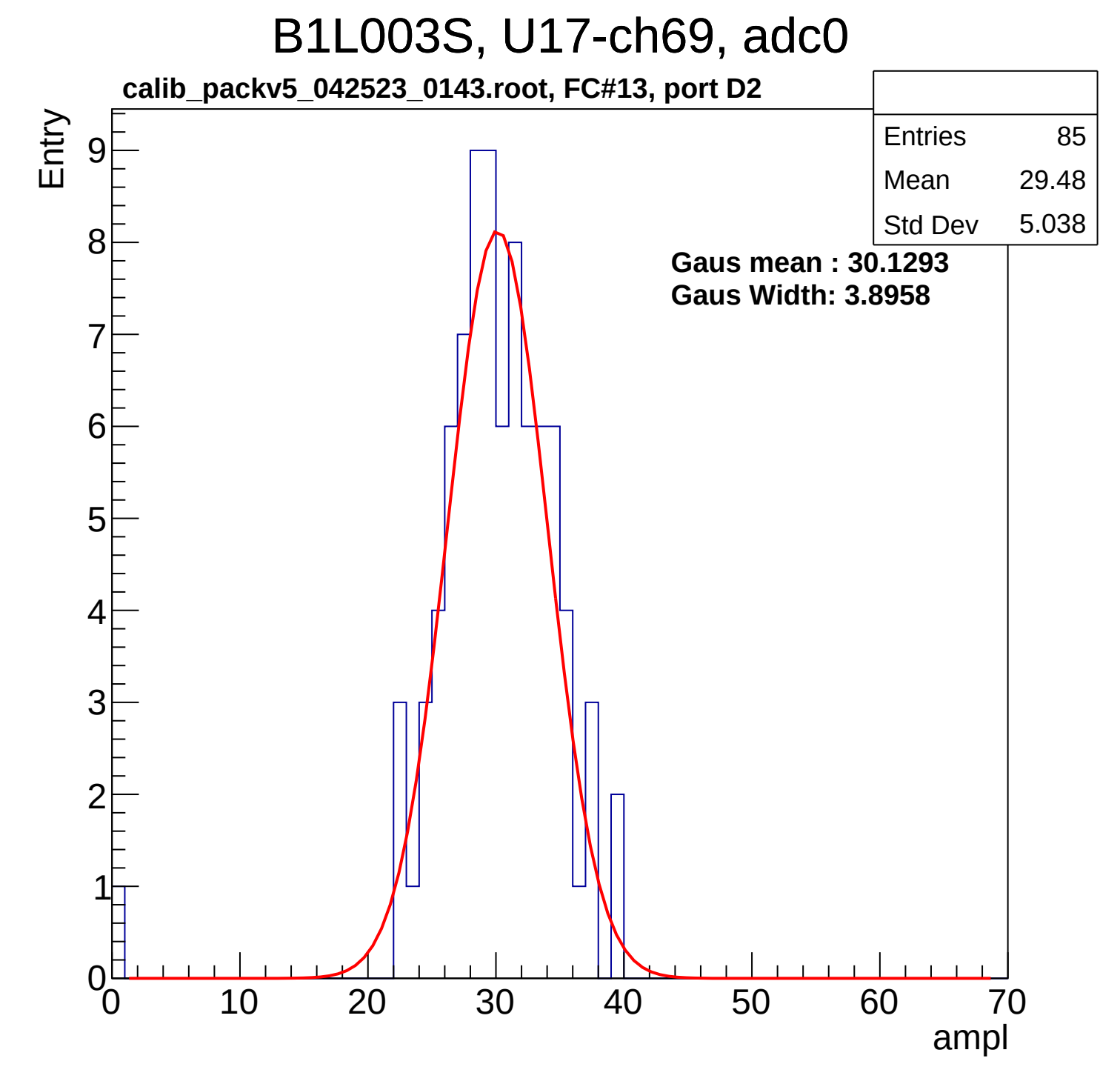
50
60
70
ampl


B1L003S, U17-ch69, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 29.48
Std Dev 5.038
Gaus mean : 30.1293
Gaus Width: 3.8958
Entry
ampl
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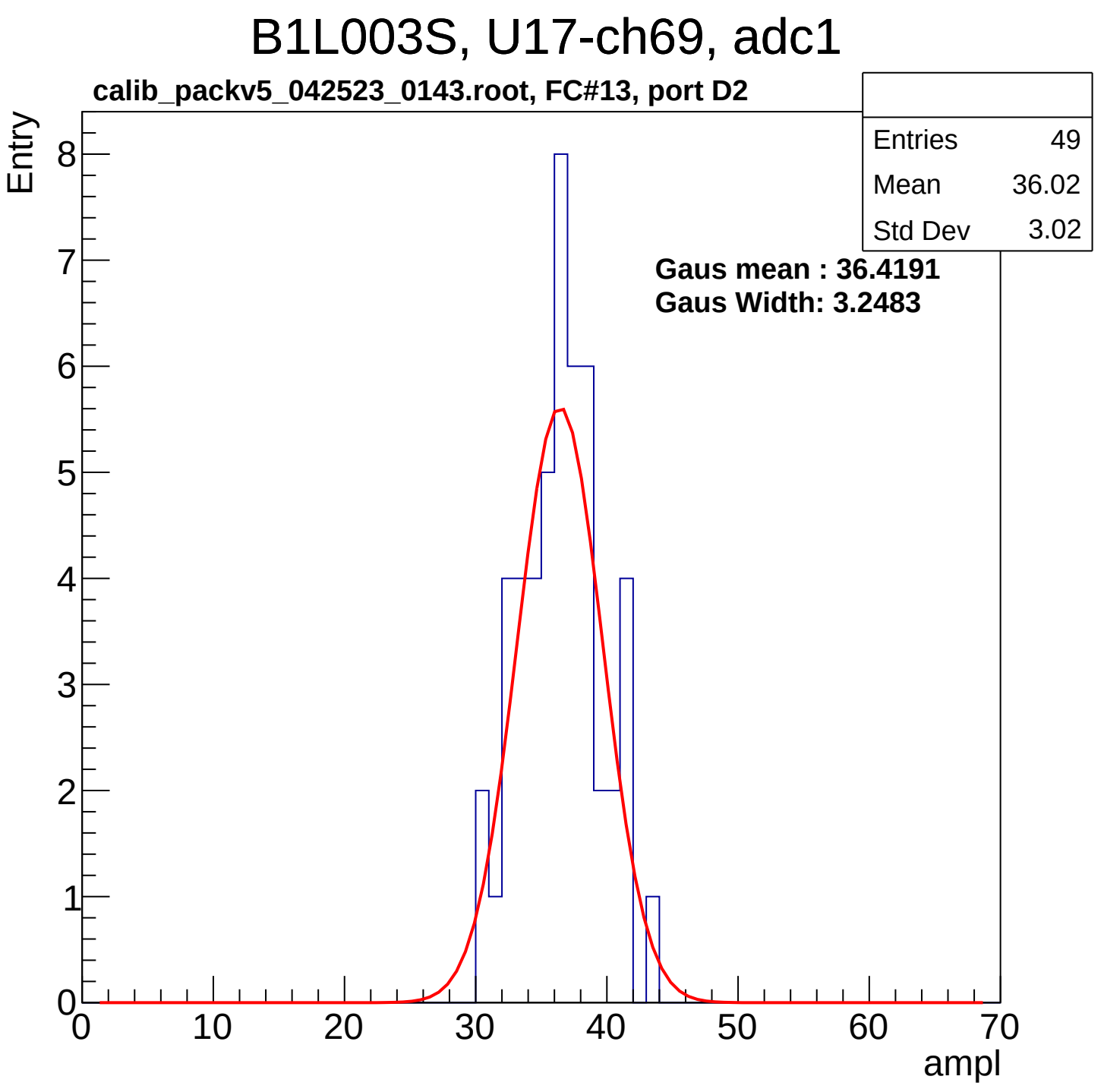
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch69, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 36.02
Std Dev 3.02
Gaus mean : 36.4191
Gaus Width: 3.2483
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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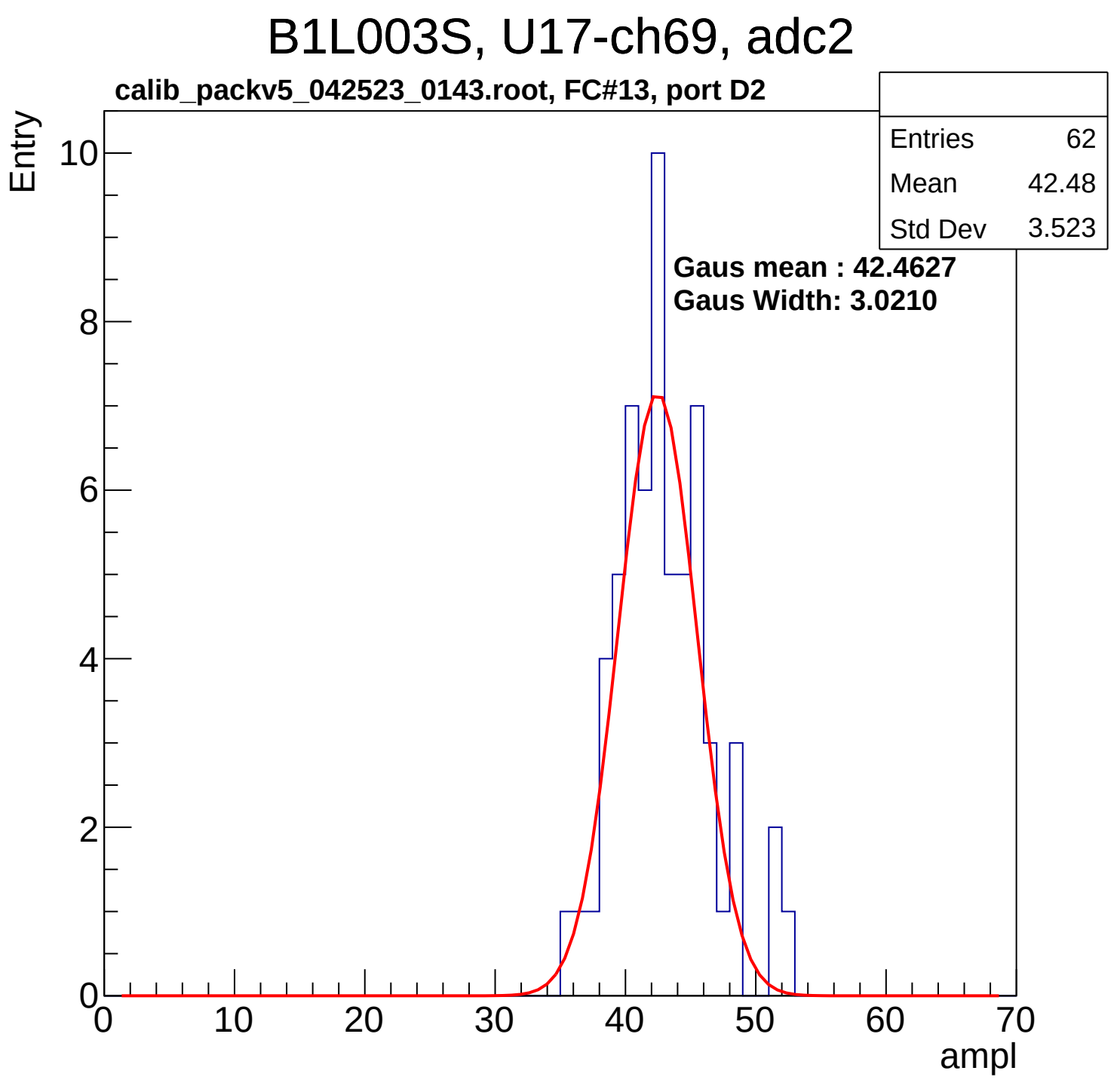
B1L003S, U17-ch69, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.48
Std Dev 3.523
Gaus mean : 42.4627
Gaus Width: 3.0210
Entry
ampl
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4
6
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10
0
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20
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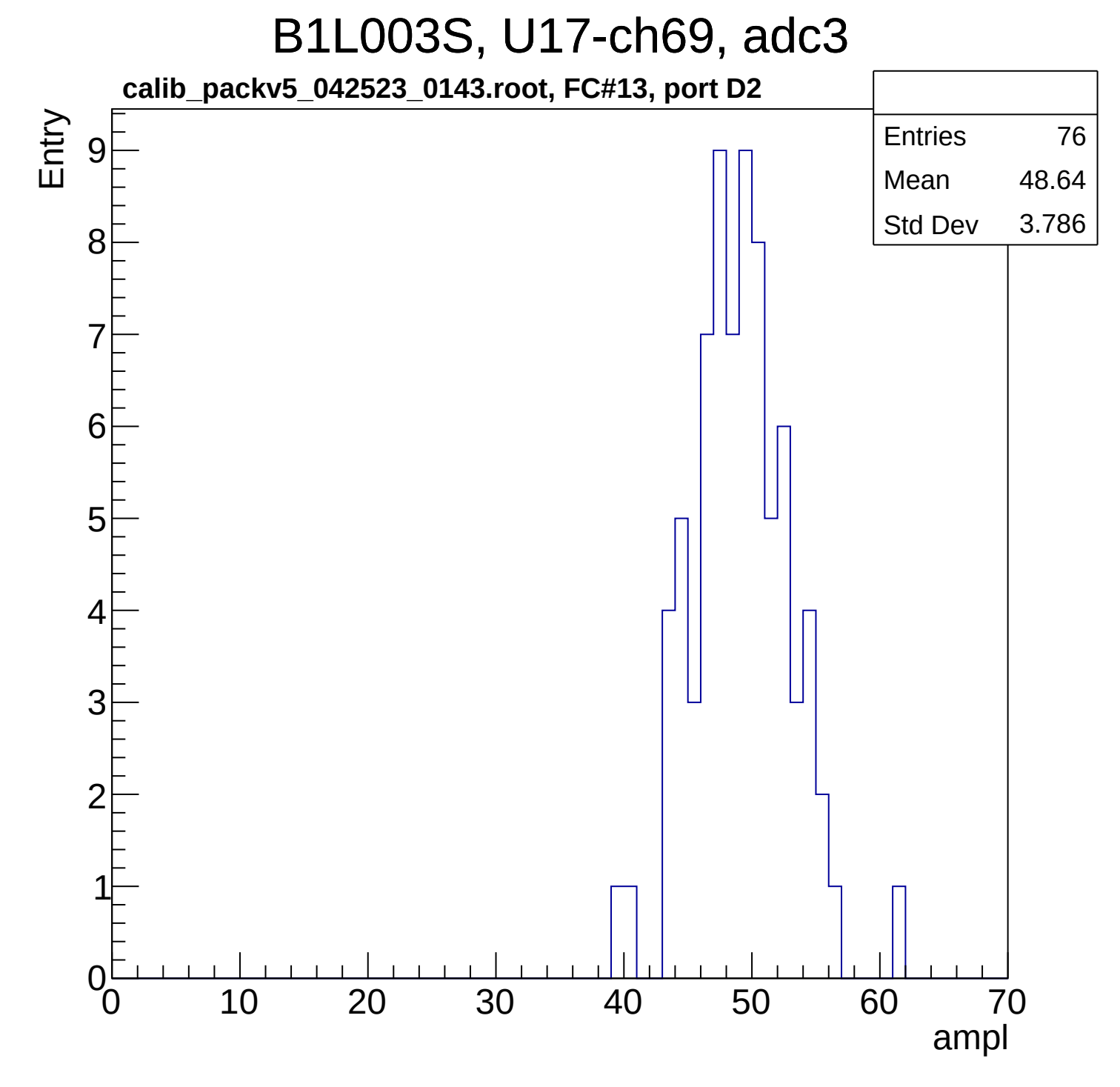
B1L003S, U17-ch69, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 48.64
Std Dev 3.786
Entry
ampl
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7
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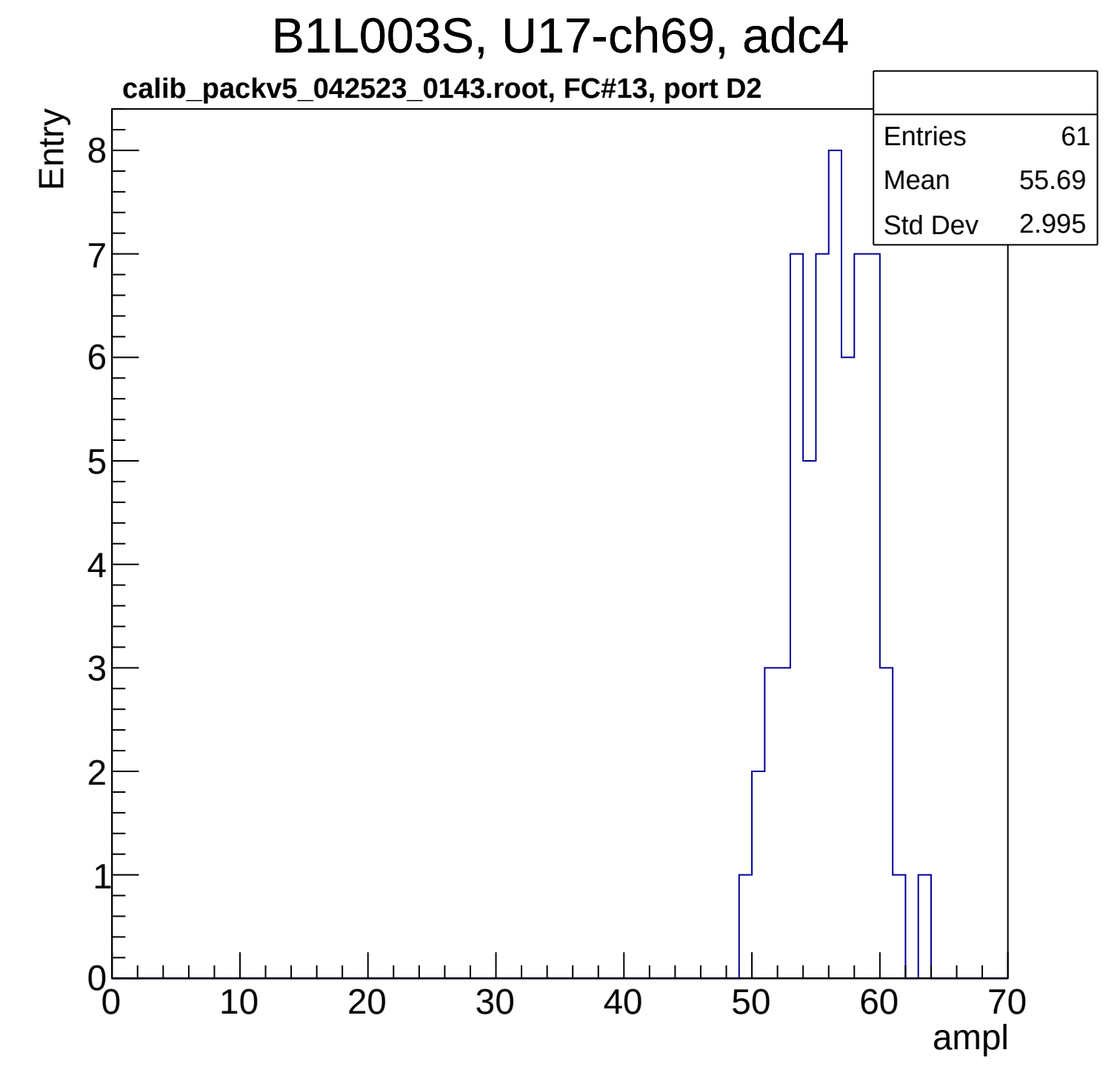
B1L003S, U17-ch69, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 55.69
Std Dev 2.995
Entry
ampl
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4
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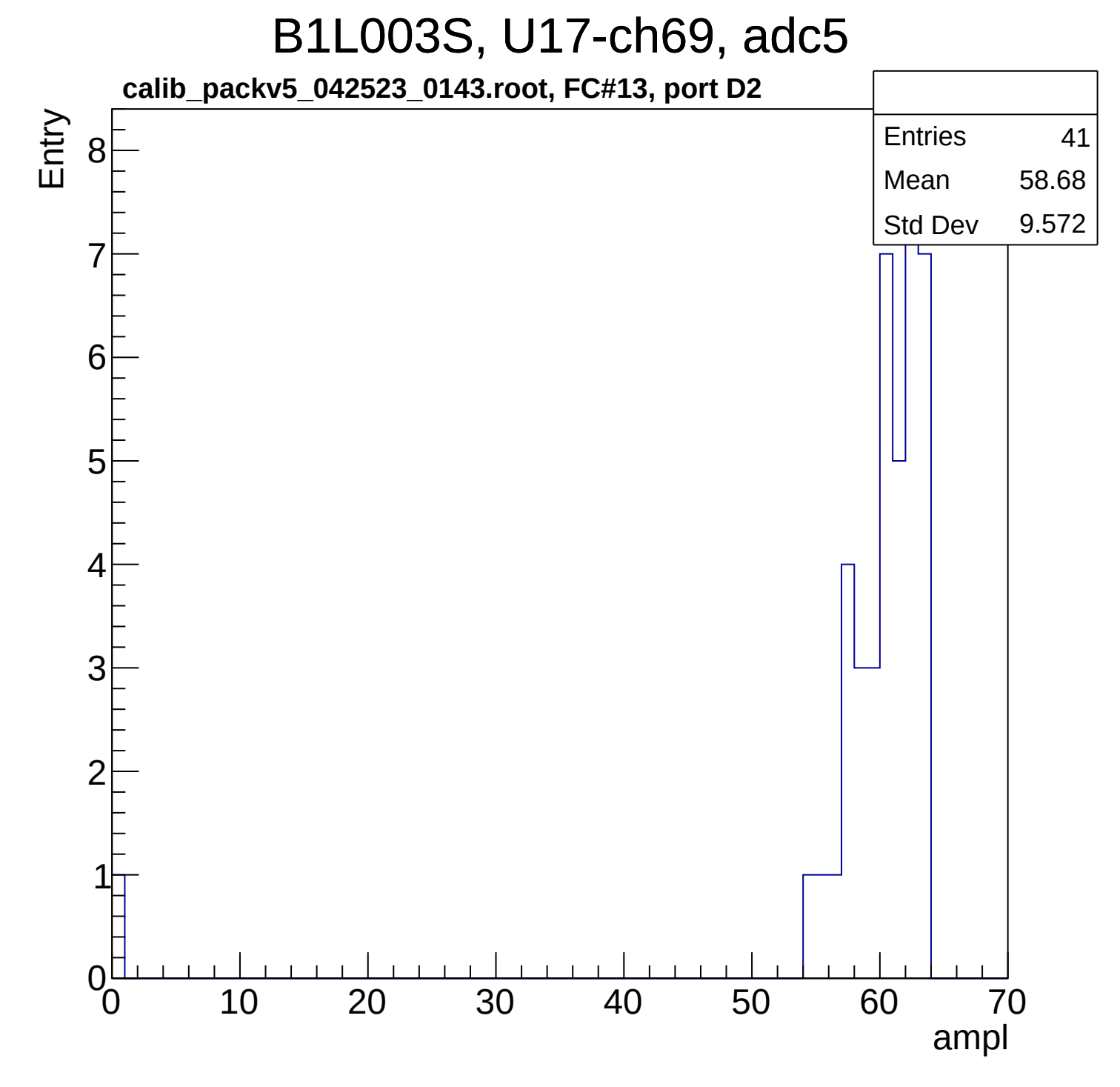
B1L003S, U17-ch69, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.68
Std Dev 9.572
Entry
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3
4
5
6
7
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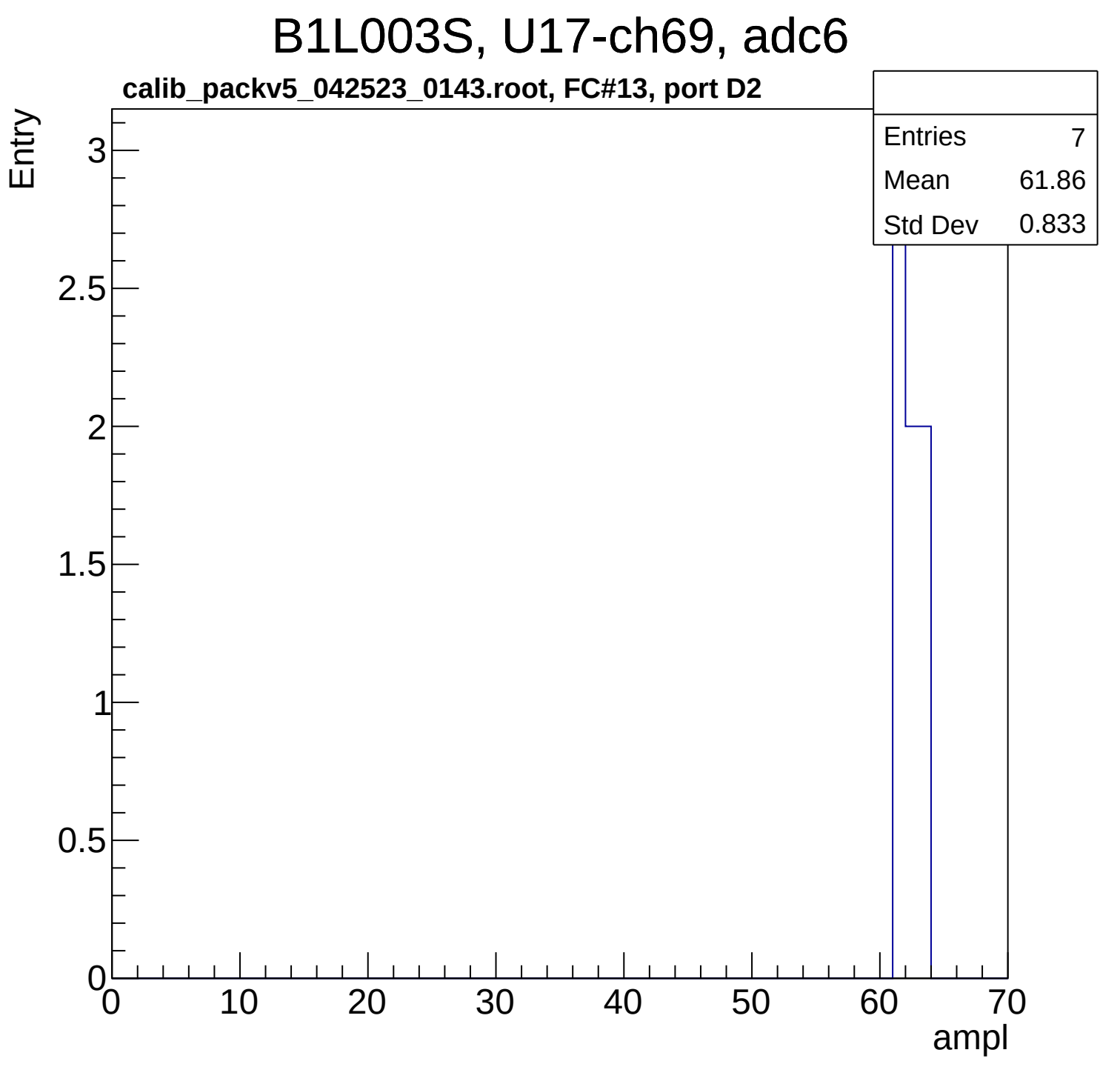
70
ampl


B1L003S, U17-ch69, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.86
Std Dev 0.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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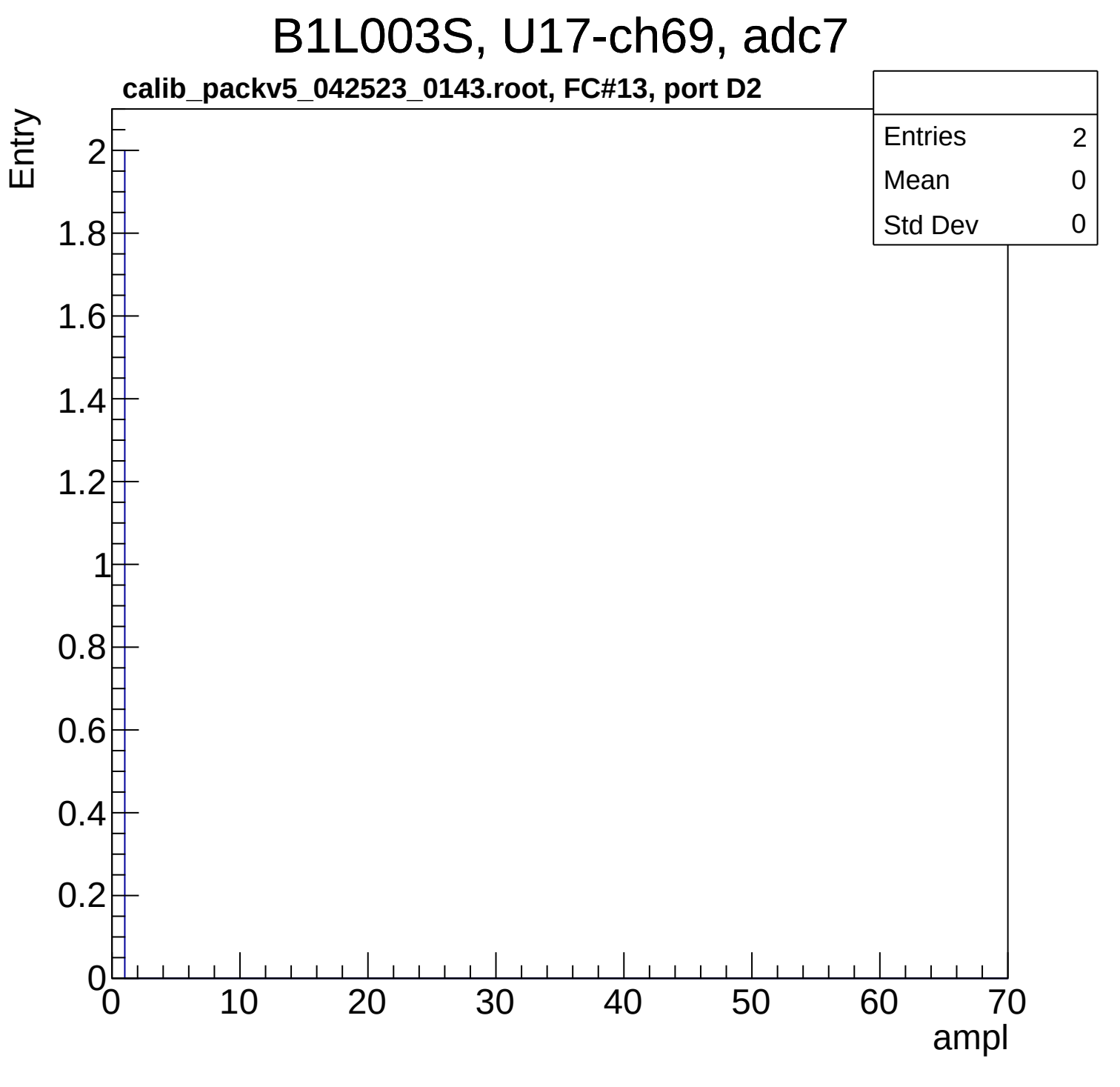
B1L003S, U17-ch69, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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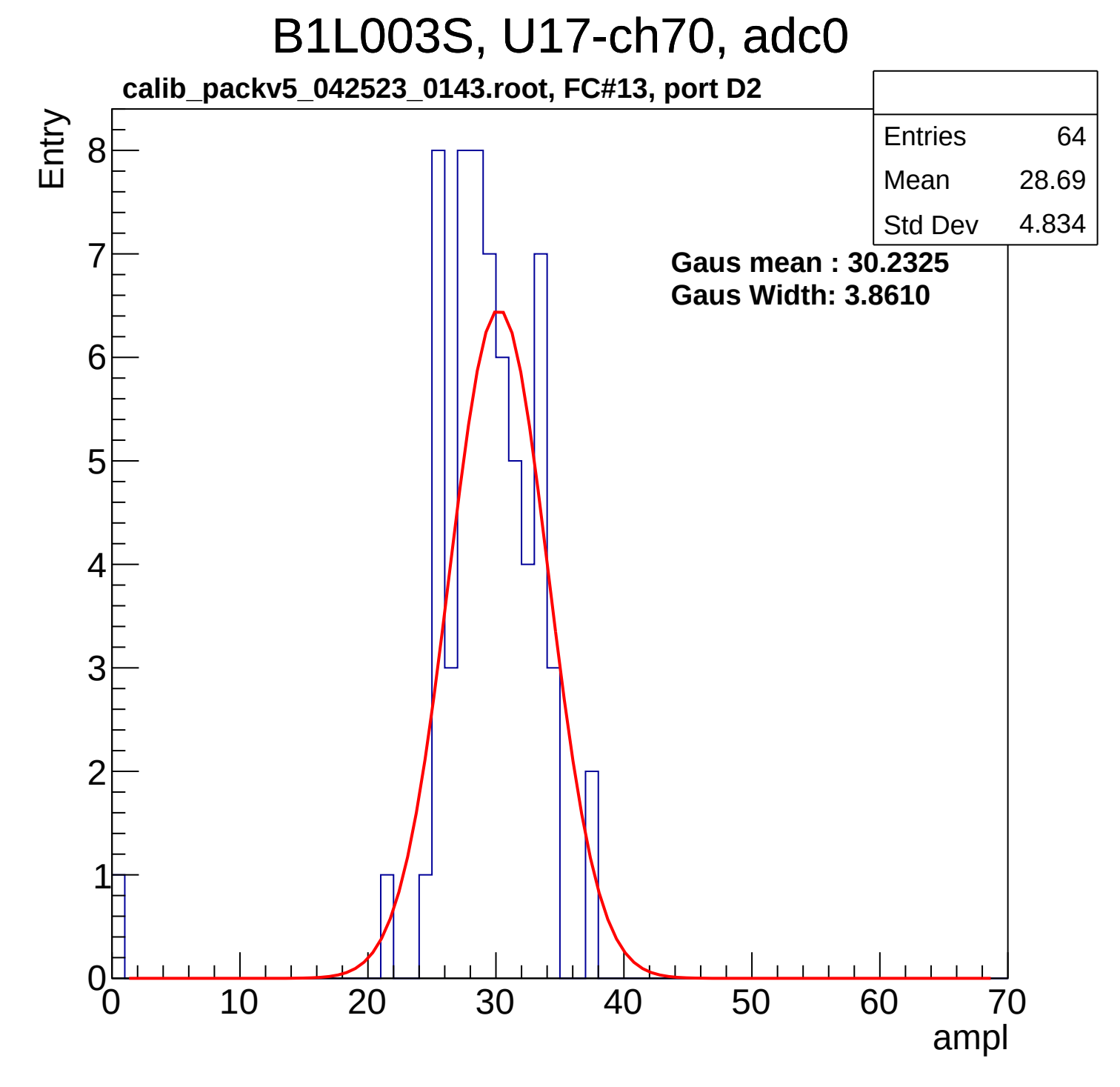
B1L003S, U17-ch70, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 28.69
Std Dev 4.834
Gaus mean : 30.2325
Gaus Width: 3.8610
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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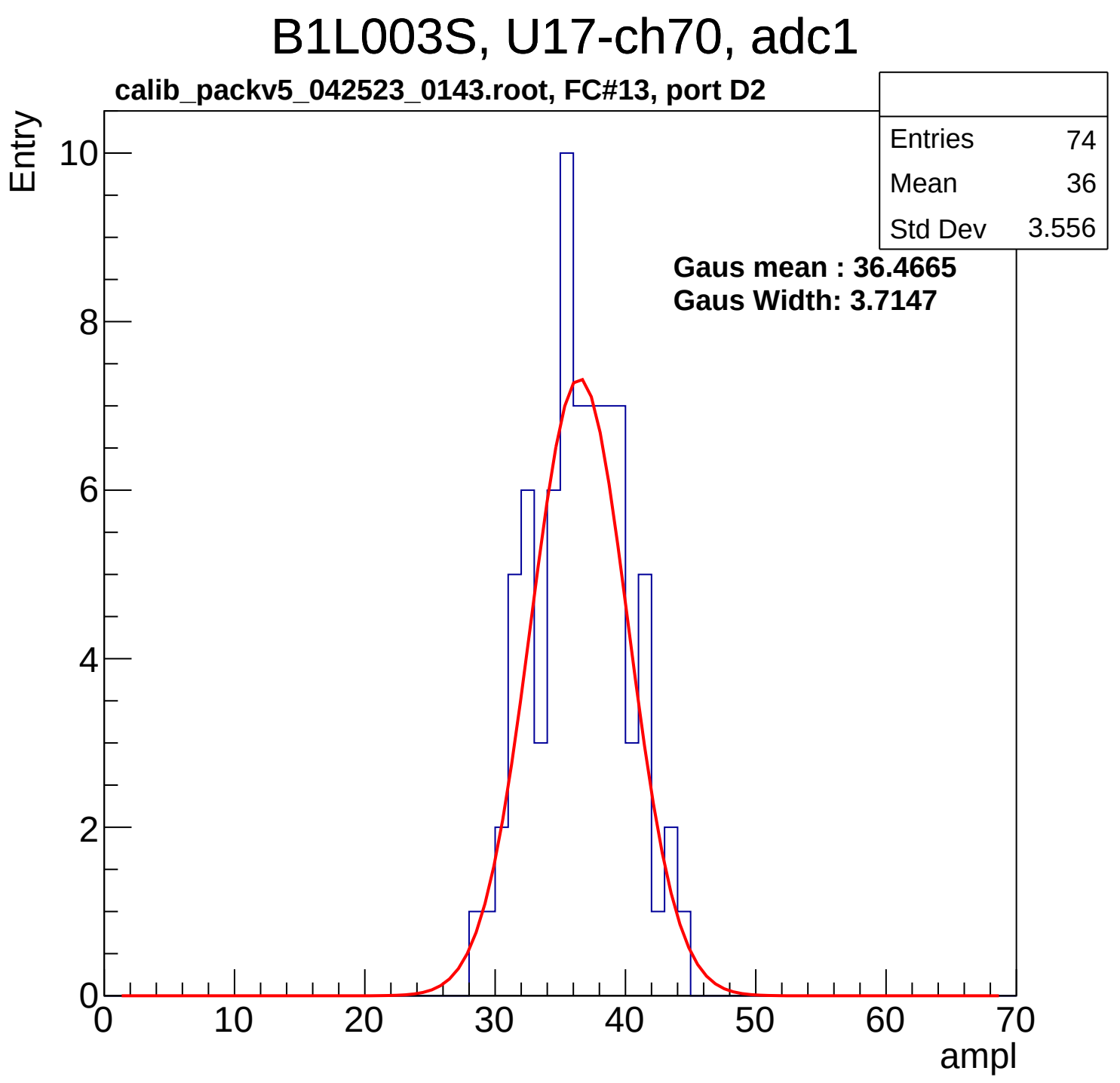
B1L003S, U17-ch70, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36
Std Dev 3.556
Gaus mean : 36.4665
Gaus Width: 3.7147
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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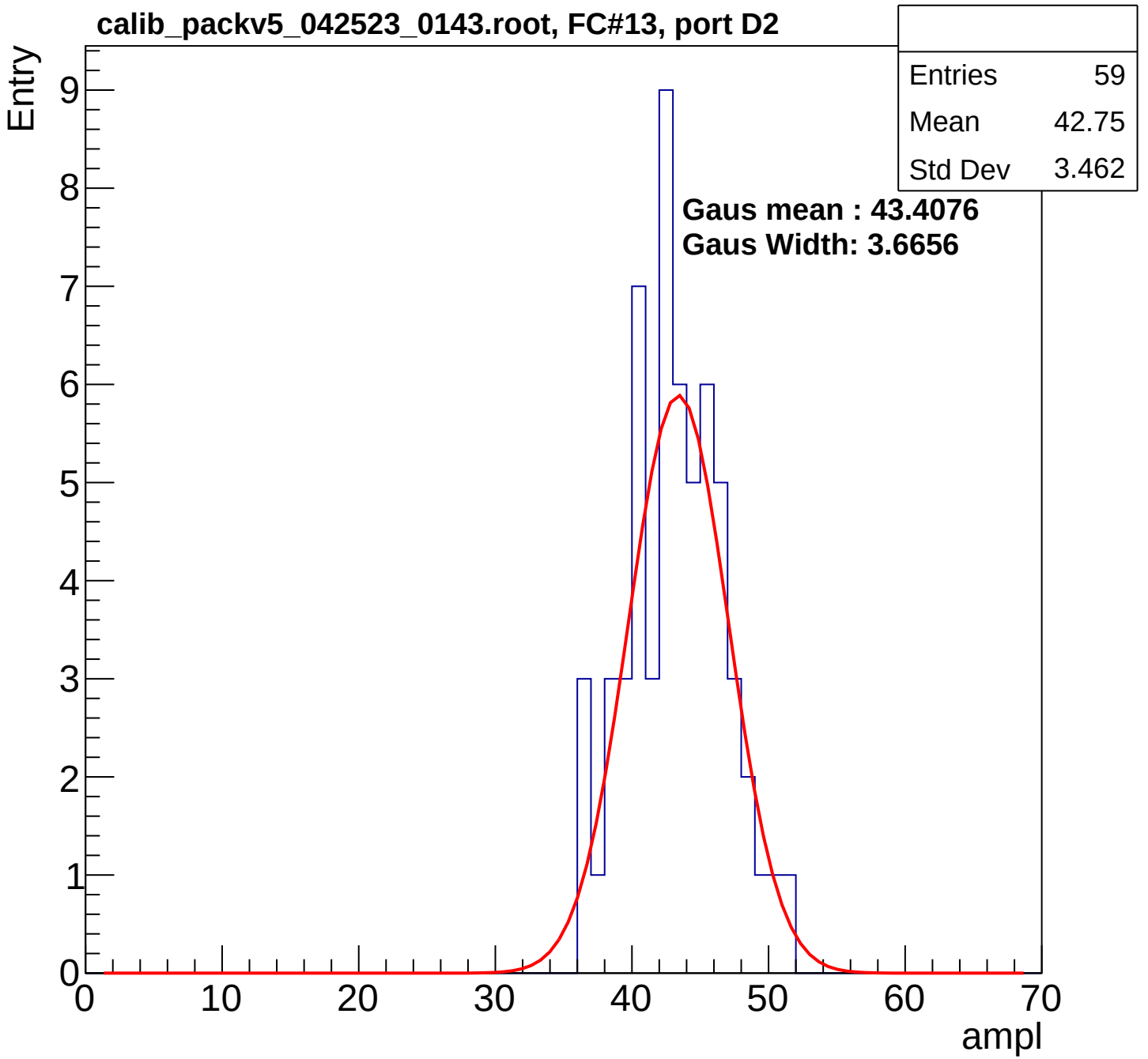
B1L003S, U17-ch70, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.75
Std Dev 3.462
Gaus mean : 43.4076
Gaus Width: 3.6656
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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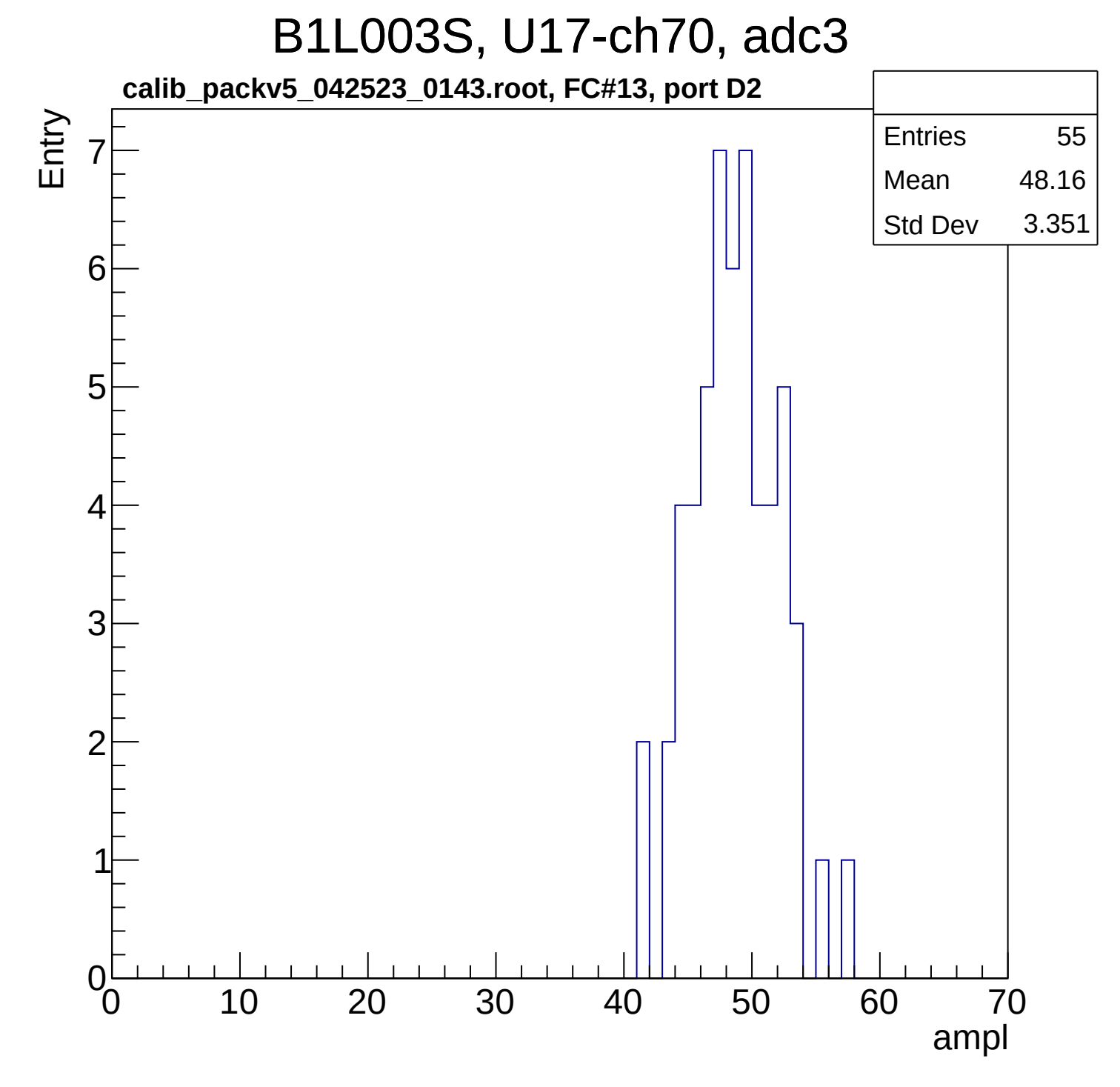
B1L003S, U17-ch70, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 48.16
Std Dev 3.351
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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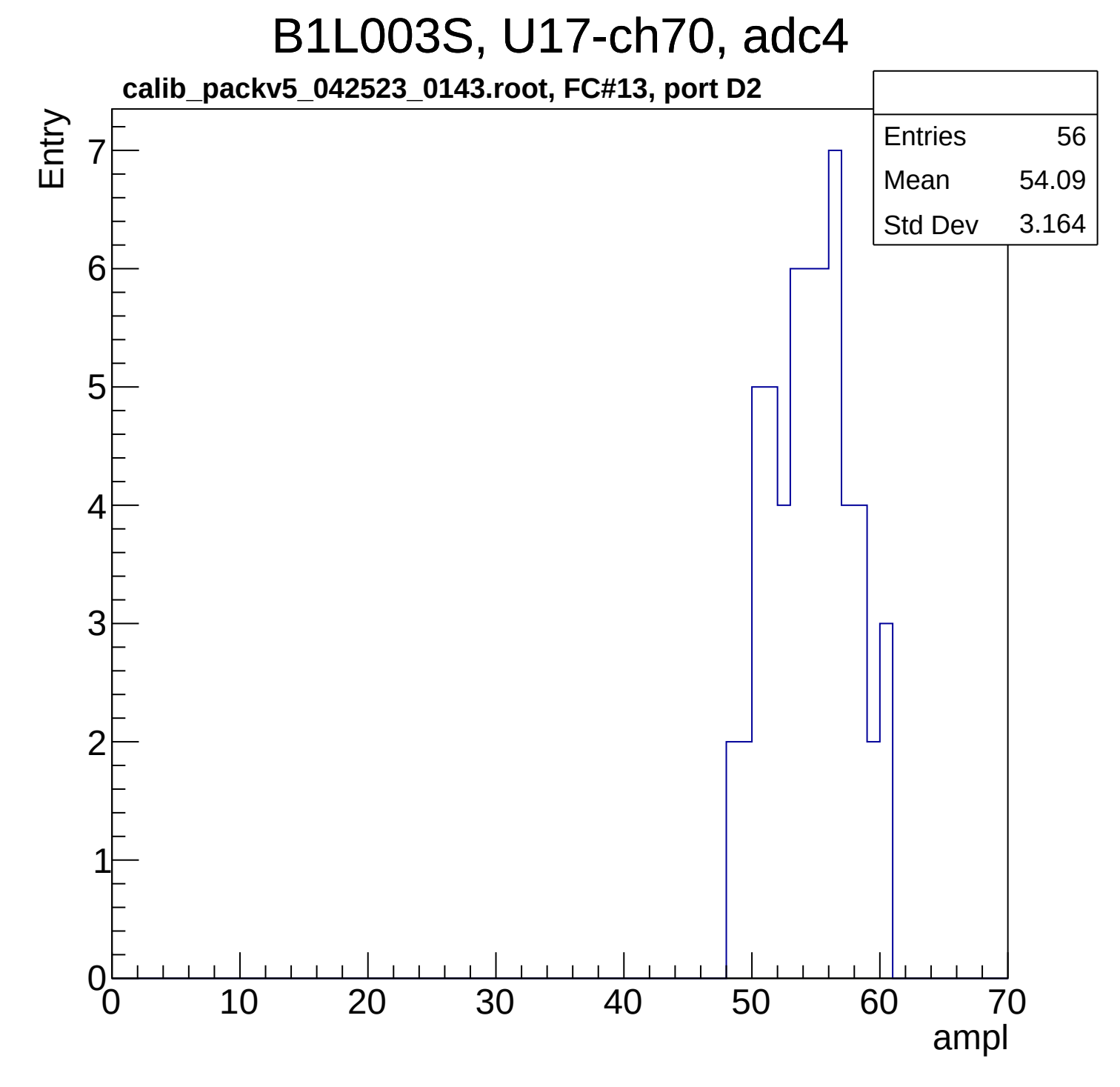
B1L003S, U17-ch70, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 54.09
Std Dev 3.164
Entry
ampl
0
1
2
3
4
5
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0
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60
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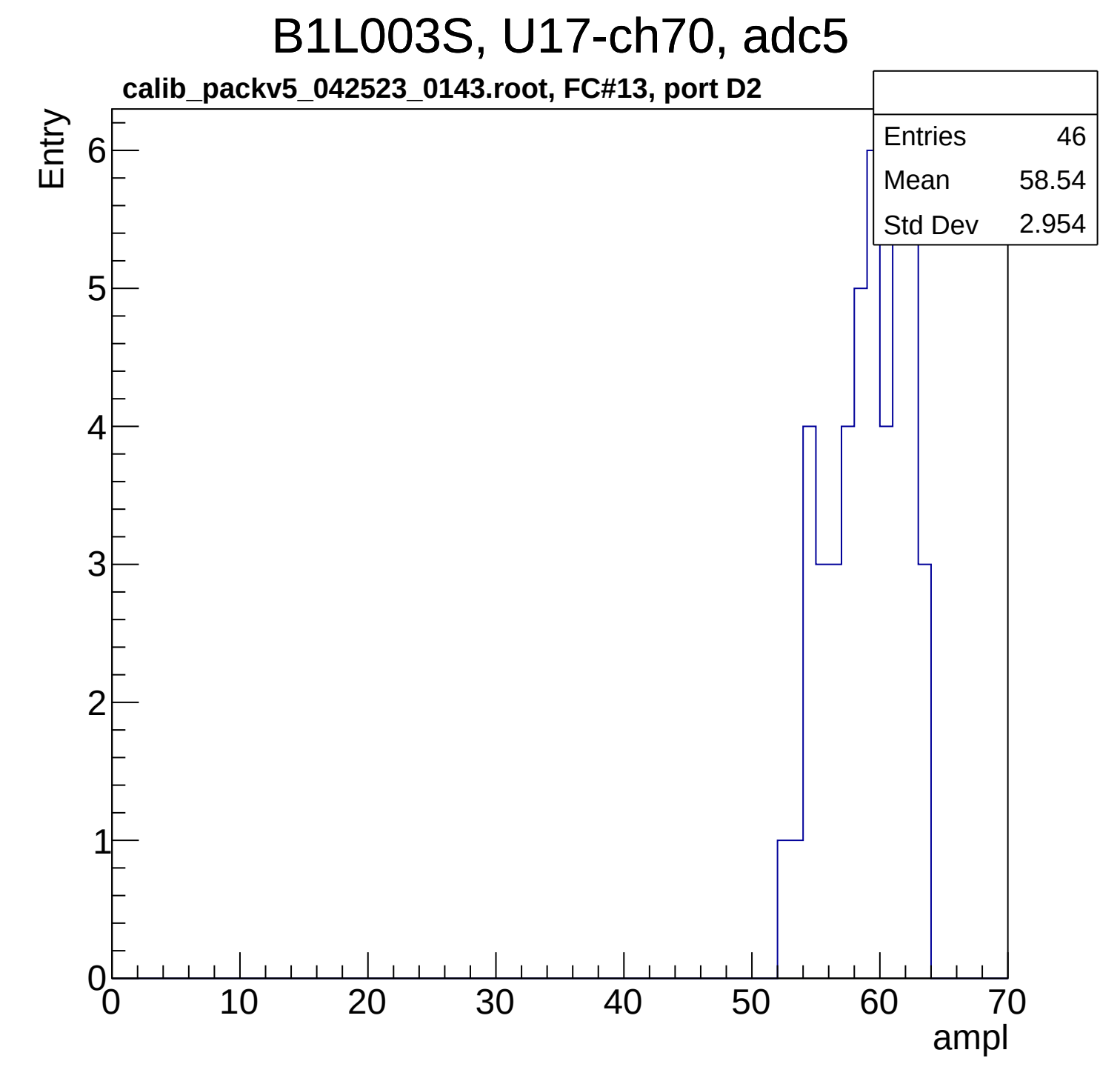
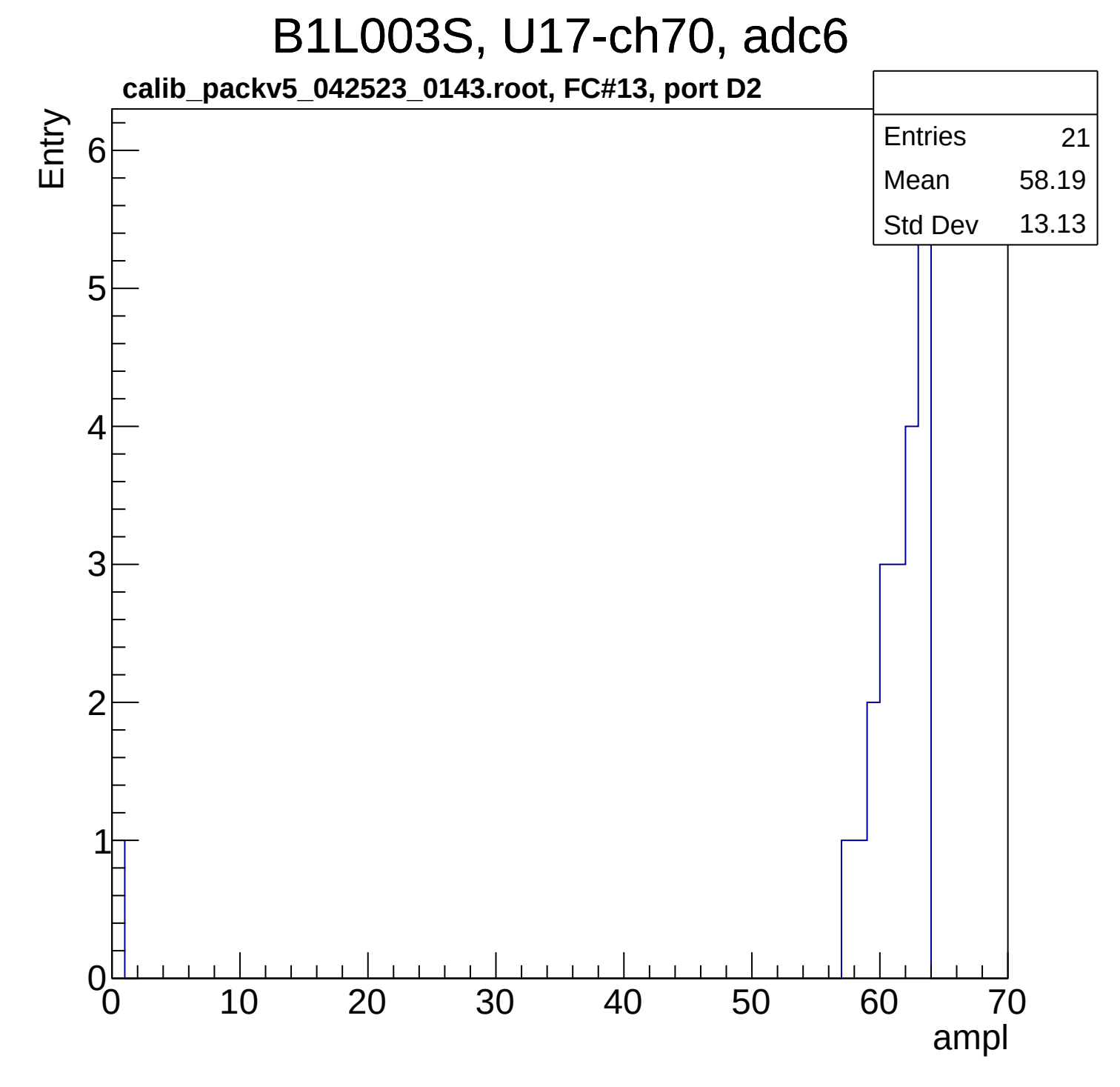
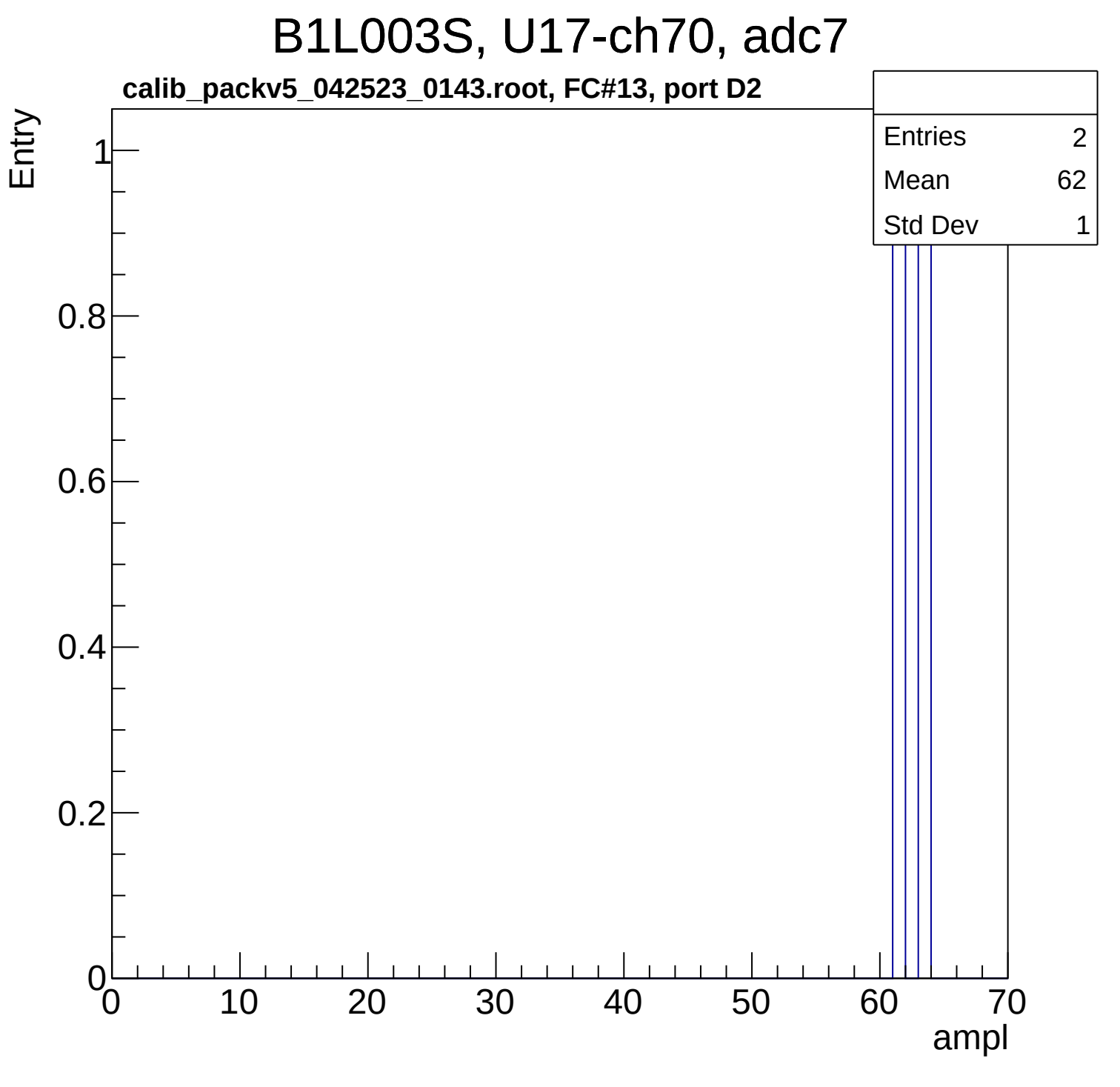
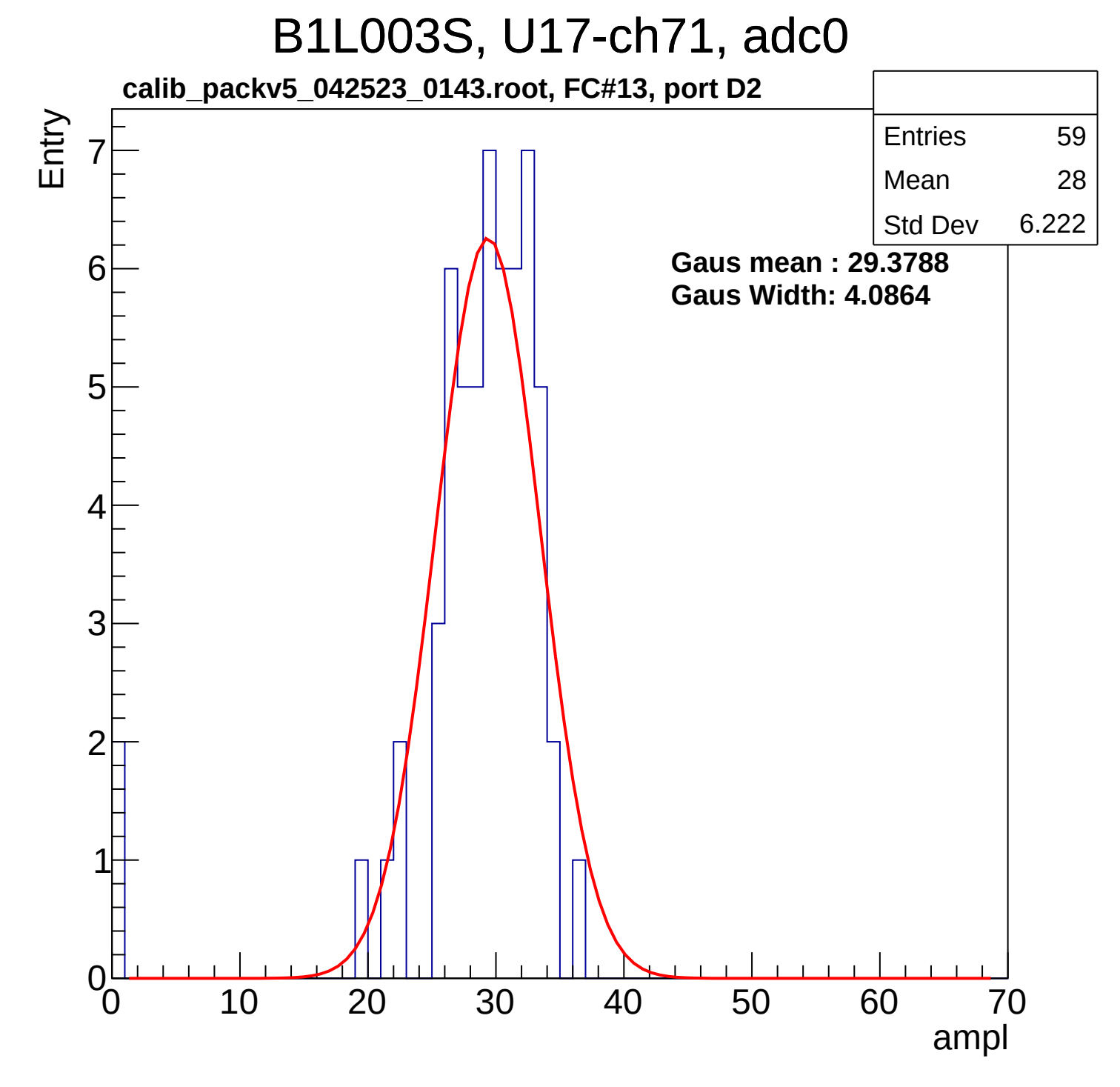
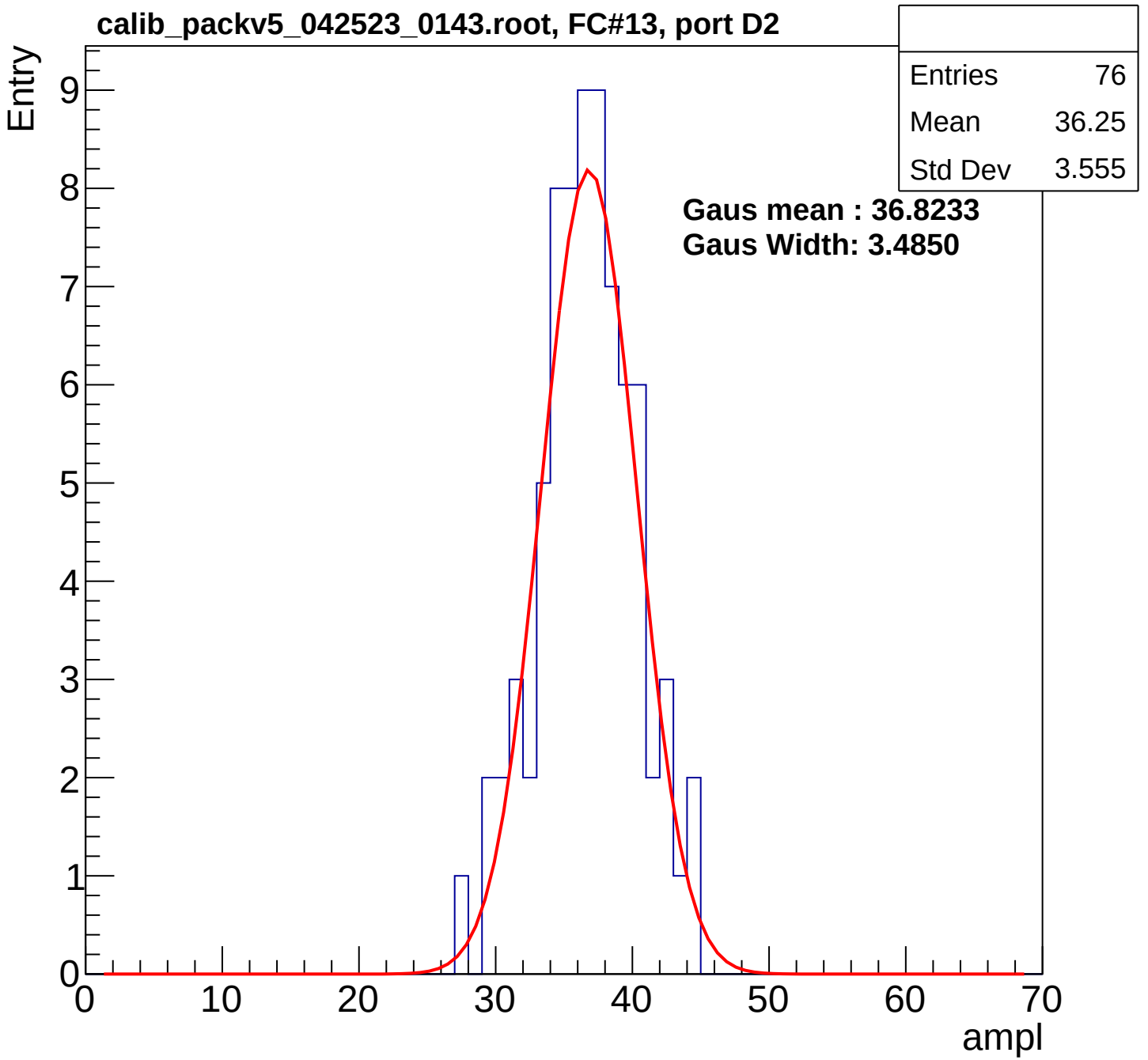
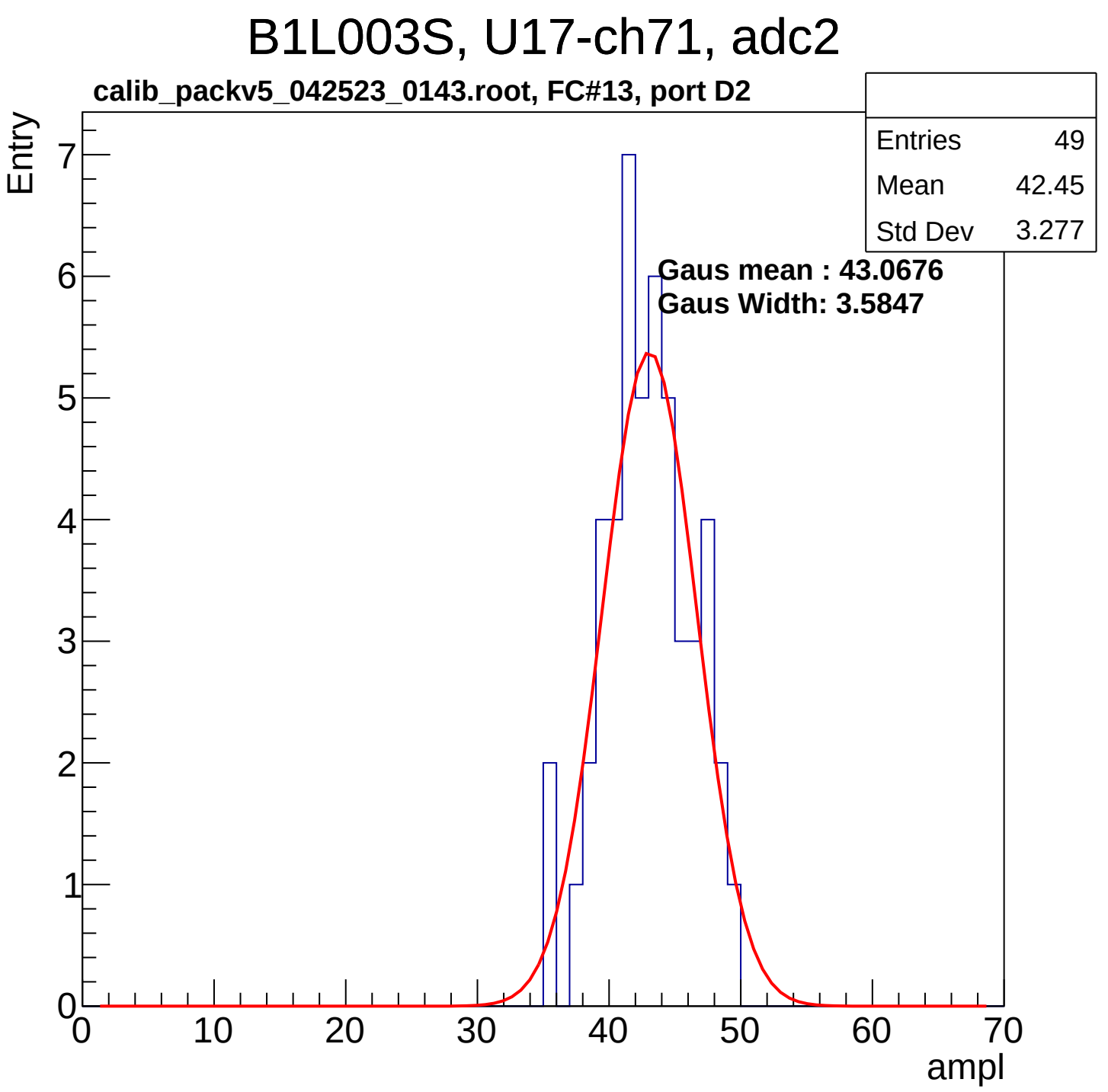

B1L003S, U17-ch70, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.54
Std Dev 2.954
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

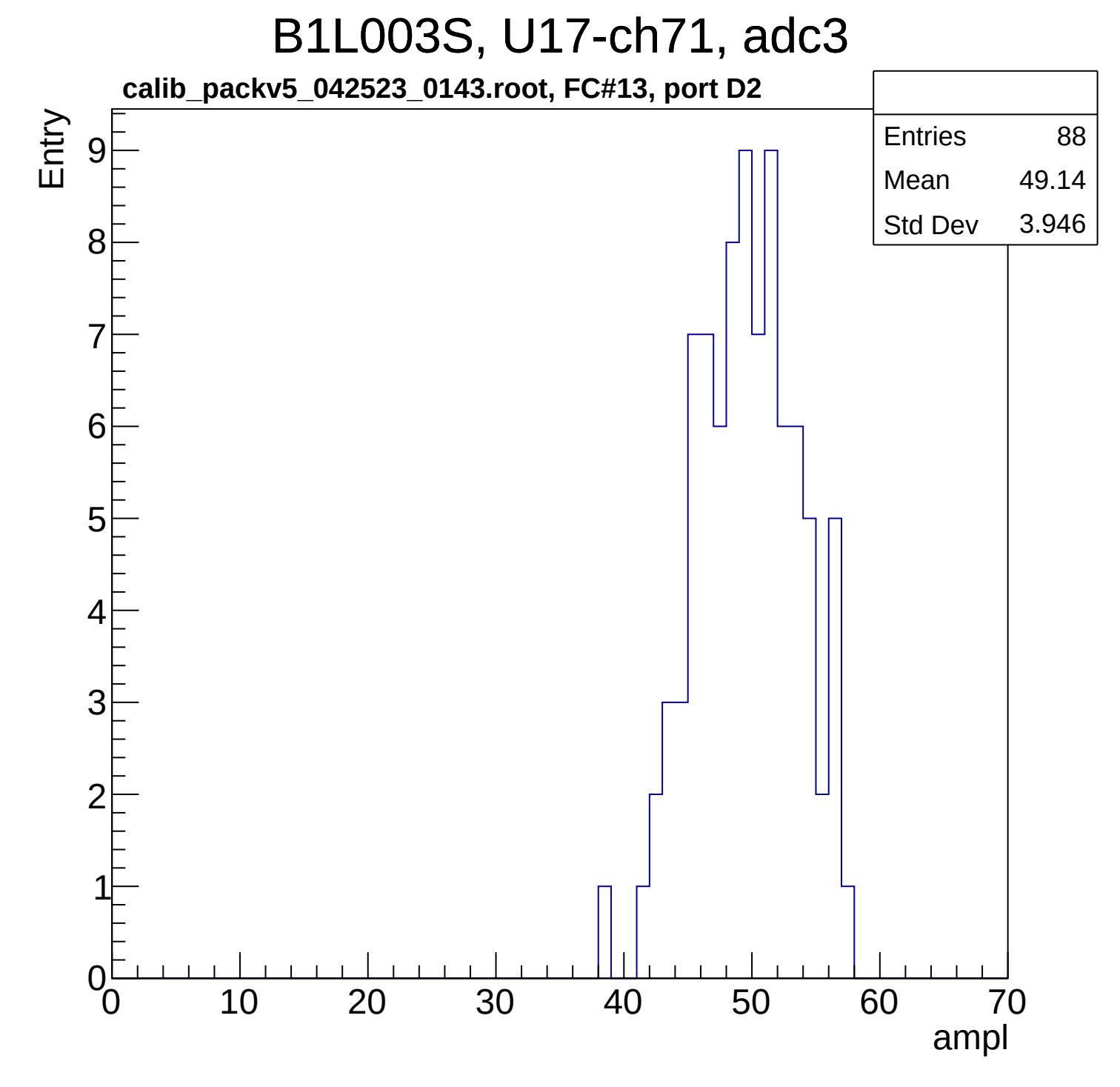

B1L003S, U17-ch70, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 58.19
Std Dev 13.13
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

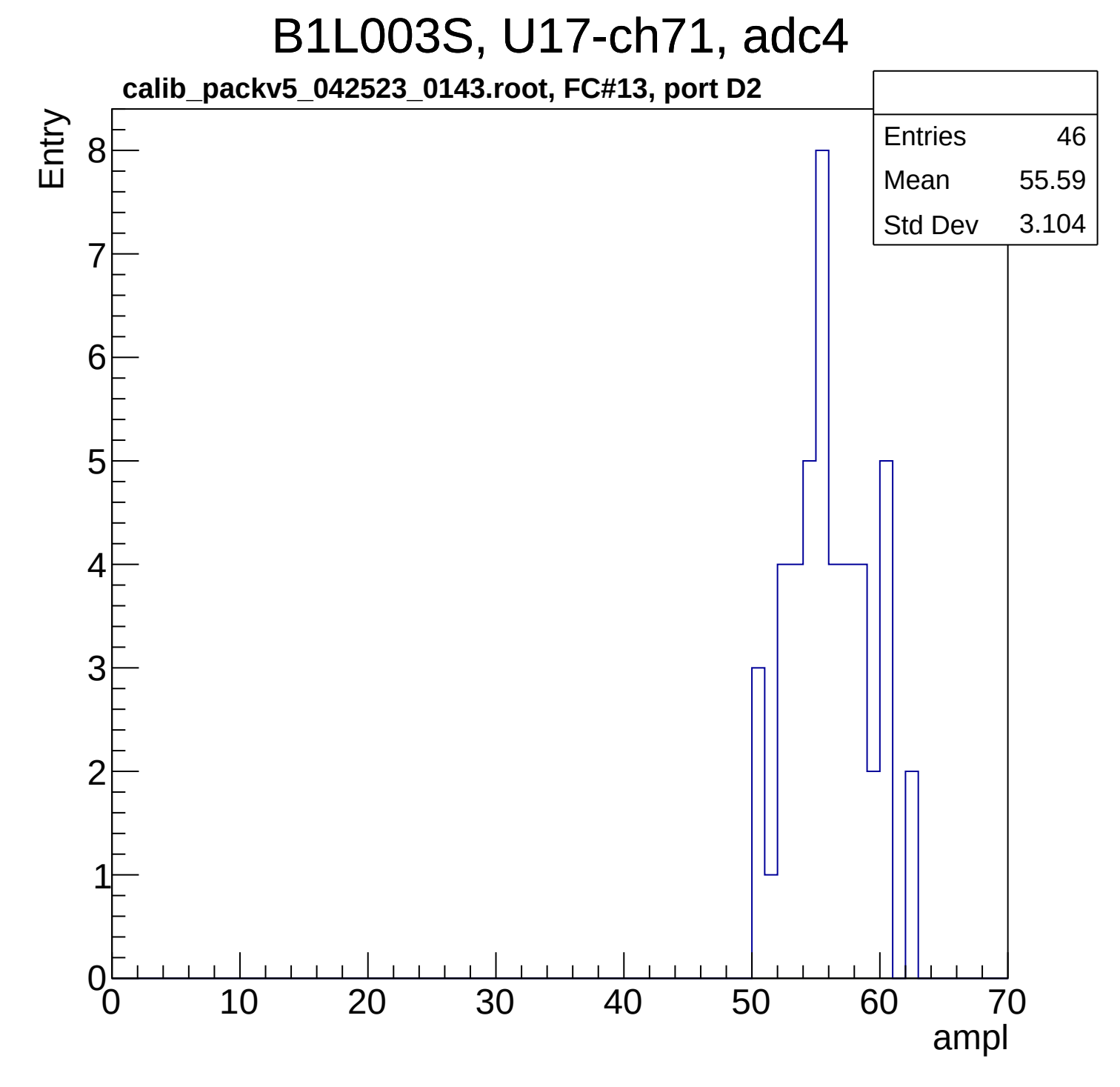

B1L003S, U17-ch70, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

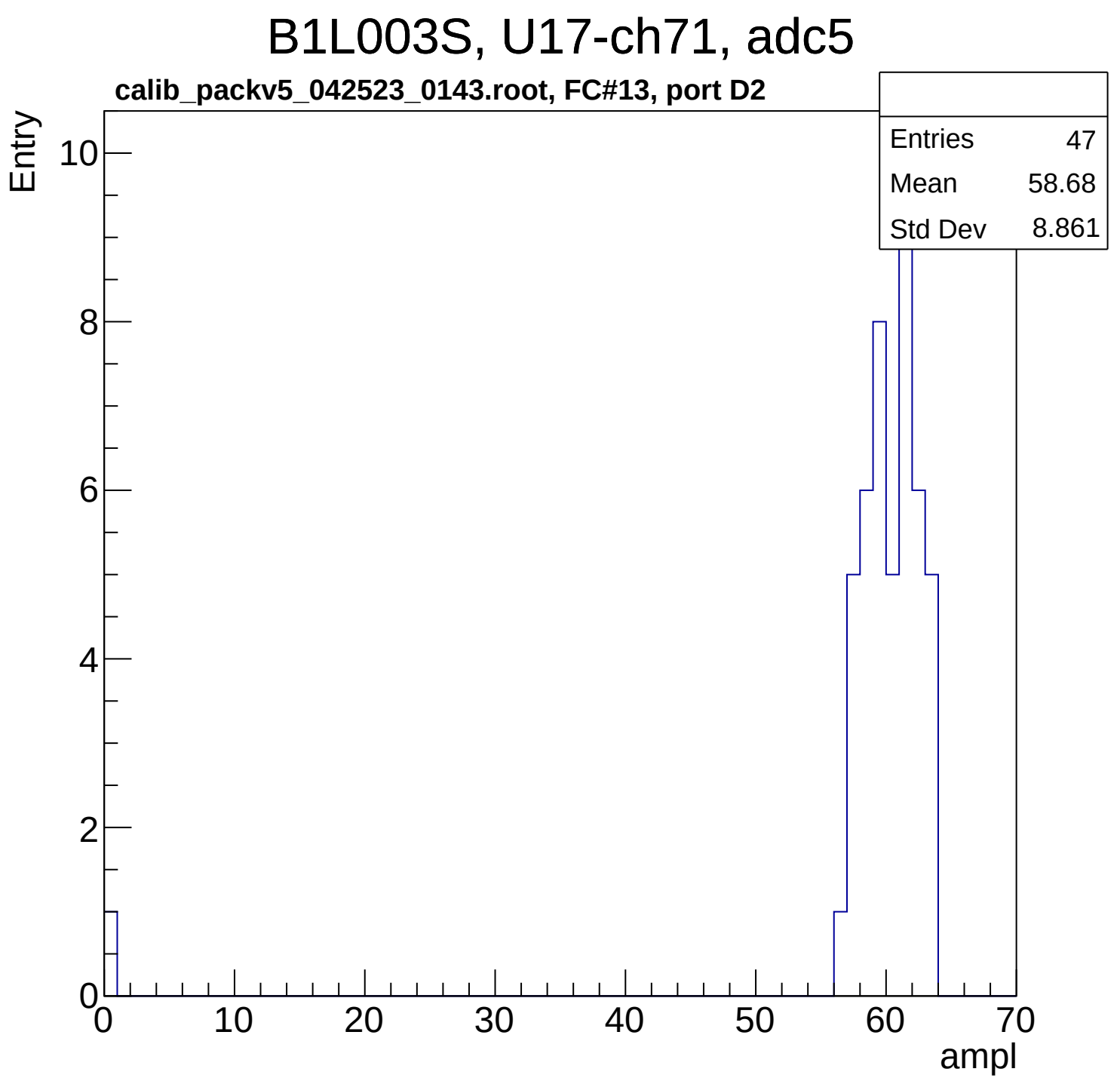

B1L003S, U17-ch71, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28
Std Dev 6.222
Gaus mean : 29.3788
Gaus Width: 4.0864
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

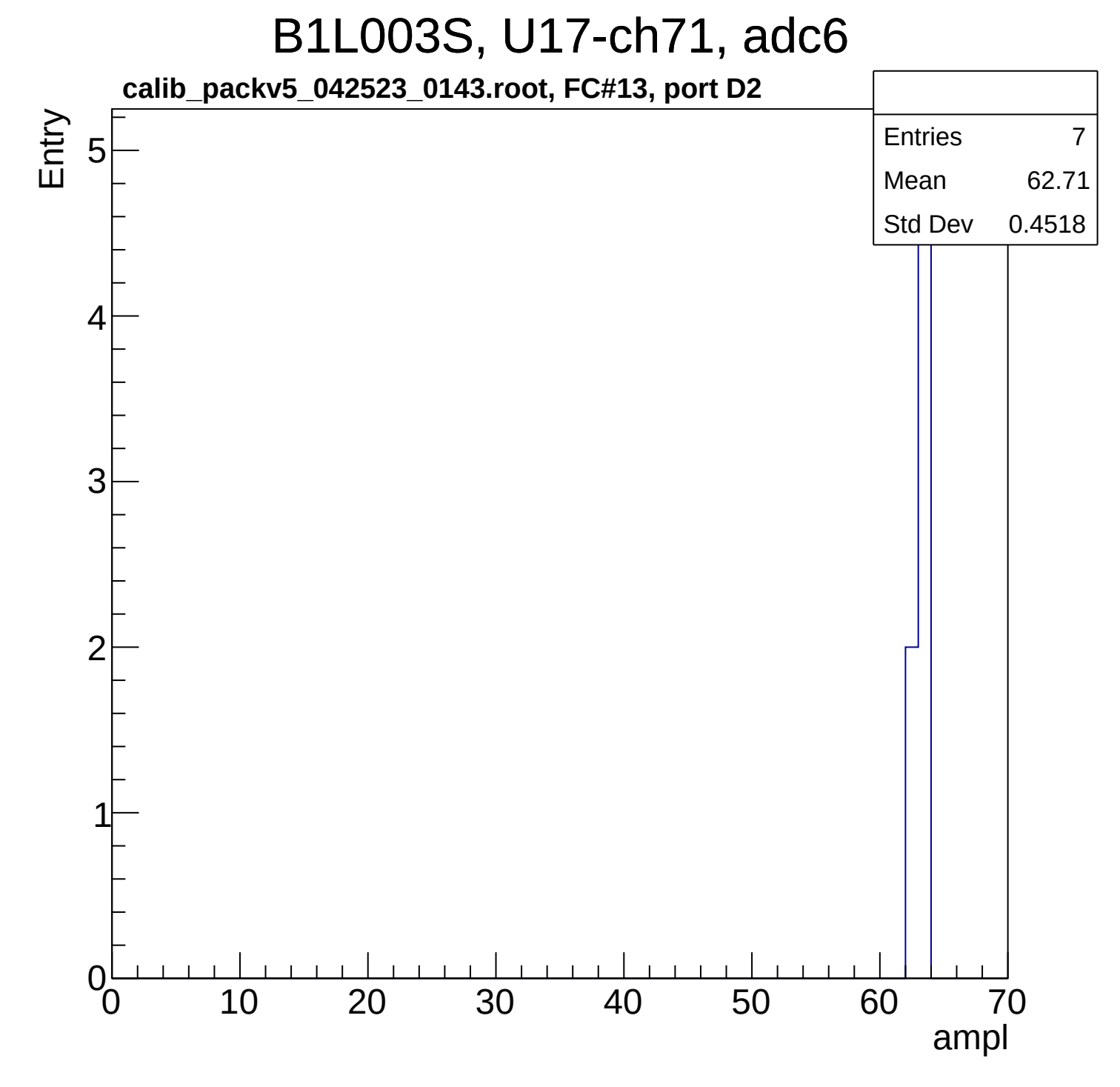

B1L003S, U17-ch71, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.25
Std Dev 3.555
Gaus mean : 36.8233
Gaus Width: 3.4850
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

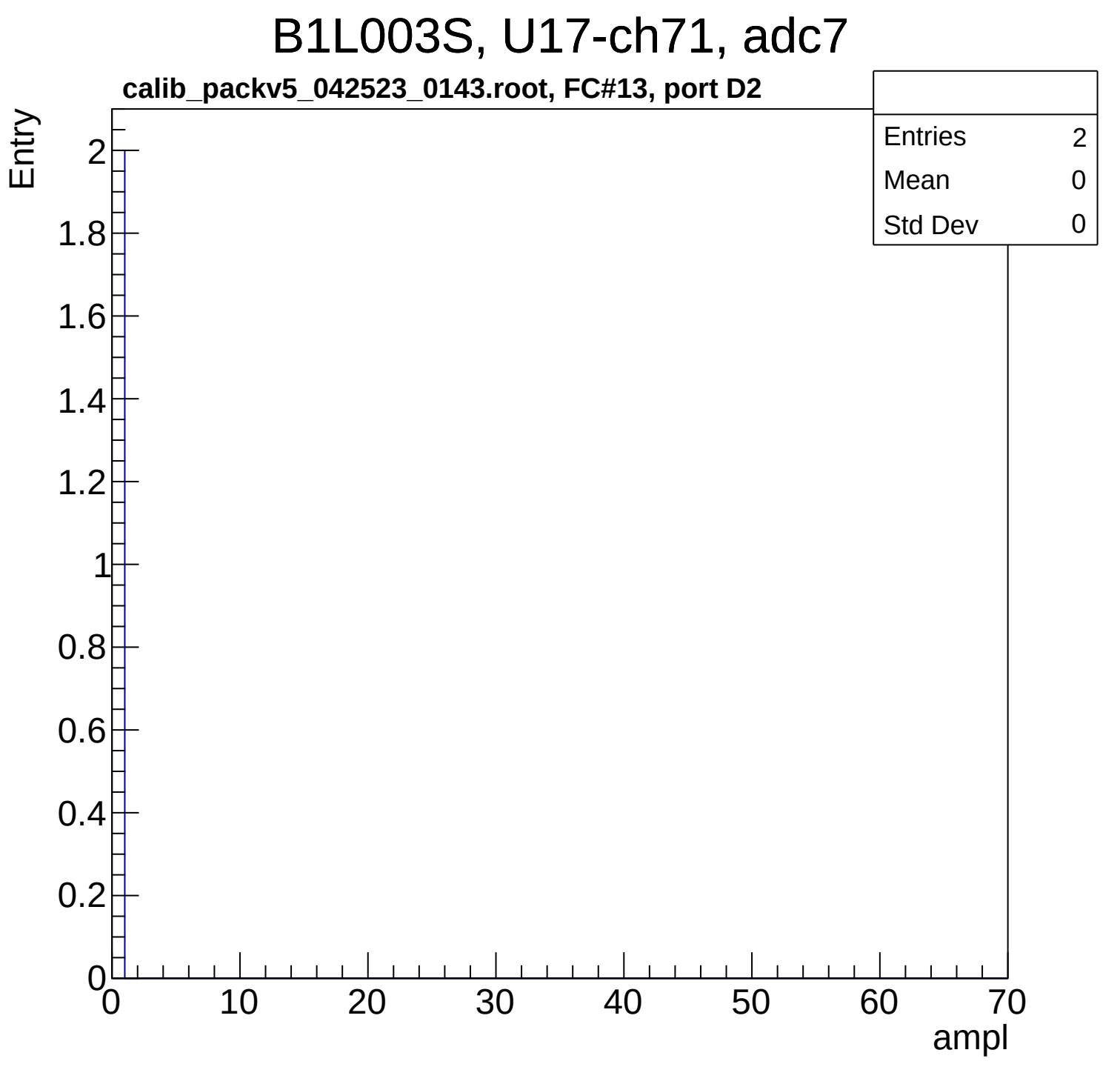

B1L003S, U17-ch71, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 42.45
Std Dev 3.277
Gaus mean : 43.0676
Gaus Width: 3.5847
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

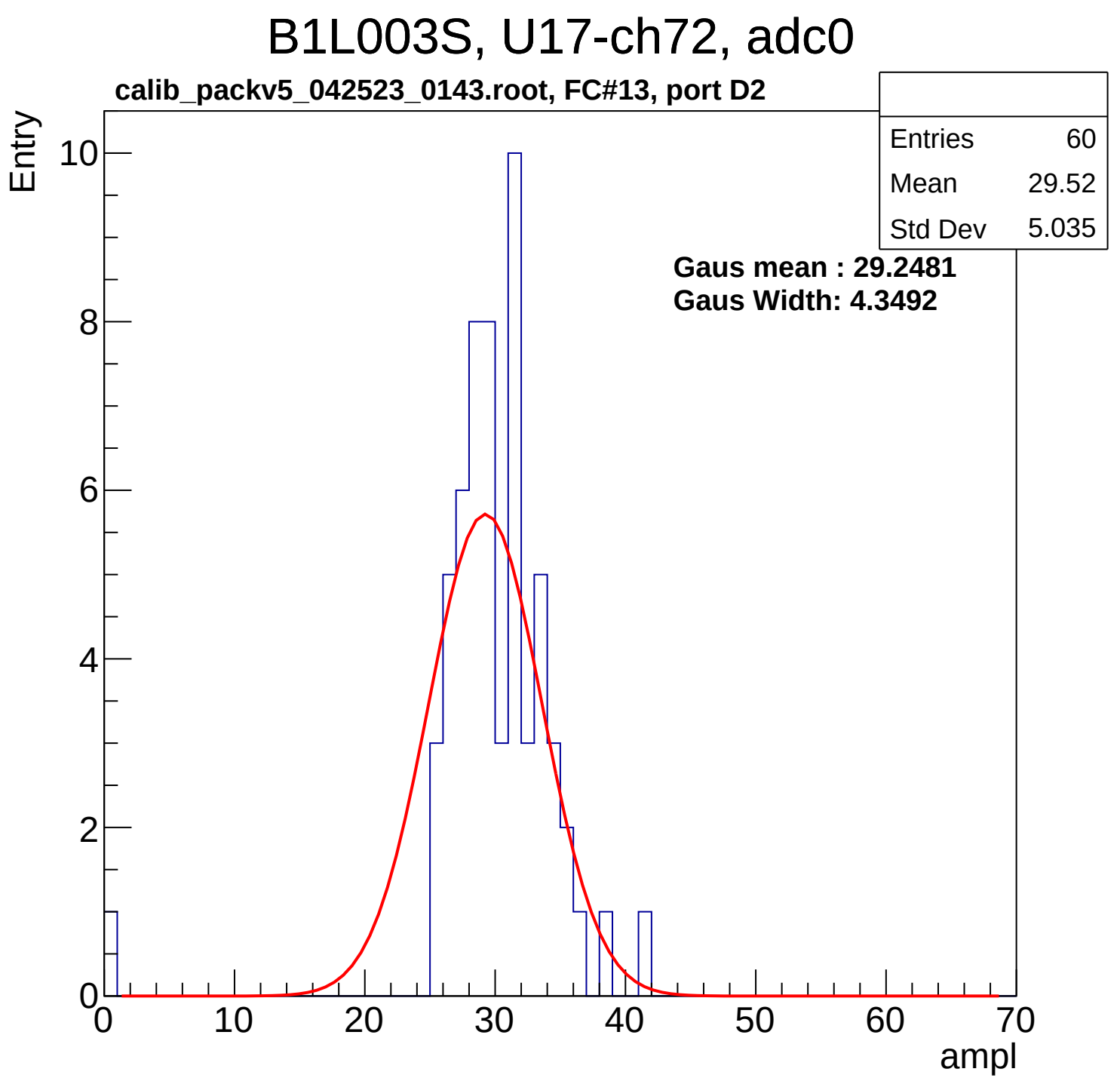

B1L003S, U17-ch71, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 88
Mean 49.14
Std Dev 3.946
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

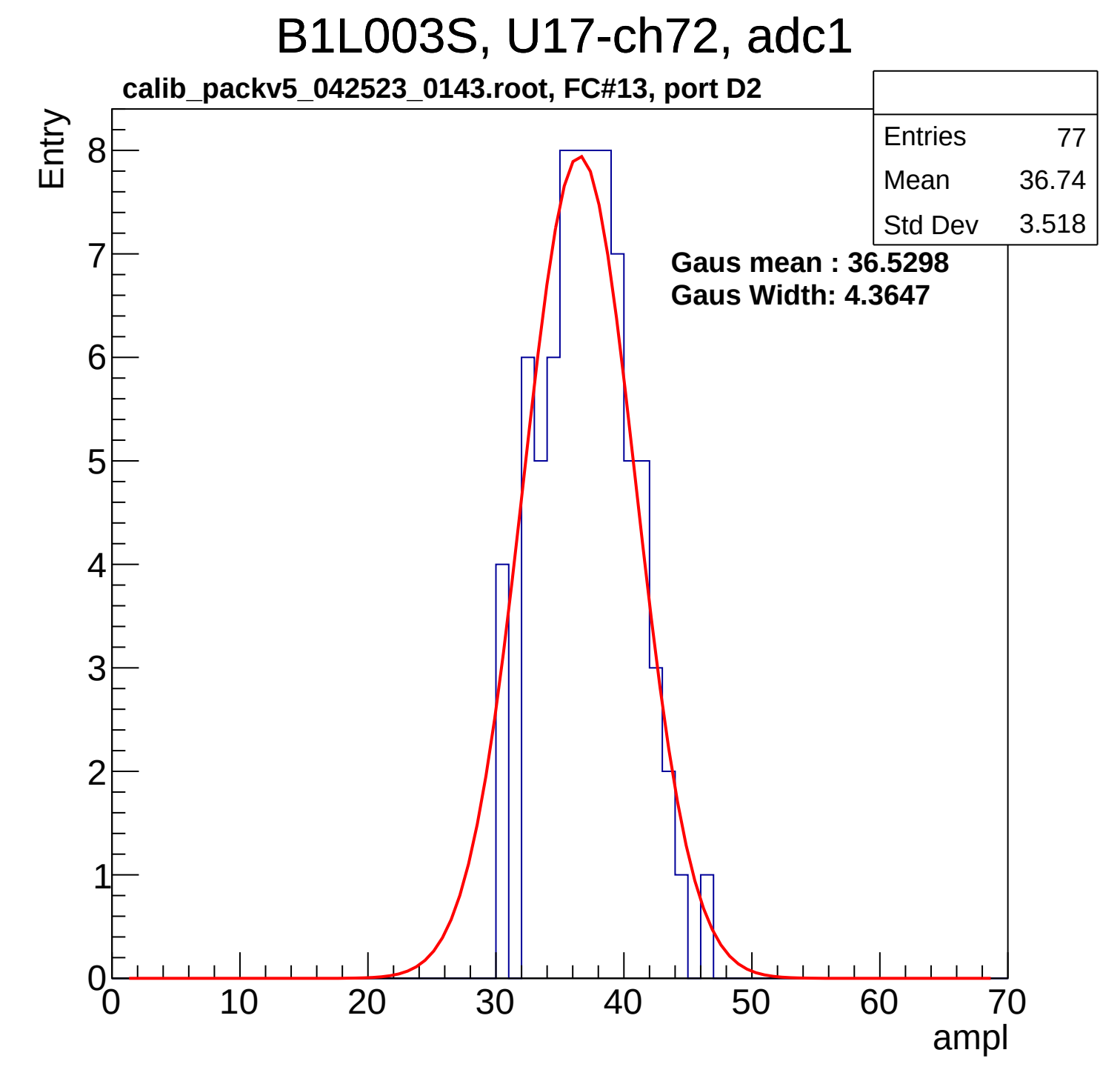

B1L003S, U17-ch71, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 55.59
Std Dev 3.104
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch71, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.68
Std Dev 8.861
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch71, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.71
Std Dev 0.4518
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch71, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U17-ch72, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 29.52
Std Dev 5.035
Gaus mean : 29.2481
Gaus Width: 4.3492
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch72, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.74
Std Dev 3.518
Gaus mean : 36.5298
Gaus Width: 4.3647
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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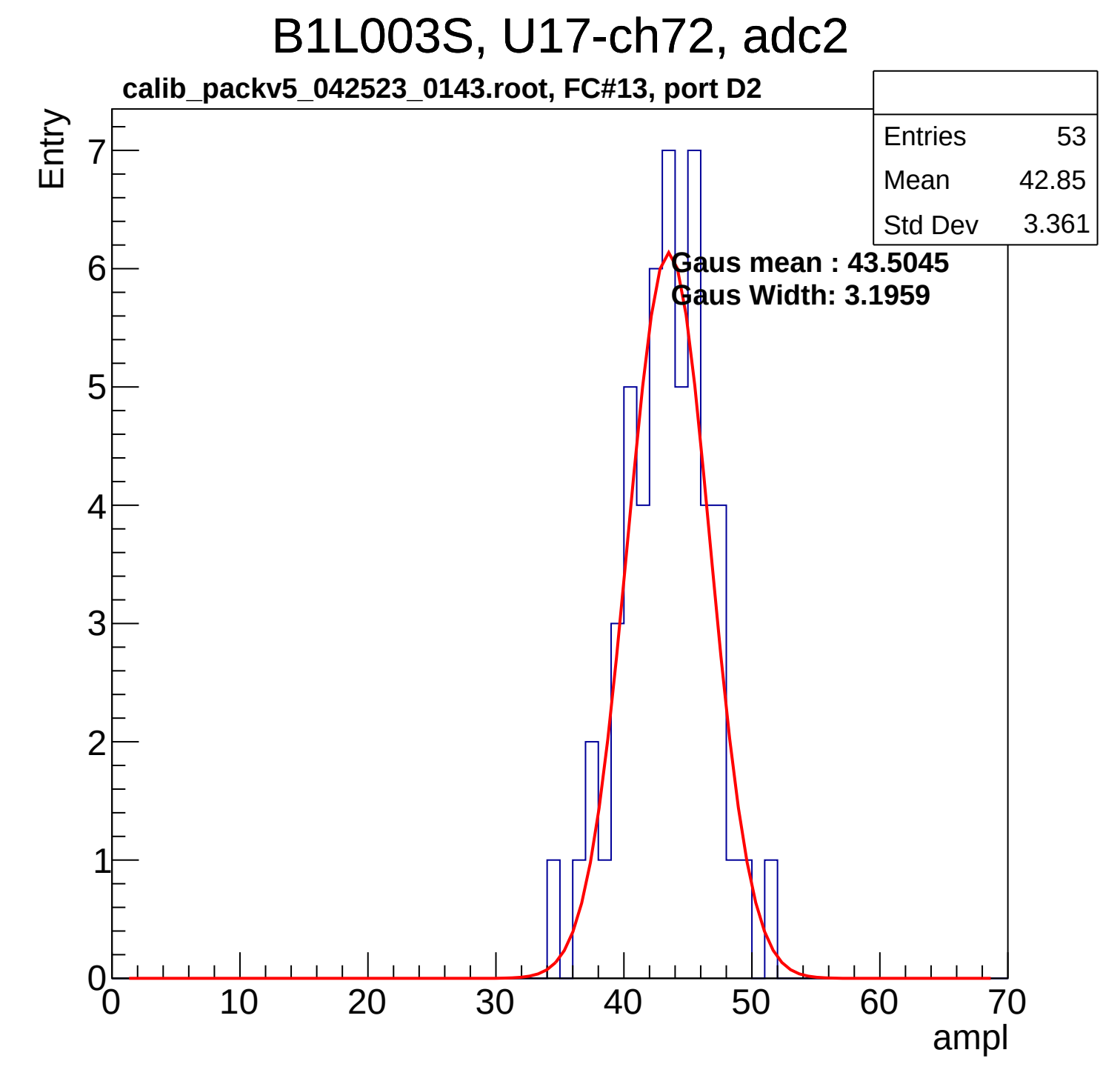
B1L003S, U17-ch72, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.85
Std Dev 3.361
Gaus mean : 43.5045
Gaus Width: 3.1959
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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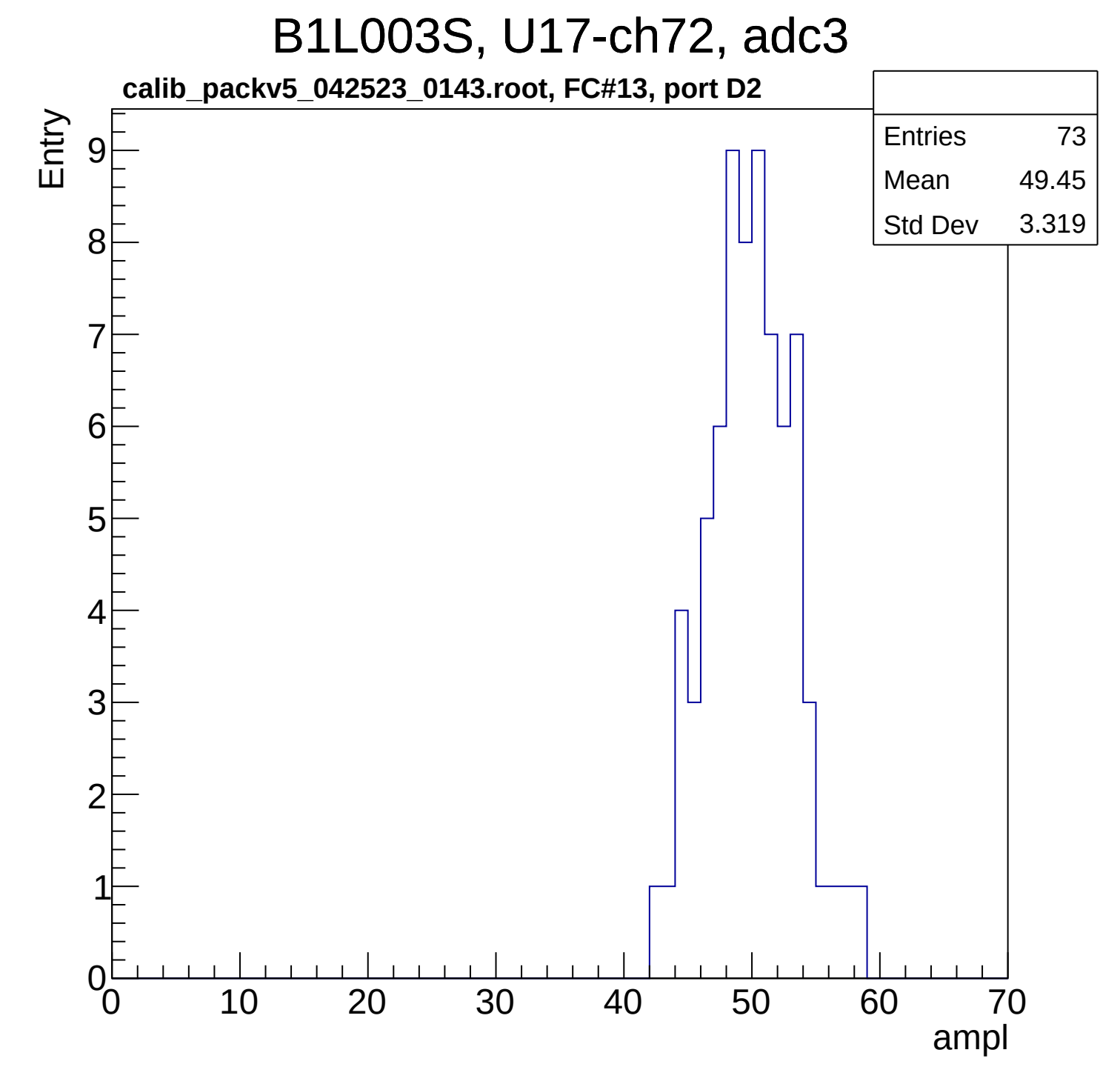
B1L003S, U17-ch72, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.45
Std Dev 3.319
Entry
ampl
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3
4
5
6
7
8
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0
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60
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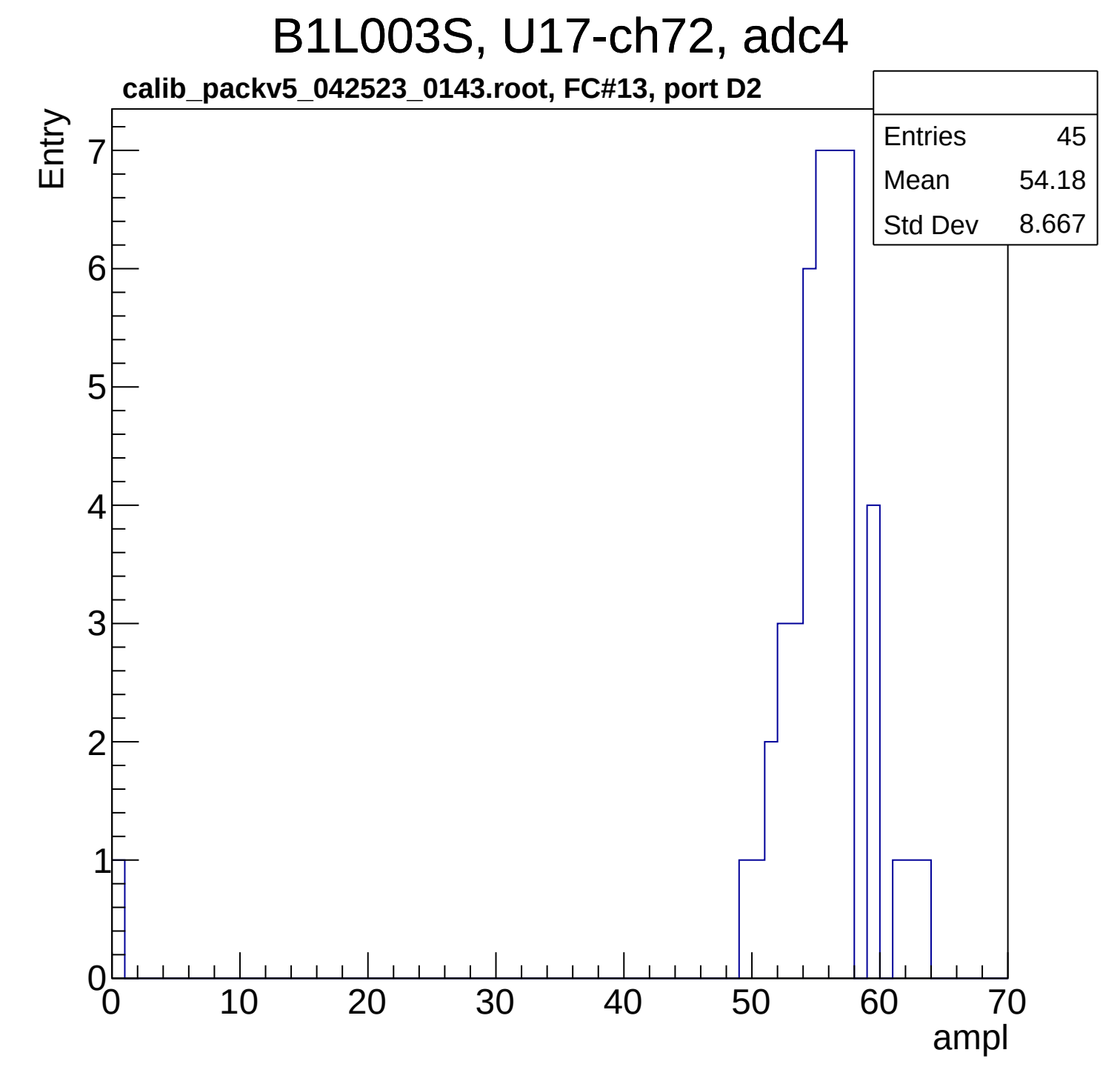
B1L003S, U17-ch72, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 54.18
Std Dev 8.667
Entry
ampl
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1
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4
5
6
7
0
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20
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60
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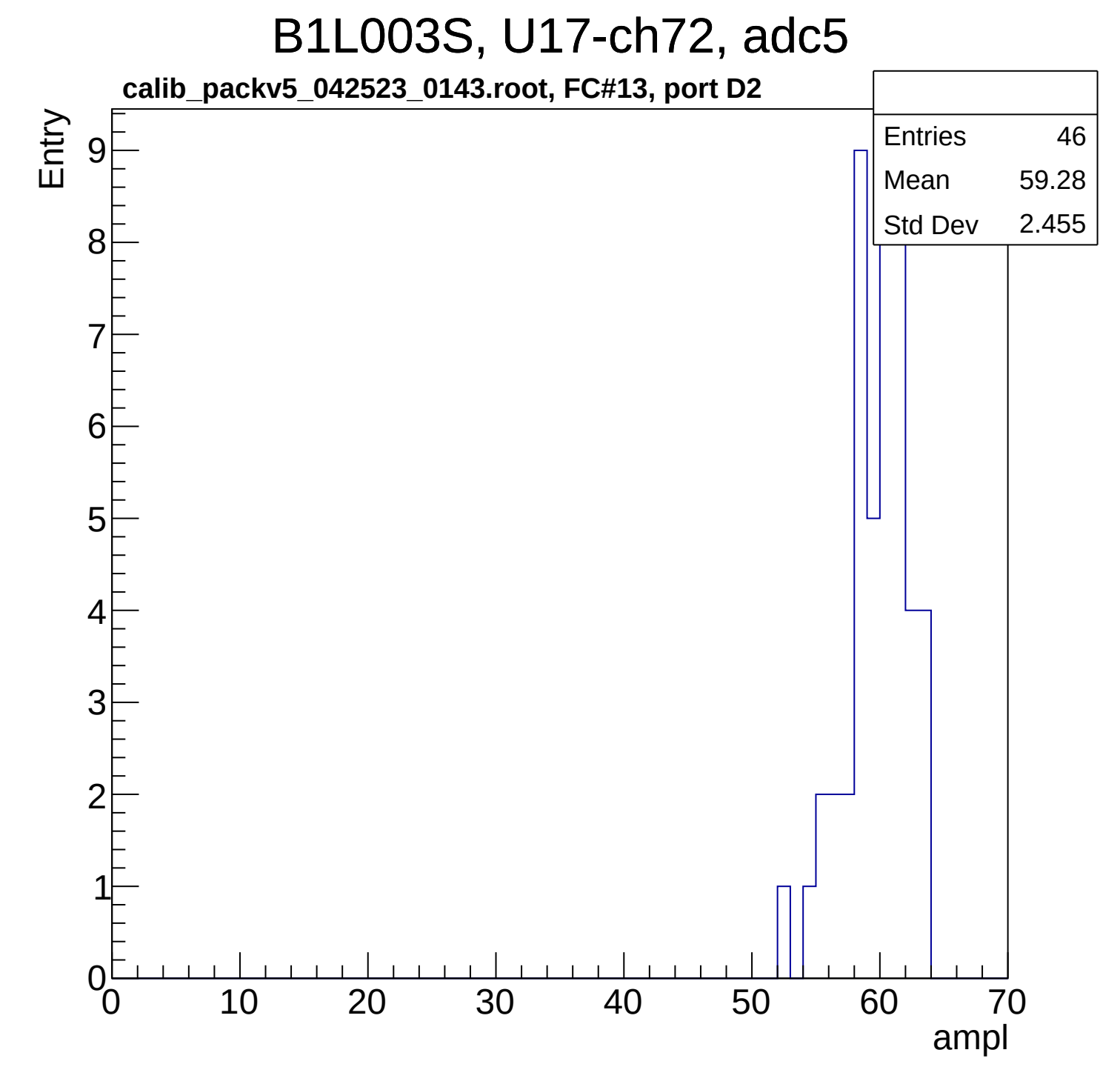
B1L003S, U17-ch72, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.28
Std Dev 2.455
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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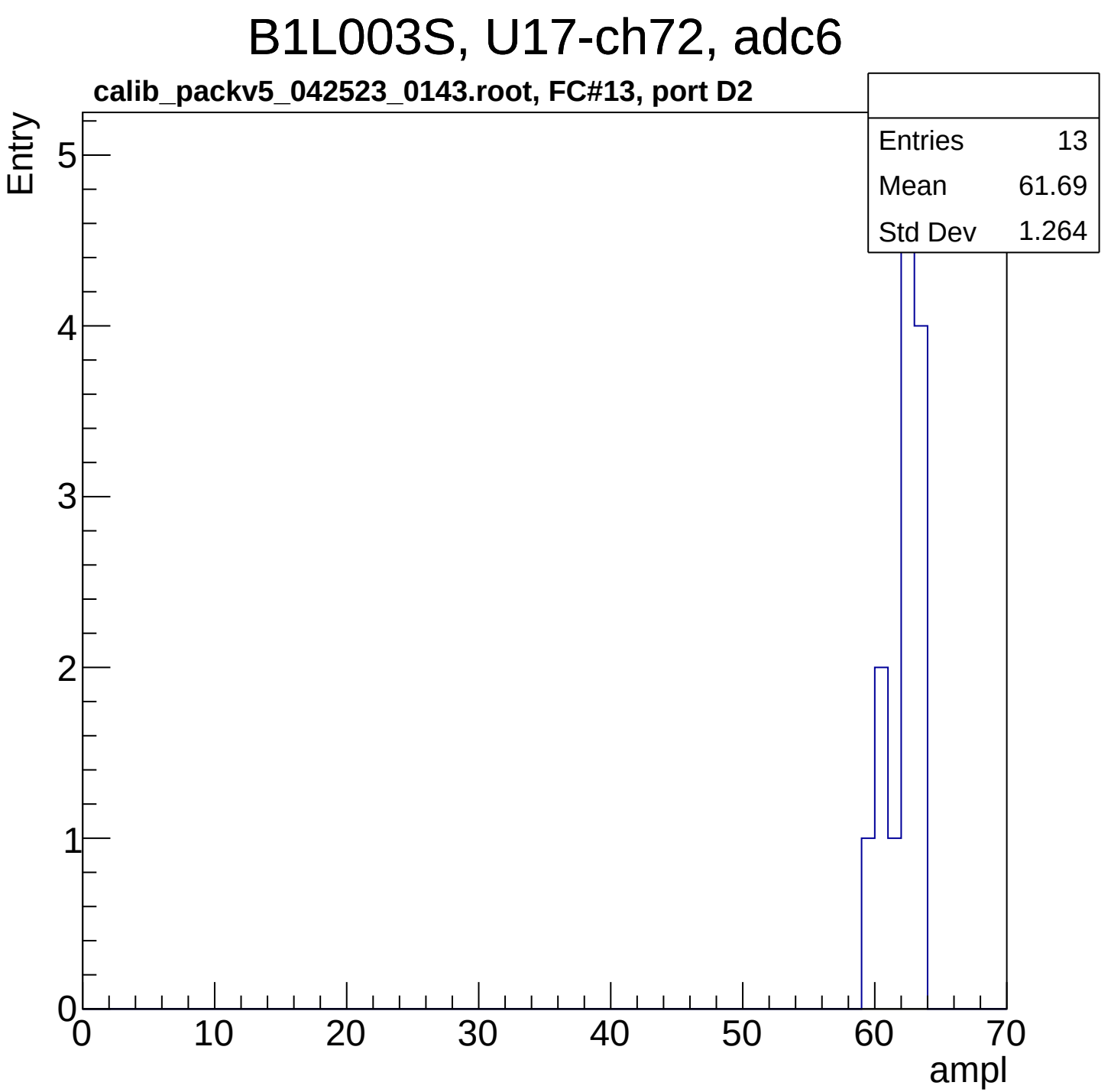
B1L003S, U17-ch72, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.69
Std Dev 1.264
Entry
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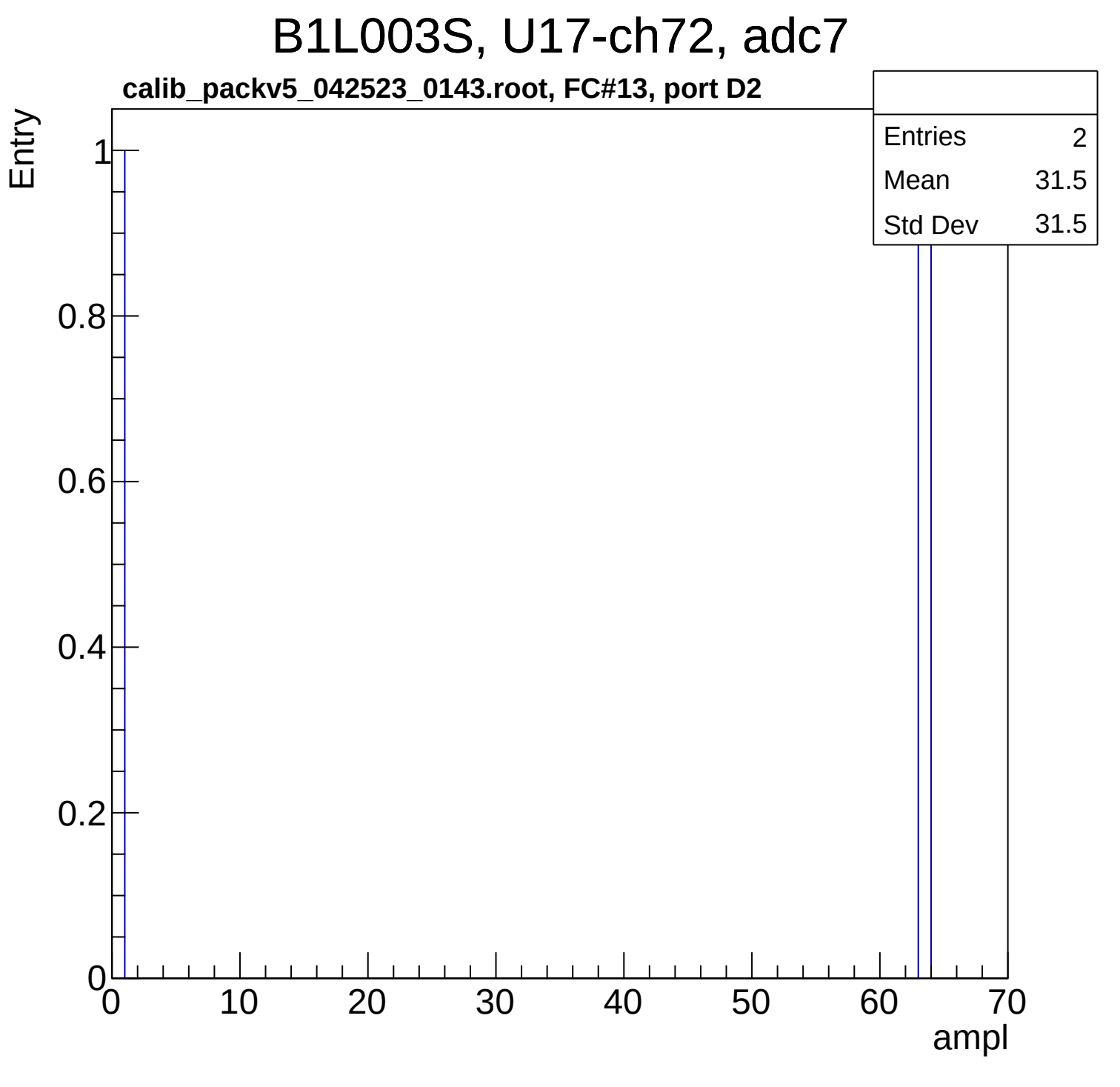
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch72, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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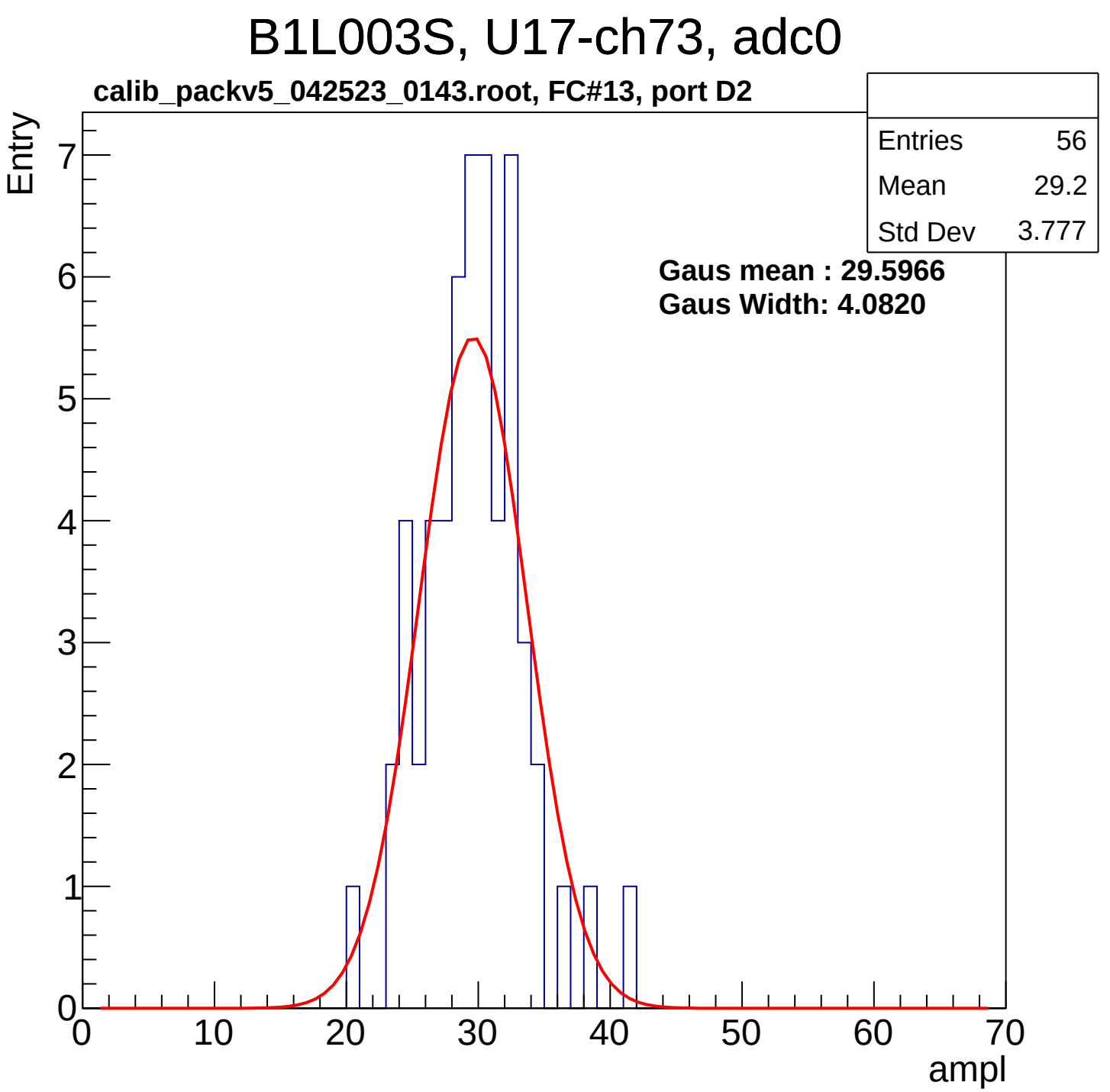
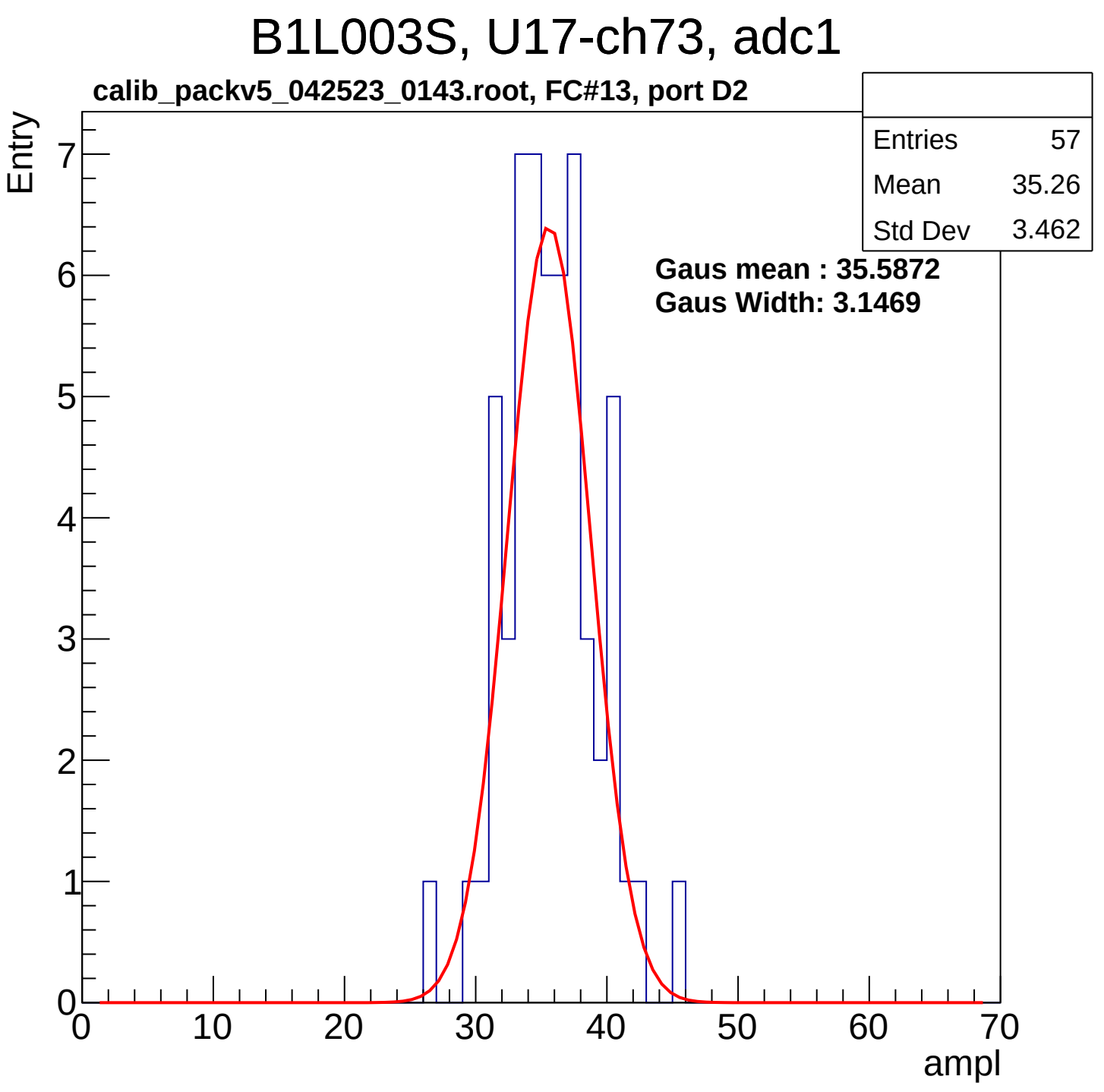
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch73, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 29.2
Std Dev 3.777
Gaus mean : 29.5966
Gaus Width: 4.0820
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch73, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.26
Std Dev 3.462
Gaus mean : 35.5872
Gaus Width: 3.1469
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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50
60
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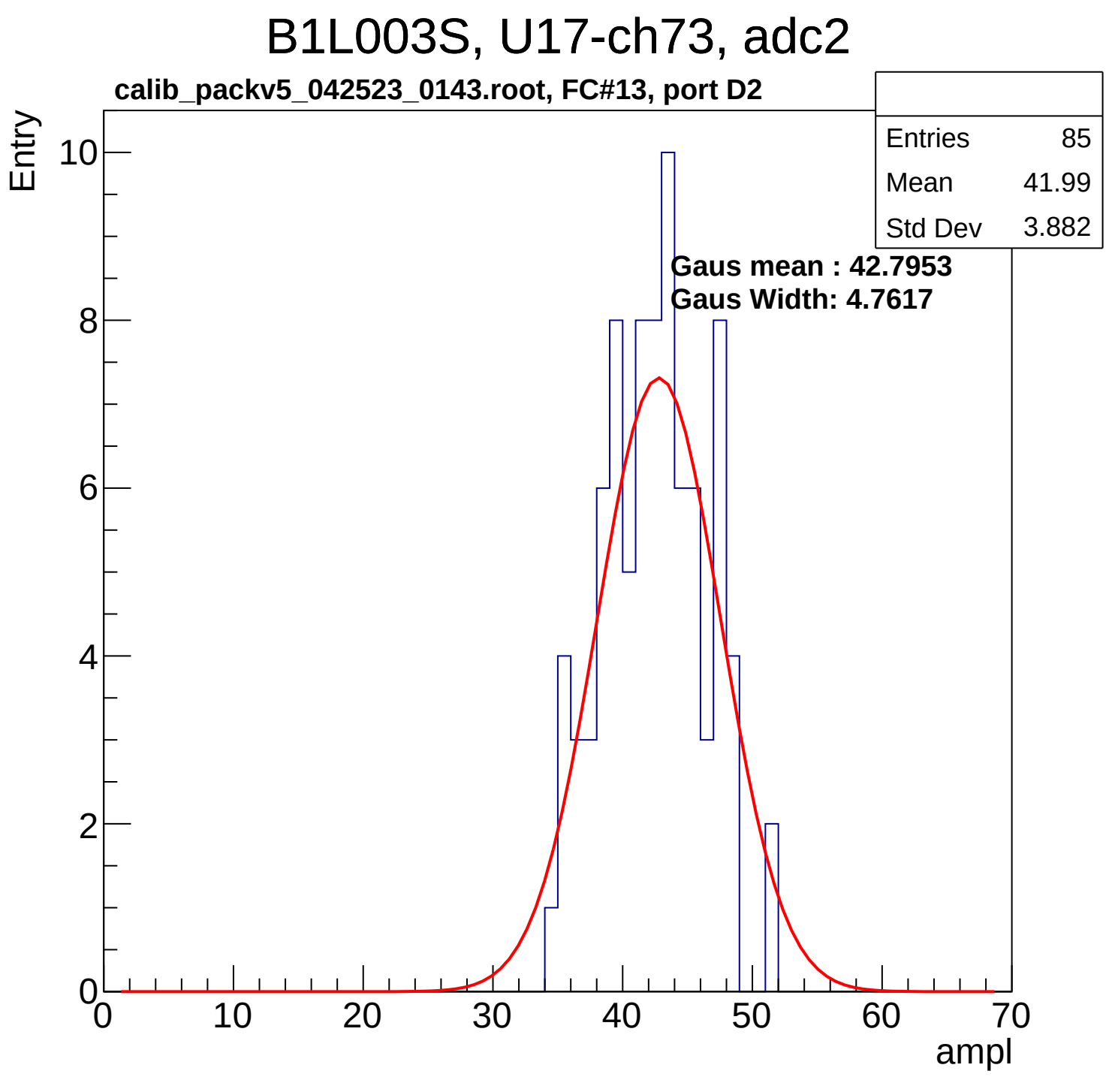
B1L003S, U17-ch73, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 41.99
Std Dev 3.882
Gaus mean : 42.7953
Gaus Width: 4.7617
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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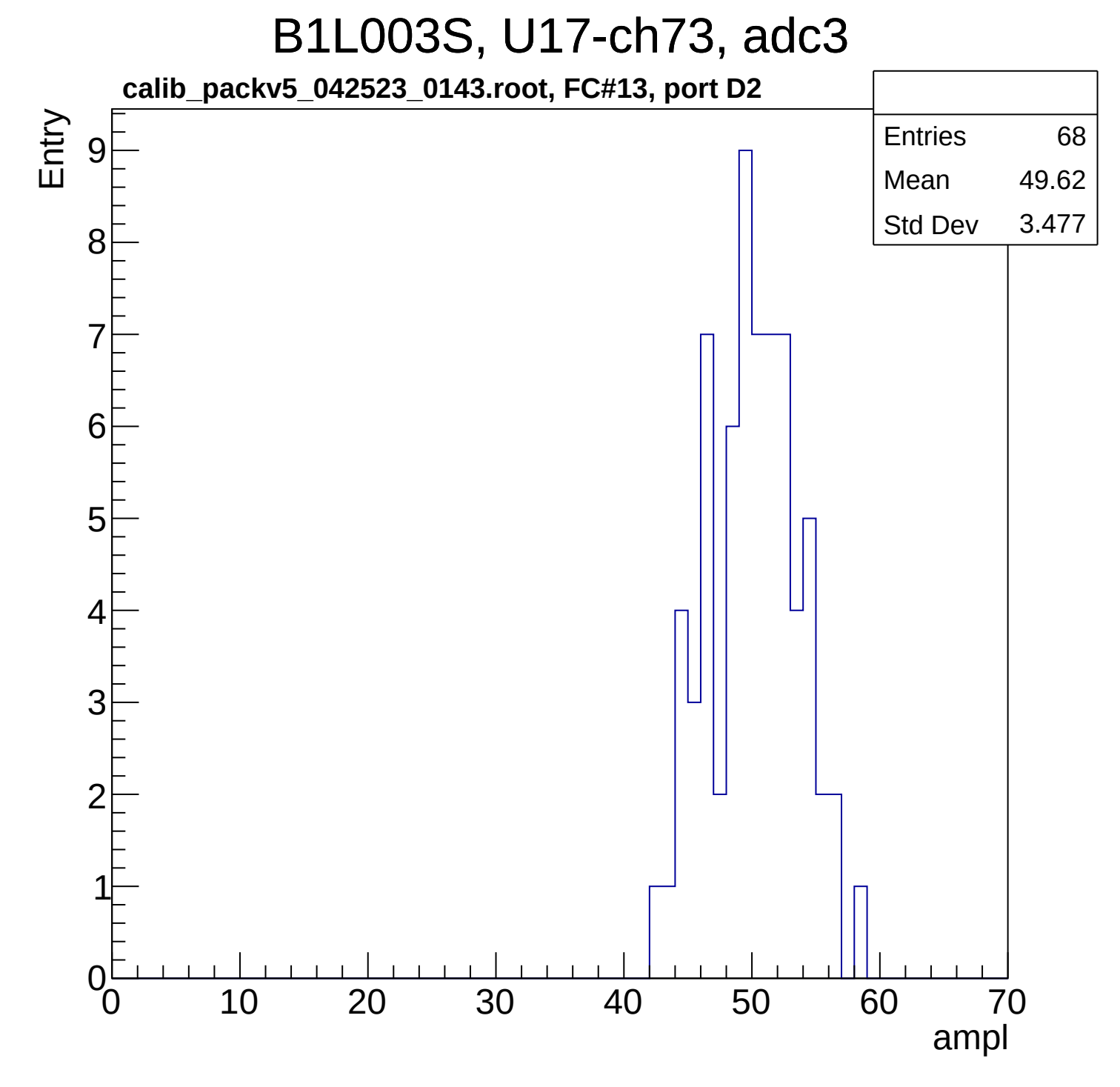
B1L003S, U17-ch73, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.62
Std Dev 3.477
Entry
ampl
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3
4
5
6
7
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9
0
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20
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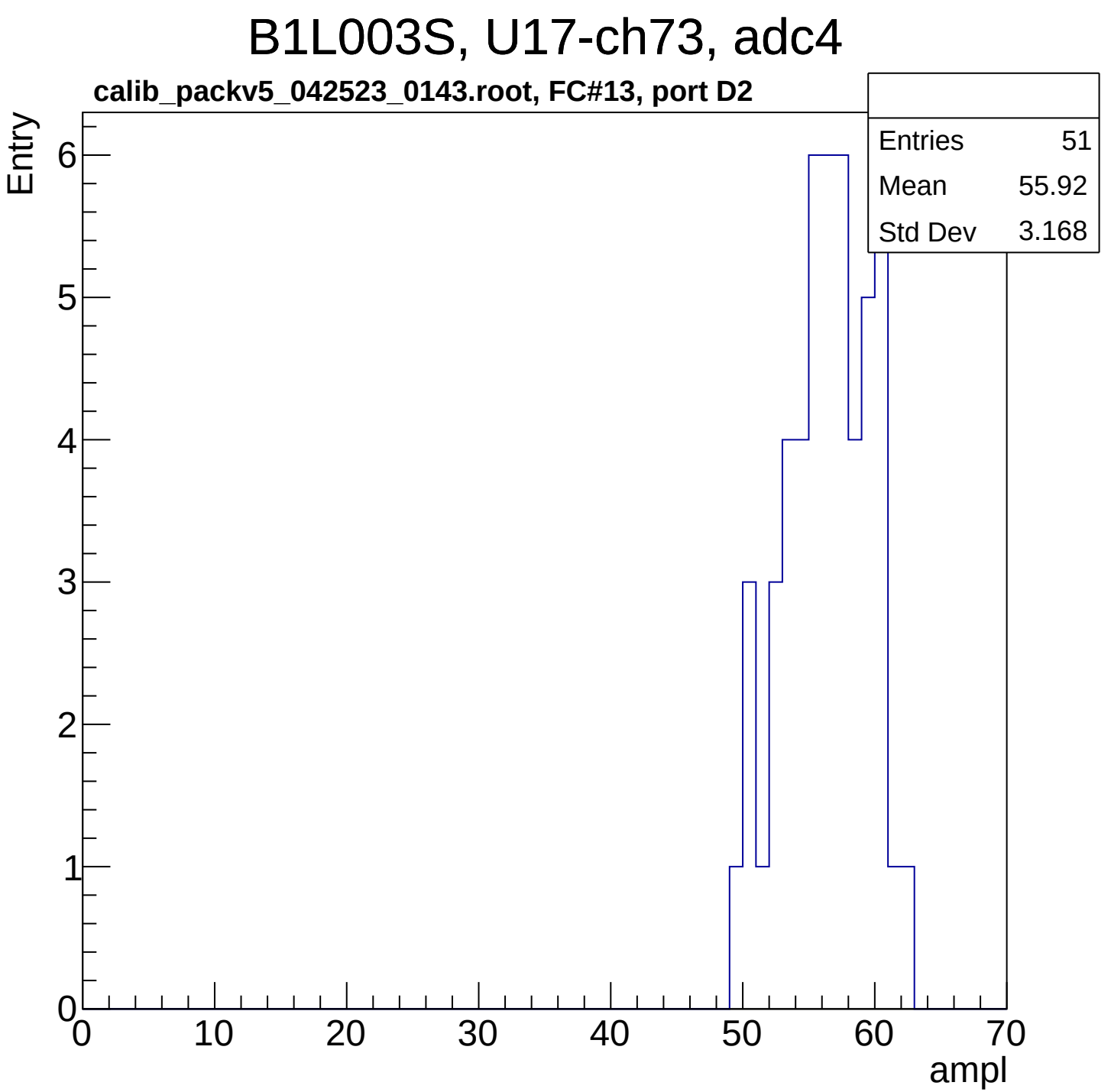
B1L003S, U17-ch73, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.92
Std Dev 3.168
Entry
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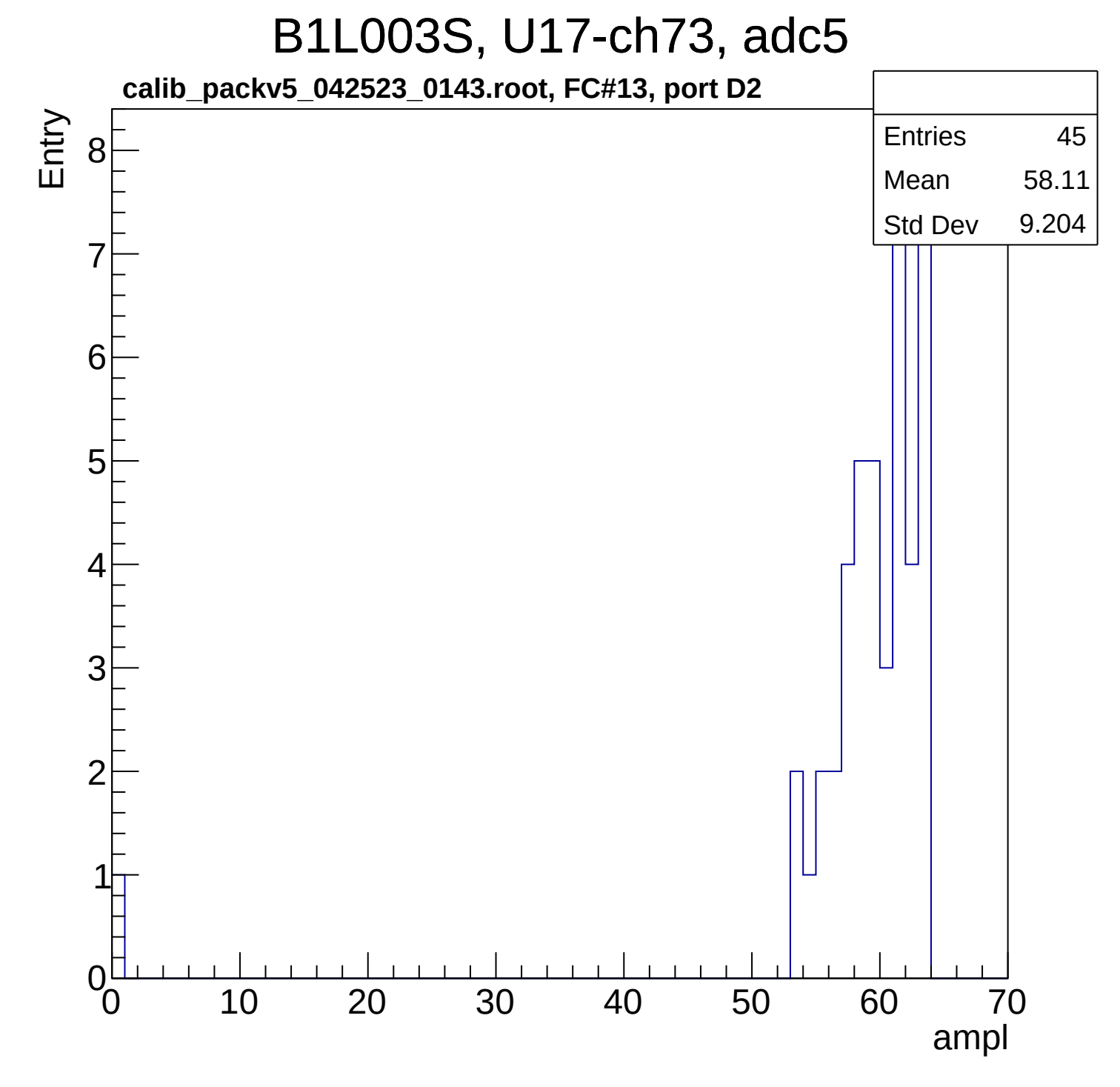
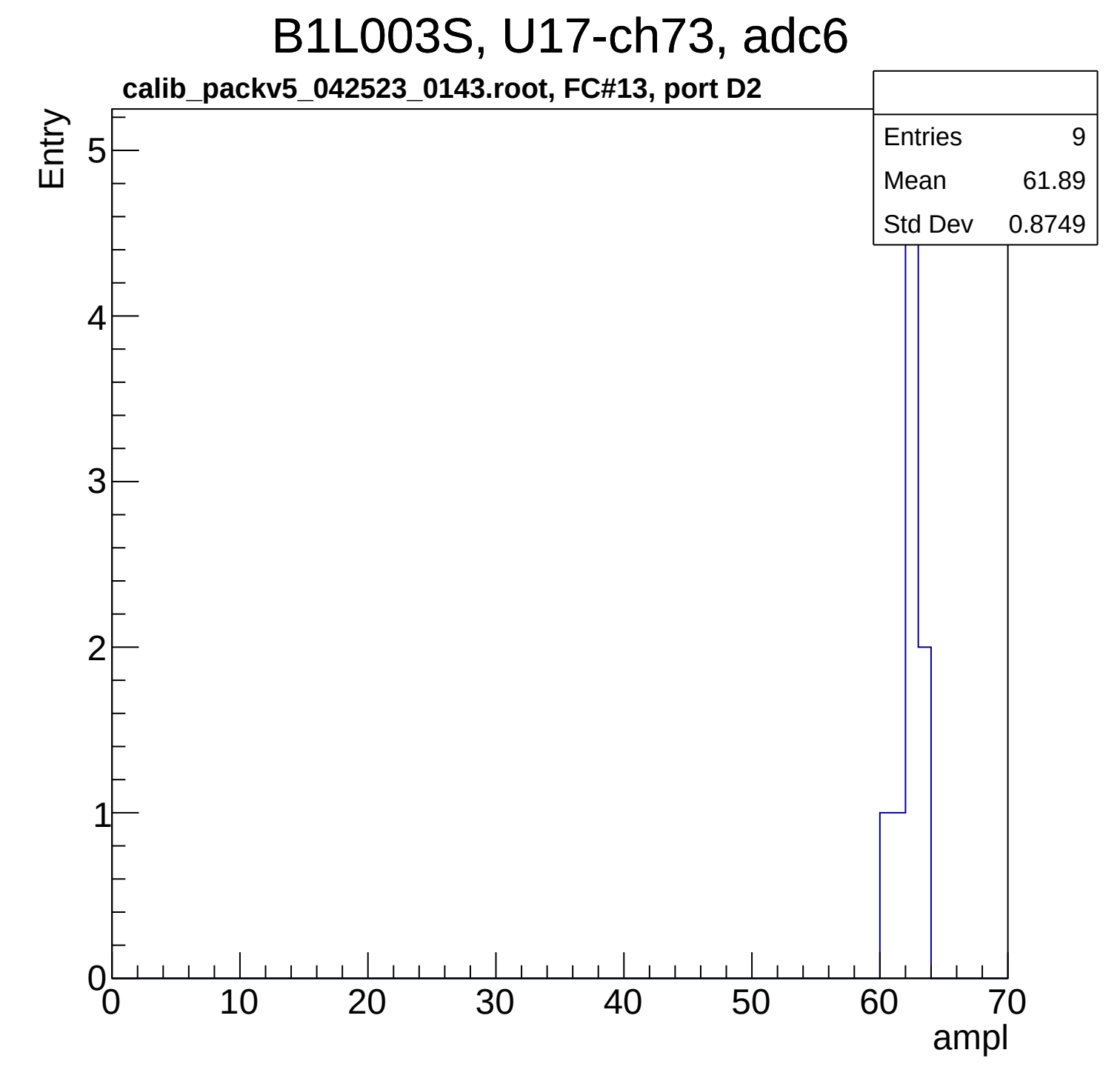
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch73, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.11
Std Dev 9.204
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch73, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.89
Std Dev 0.8749
Entry
0
1
2
3
4
5
0
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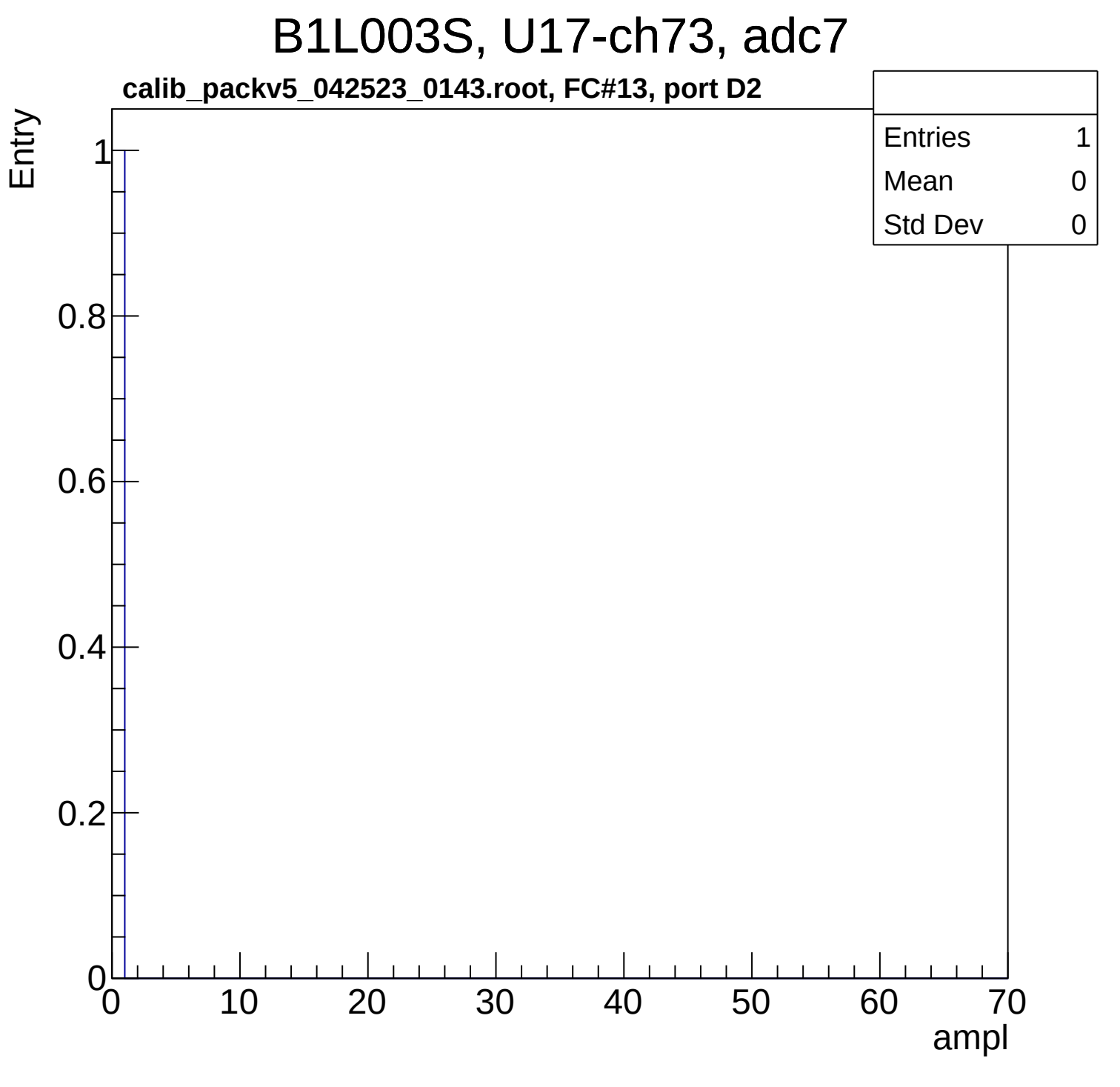
50
60
70
ampl


B1L003S, U17-ch73, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
20
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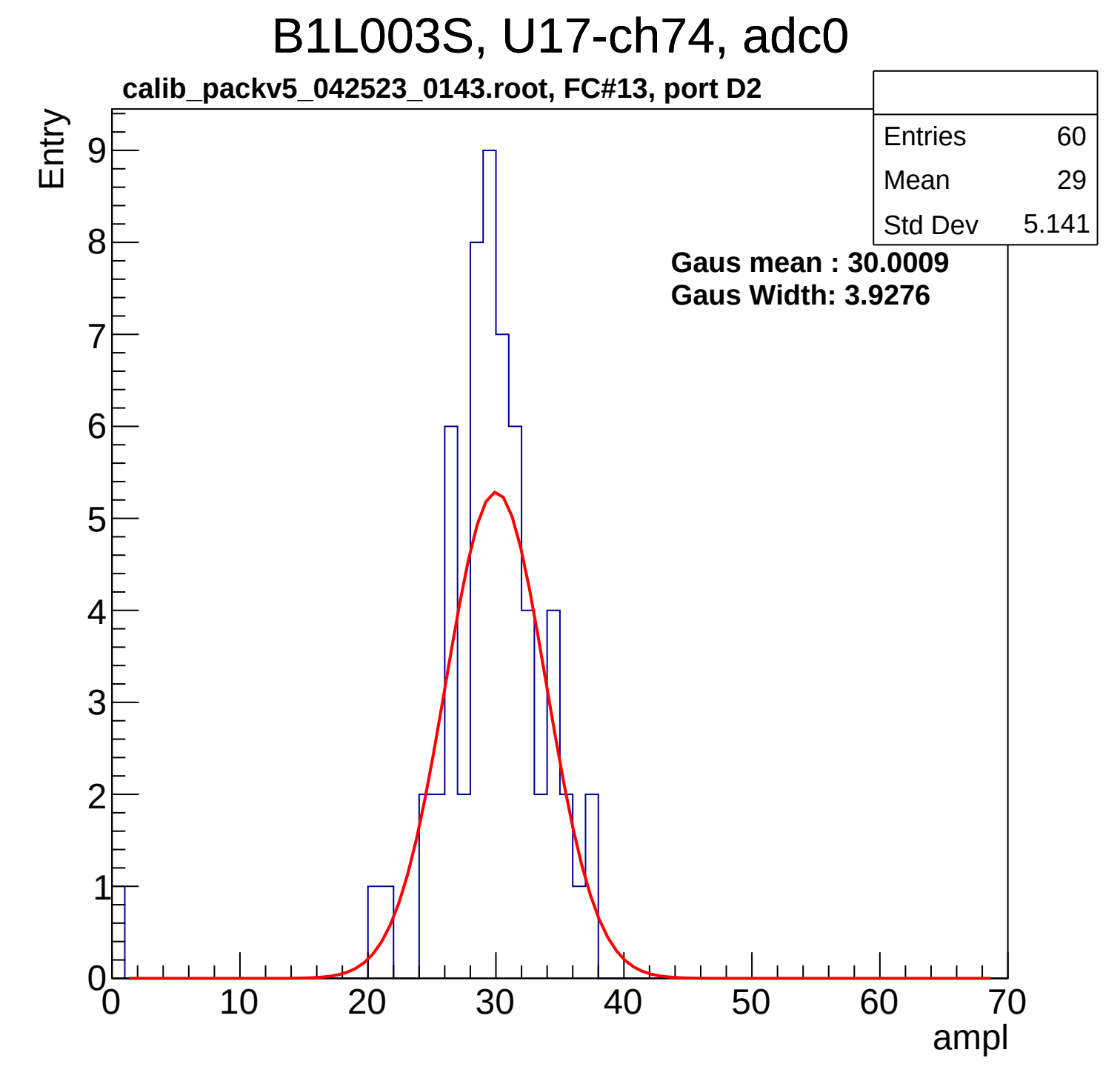
50
60
70
ampl


B1L003S, U17-ch74, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 29
Std Dev 5.141
Gaus mean : 30.0009
Gaus Width: 3.9276
Entry
ampl
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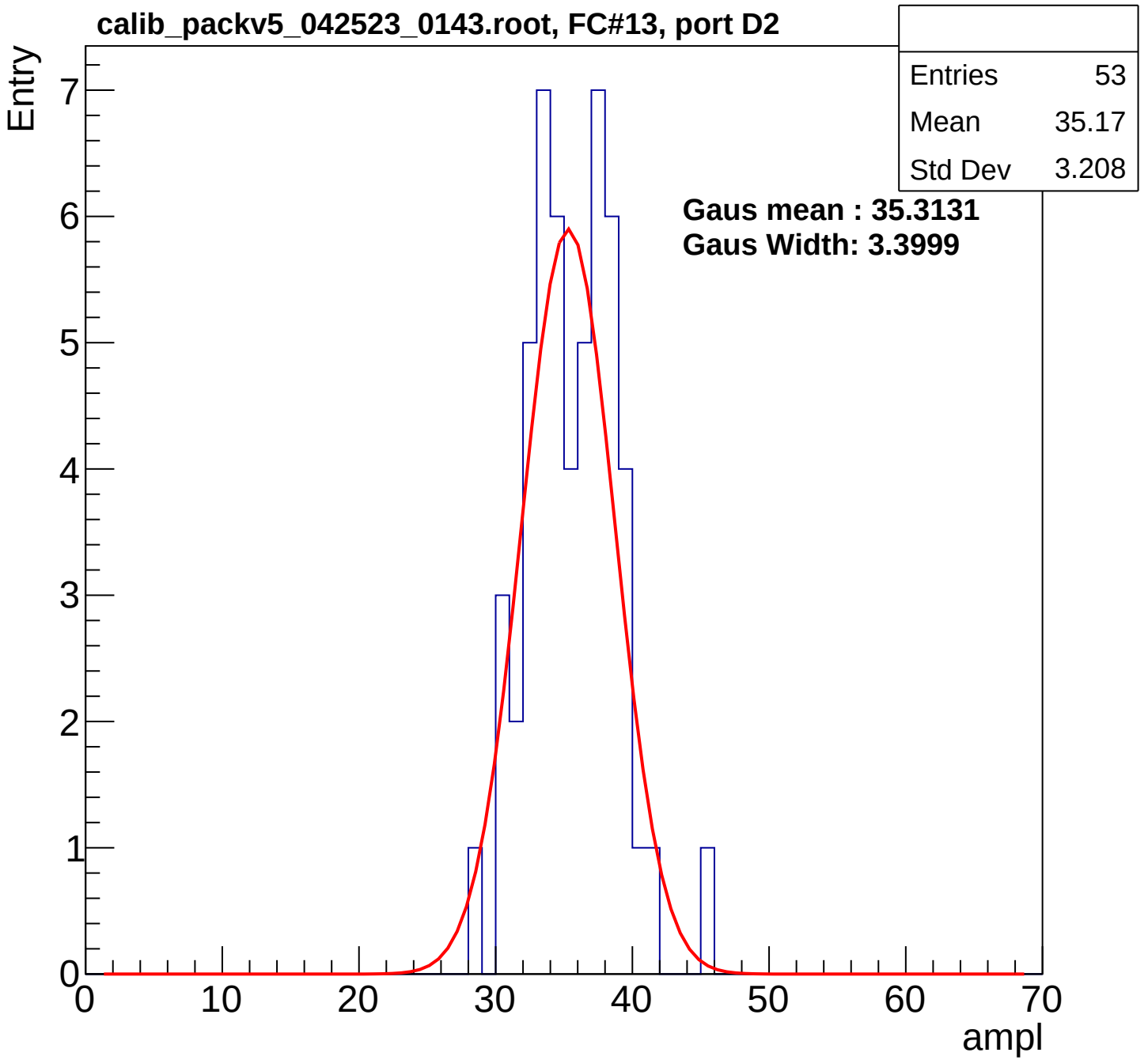
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch74, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 35.17
Std Dev 3.208
Gaus mean : 35.3131
Gaus Width: 3.3999
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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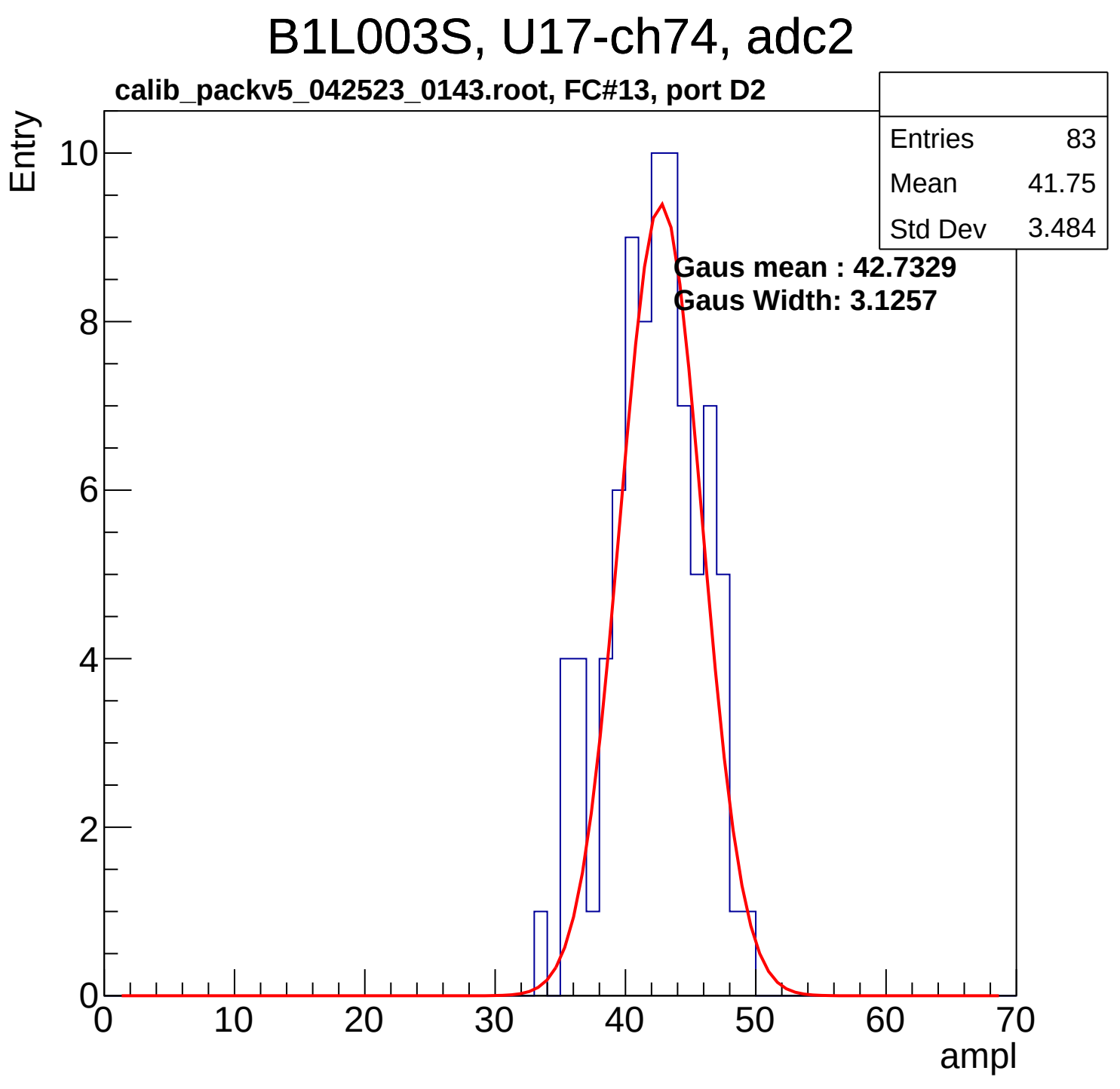
B1L003S, U17-ch74, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 41.75
Std Dev 3.484
Gaus mean : 42.7329
Gaus Width: 3.1257
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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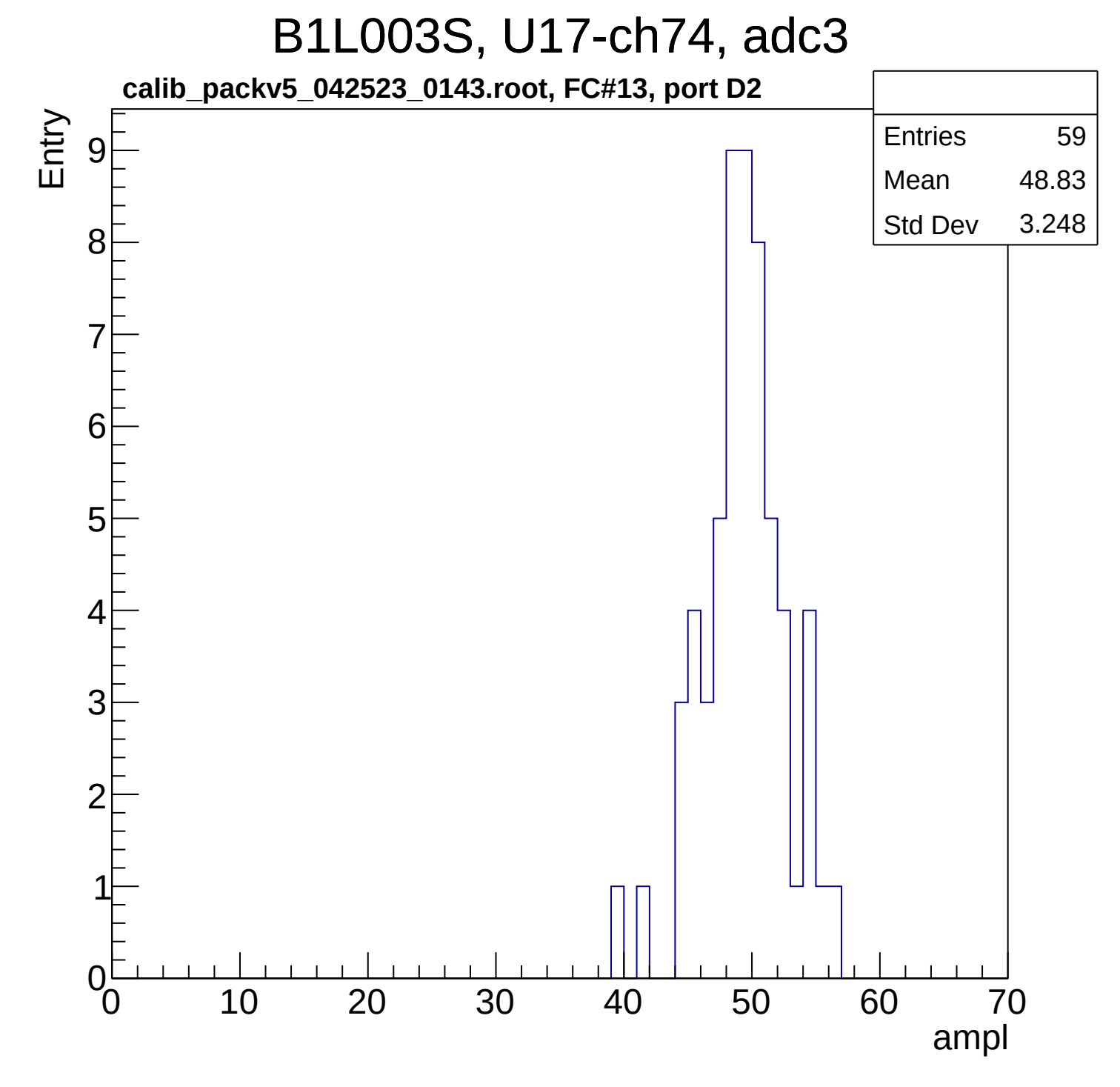
B1L003S, U17-ch74, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48.83
Std Dev 3.248
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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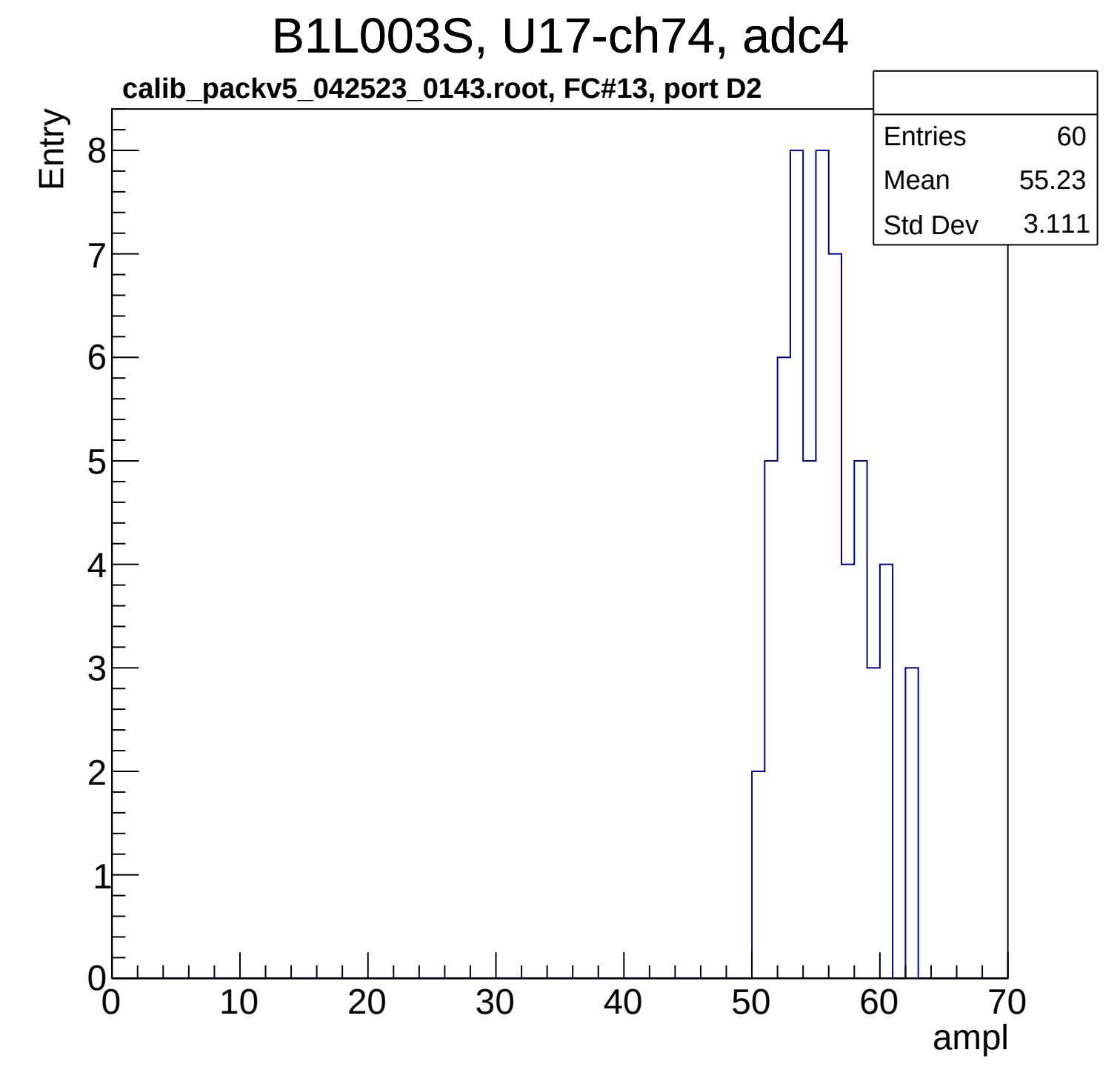
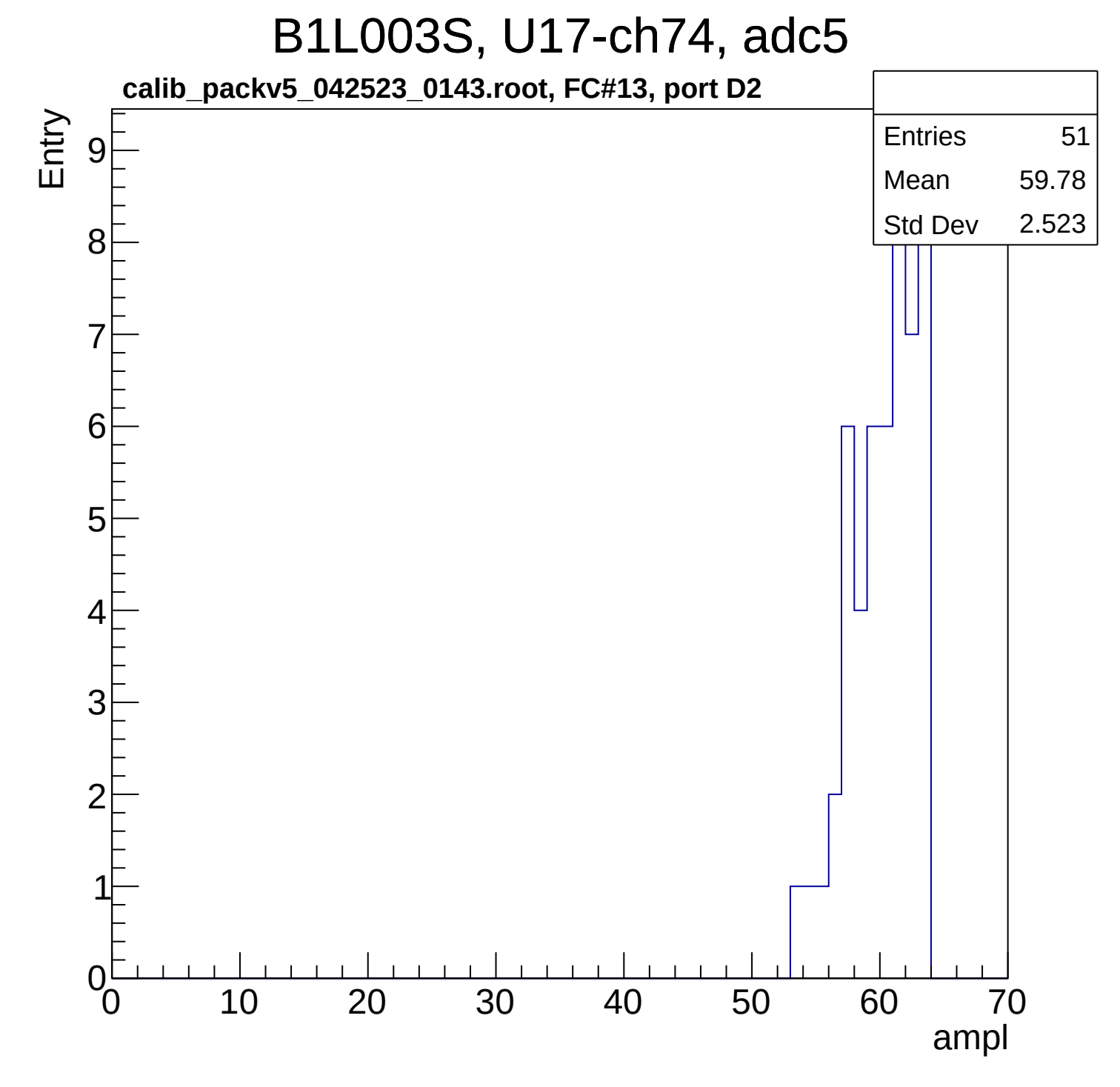

B1L003S, U17-ch74, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.23
Std Dev 3.111
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch74, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.78
Std Dev 2.523
Entry
ampl
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2
3
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5
6
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9
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60
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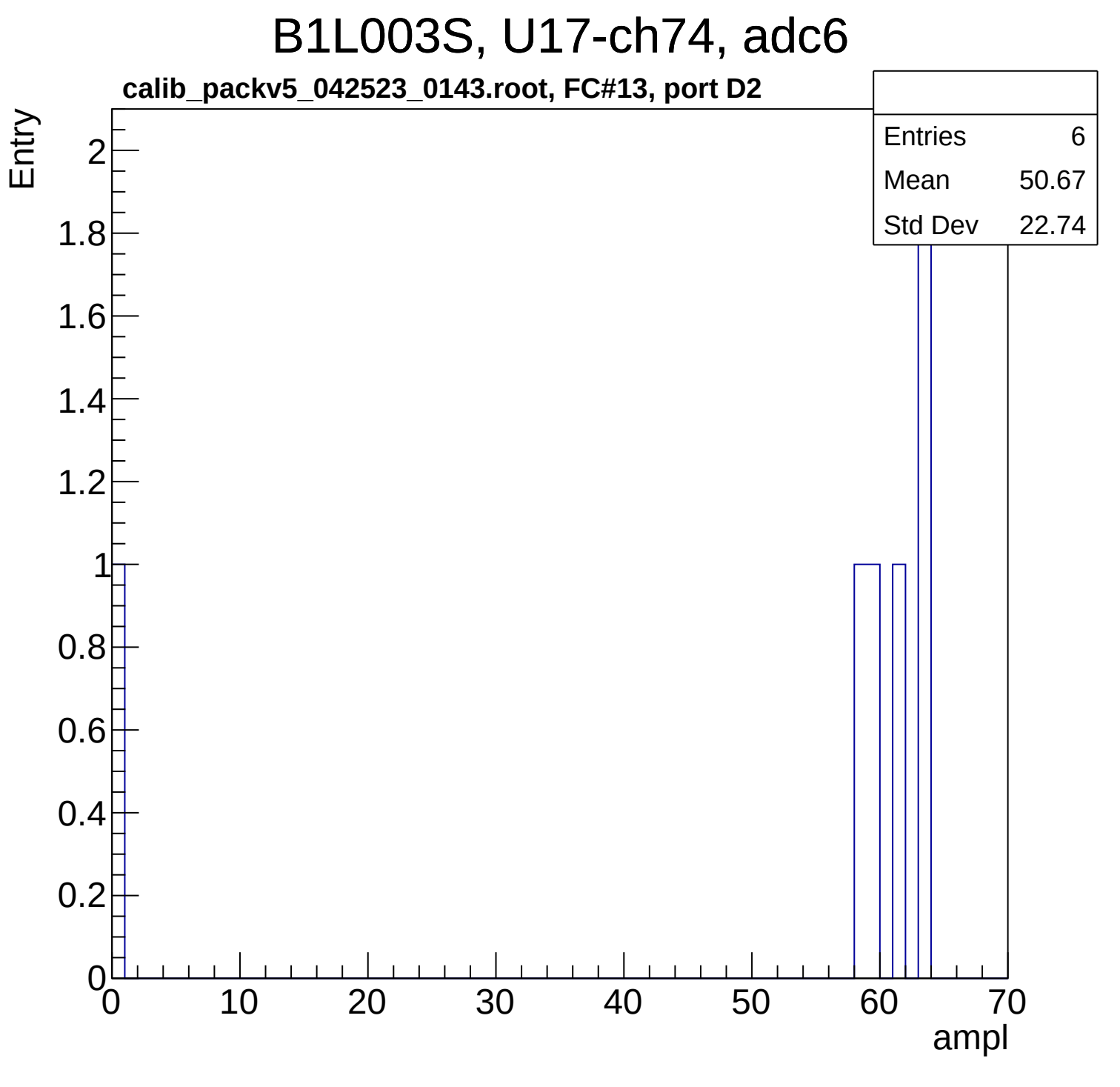
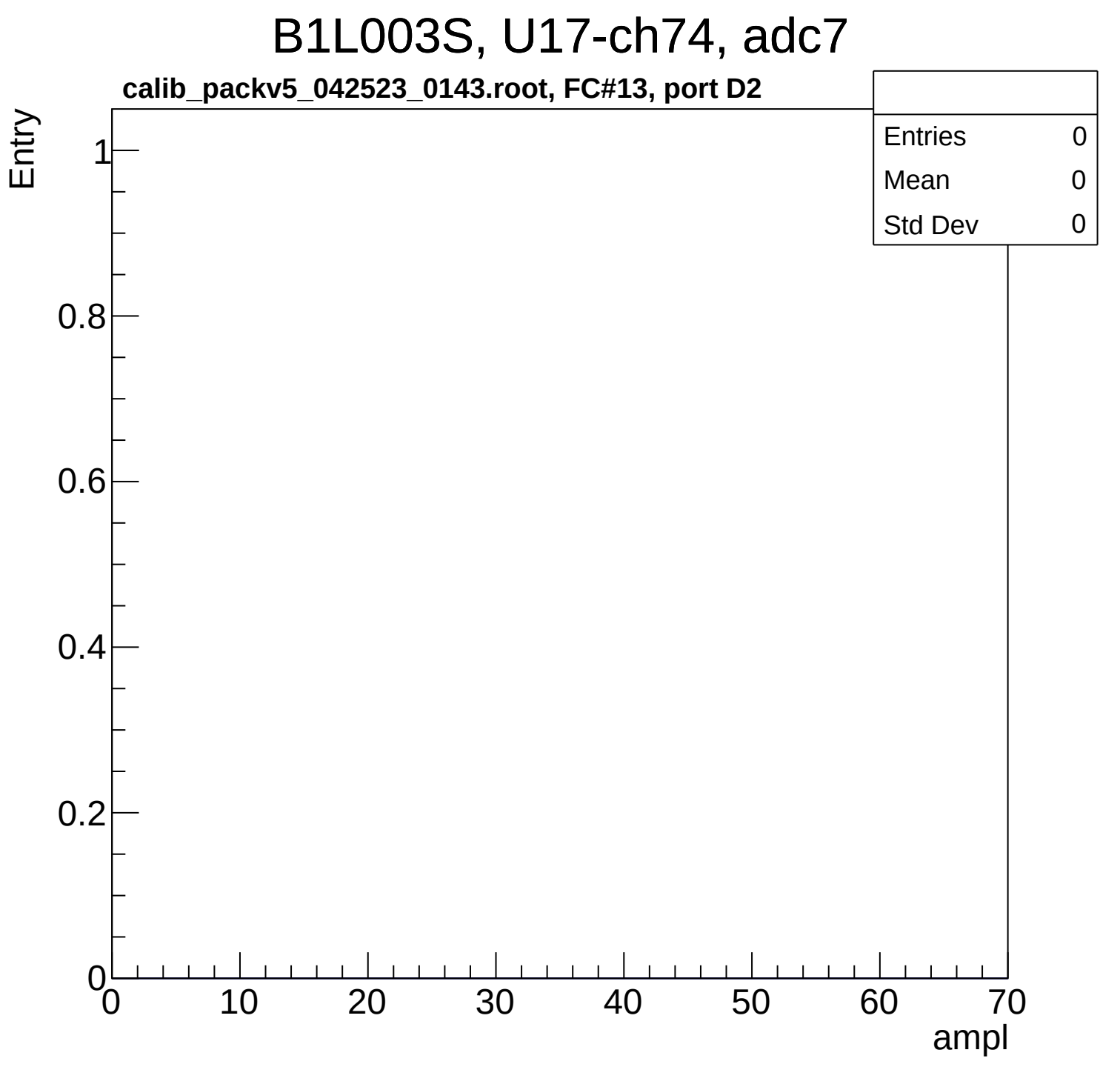

B1L003S, U17-ch74, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 50.67
Std Dev 22.74
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U17-ch74, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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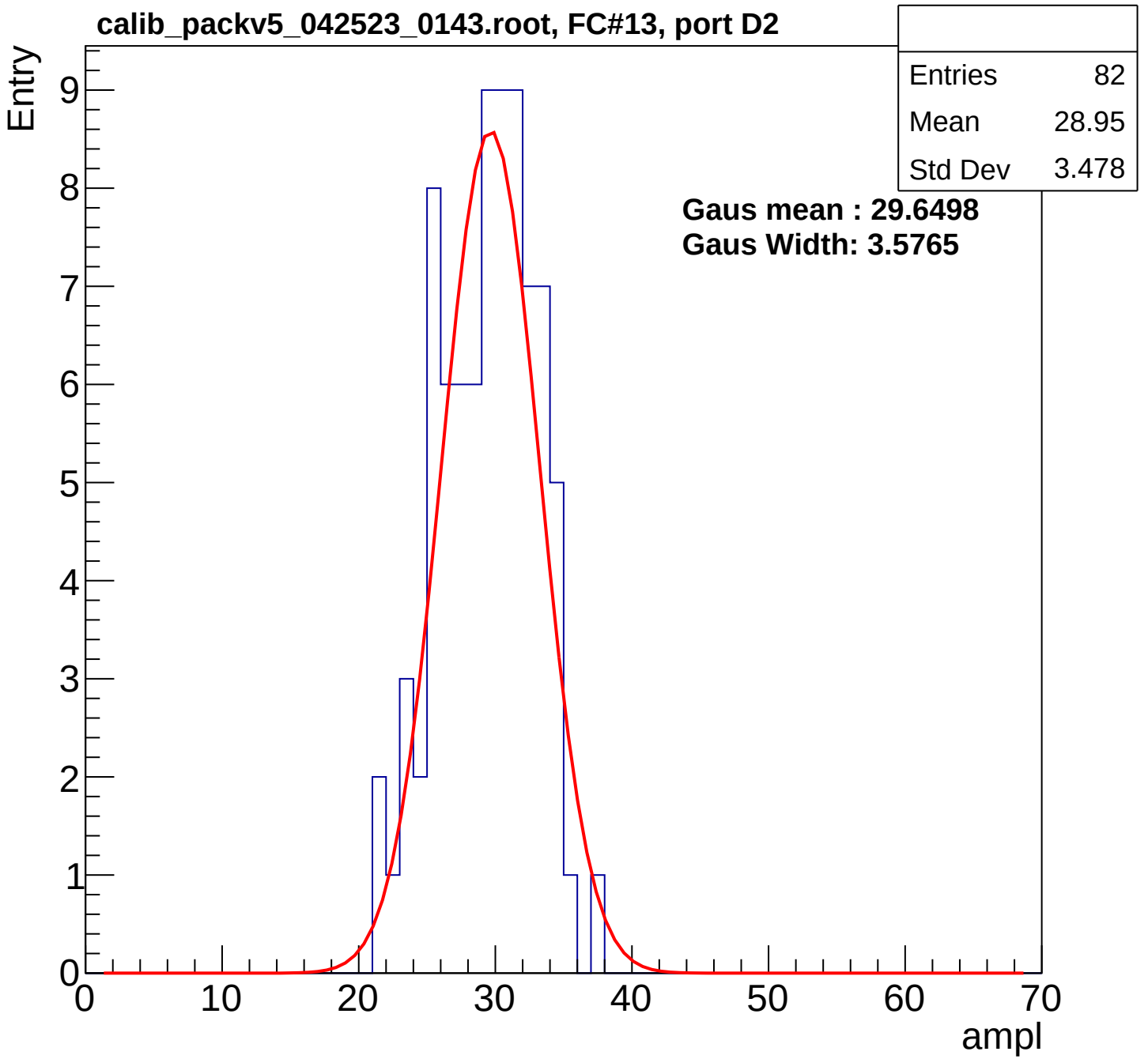
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch75, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 28.95
Std Dev 3.478
Gaus mean : 29.6498
Gaus Width: 3.5765
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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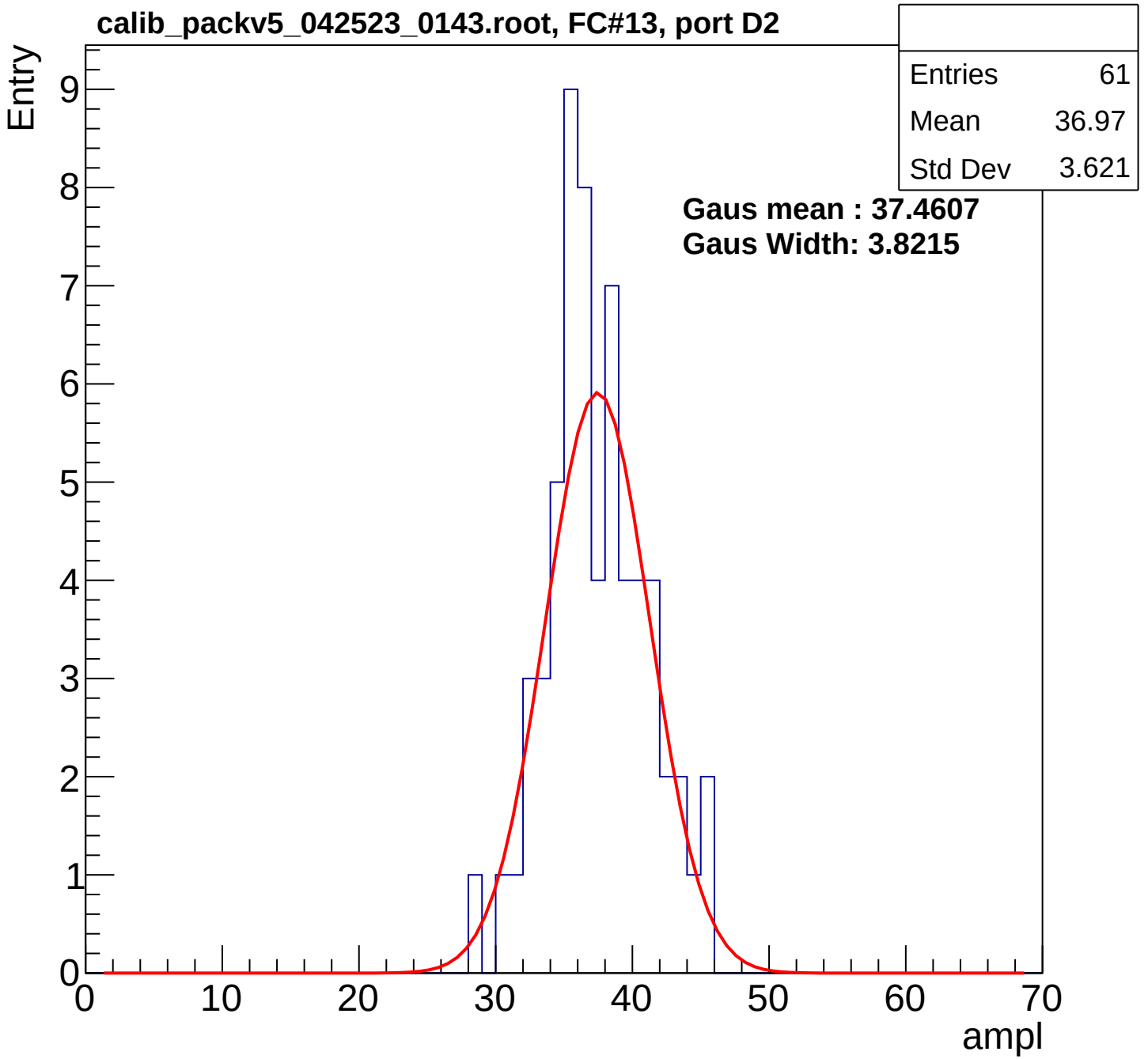
B1L003S, U17-ch75, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.97
Std Dev 3.621
Gaus mean : 37.4607
Gaus Width: 3.8215
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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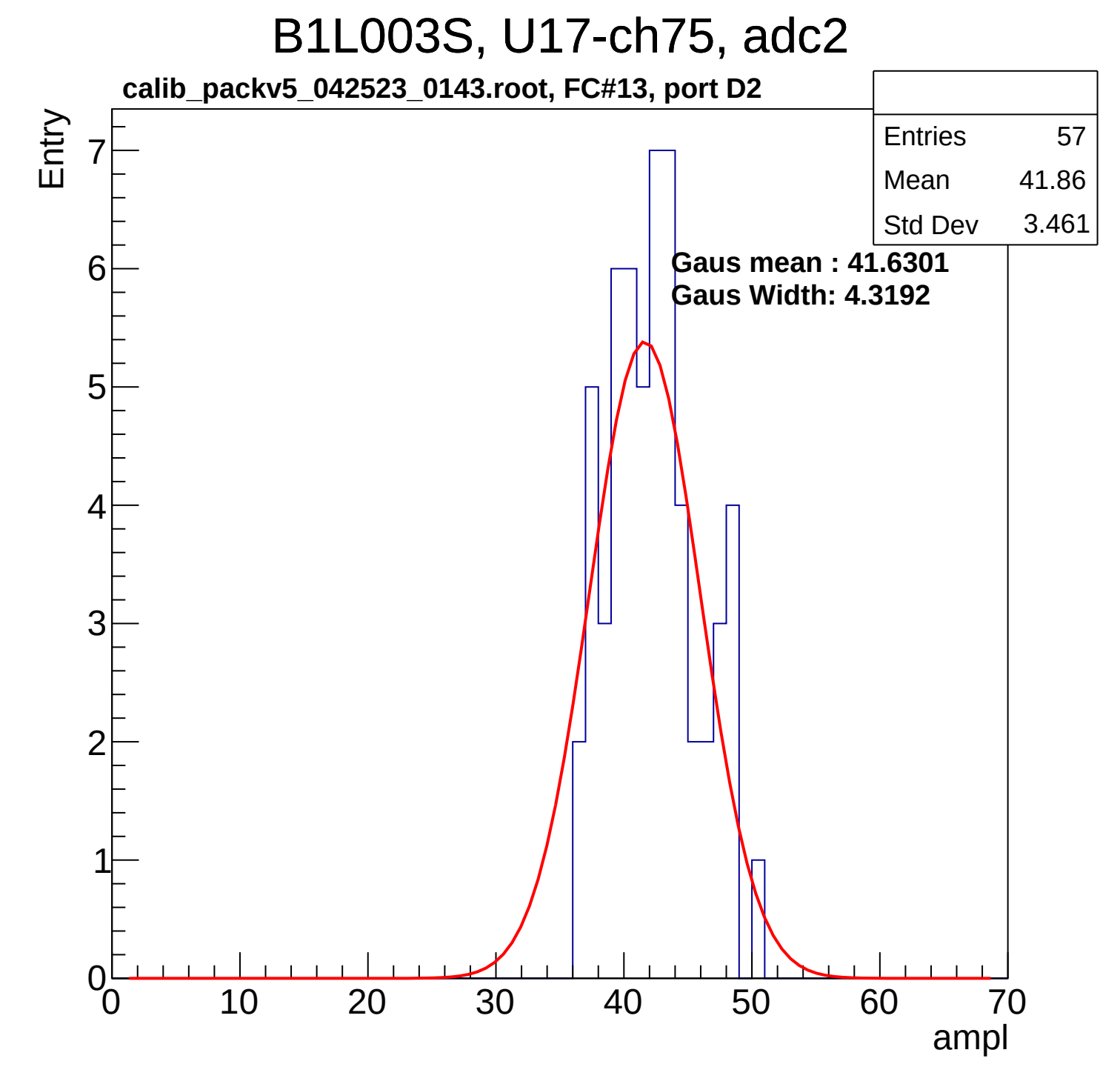
B1L003S, U17-ch75, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 41.86
Std Dev 3.461
Gaus mean : 41.6301
Gaus Width: 4.3192
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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60
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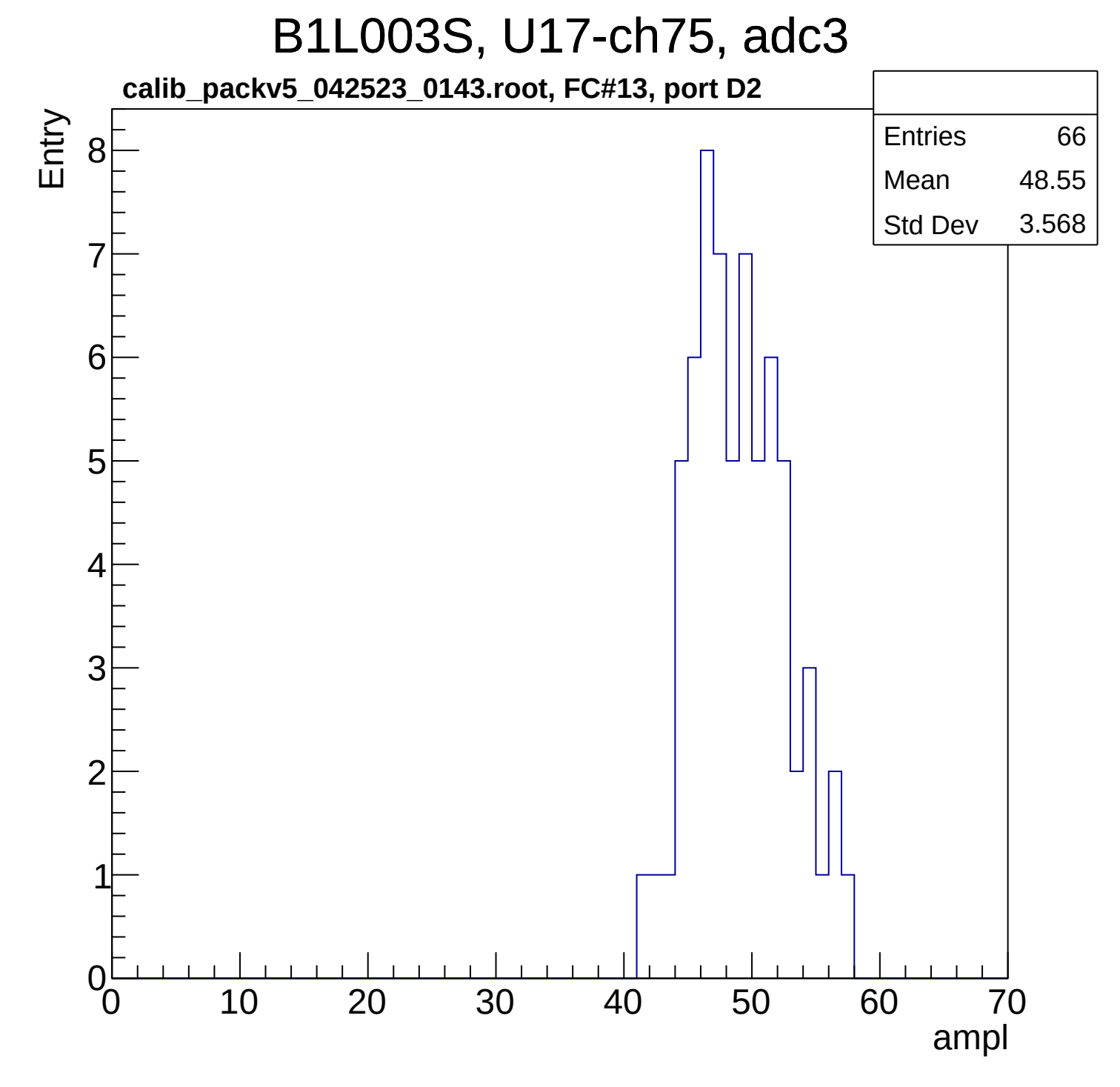
B1L003S, U17-ch75, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.55
Std Dev 3.568
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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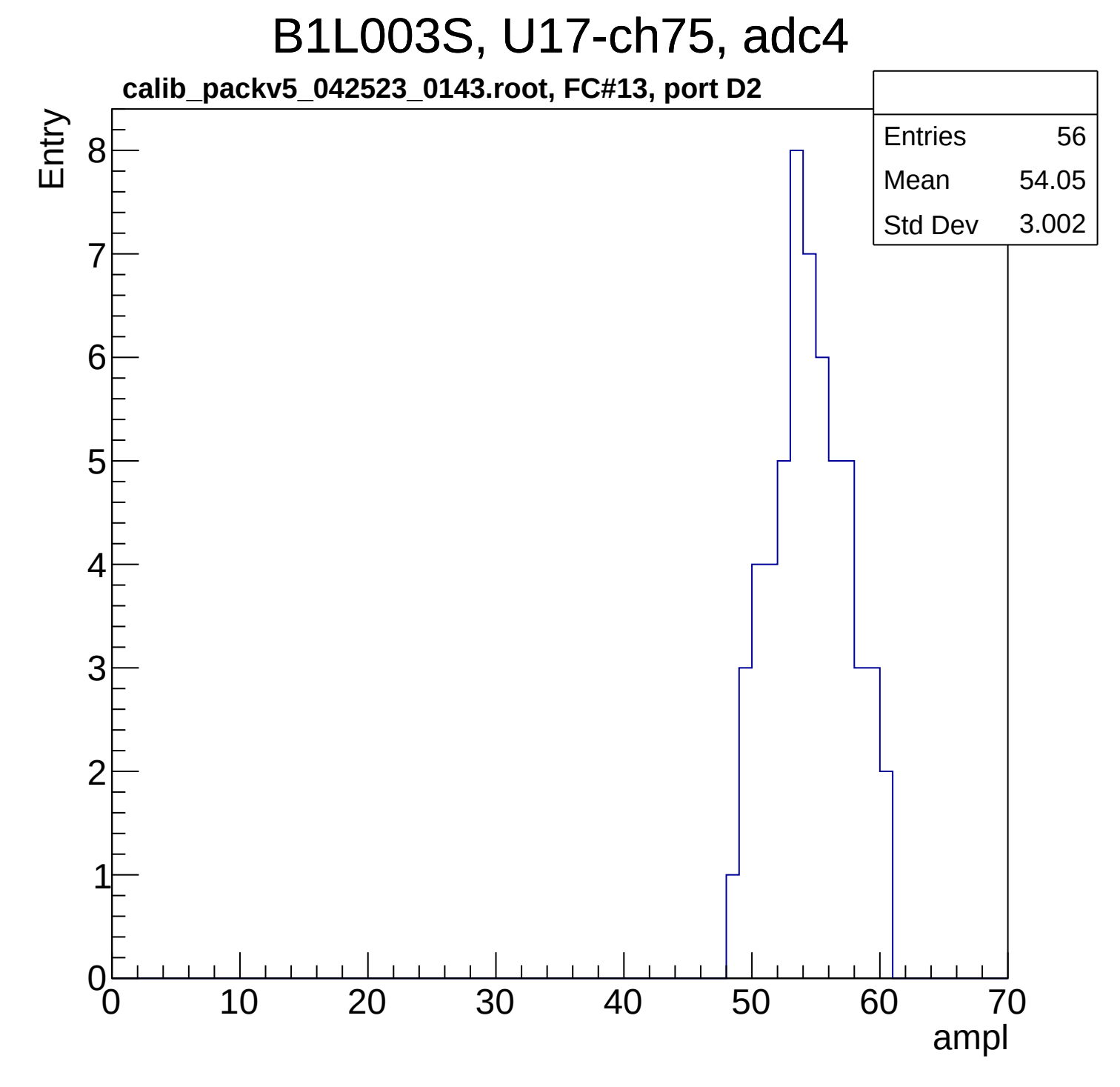
B1L003S, U17-ch75, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 54.05
Std Dev 3.002
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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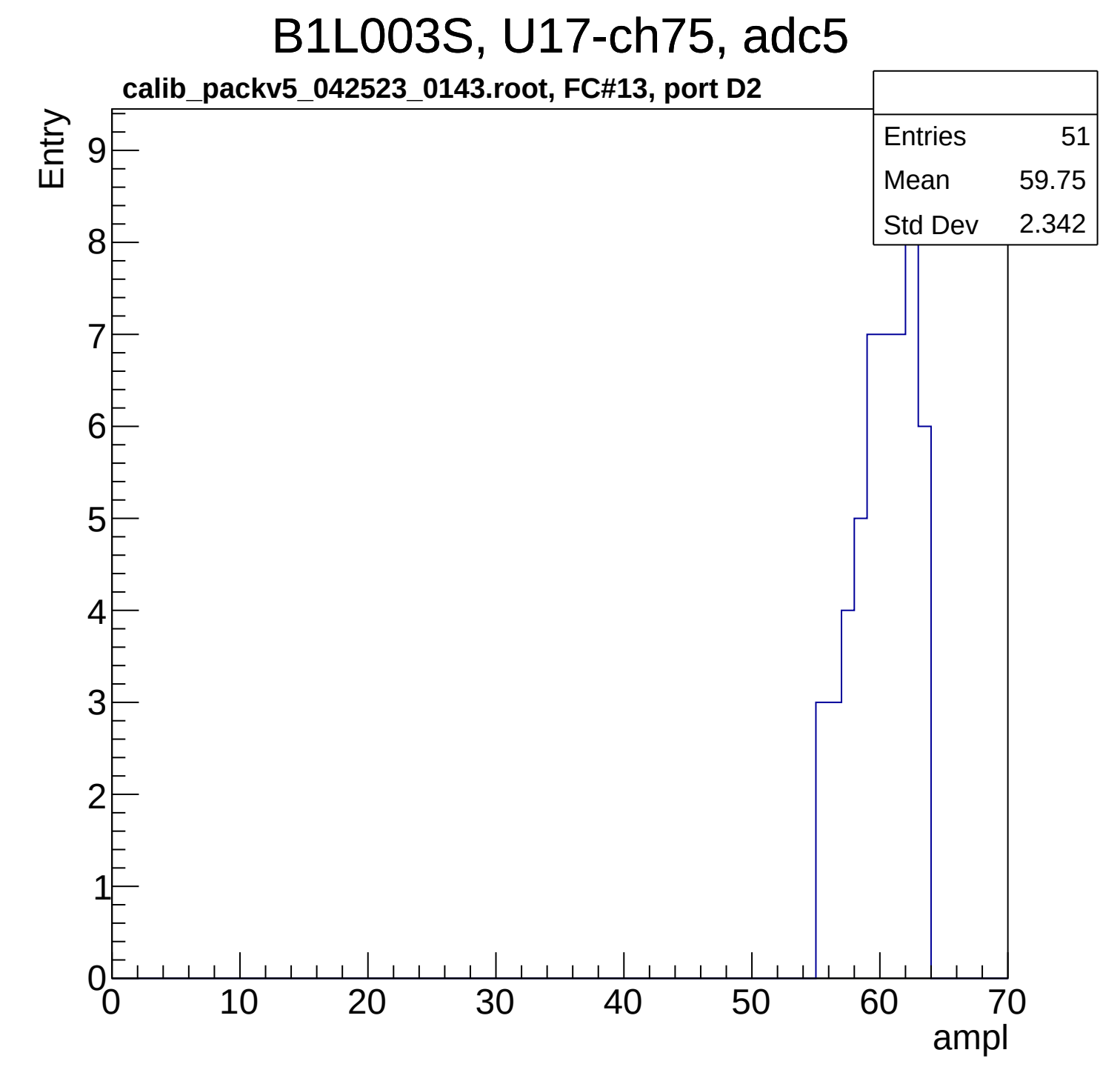
B1L003S, U17-ch75, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.75
Std Dev 2.342
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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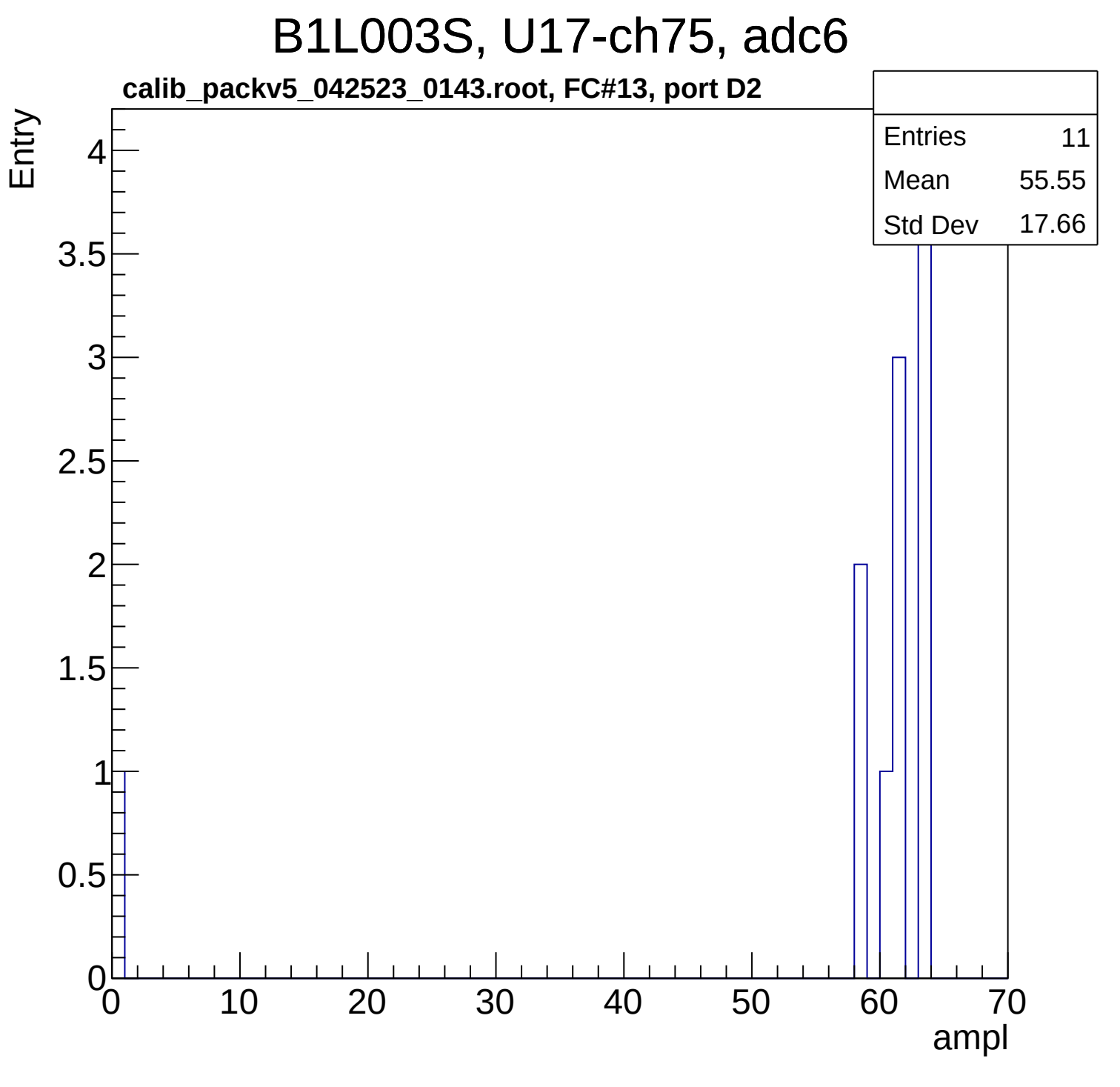
B1L003S, U17-ch75, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.55
Std Dev 17.66
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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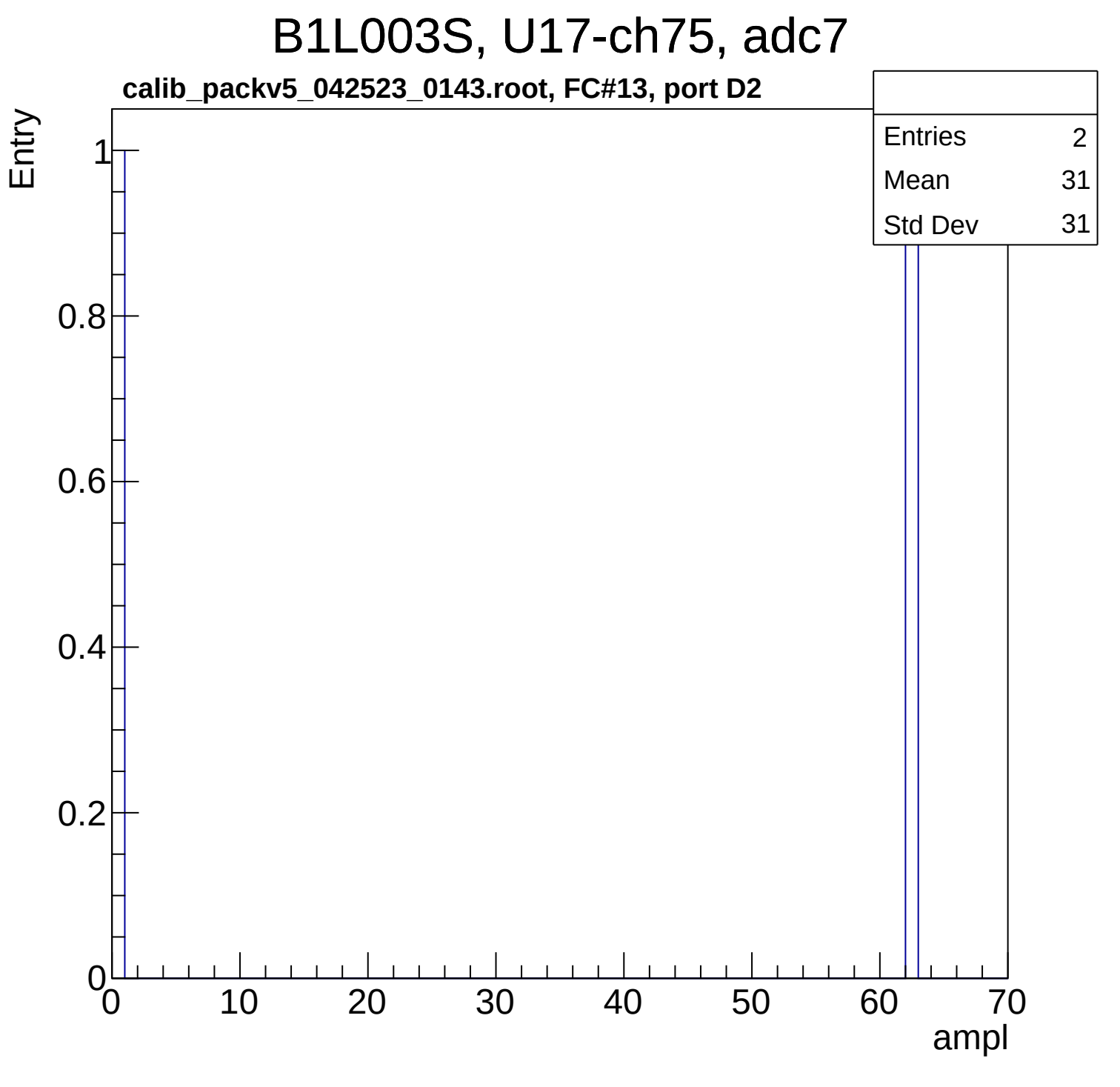
40
50
60
70


B1L003S, U17-ch75, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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0.6
0.4
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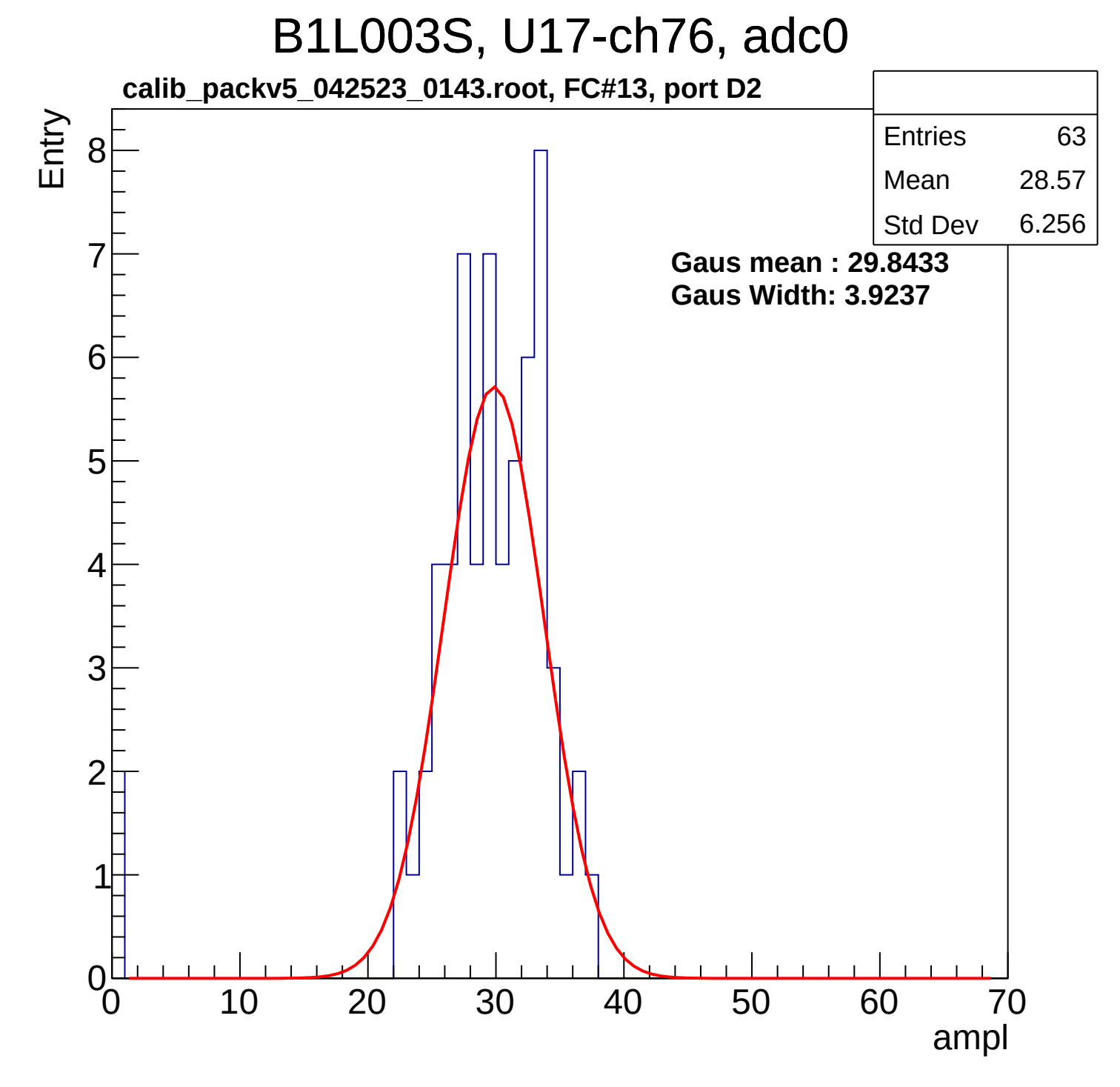
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch76, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.57
Std Dev 6.256
Gaus mean : 29.8433
Gaus Width: 3.9237
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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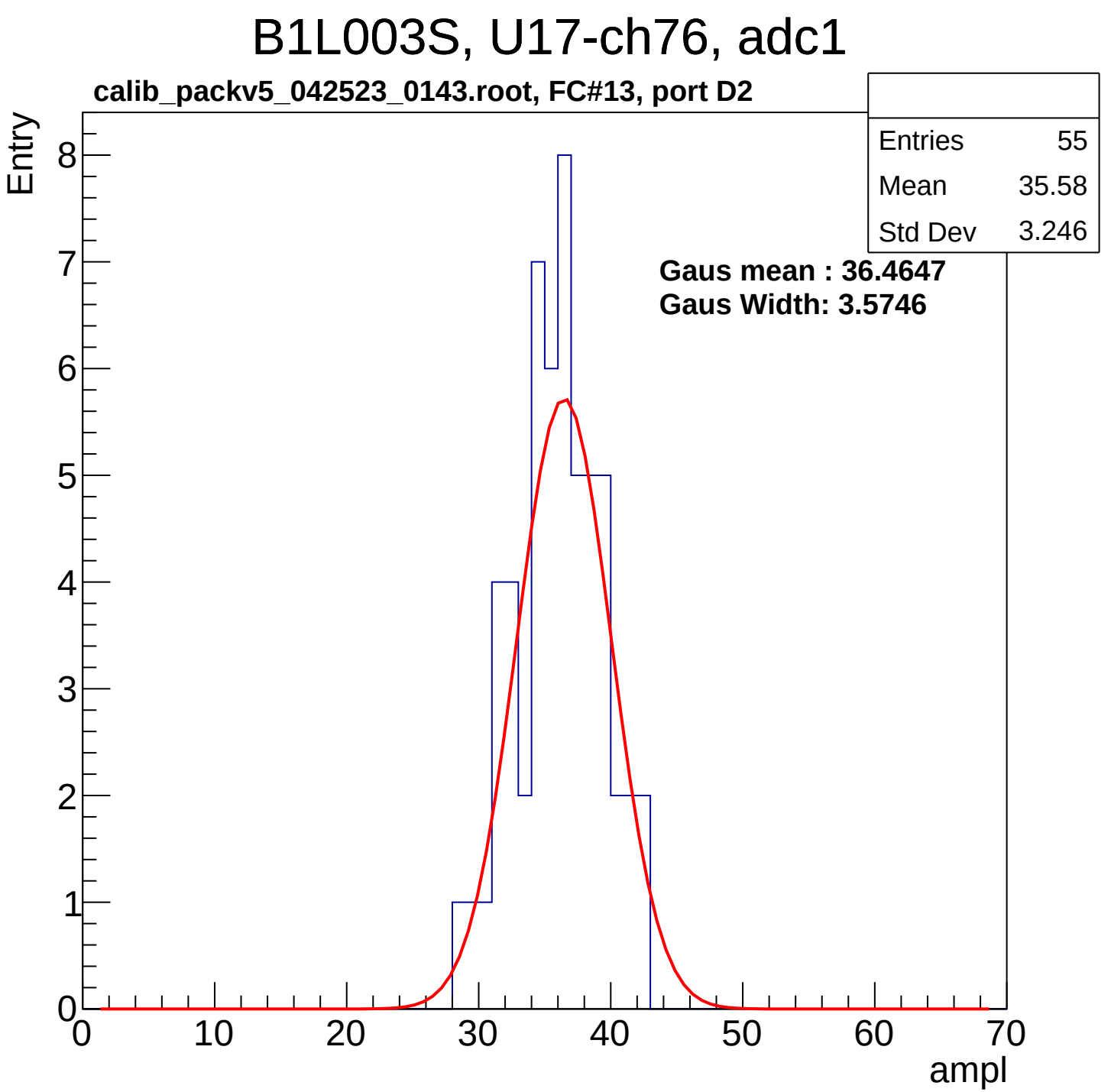
B1L003S, U17-ch76, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 35.58
Std Dev 3.246
Gaus mean : 36.4647
Gaus Width: 3.5746
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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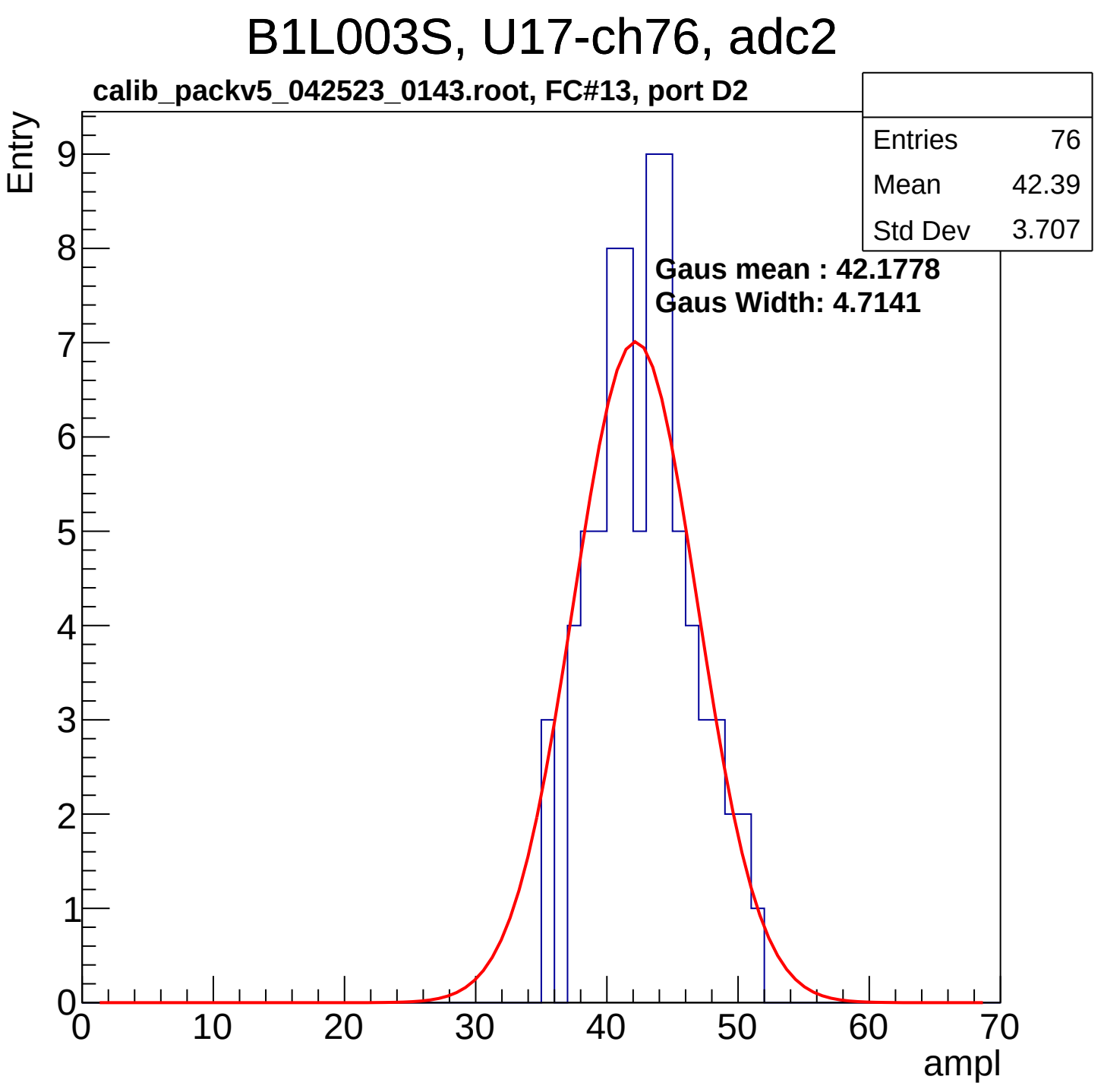
B1L003S, U17-ch76, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 42.39
Std Dev 3.707
Gaus mean : 42.1778
Gaus Width: 4.7141
Entry
ampl
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3
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5
6
7
8
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0
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40
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60
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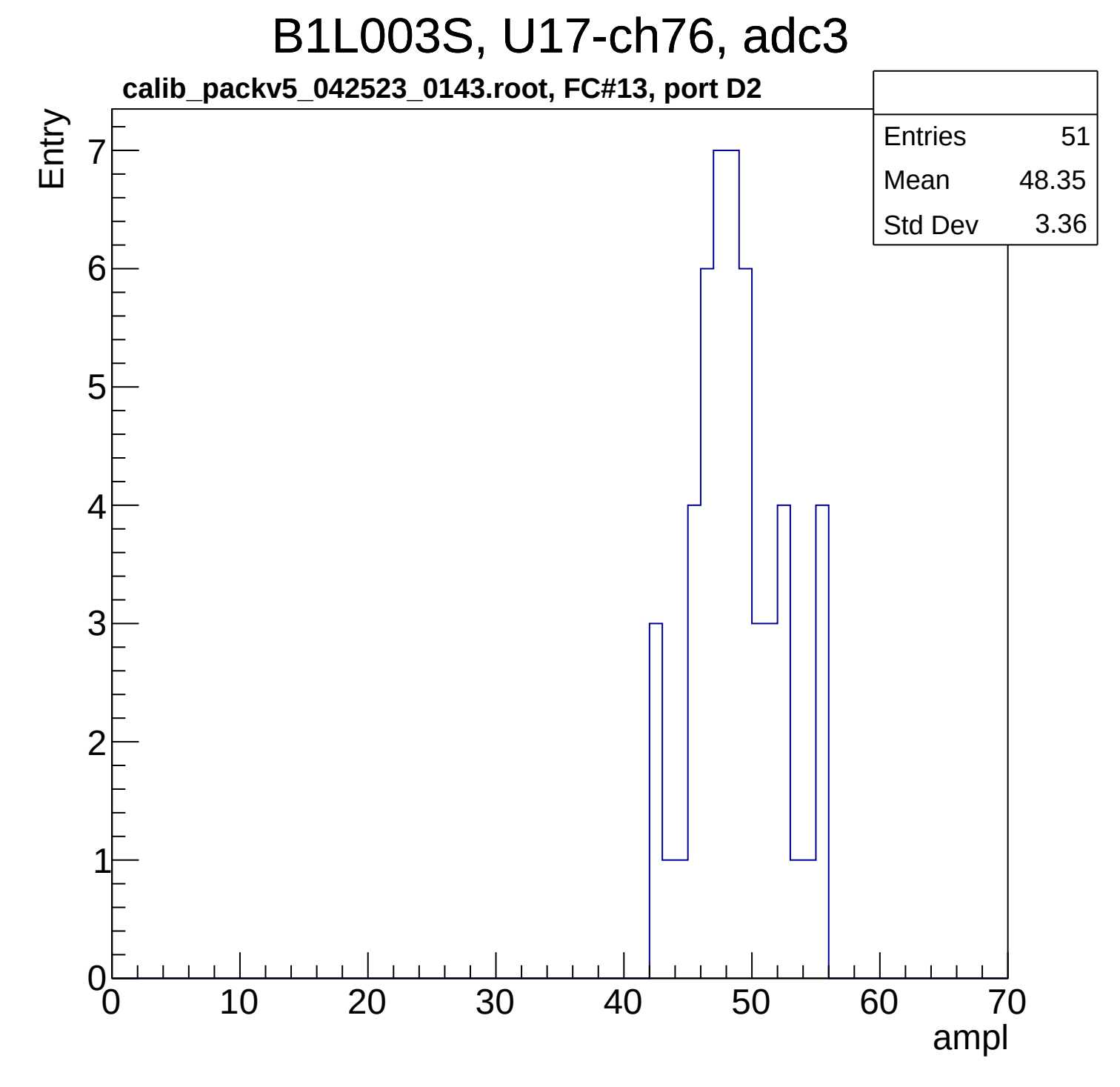
B1L003S, U17-ch76, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 48.35
Std Dev 3.36
Entry
ampl
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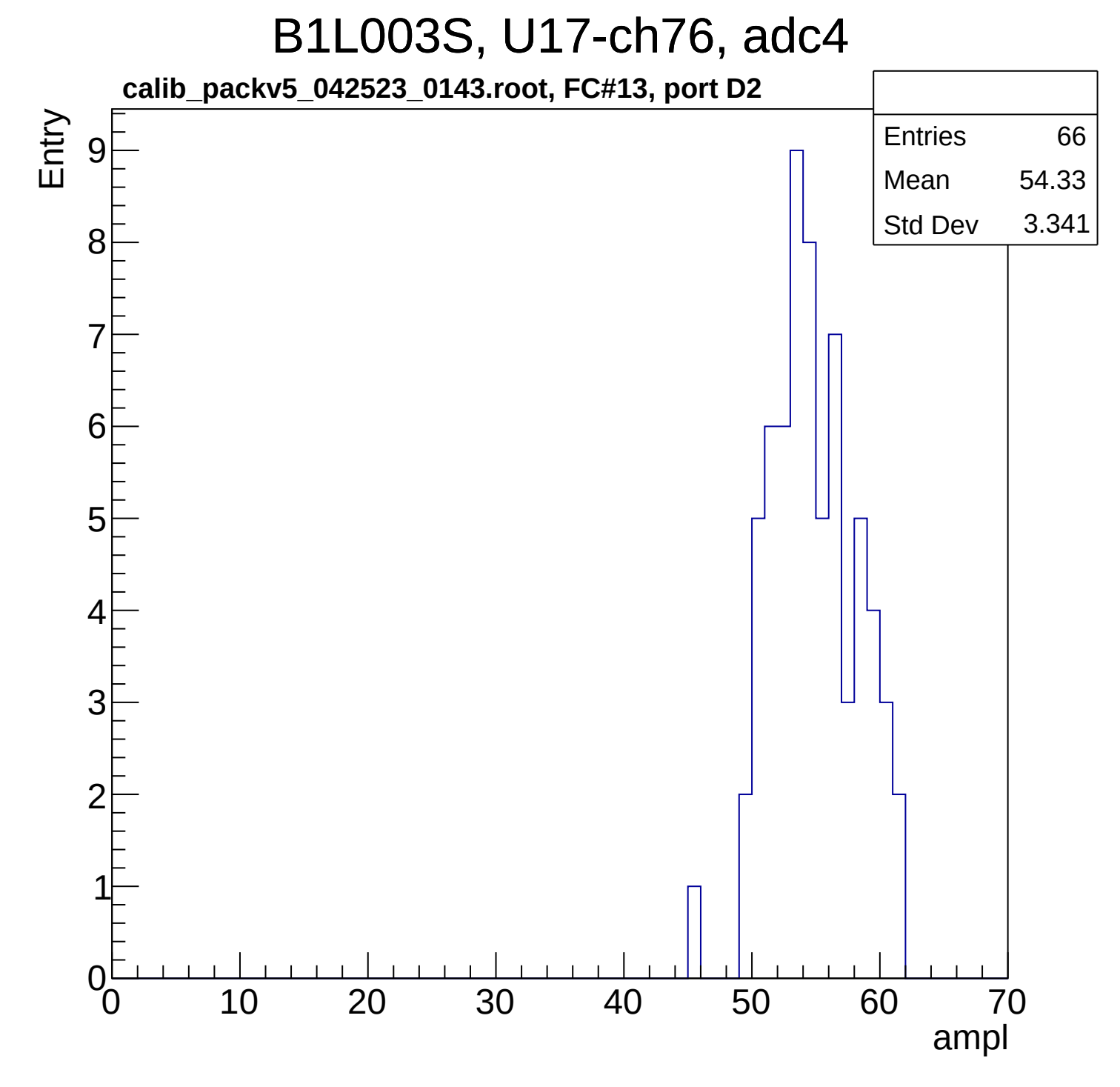
B1L003S, U17-ch76, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 54.33
Std Dev 3.341
Entry
ampl
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0
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60
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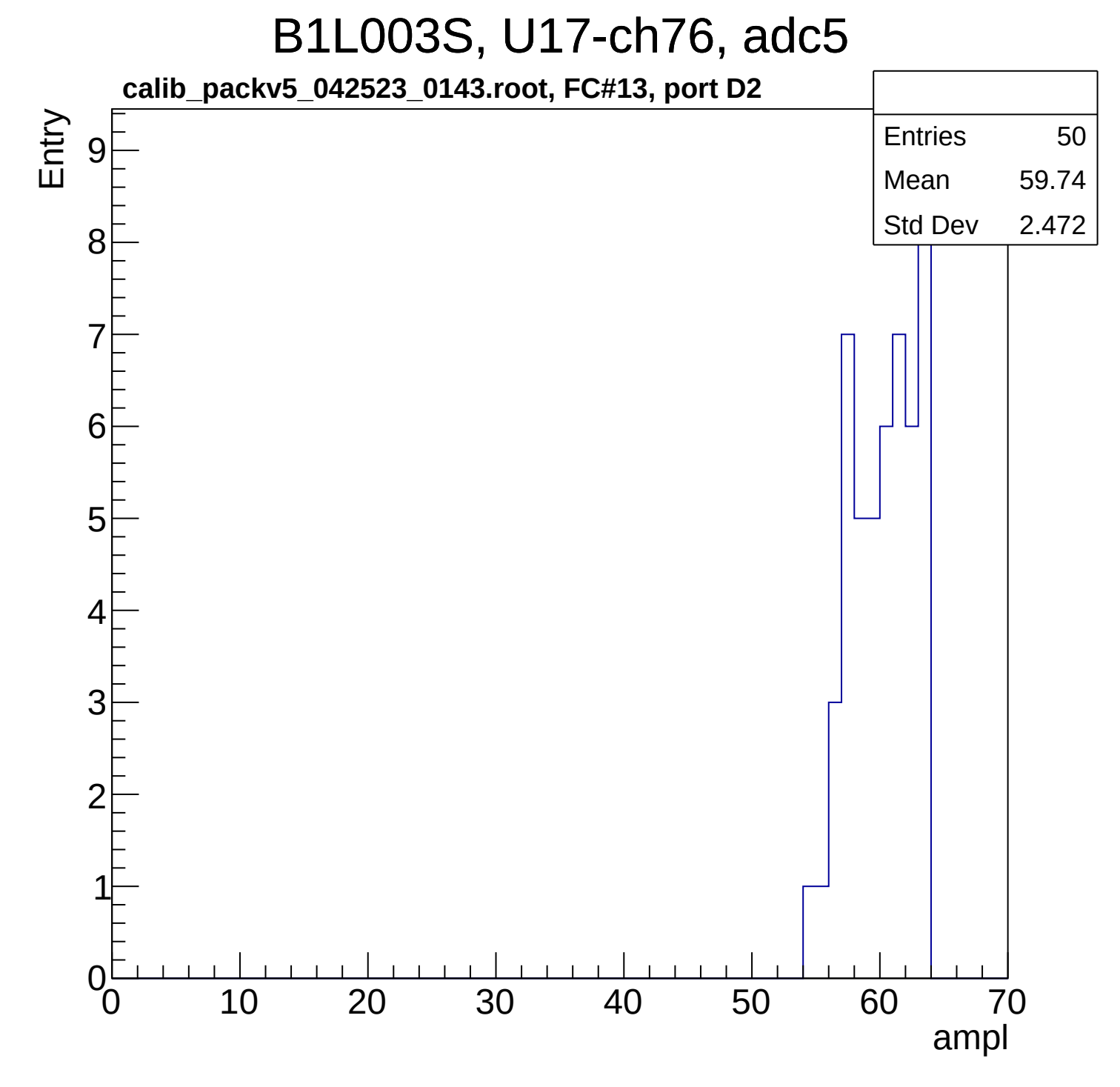
B1L003S, U17-ch76, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.74
Std Dev 2.472
Entry
ampl
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0
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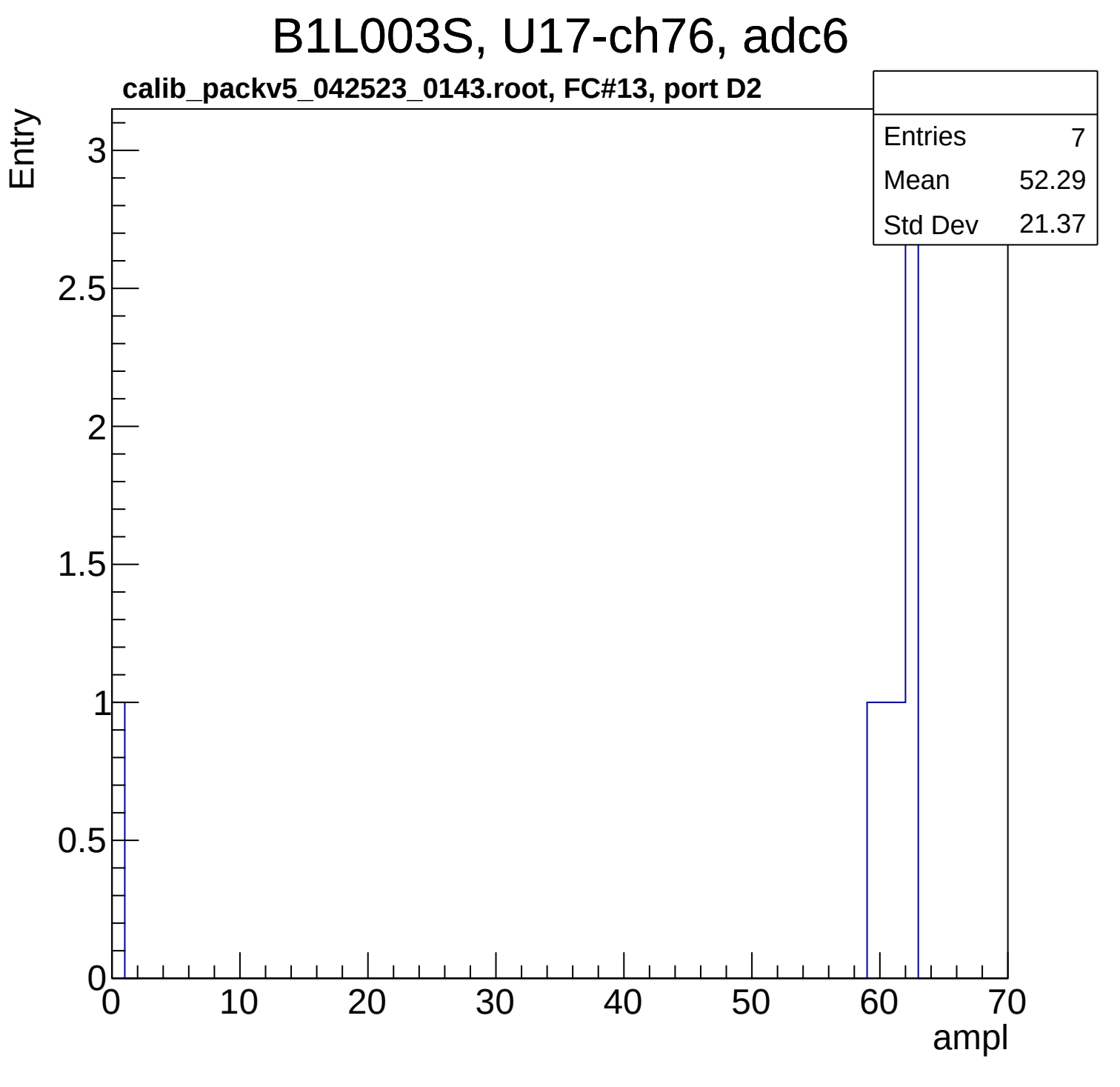
B1L003S, U17-ch76, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 52.29
Std Dev 21.37
Entry
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1.5
2
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10
20
30
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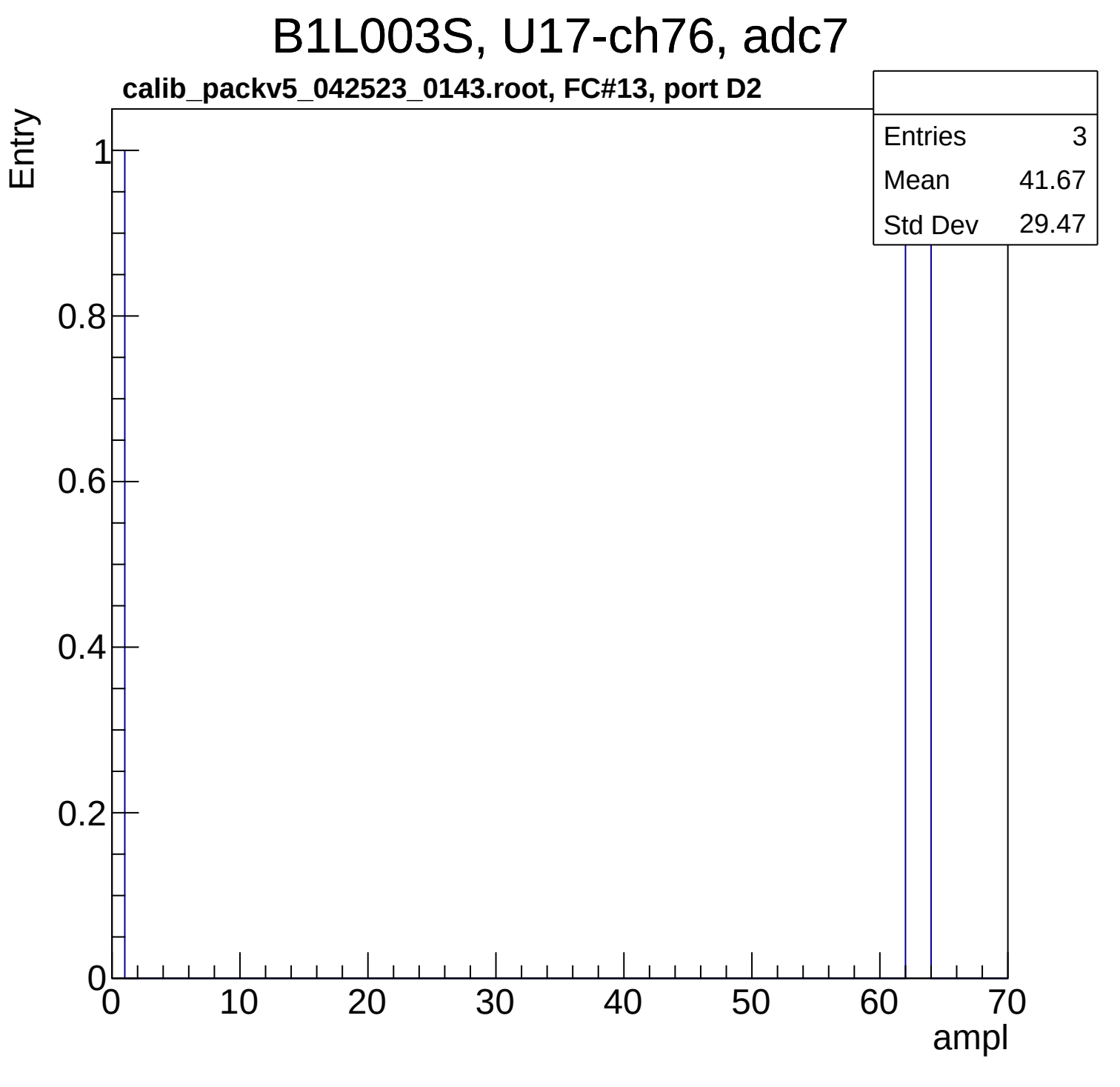
50
60
70
ampl


B1L003S, U17-ch76, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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50
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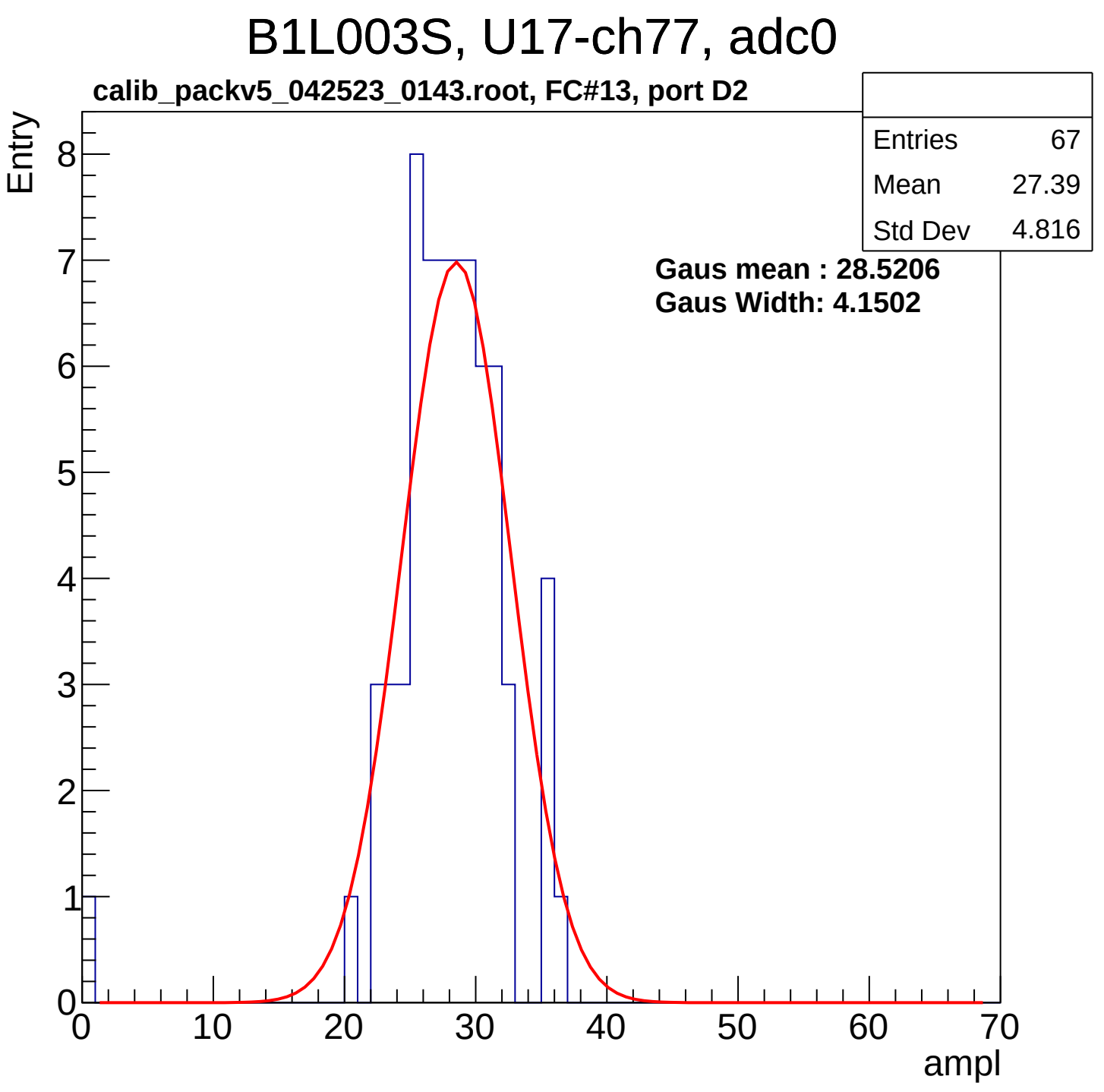
70
ampl


B1L003S, U17-ch77, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 27.39
Std Dev 4.816
Gaus mean : 28.5206
Gaus Width: 4.1502
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
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60
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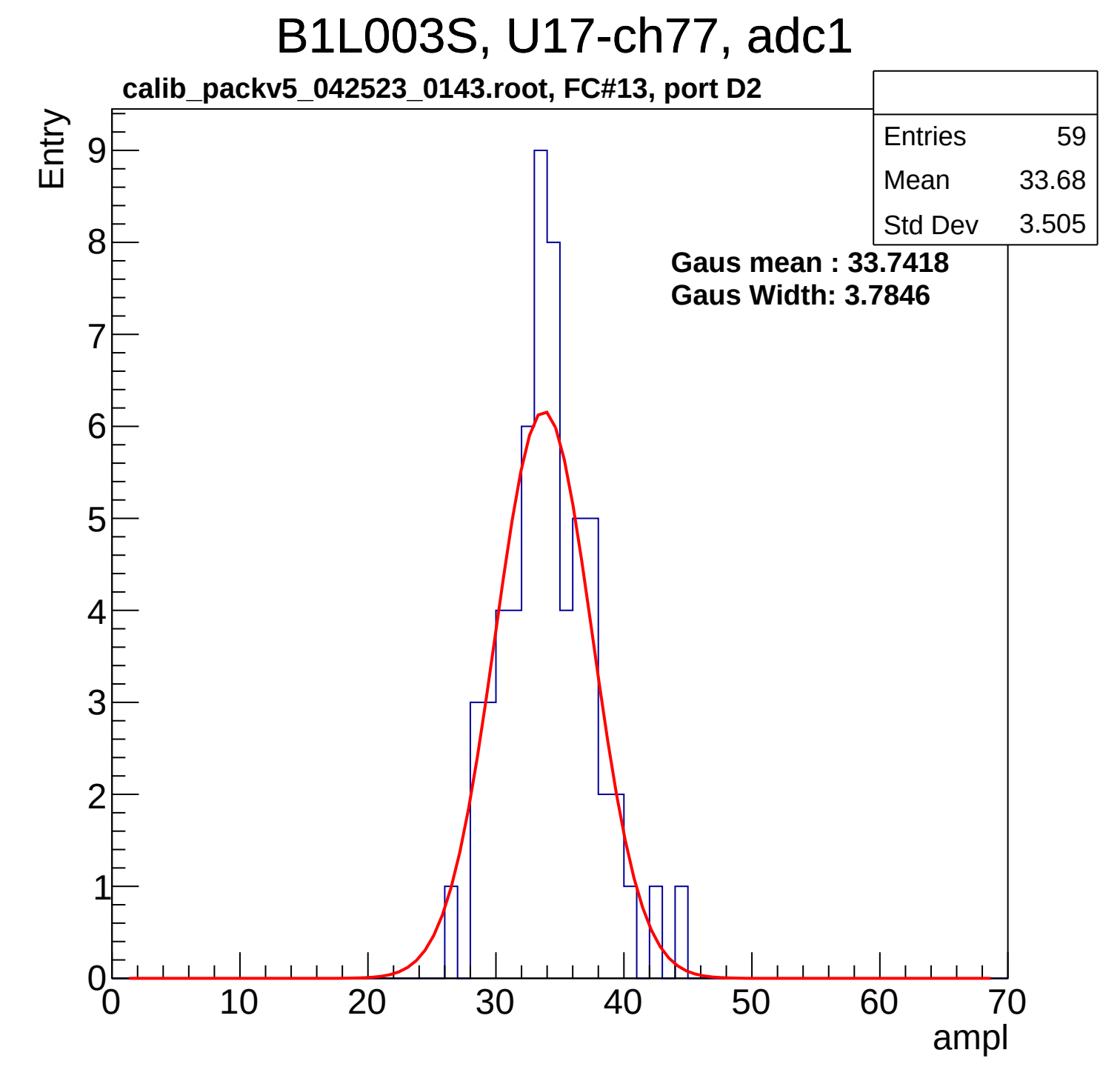
B1L003S, U17-ch77, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 33.68
Std Dev 3.505
Gaus mean : 33.7418
Gaus Width: 3.7846
Entry
ampl
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2
3
4
5
6
7
8
9
0
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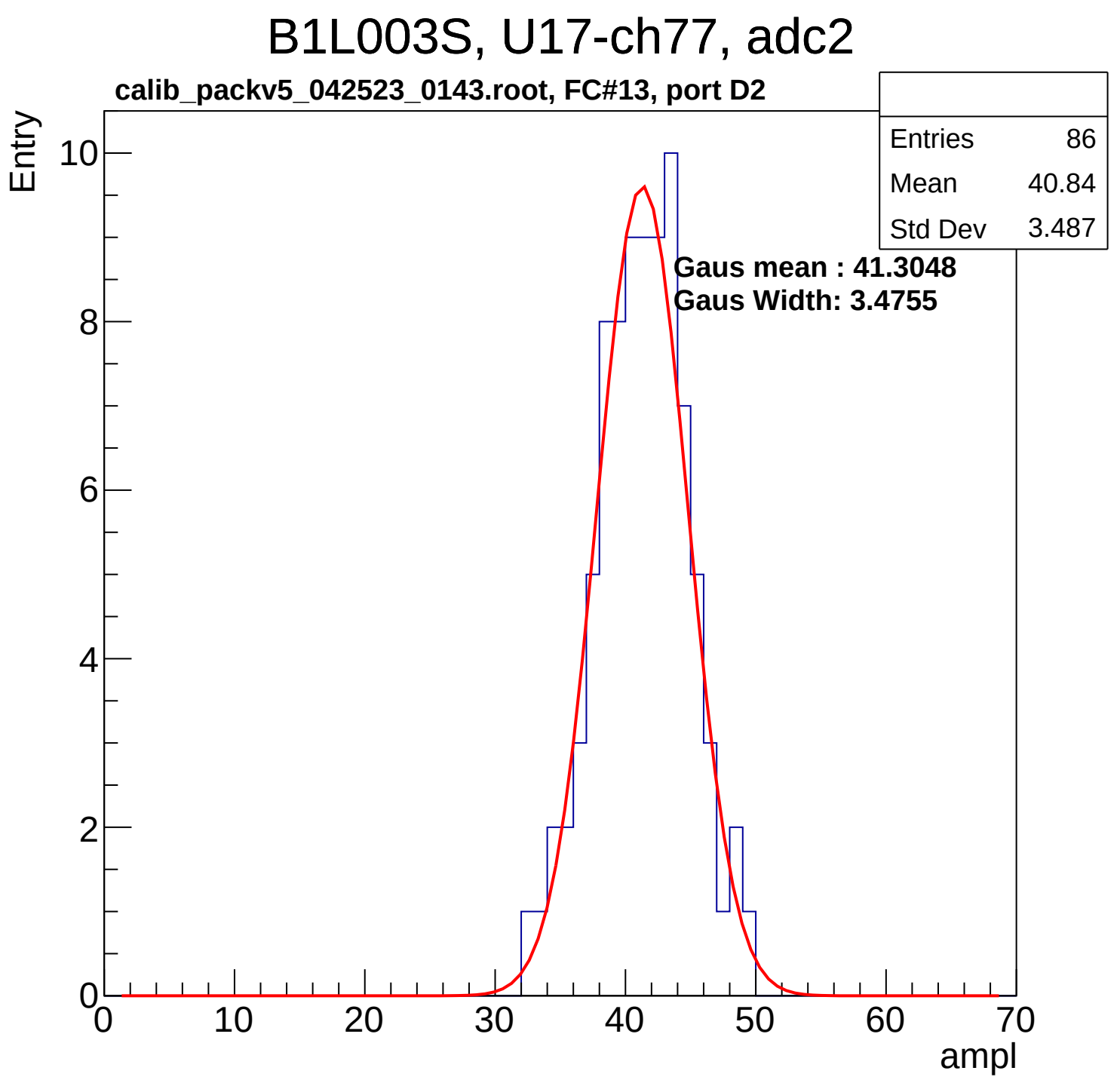
B1L003S, U17-ch77, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 40.84
Std Dev 3.487
Gaus mean : 41.3048
Gaus Width: 3.4755
Entry
ampl
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4
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8
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0
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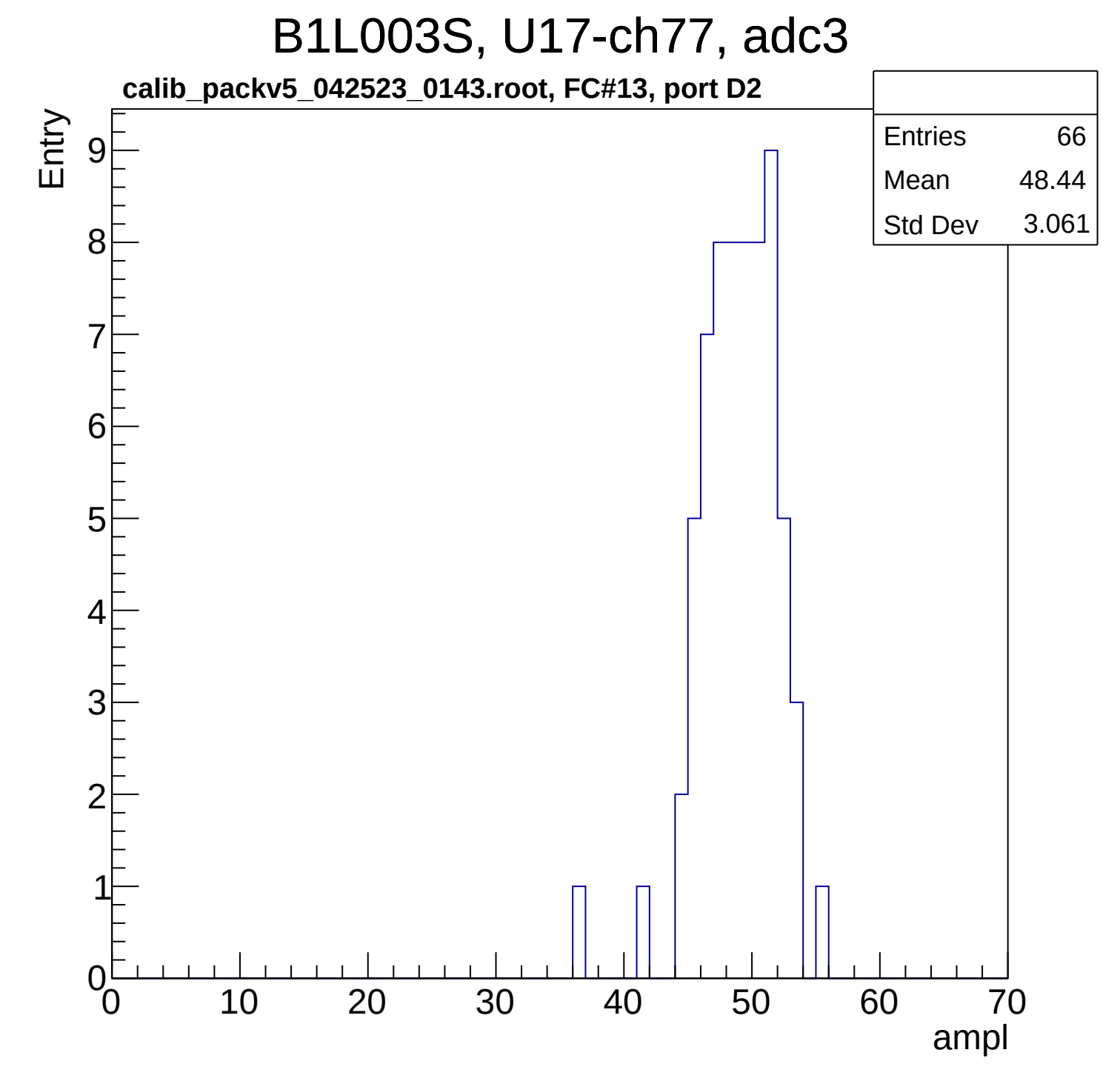
B1L003S, U17-ch77, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.44
Std Dev 3.061
Entry
ampl
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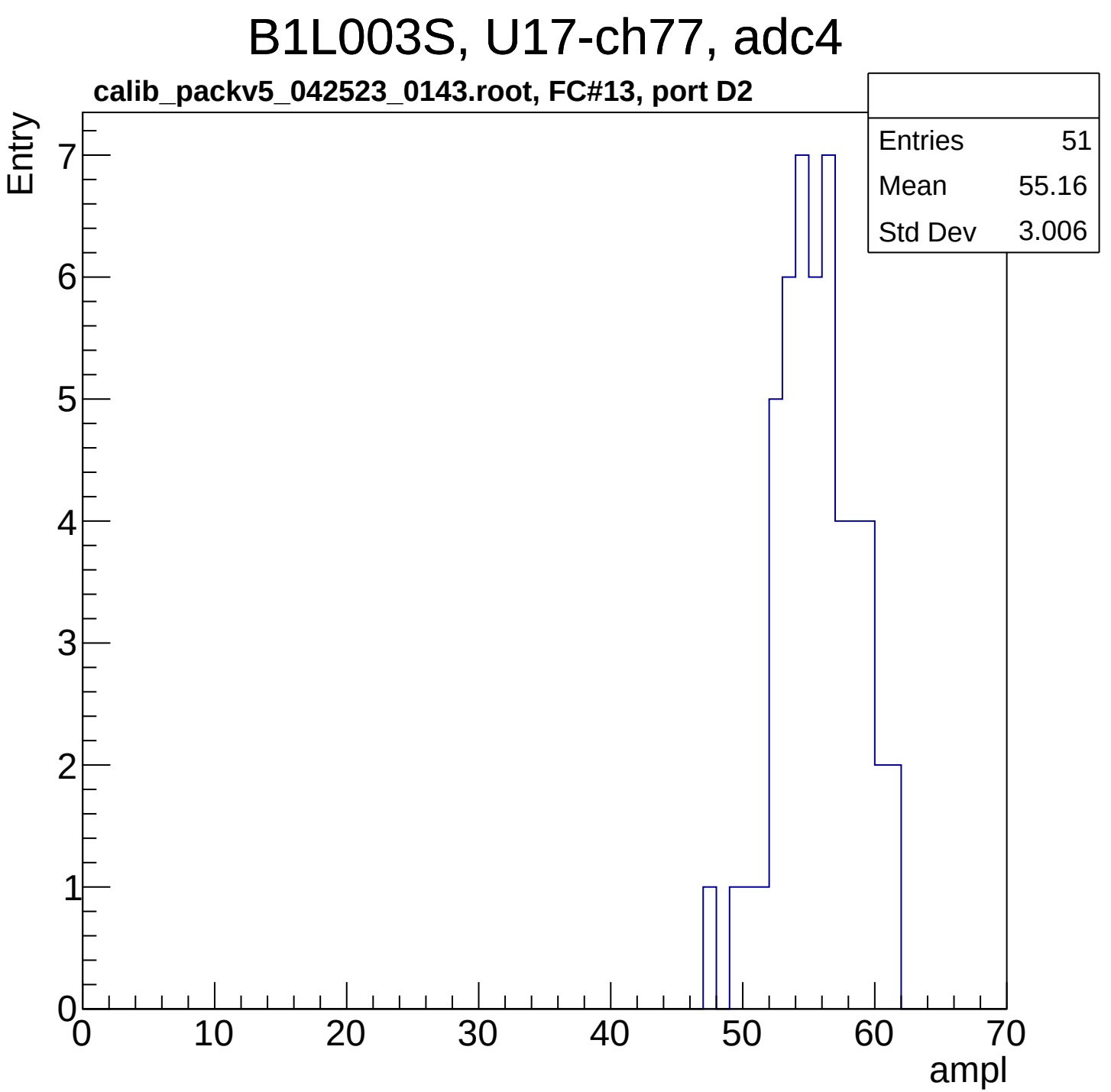
B1L003S, U17-ch77, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.16
Std Dev 3.006
Entry
ampl
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2
3
4
5
6
7
0
10
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30
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60
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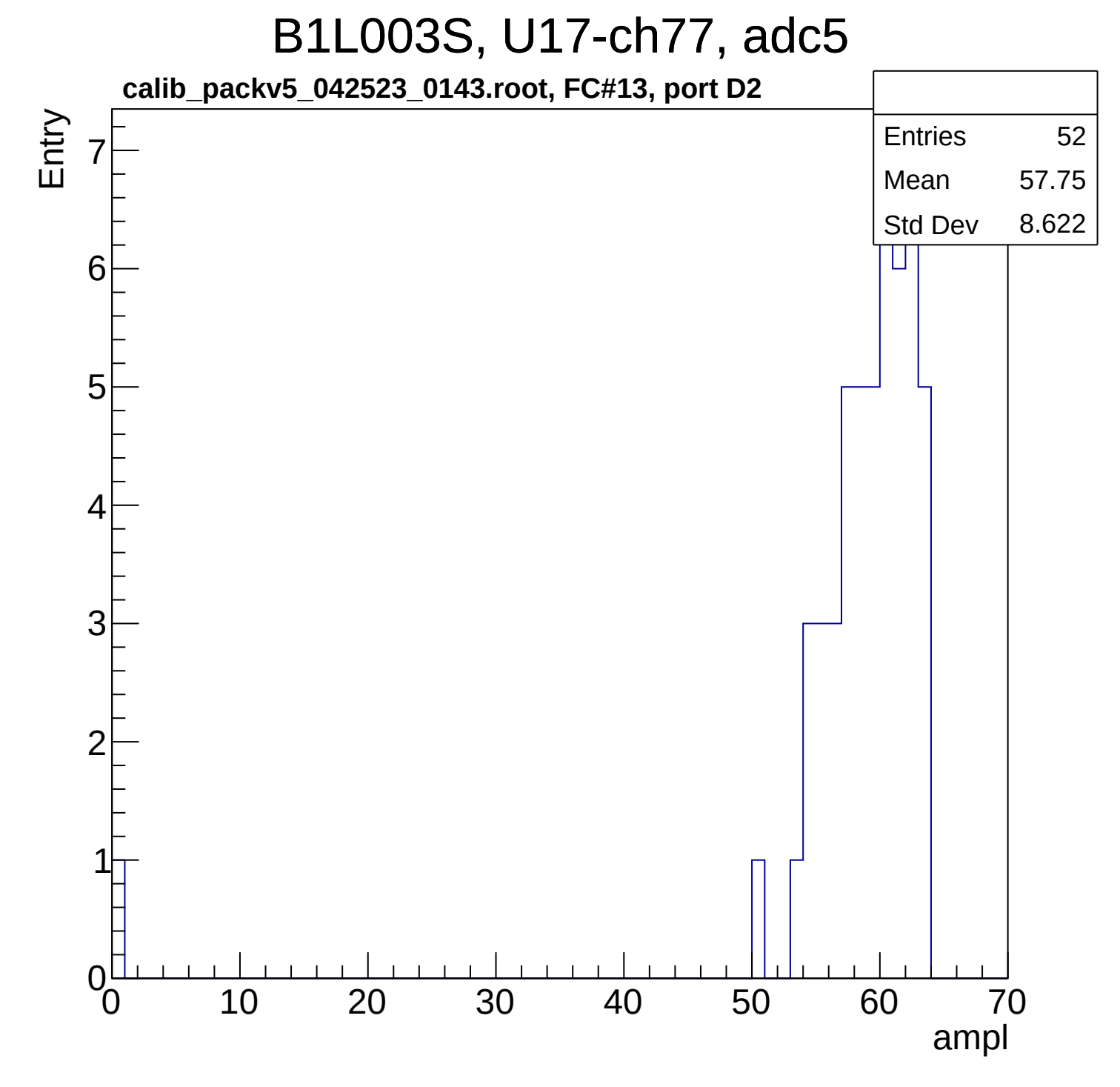
B1L003S, U17-ch77, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 57.75
Std Dev 8.622
Entry
ampl
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0
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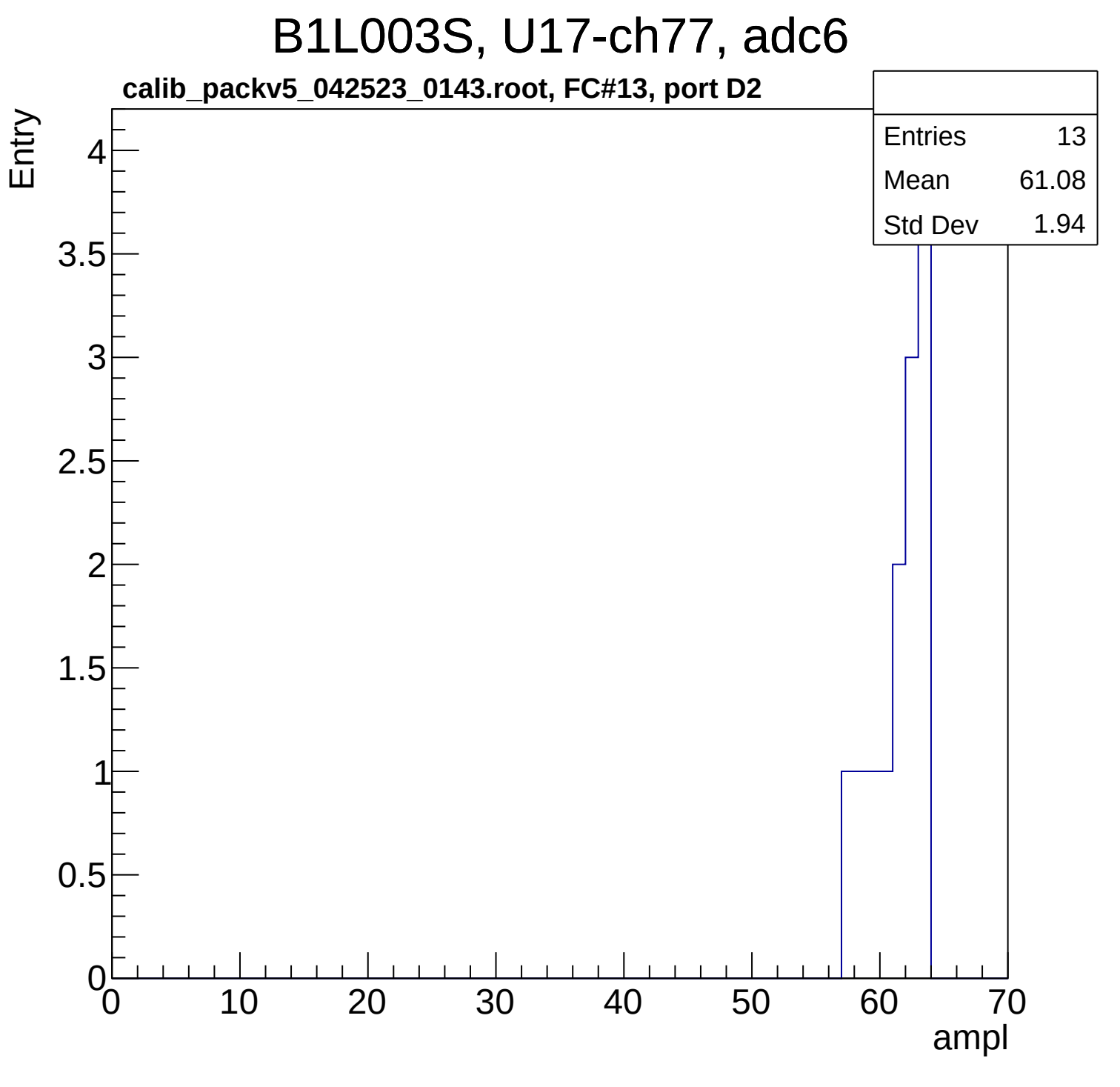
B1L003S, U17-ch77, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.08
Std Dev 1.94
Entry
ampl
0
0.5
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1.5
2
2.5
3
3.5
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0
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60
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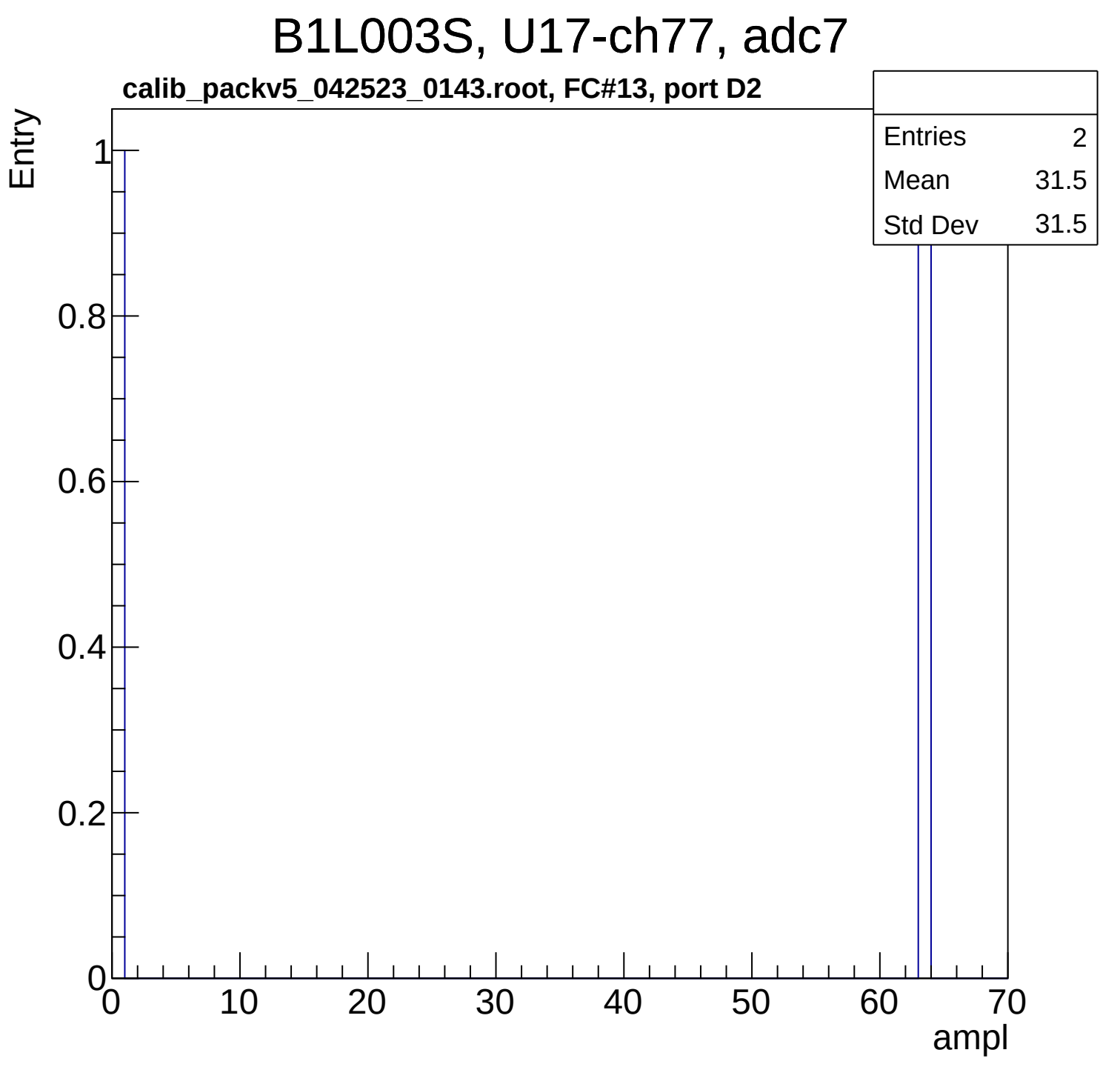
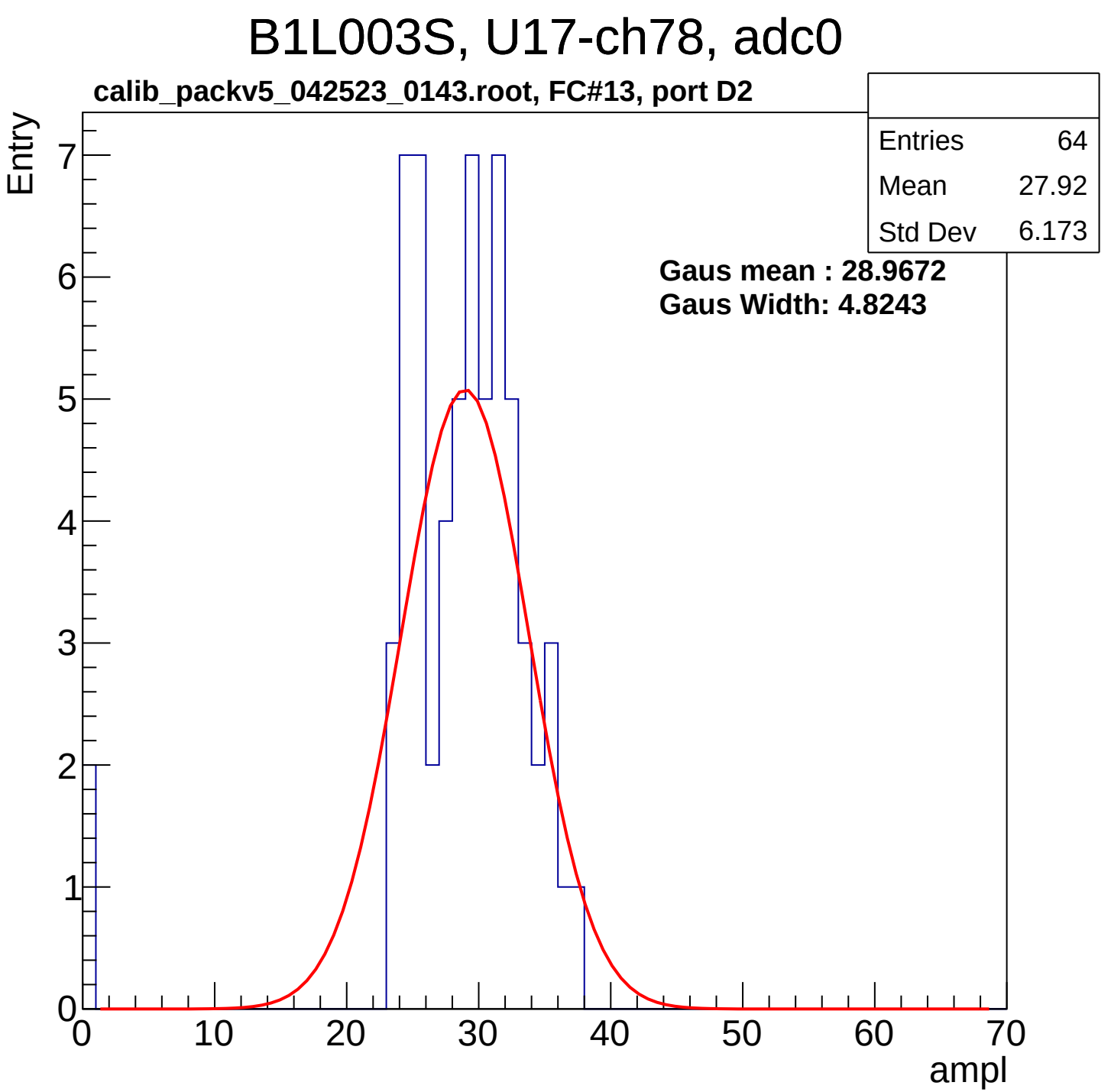

B1L003S, U17-ch77, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch78, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 27.92
Std Dev 6.173
Gaus mean : 28.9672
Gaus Width: 4.8243
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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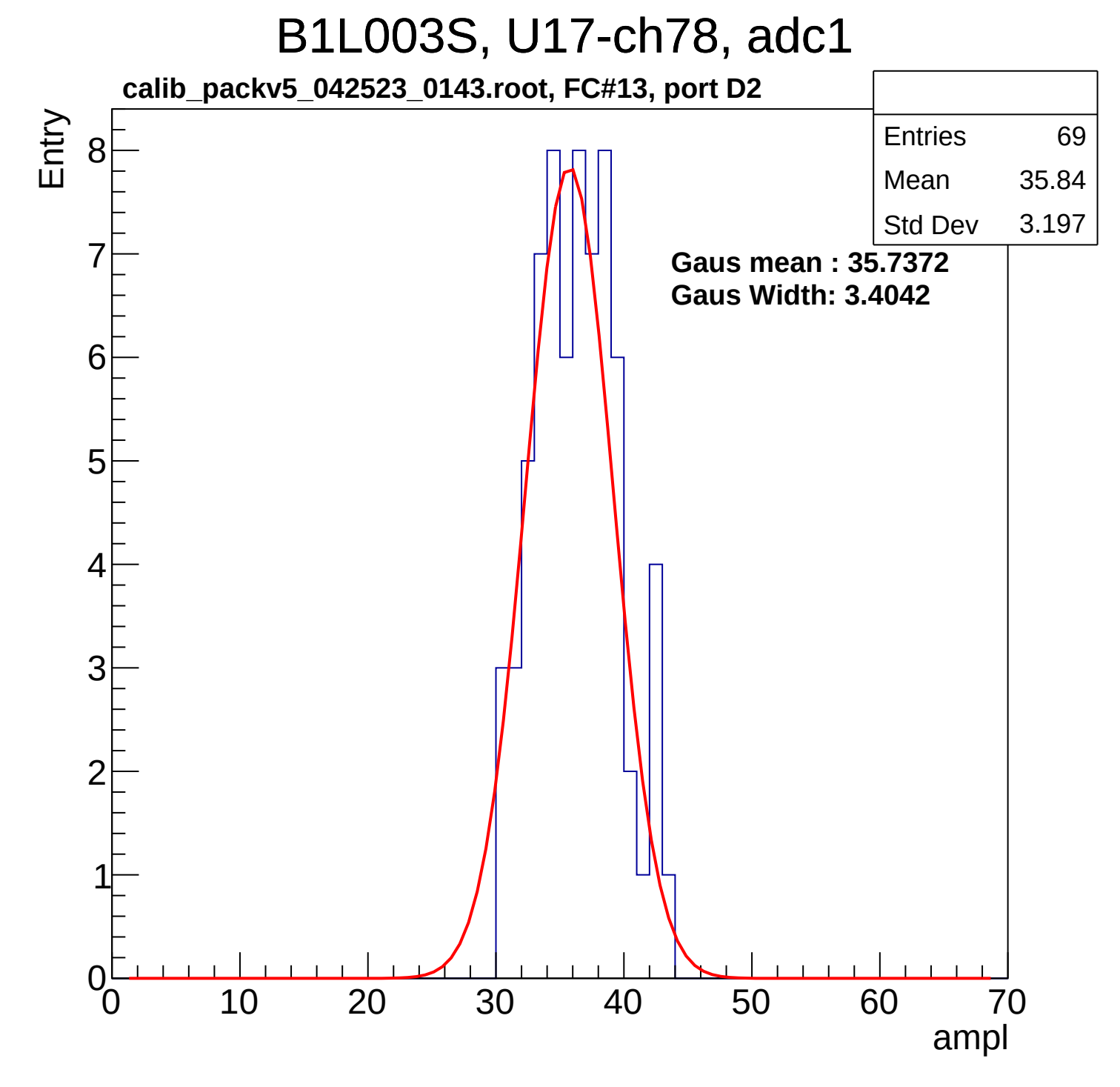
B1L003S, U17-ch78, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.84
Std Dev 3.197
Gaus mean : 35.7372
Gaus Width: 3.4042
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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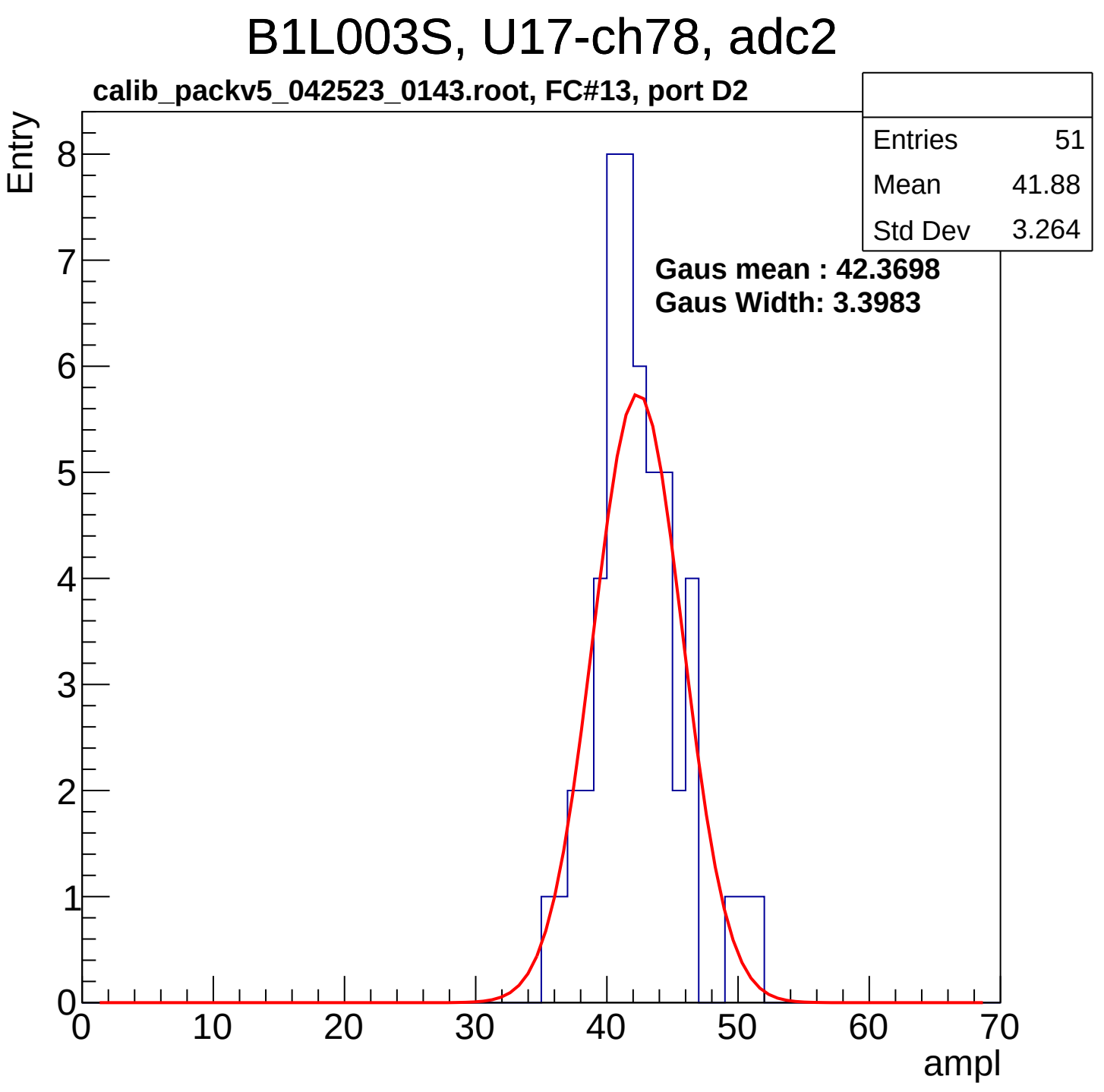
B1L003S, U17-ch78, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 41.88
Std Dev 3.264
Gaus mean : 42.3698
Gaus Width: 3.3983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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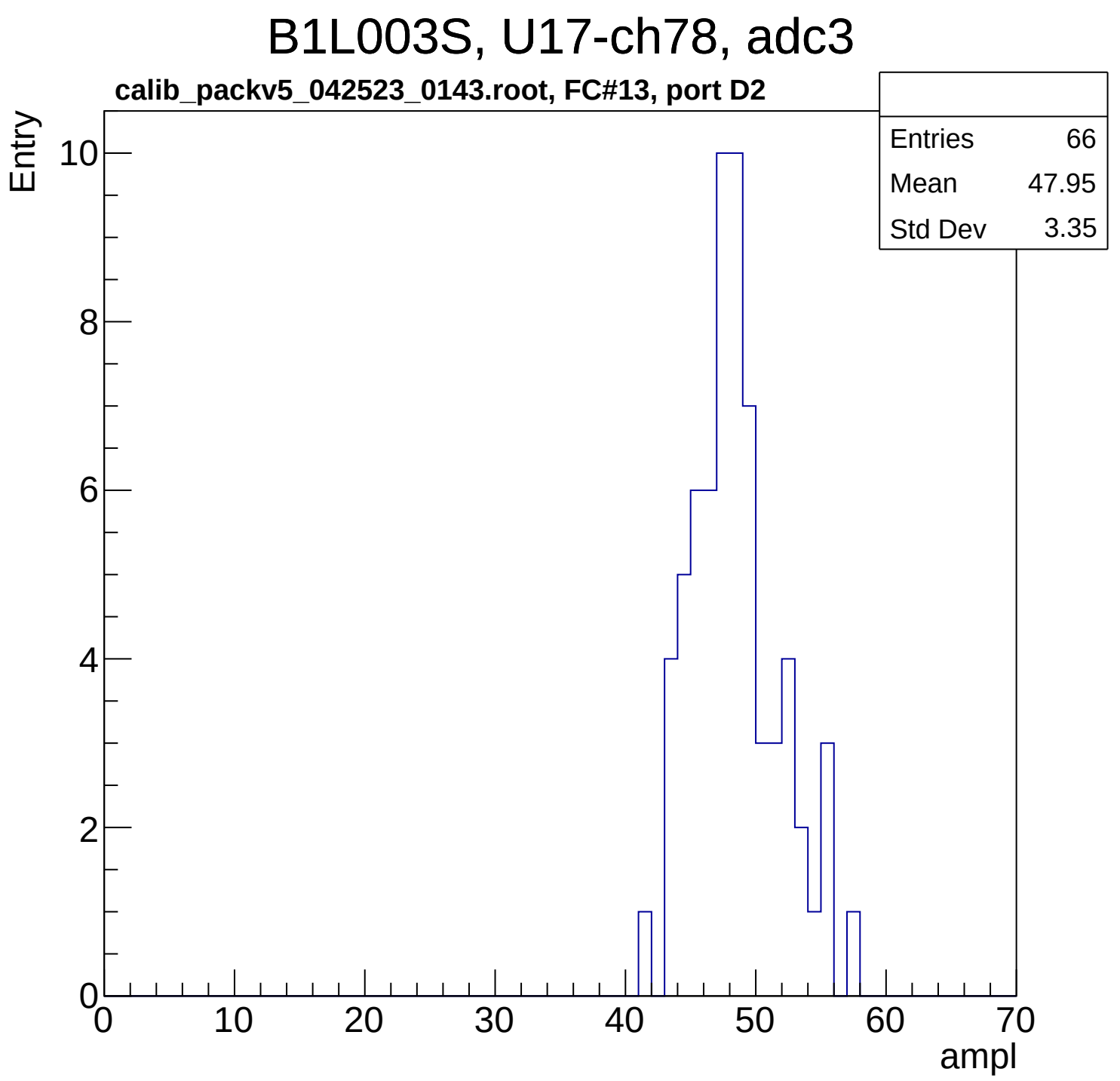
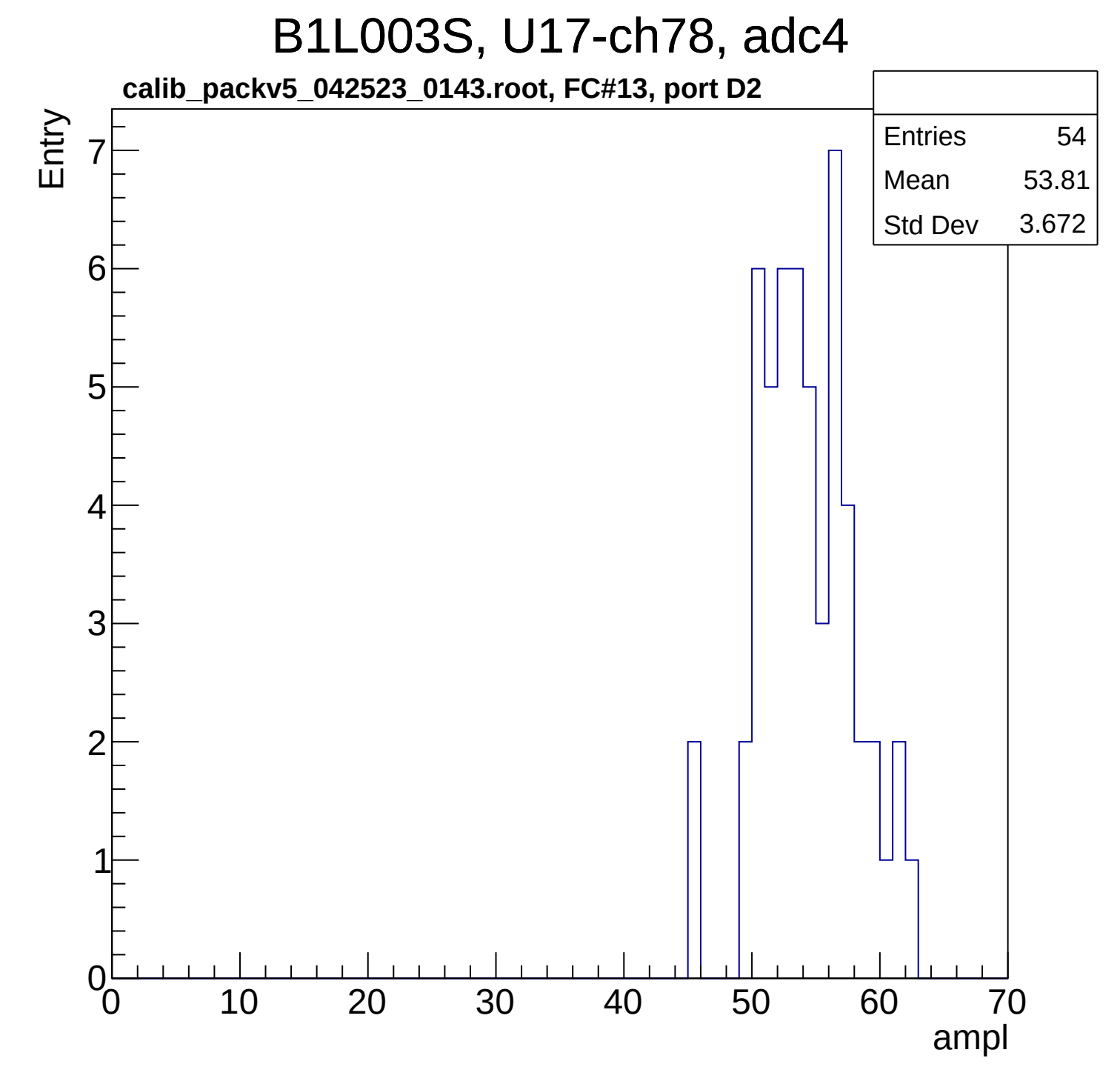

B1L003S, U17-ch78, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 47.95
Std Dev 3.35
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch78, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.81
Std Dev 3.672
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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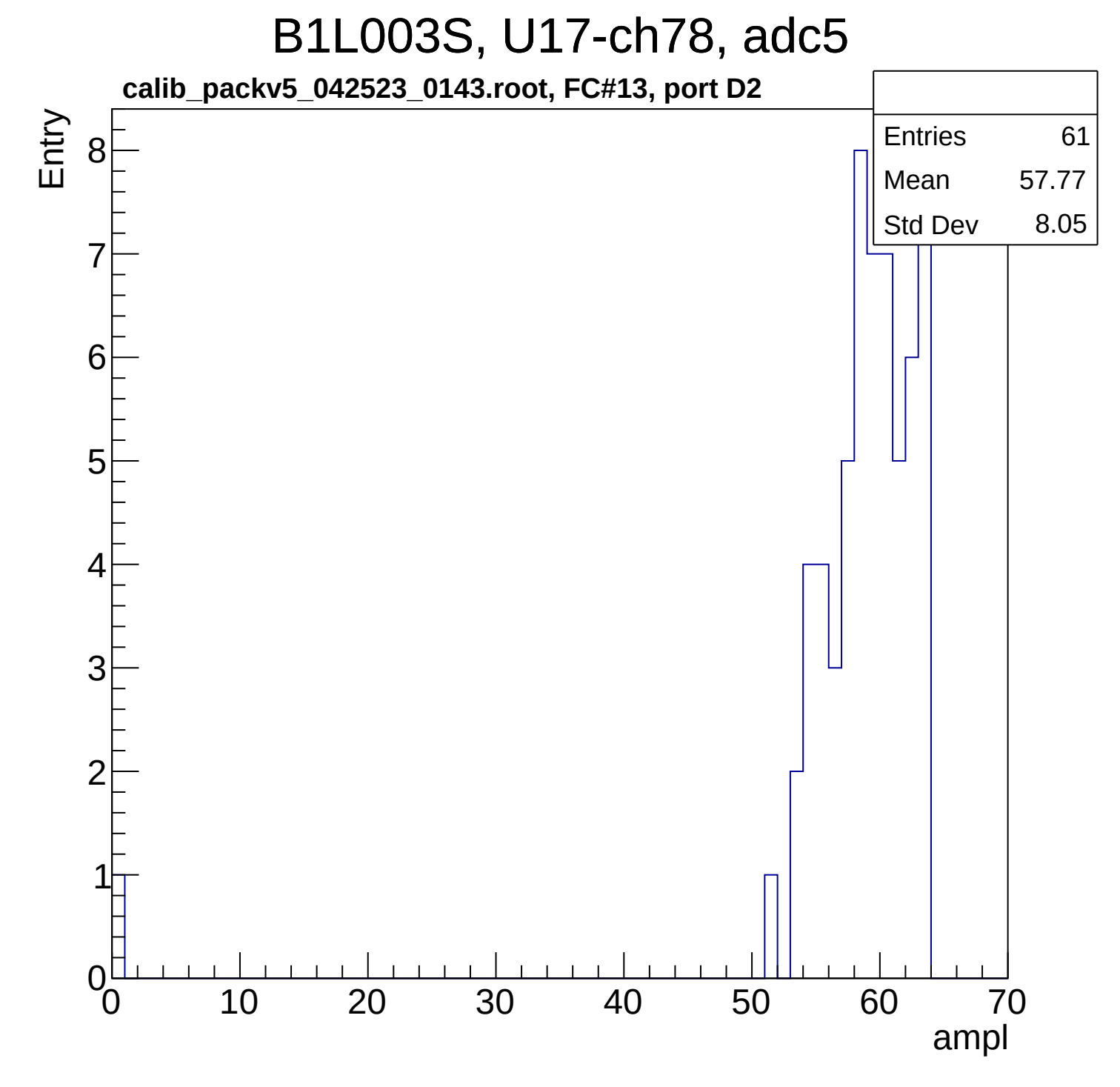
B1L003S, U17-ch78, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 57.77
Std Dev 8.05
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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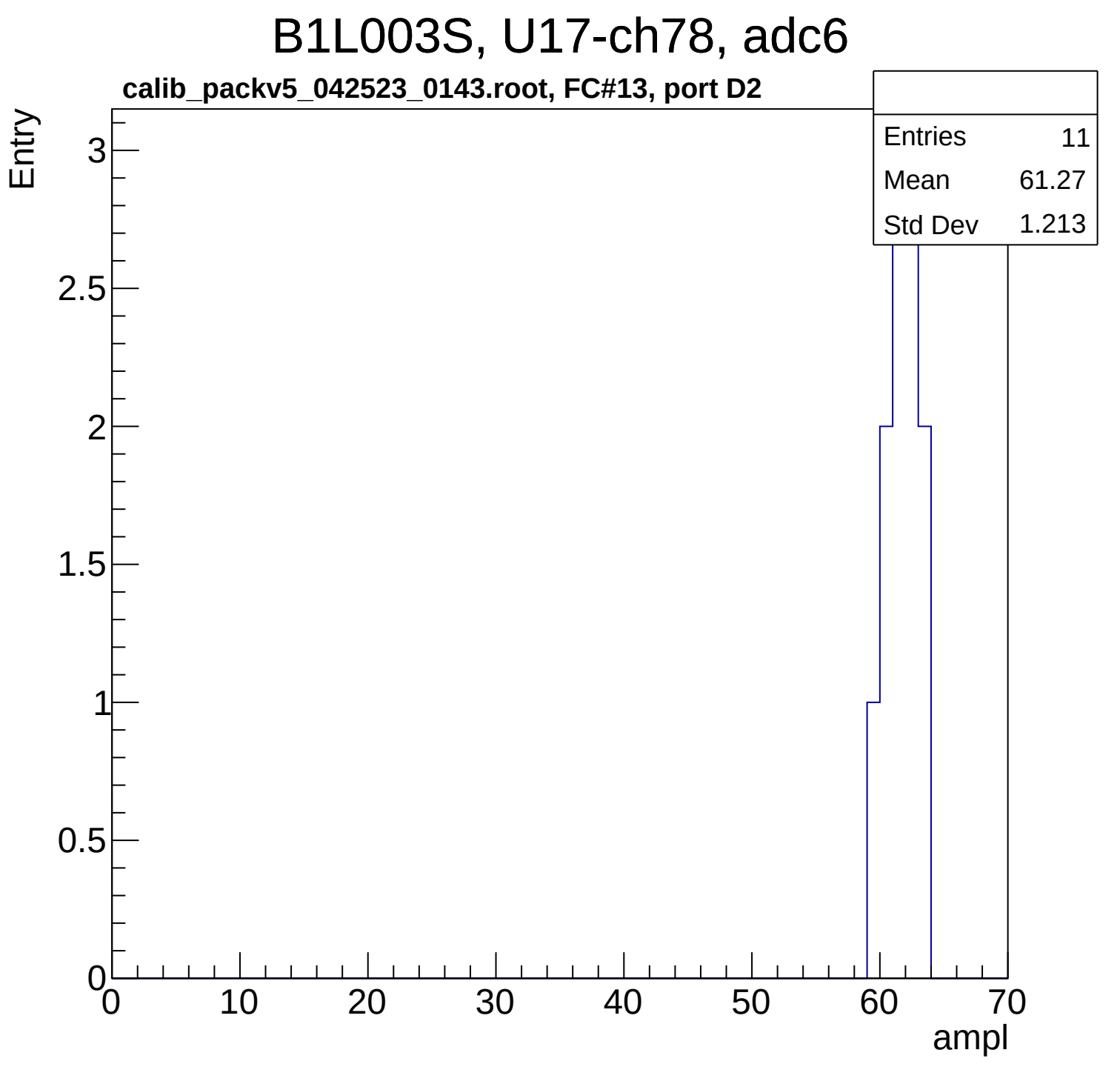
B1L003S, U17-ch78, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.27
Std Dev 1.213
Entry
ampl
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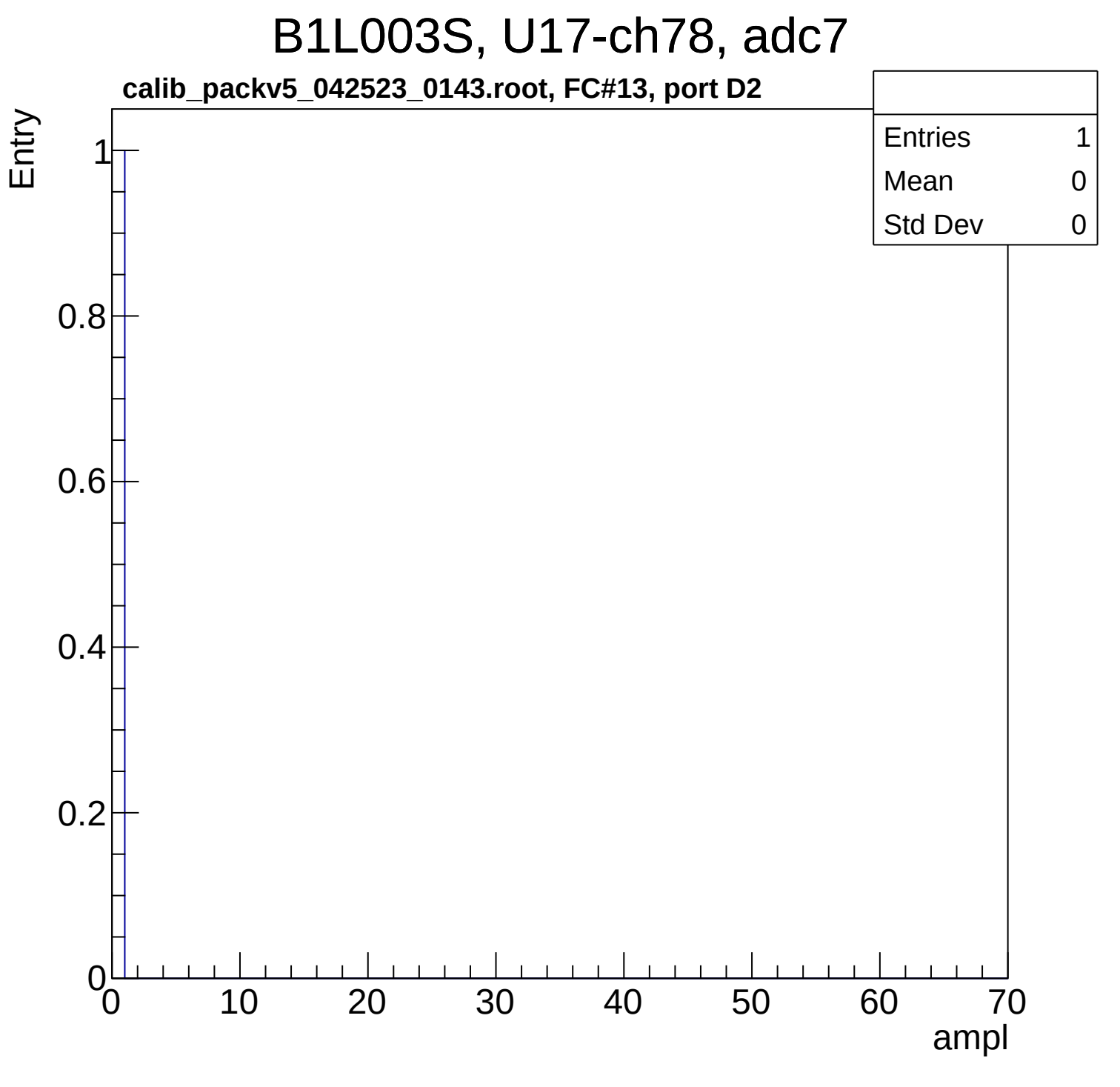
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch78, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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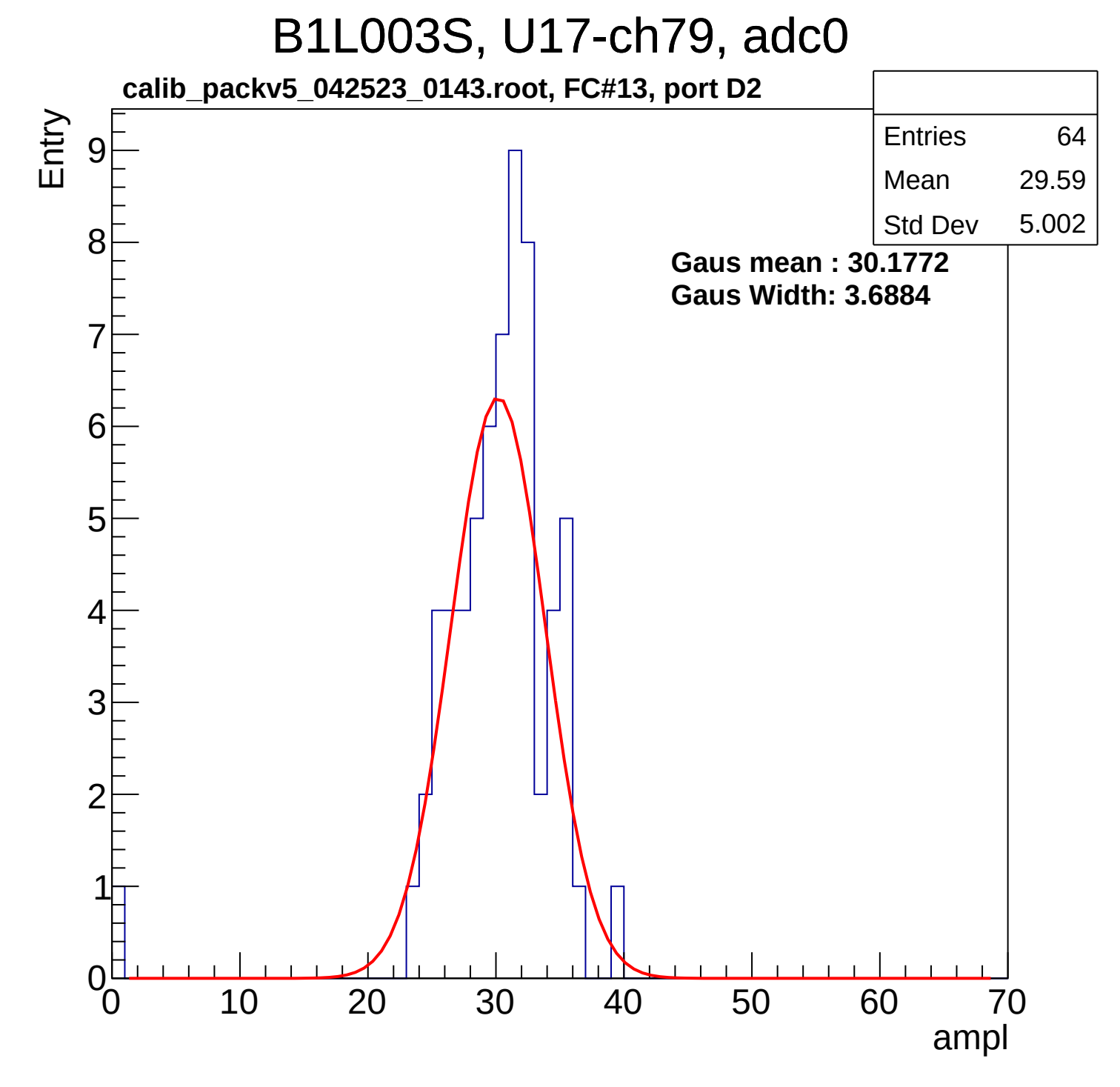
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch79, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.59
Std Dev 5.002
Gaus mean : 30.1772
Gaus Width: 3.6884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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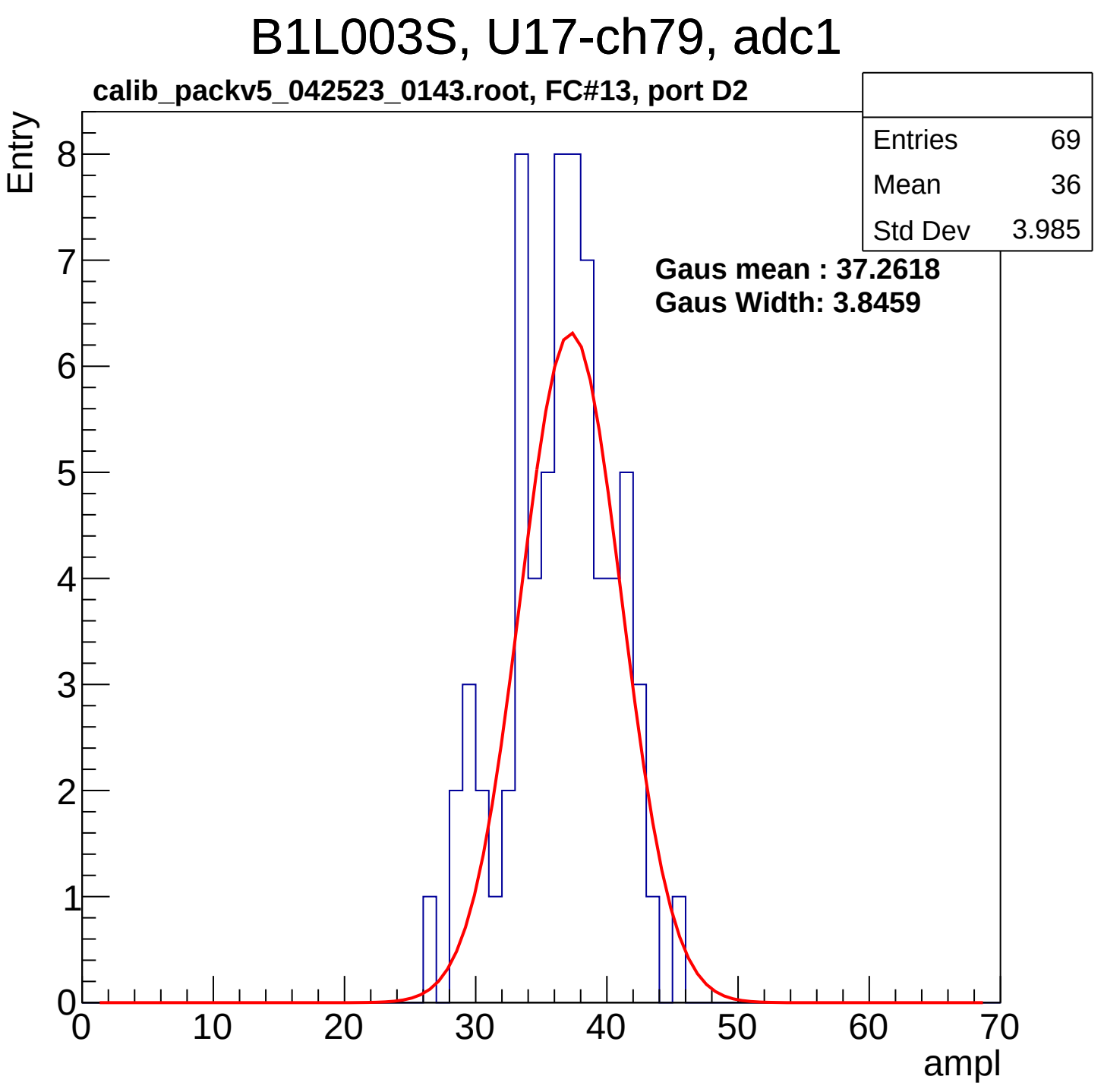
B1L003S, U17-ch79, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36
Std Dev 3.985
Gaus mean : 37.2618
Gaus Width: 3.8459
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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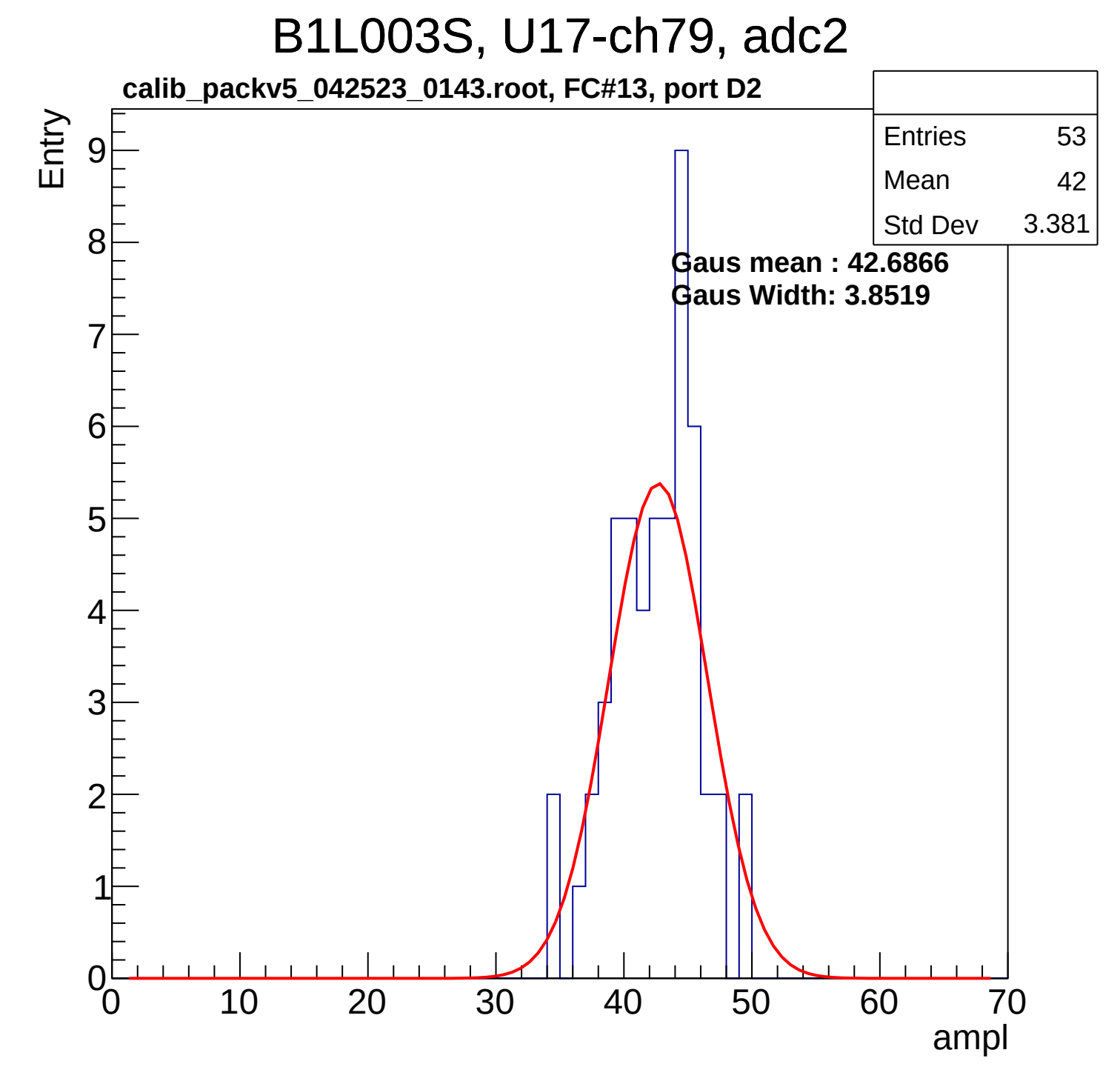
B1L003S, U17-ch79, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42
Std Dev 3.381
Gaus mean : 42.6866
Gaus Width: 3.8519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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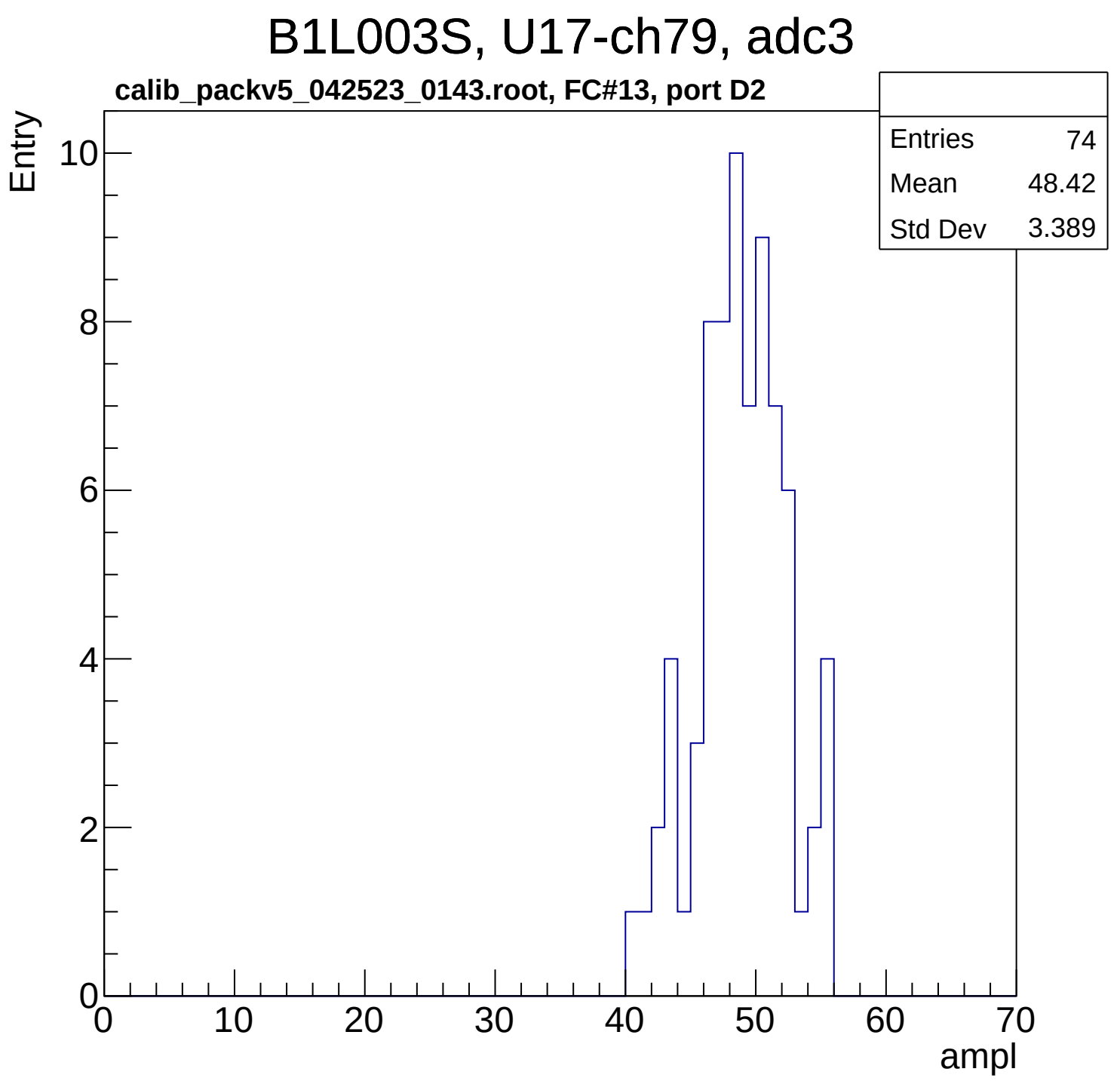
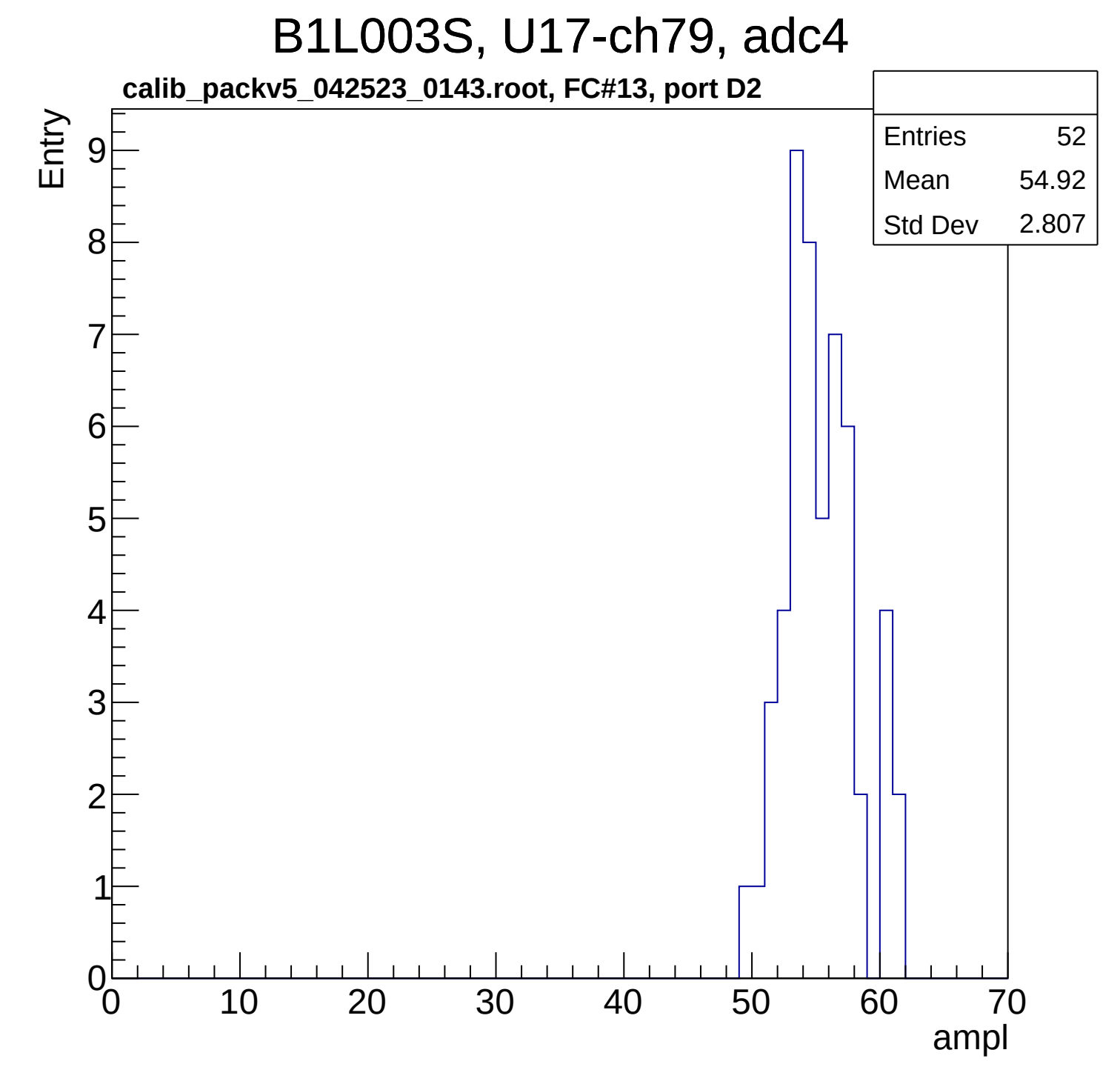
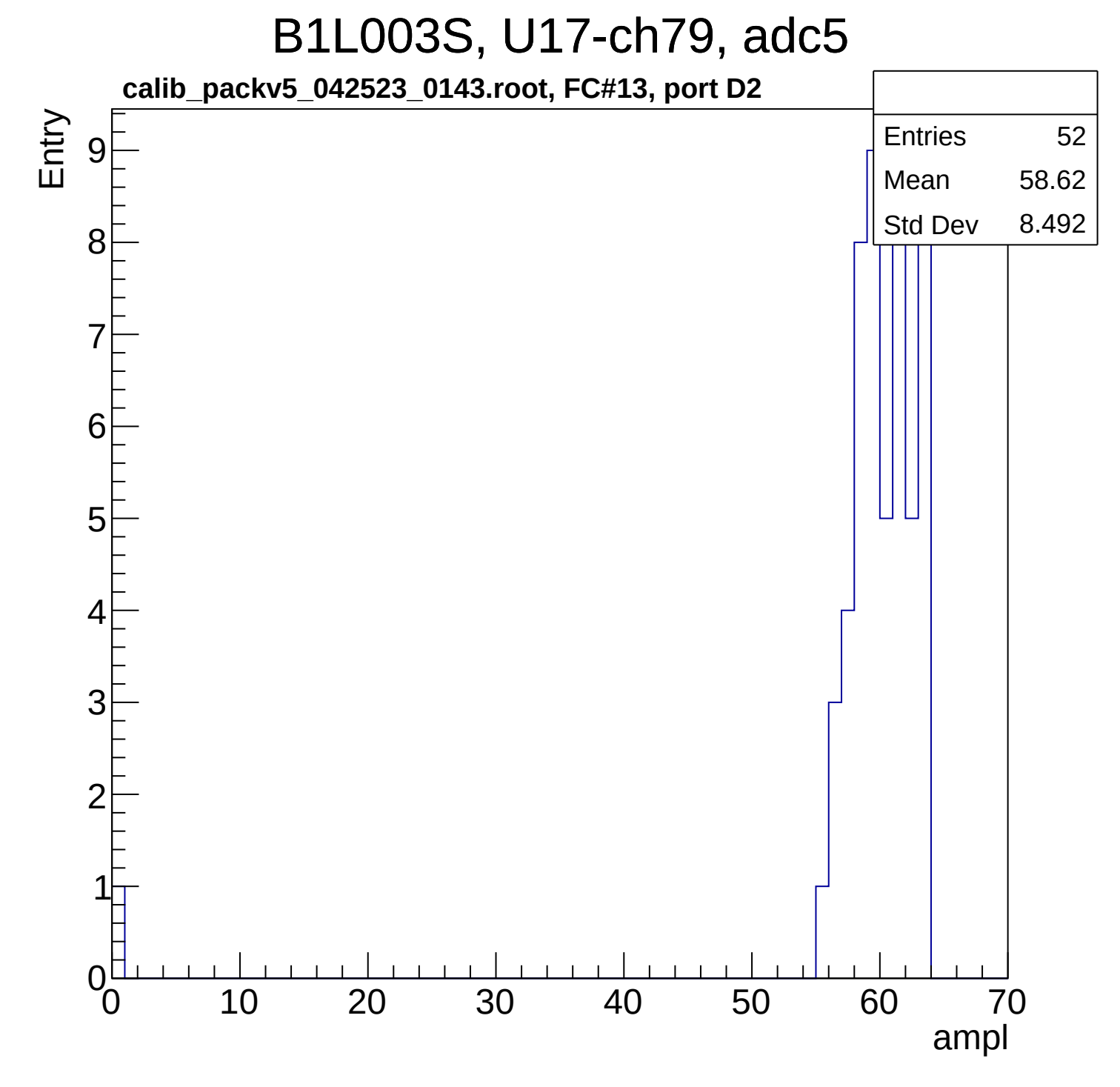

B1L003S, U17-ch79, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.42
Std Dev 3.389
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch79, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.92
Std Dev 2.807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch79, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.62
Std Dev 8.492
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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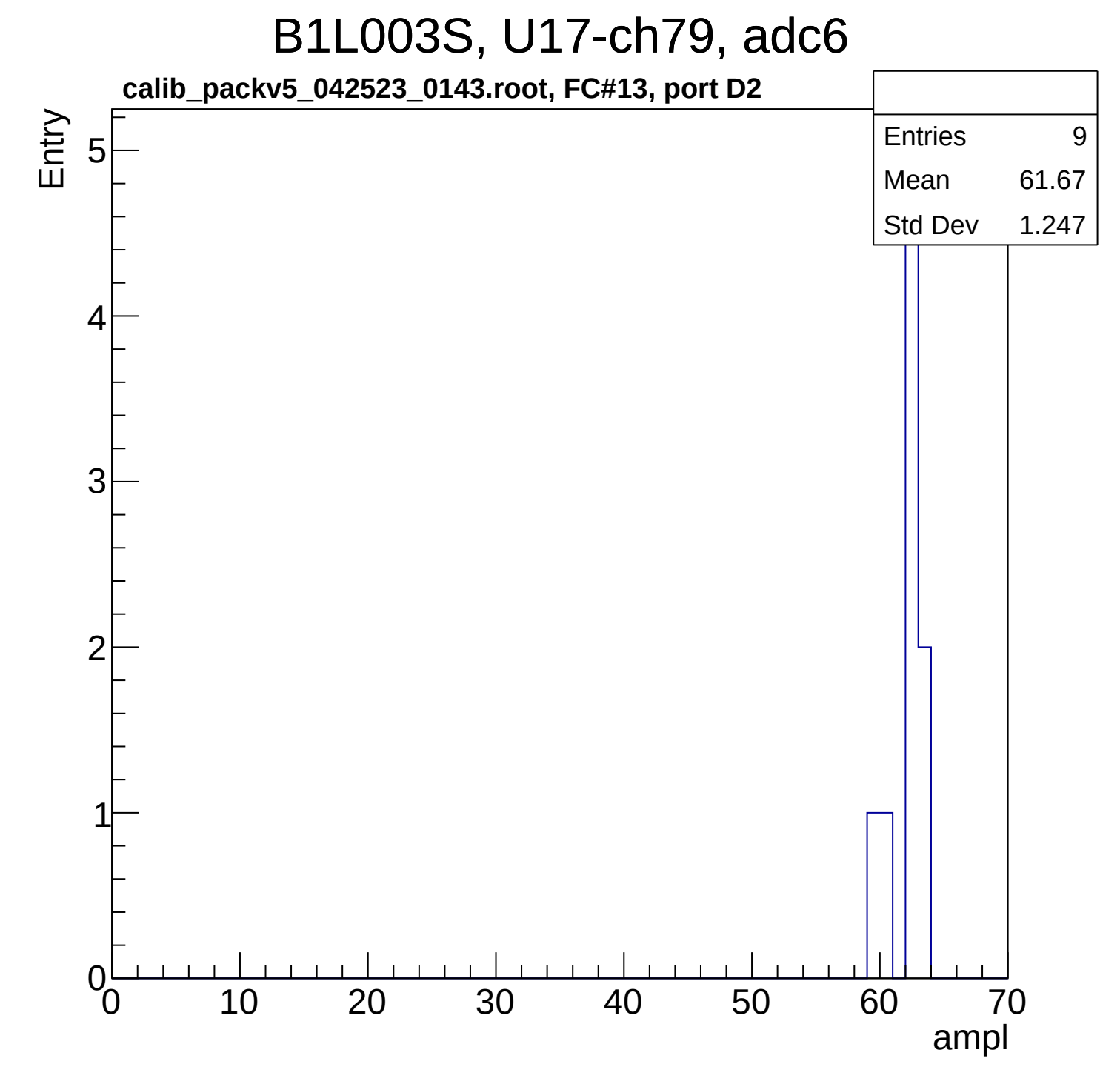
B1L003S, U17-ch79, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.67
Std Dev 1.247
Entry
ampl
0
1
2
3
4
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0
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20
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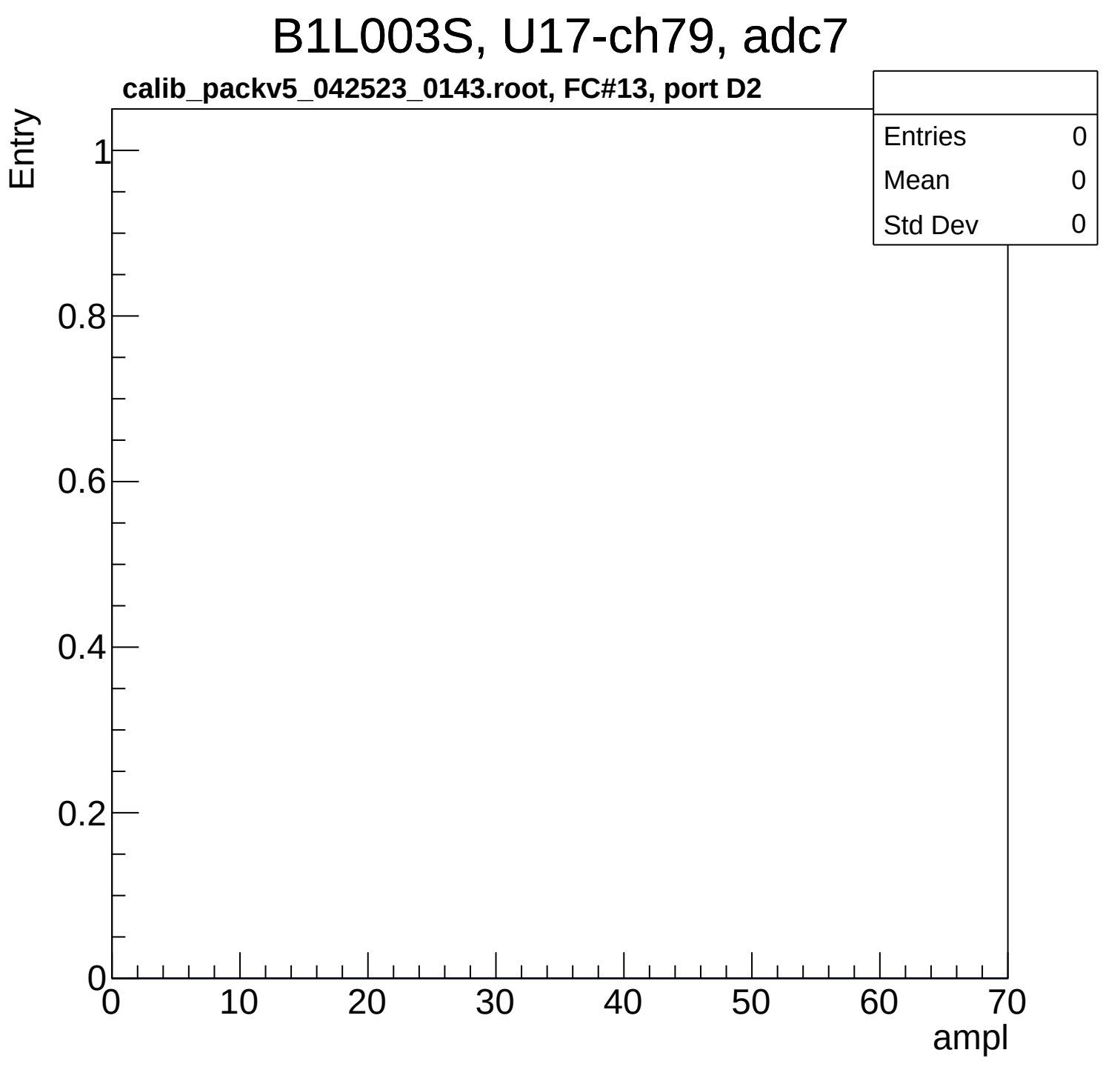
40
50
60
70


B1L003S, U17-ch79, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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30
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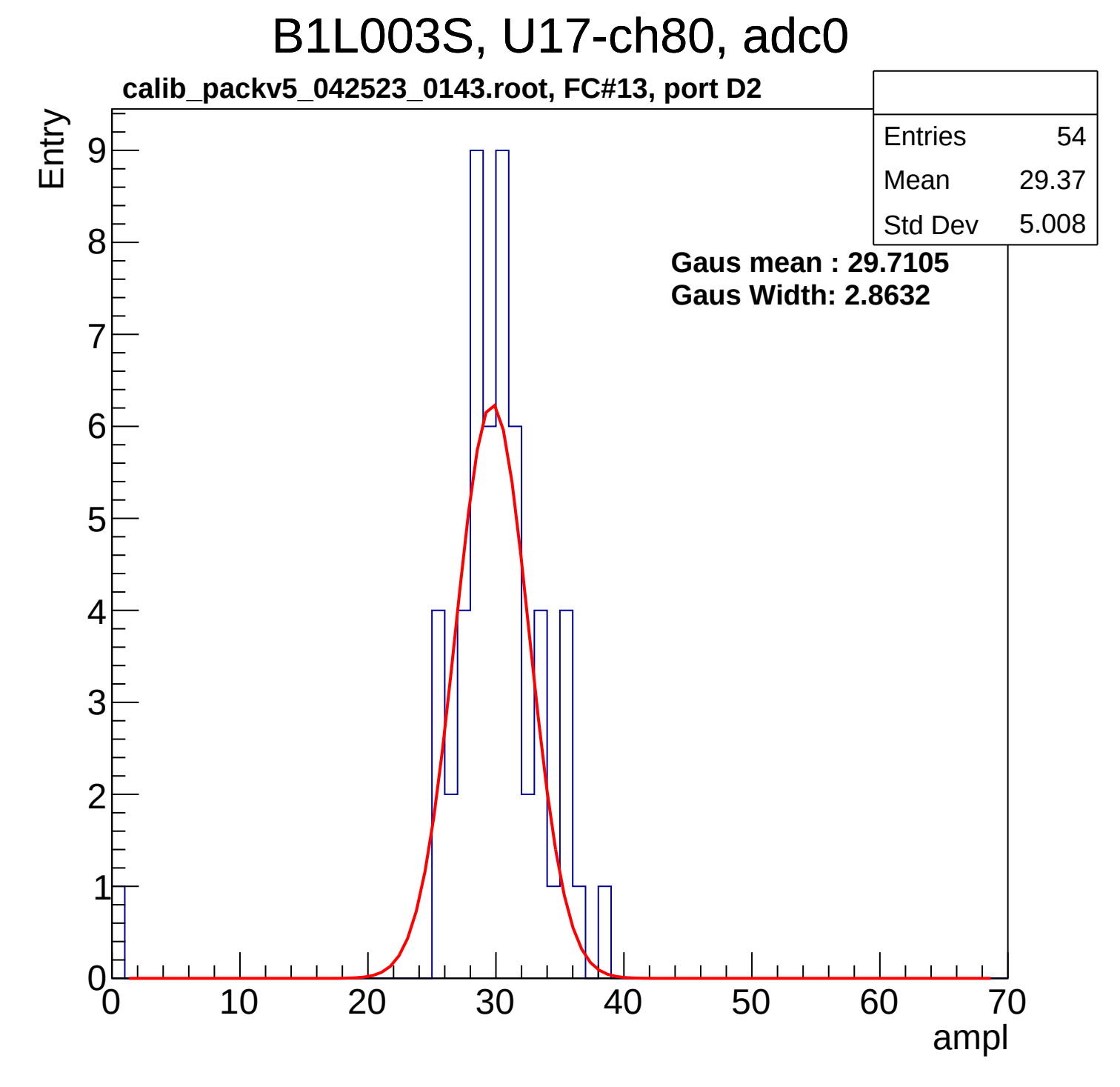
50
60
70
ampl


B1L003S, U17-ch80, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.37
Std Dev 5.008
Gaus mean : 29.7105
Gaus Width: 2.8632
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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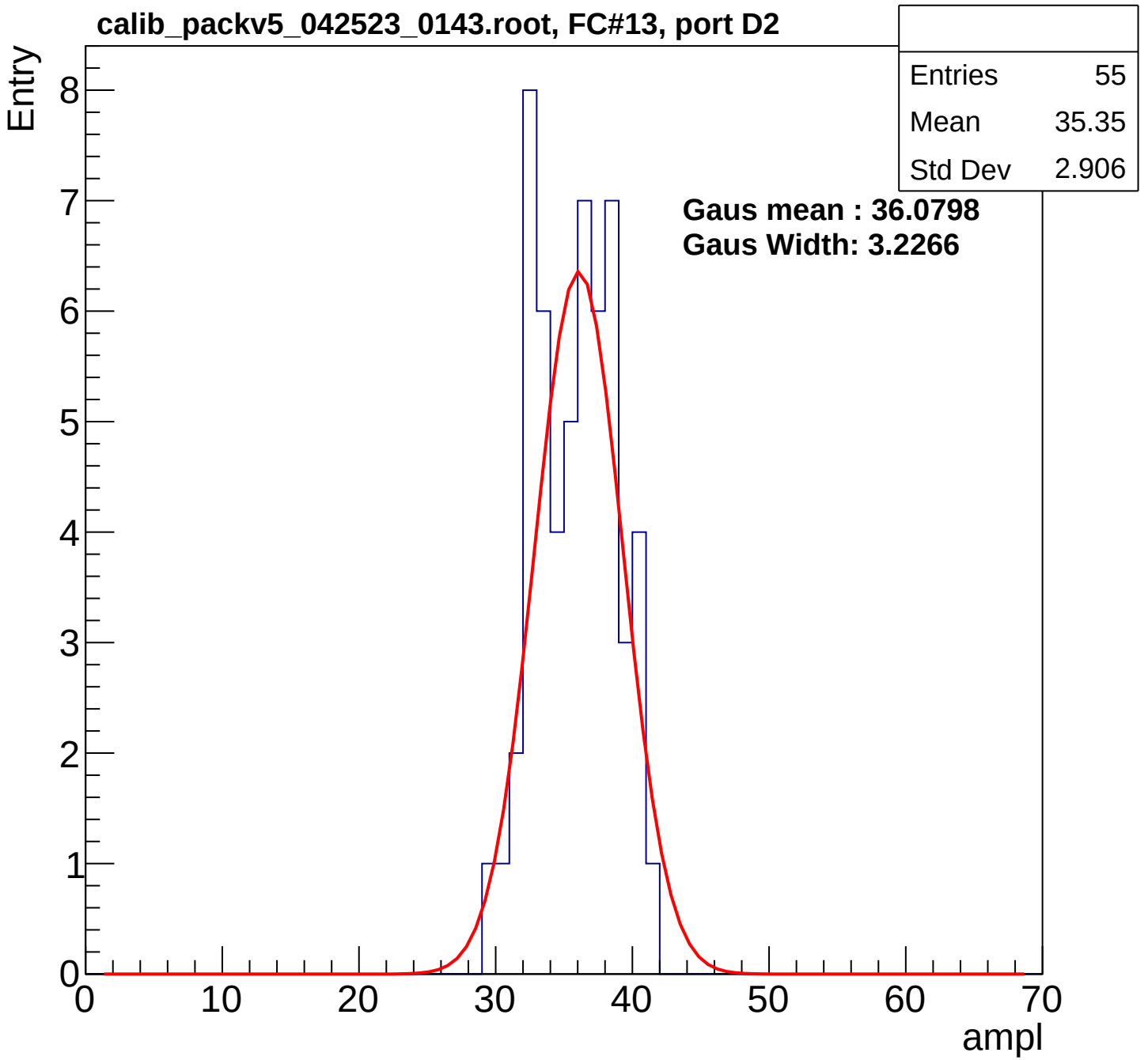
60
70


B1L003S, U17-ch80, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 35.35
Std Dev 2.906
Gaus mean : 36.0798
Gaus Width: 3.2266
Entry
ampl
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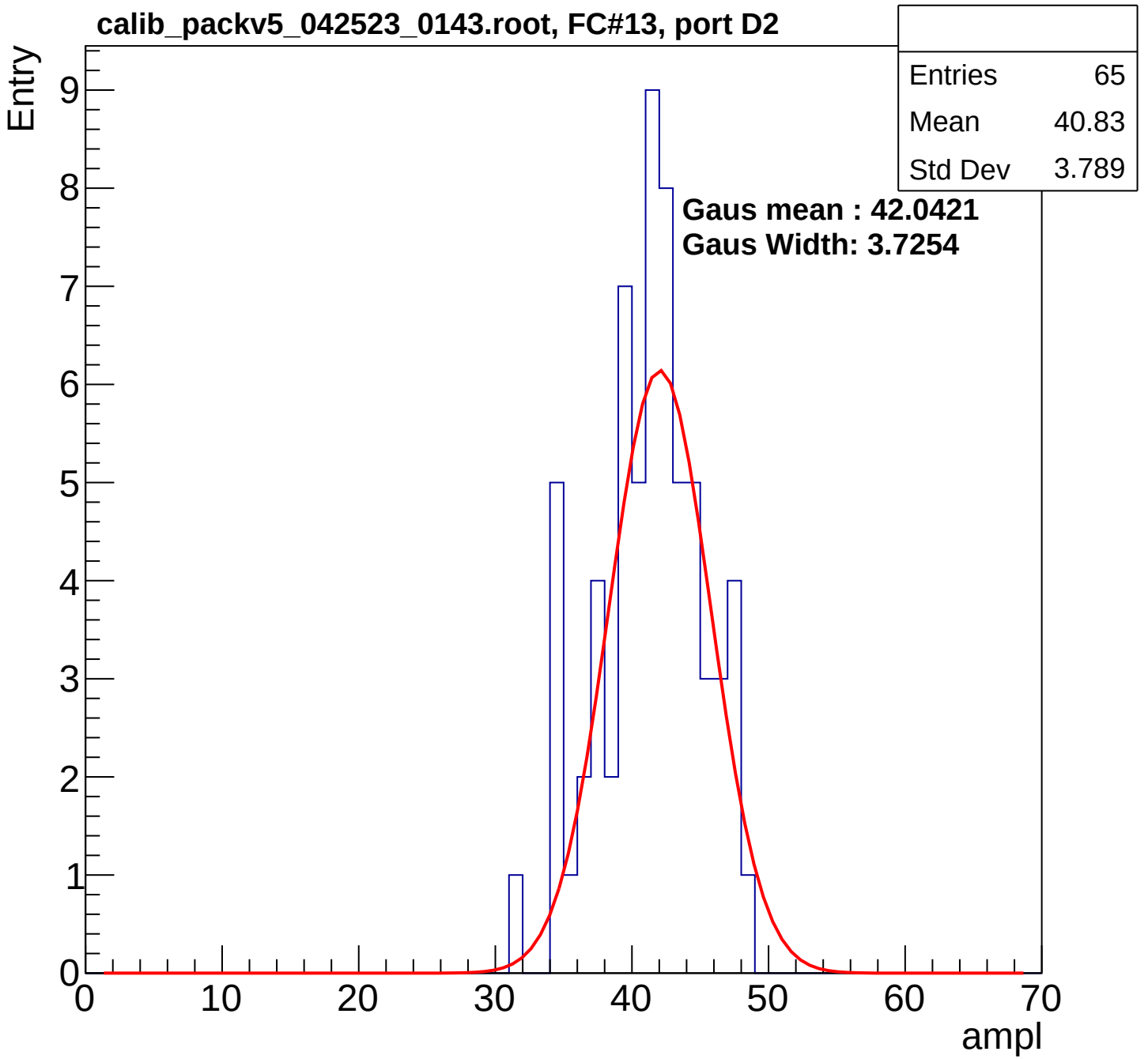
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch80, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 40.83
Std Dev 3.789
Gaus mean : 42.0421
Gaus Width: 3.7254
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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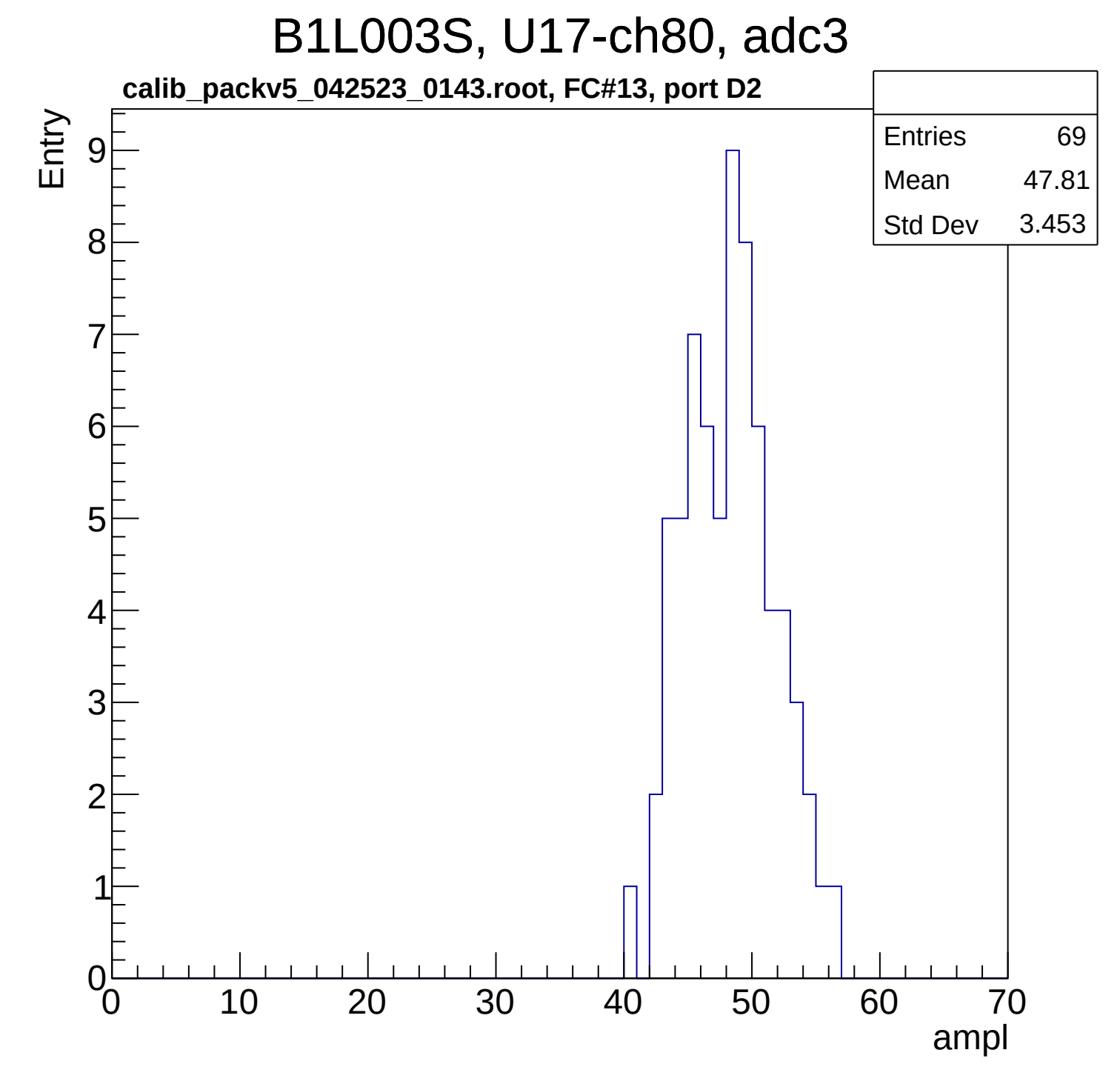
B1L003S, U17-ch80, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 47.81
Std Dev 3.453
Entry
ampl
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3
4
5
6
7
8
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0
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60
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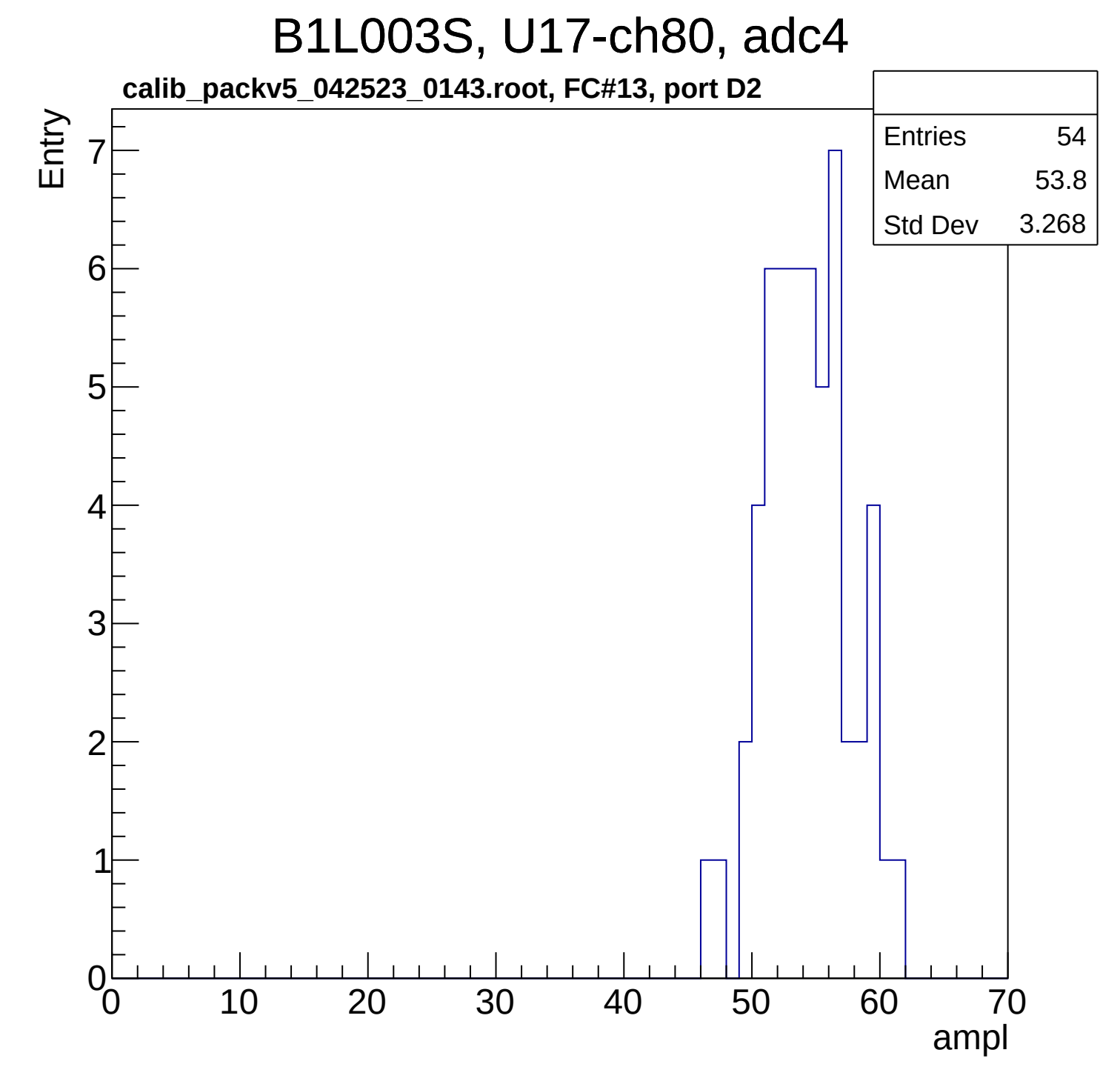
B1L003S, U17-ch80, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.8
Std Dev 3.268
Entry
ampl
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4
5
6
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0
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60
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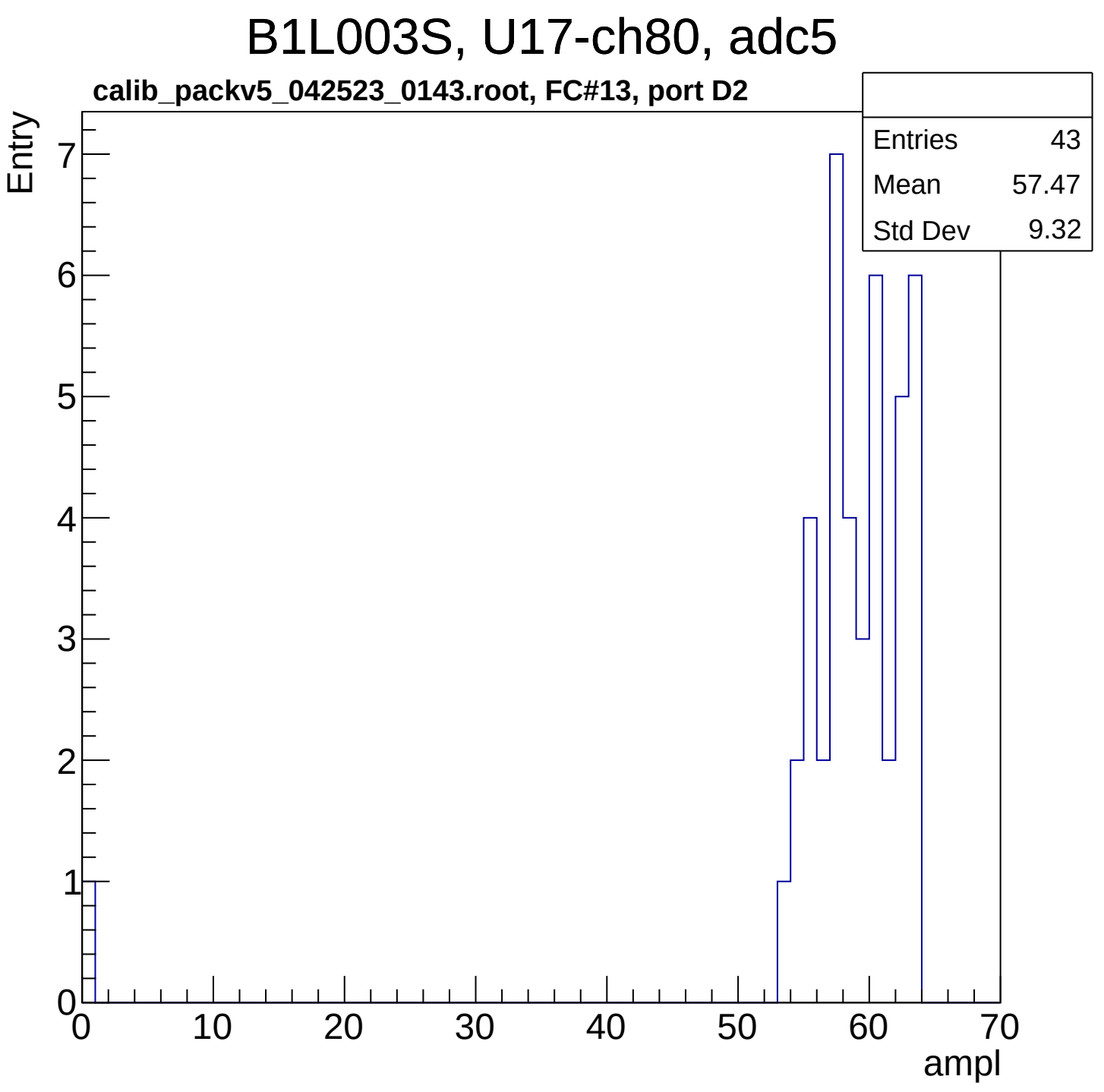
B1L003S, U17-ch80, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 57.47
Std Dev 9.32
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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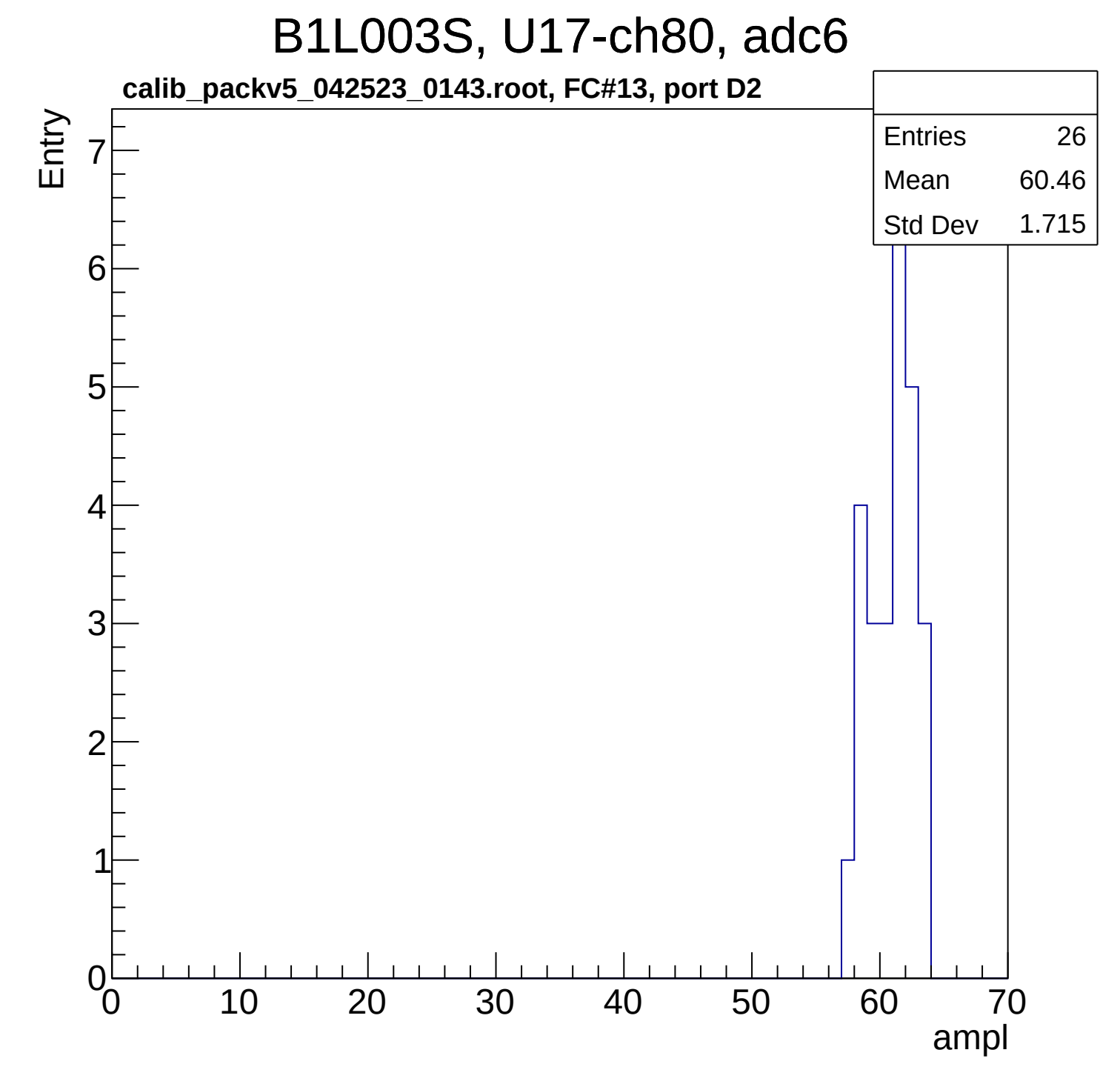
B1L003S, U17-ch80, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 26
Mean 60.46
Std Dev 1.715
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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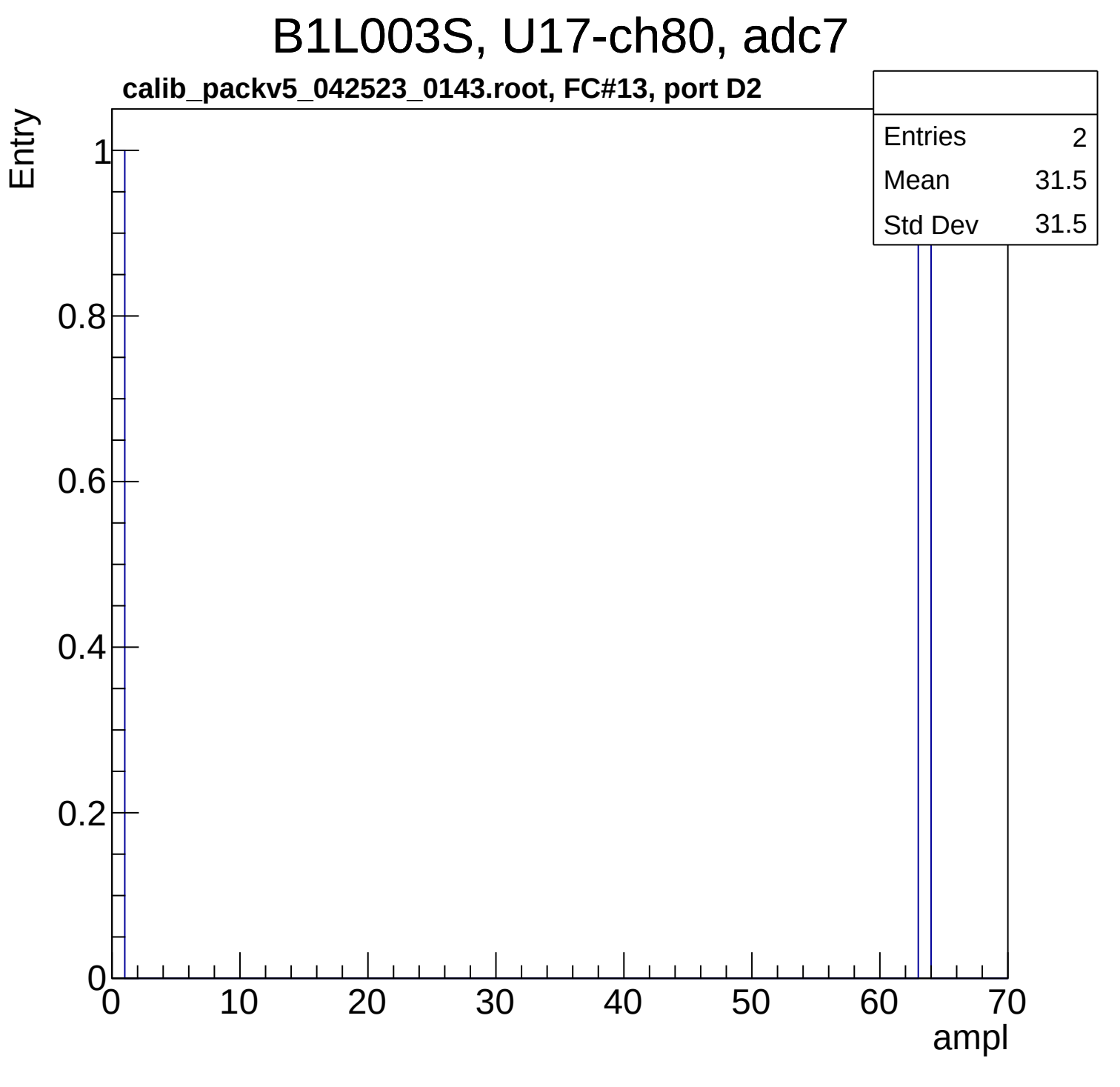
B1L003S, U17-ch80, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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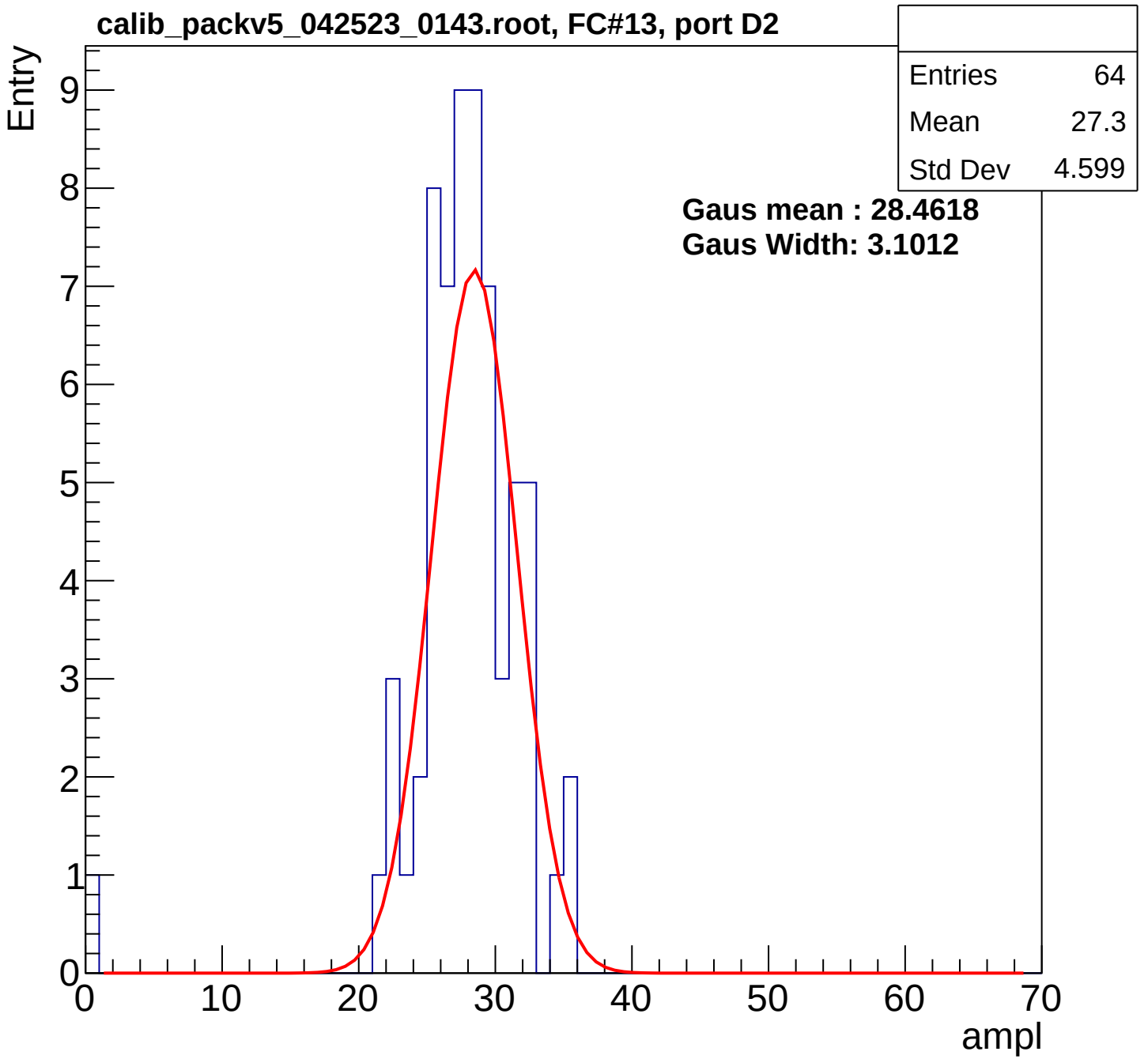
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch81, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 27.3
Std Dev 4.599
Gaus mean : 28.4618
Gaus Width: 3.1012
Entry
ampl
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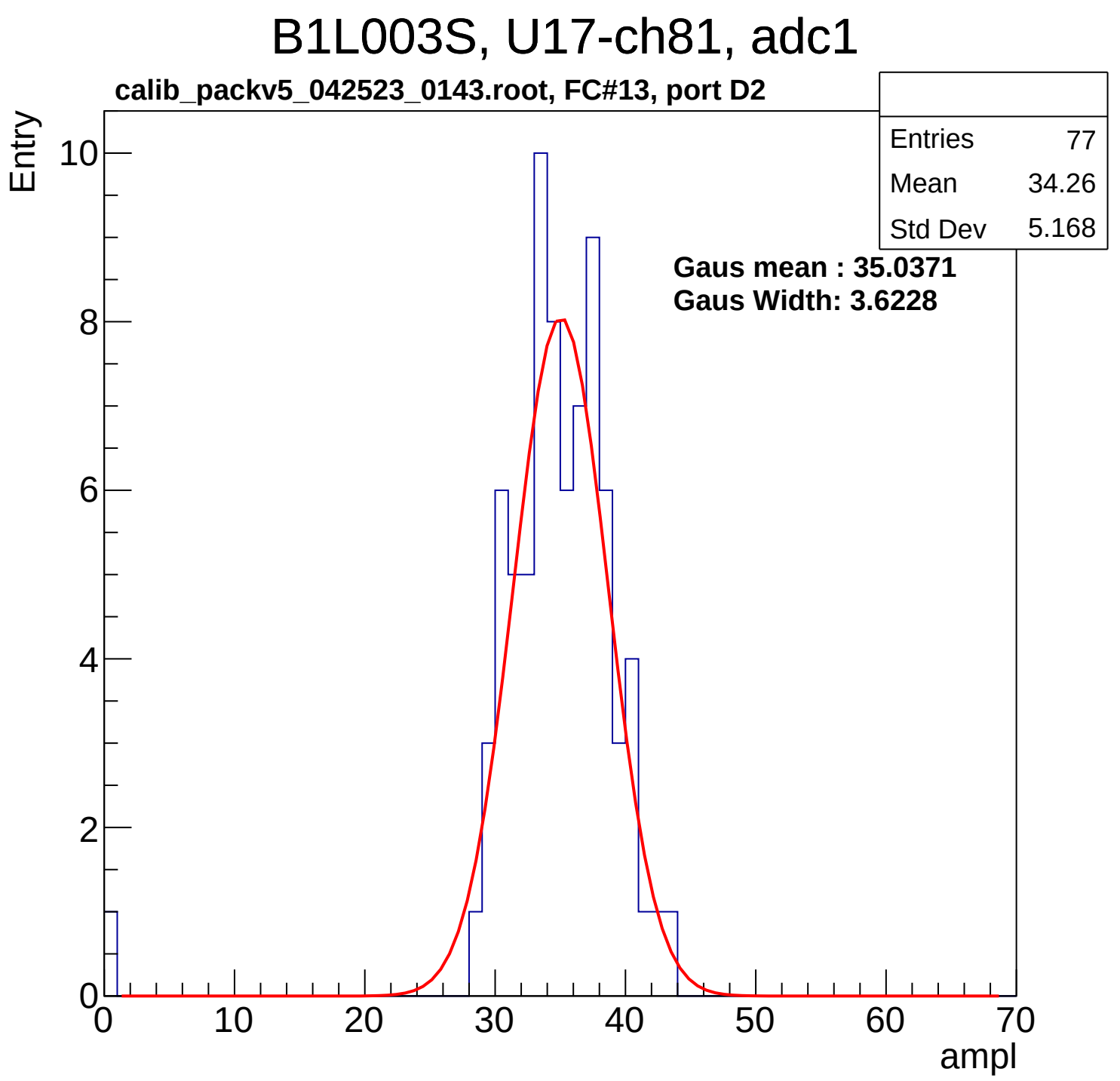
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch81, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 34.26
Std Dev 5.168
Gaus mean : 35.0371
Gaus Width: 3.6228
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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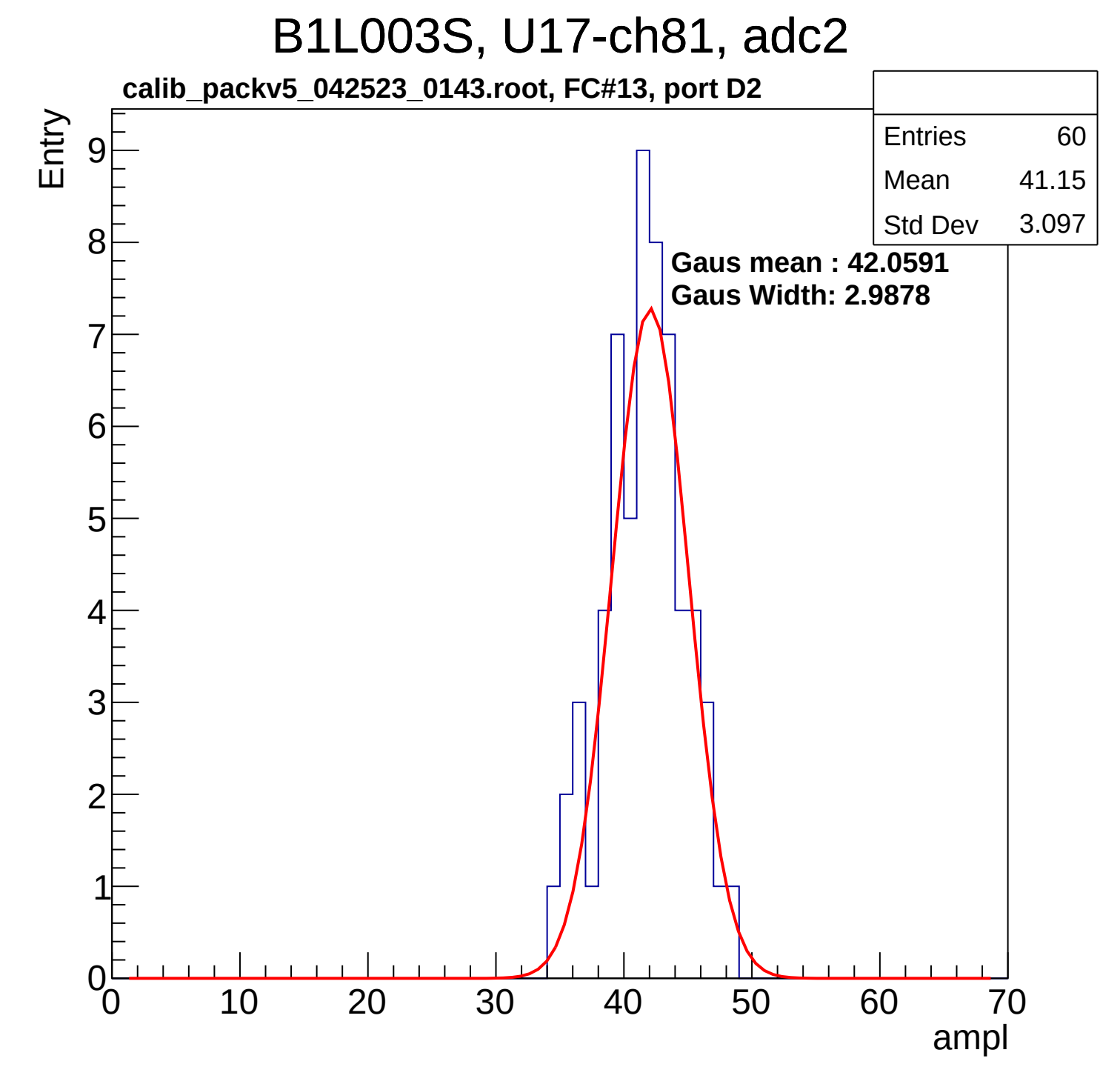
B1L003S, U17-ch81, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.15
Std Dev 3.097
Gaus mean : 42.0591
Gaus Width: 2.9878
Entry
ampl
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2
3
4
5
6
7
8
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0
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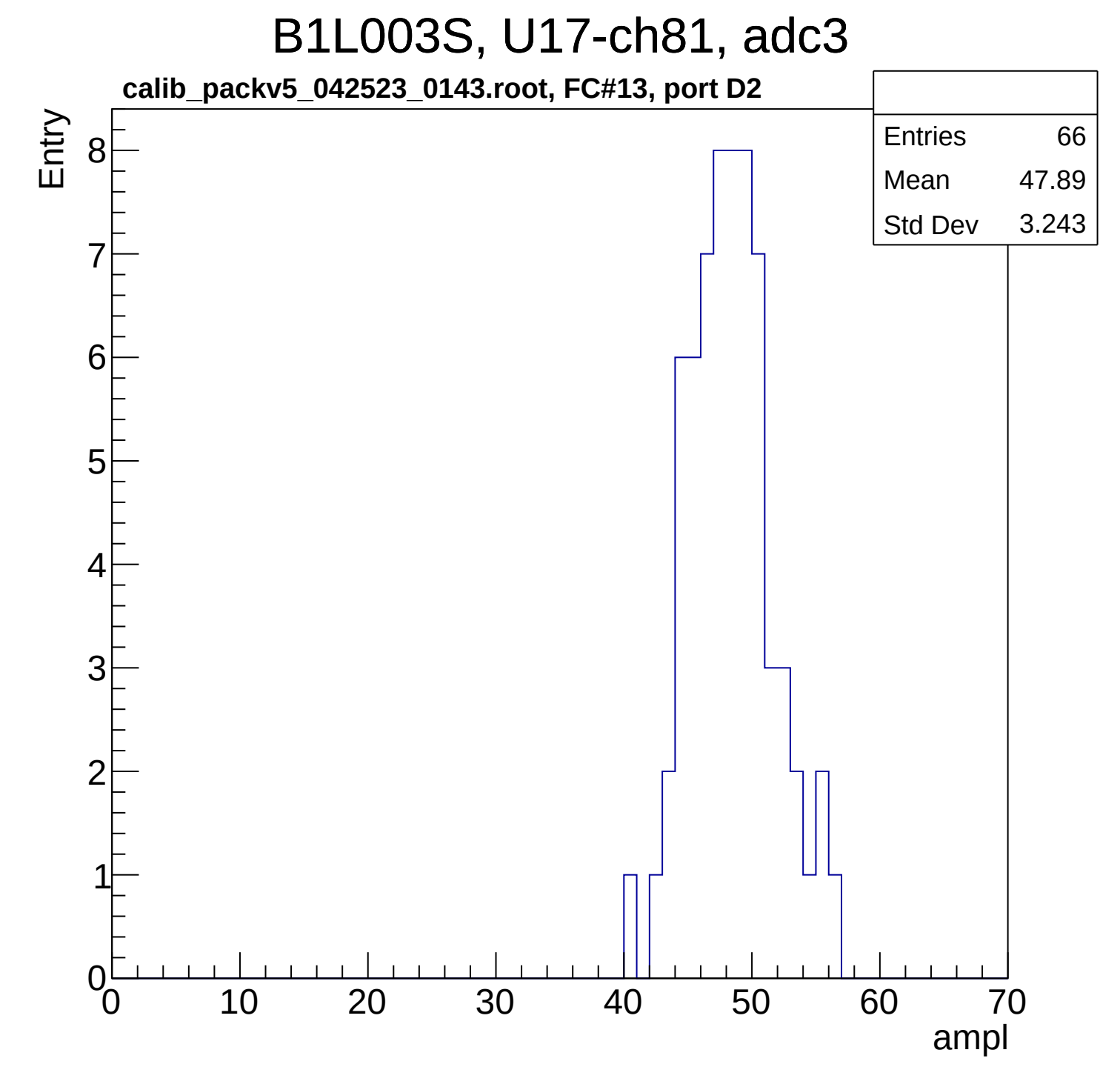
B1L003S, U17-ch81, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 47.89
Std Dev 3.243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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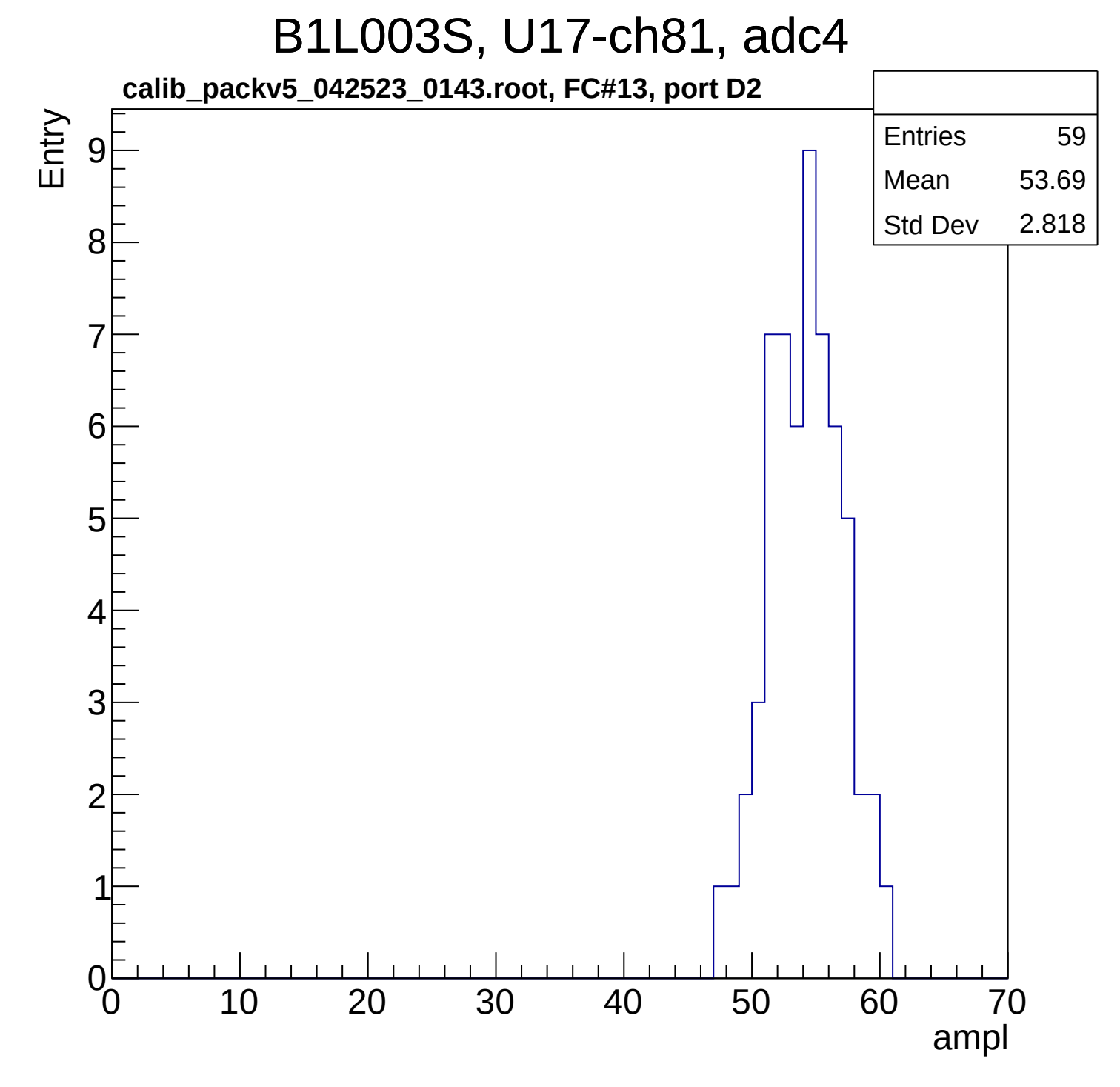
B1L003S, U17-ch81, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 53.69
Std Dev 2.818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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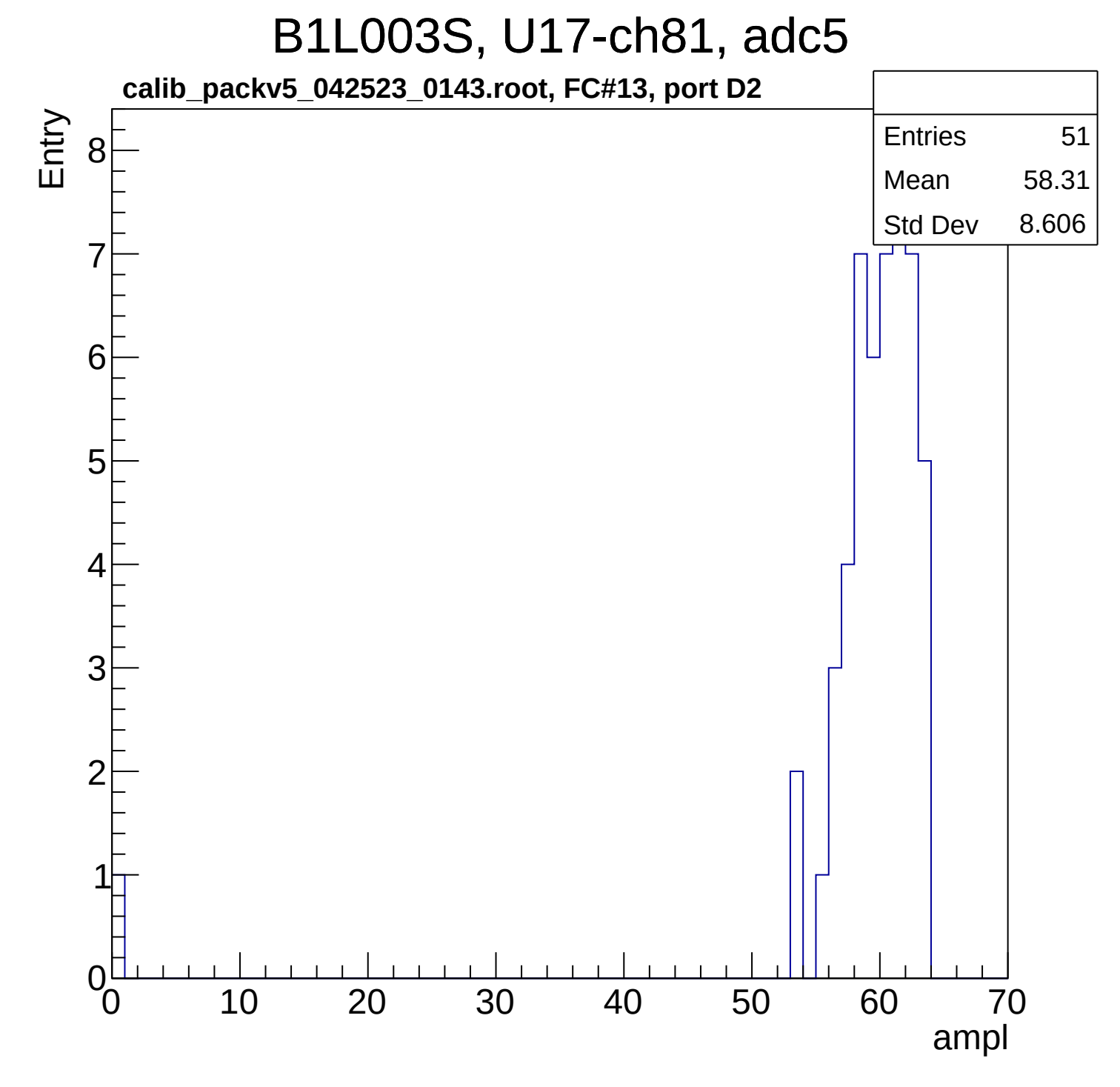
B1L003S, U17-ch81, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.31
Std Dev 8.606
Entry
ampl
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1
2
3
4
5
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7
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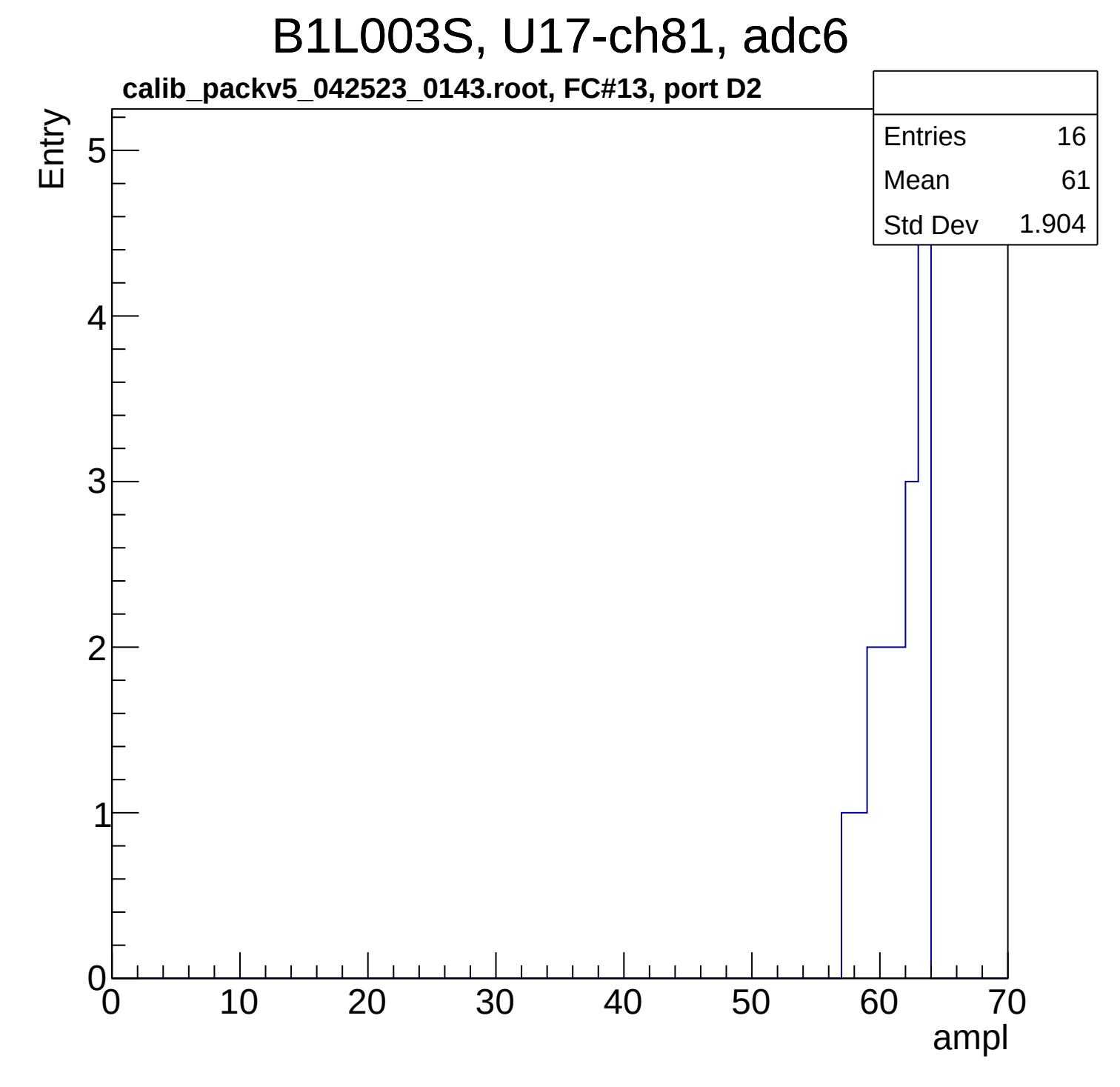
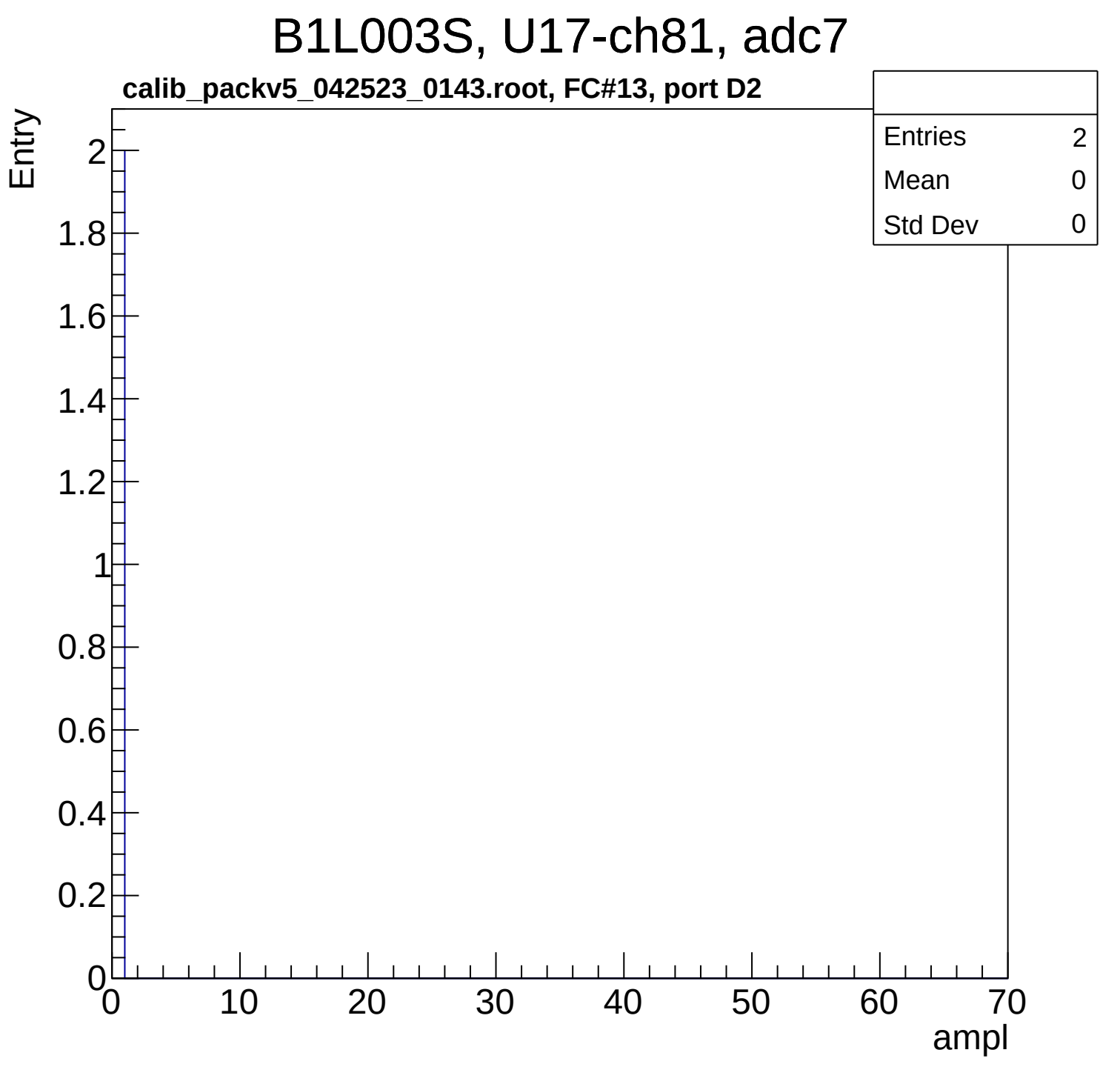

B1L003S, U17-ch81, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 61
Std Dev 1.904
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch81, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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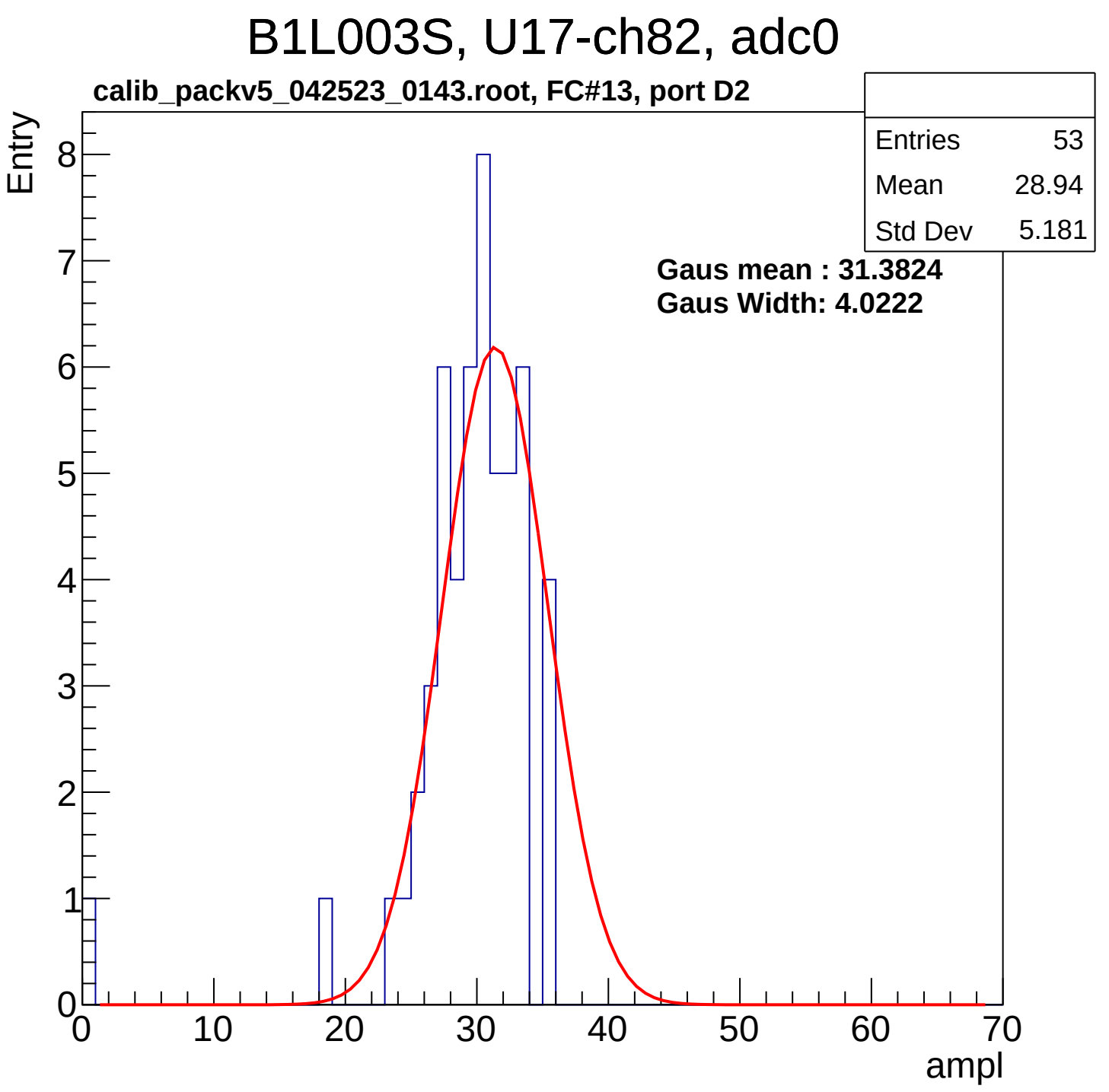
B1L003S, U17-ch82, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 28.94
Std Dev 5.181
Gaus mean : 31.3824
Gaus Width: 4.0222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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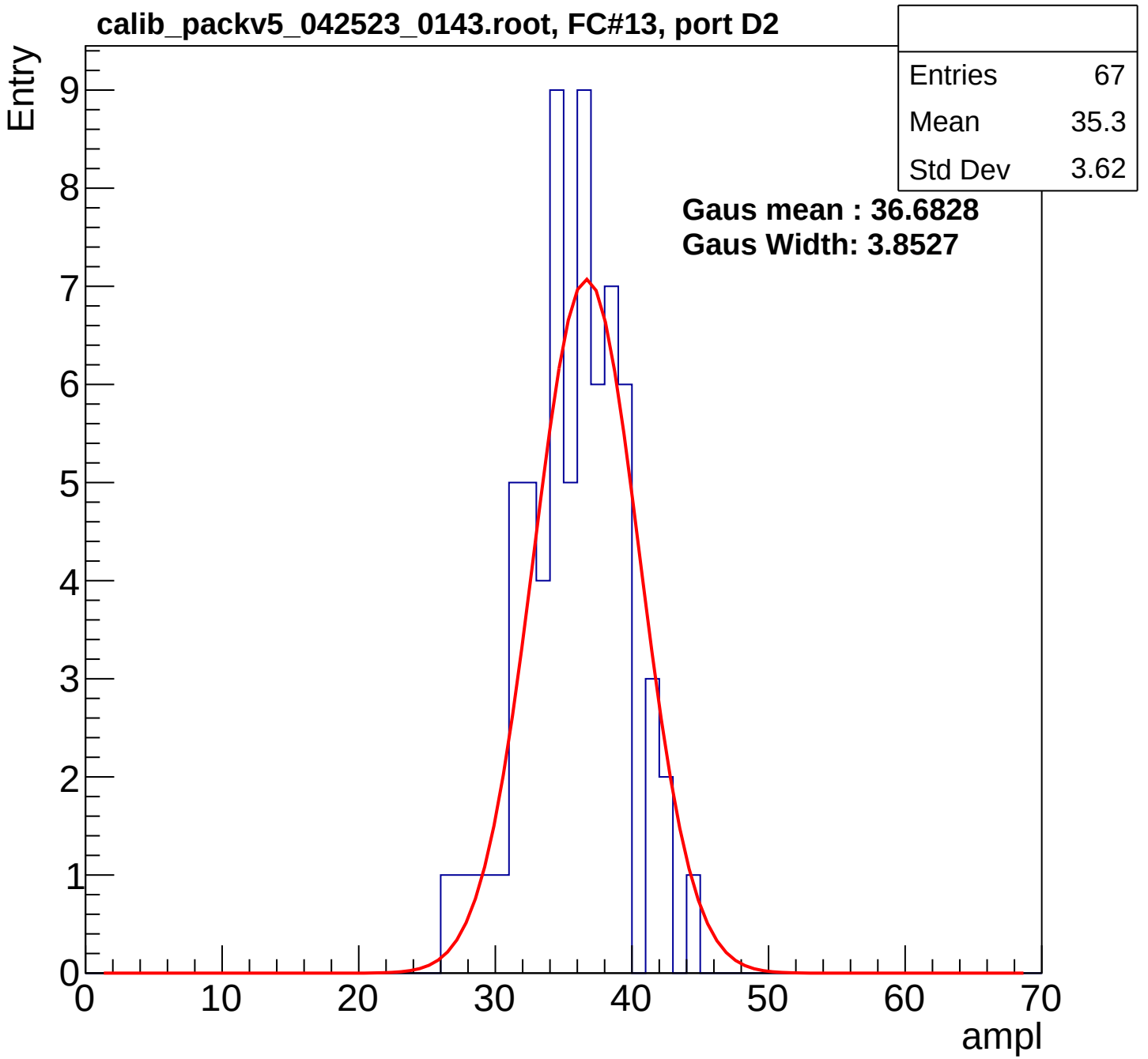
B1L003S, U17-ch82, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 35.3
Std Dev 3.62
Gaus mean : 36.6828
Gaus Width: 3.8527
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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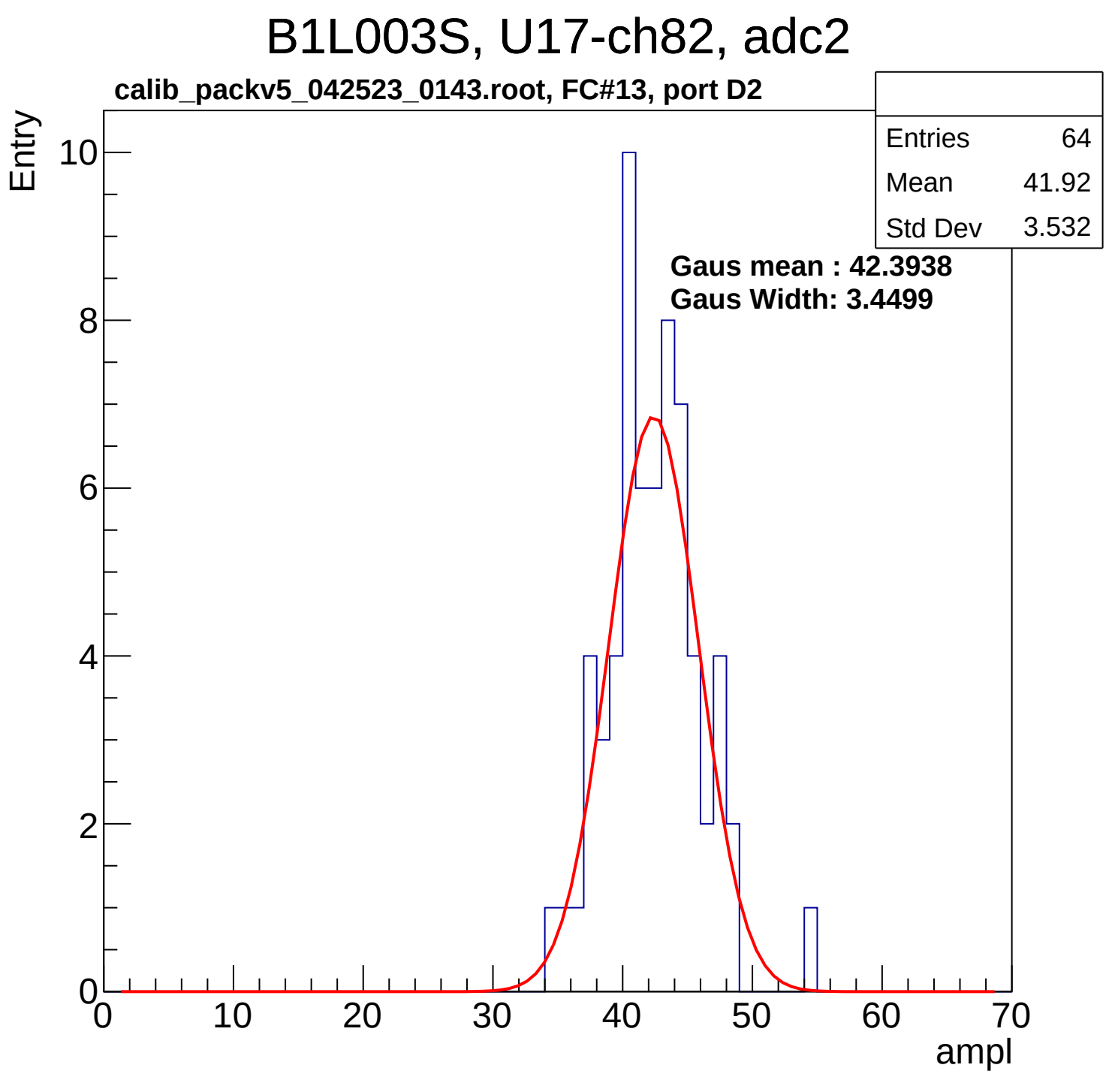
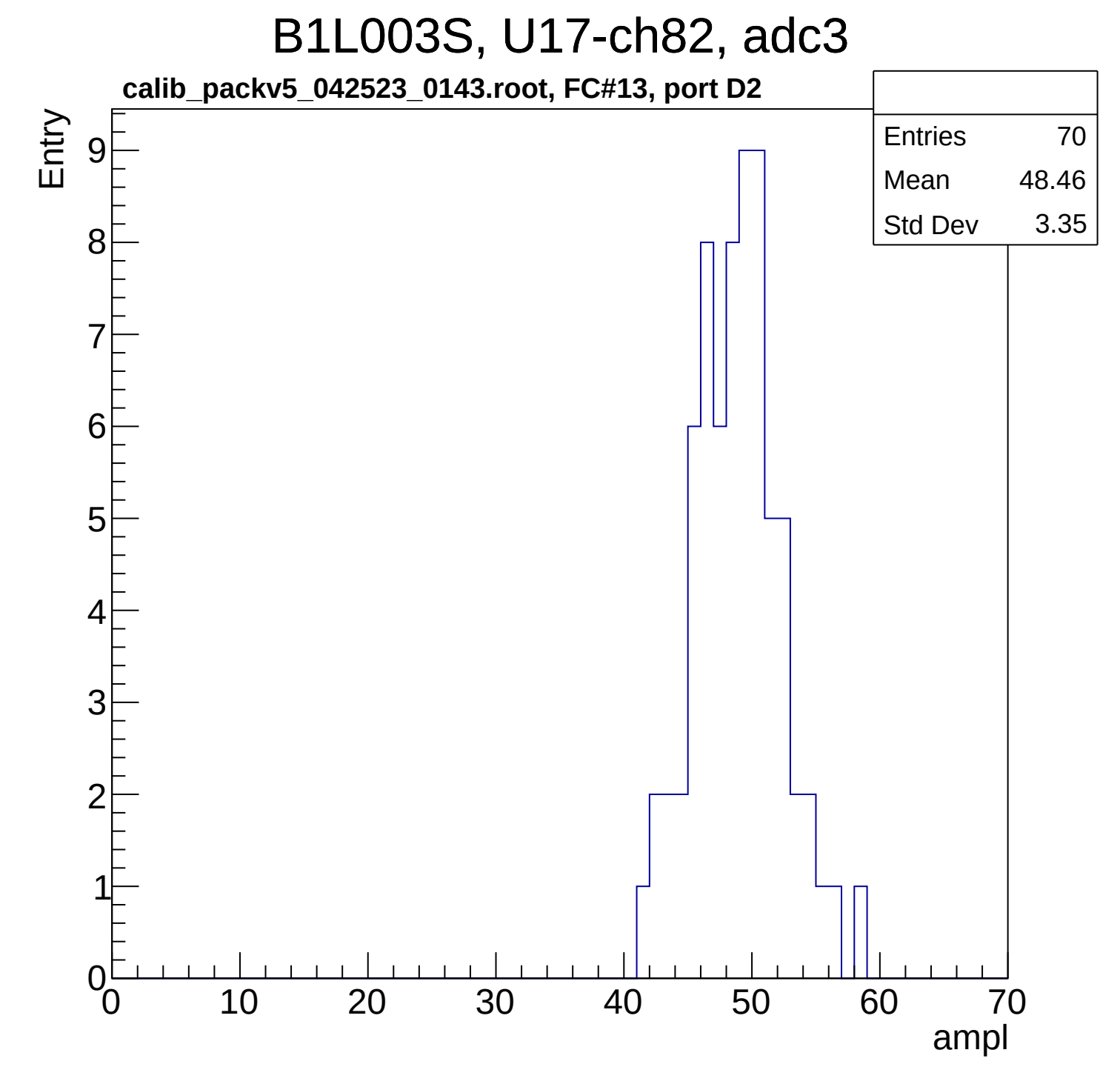

B1L003S, U17-ch82, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 41.92
Std Dev 3.532
Gaus mean : 42.3938
Gaus Width: 3.4499
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch82, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 48.46
Std Dev 3.35
Entry
ampl
0
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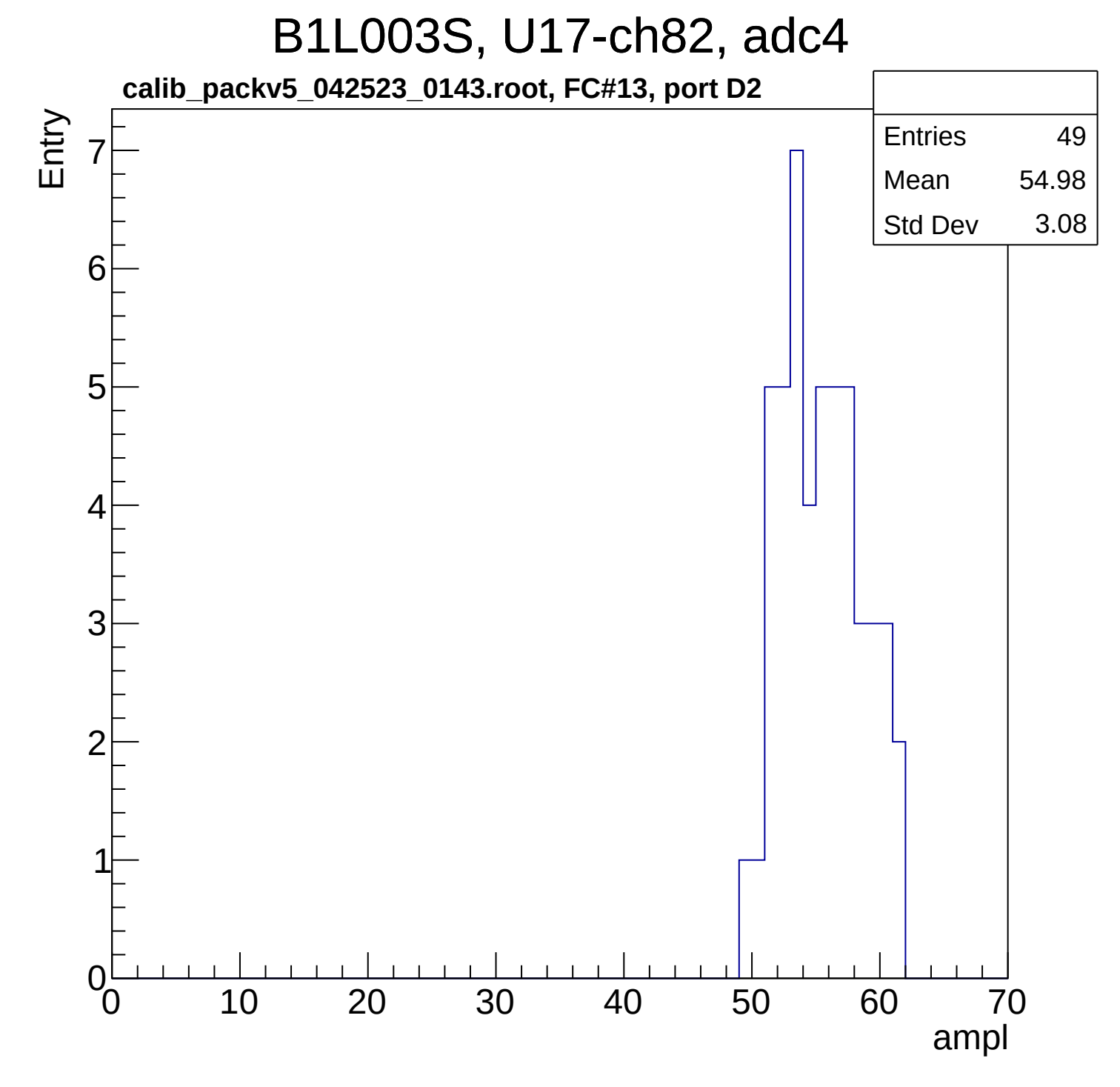
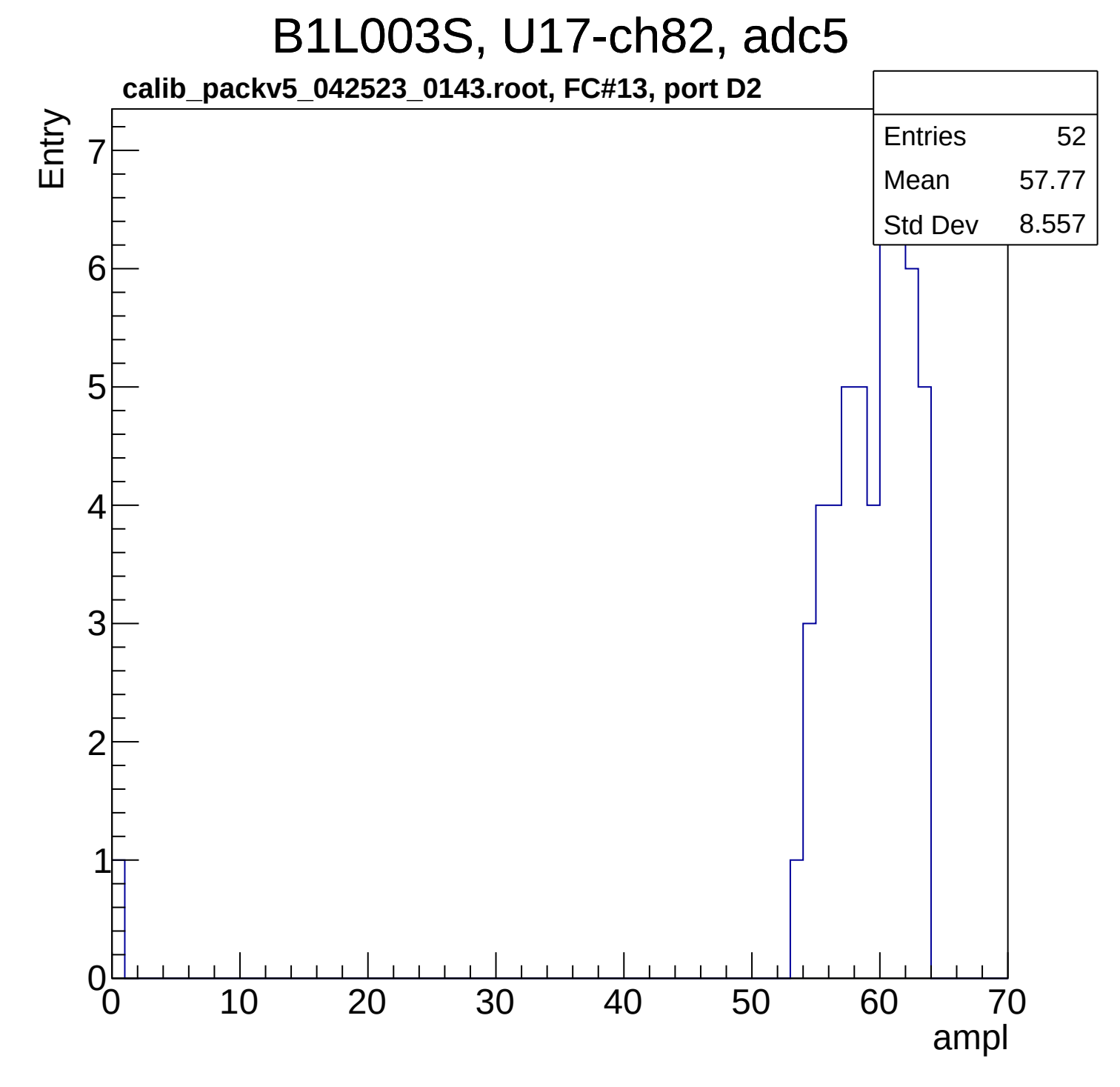
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch82, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.98
Std Dev 3.08
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch82, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 57.77
Std Dev 8.557
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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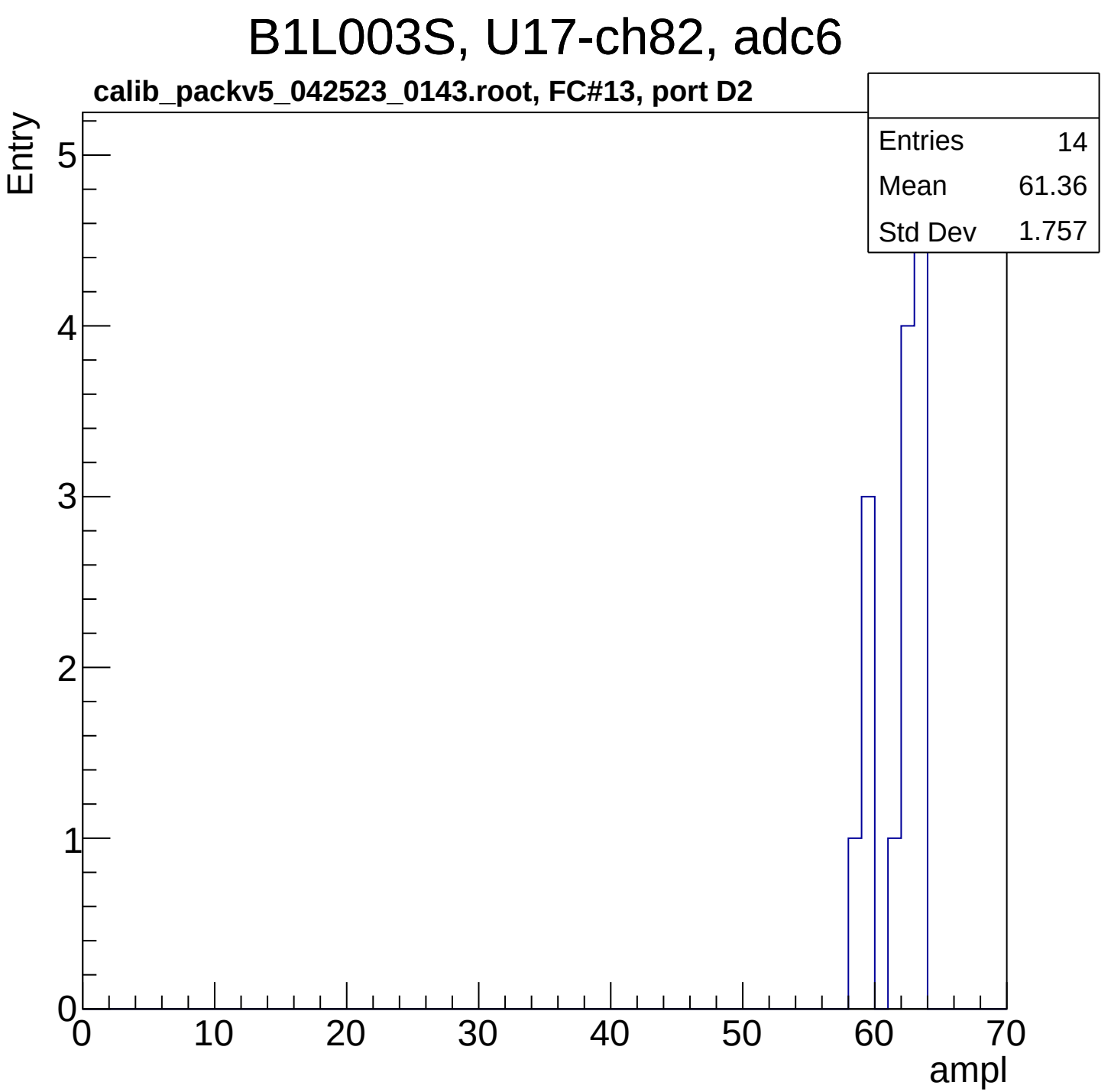
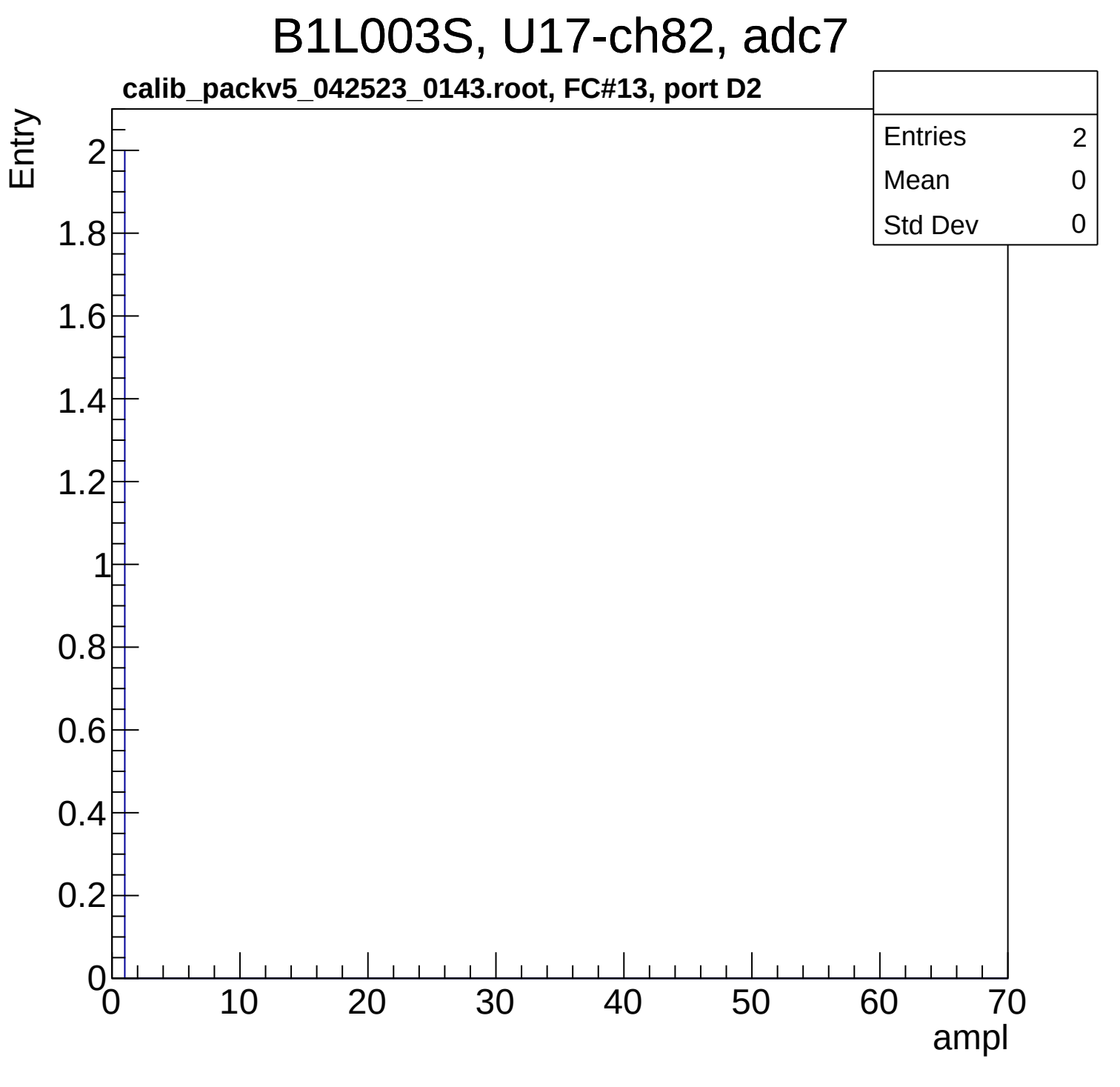
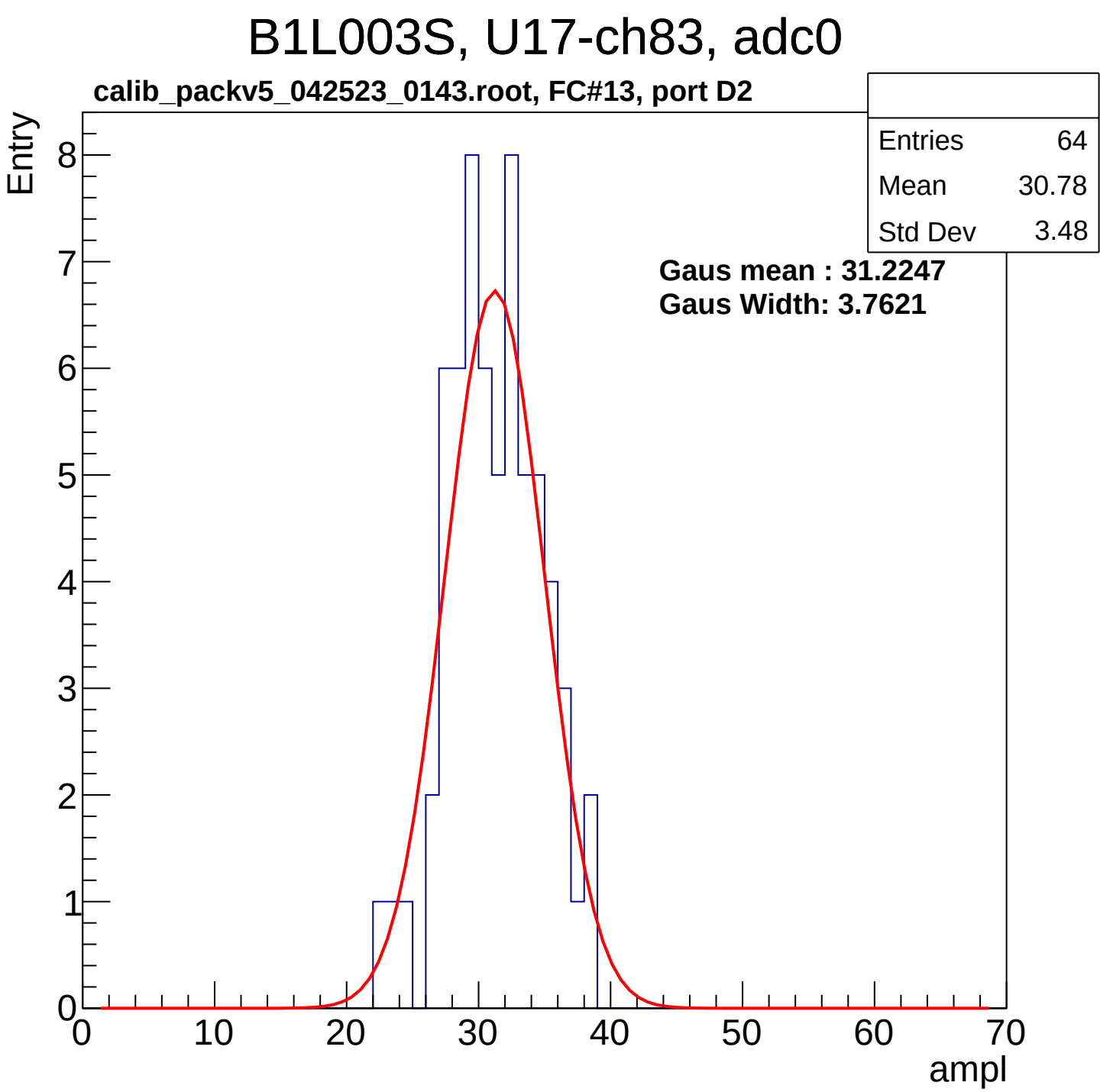
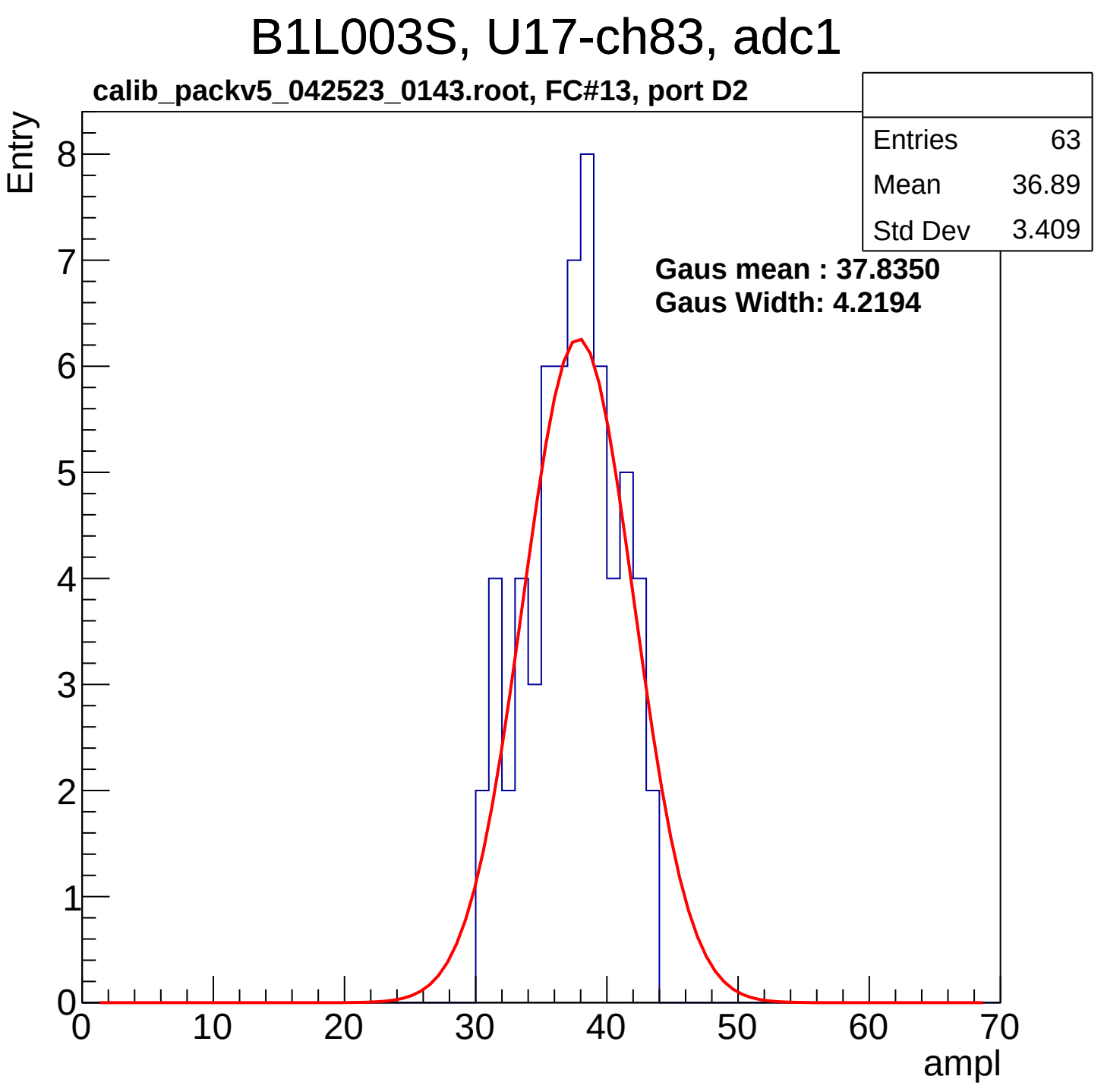

B1L003S, U17-ch82, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.36
Std Dev 1.757
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch82, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U17-ch83, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 30.78
Std Dev 3.48
Gaus mean : 31.2247
Gaus Width: 3.7621
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch83, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.89
Std Dev 3.409
Gaus mean : 37.8350
Gaus Width: 4.2194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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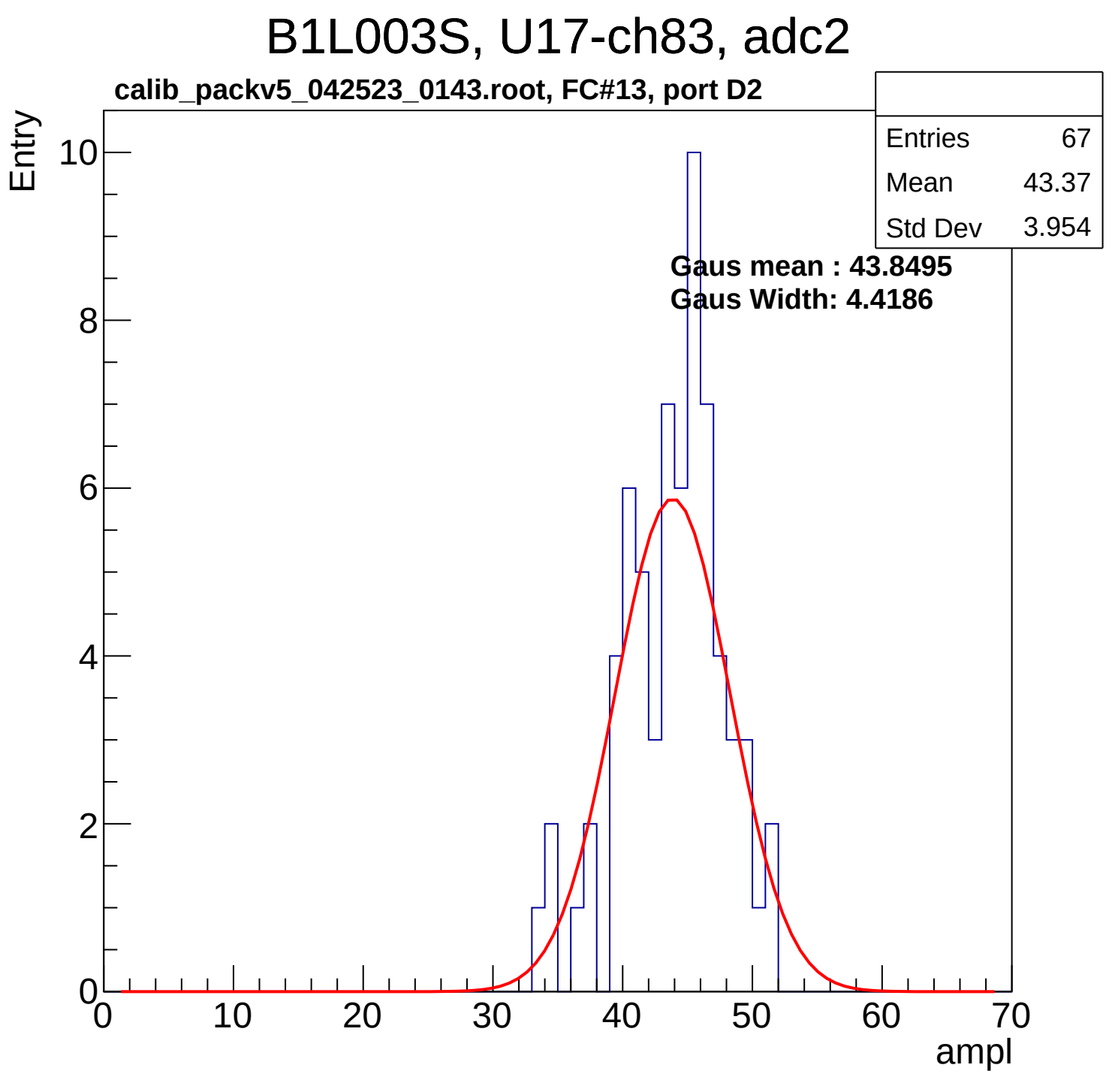
B1L003S, U17-ch83, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.37
Std Dev 3.954
Gaus mean : 43.8495
Gaus Width: 4.4186
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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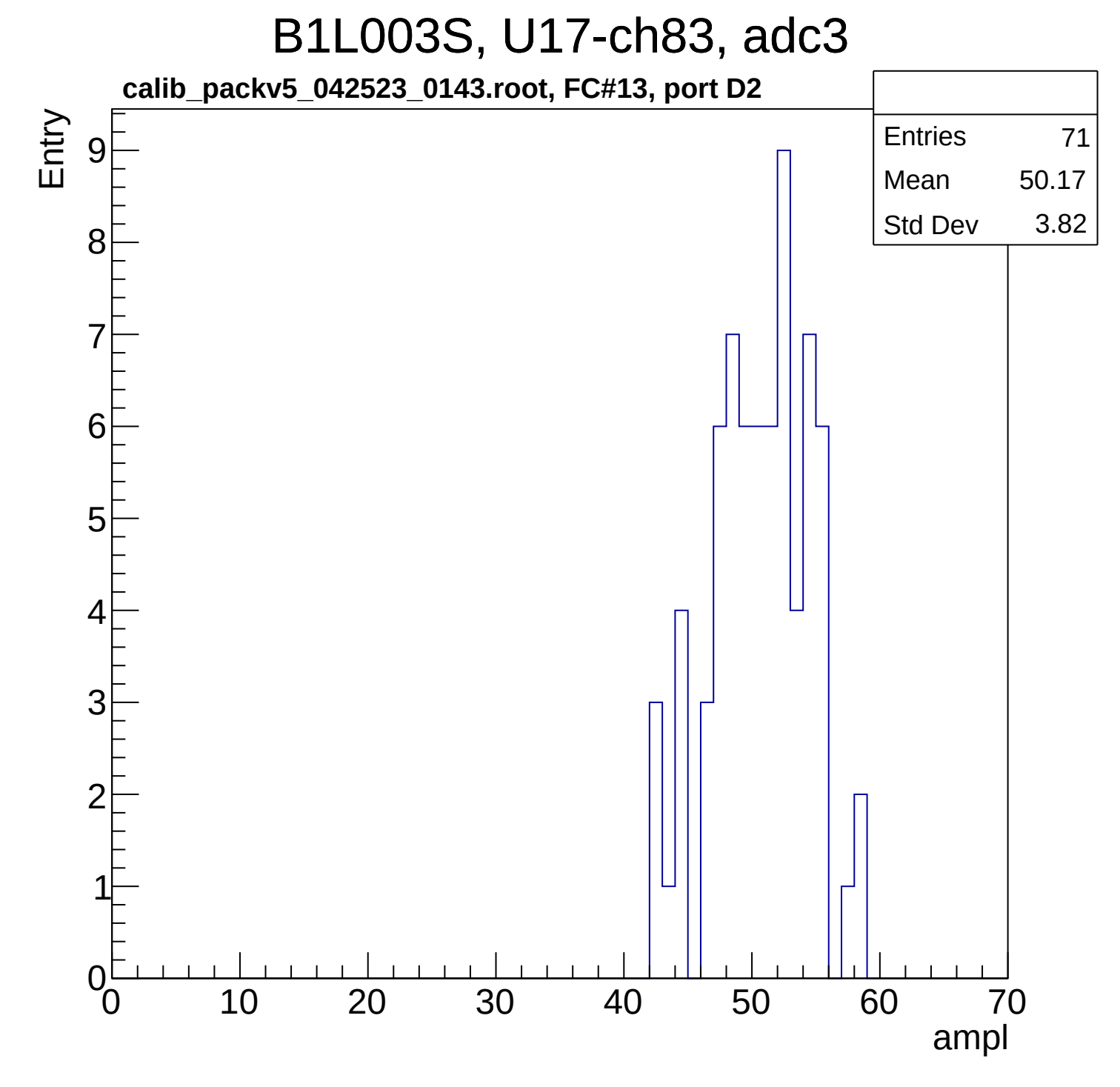
B1L003S, U17-ch83, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 50.17
Std Dev 3.82
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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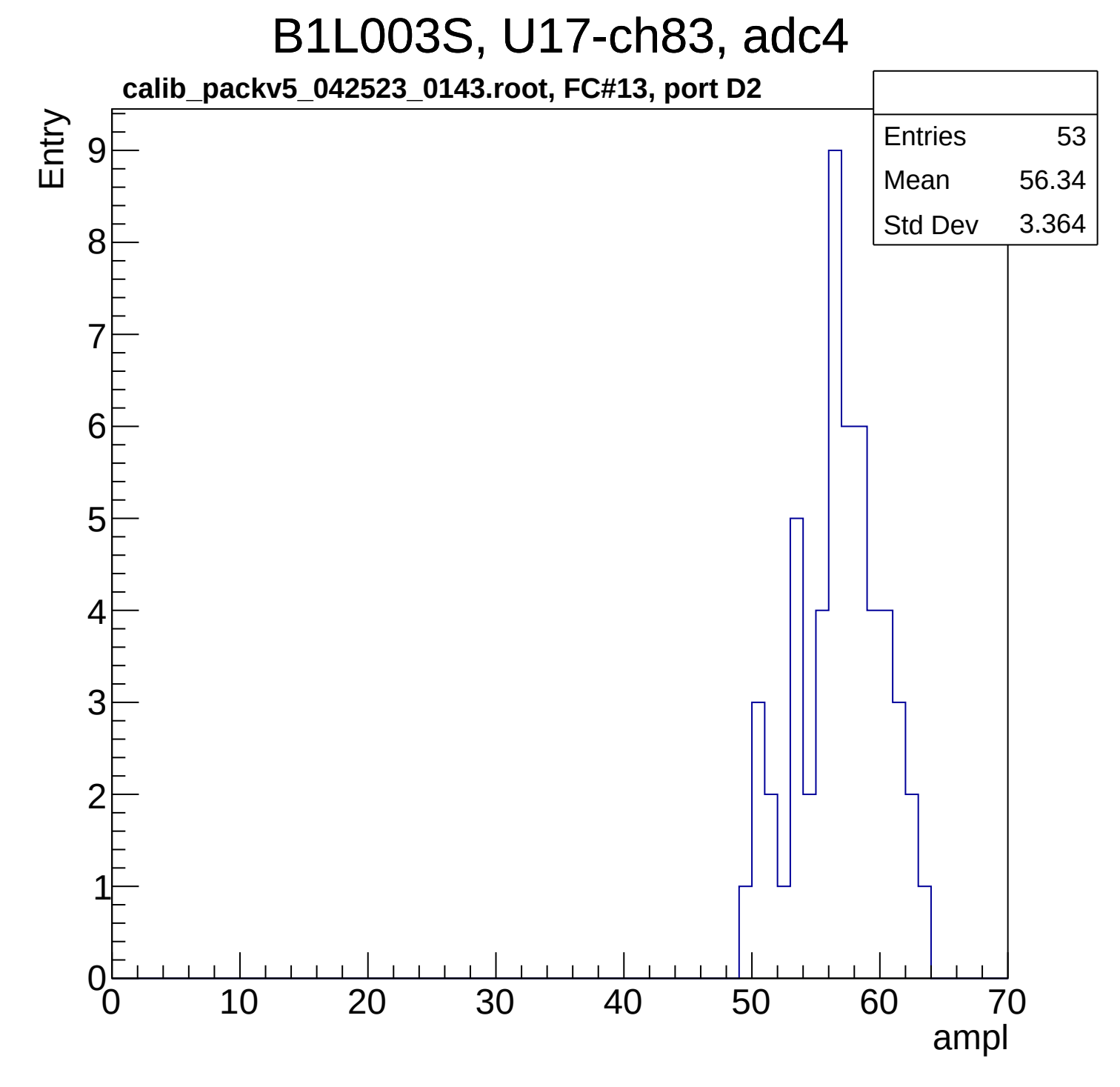
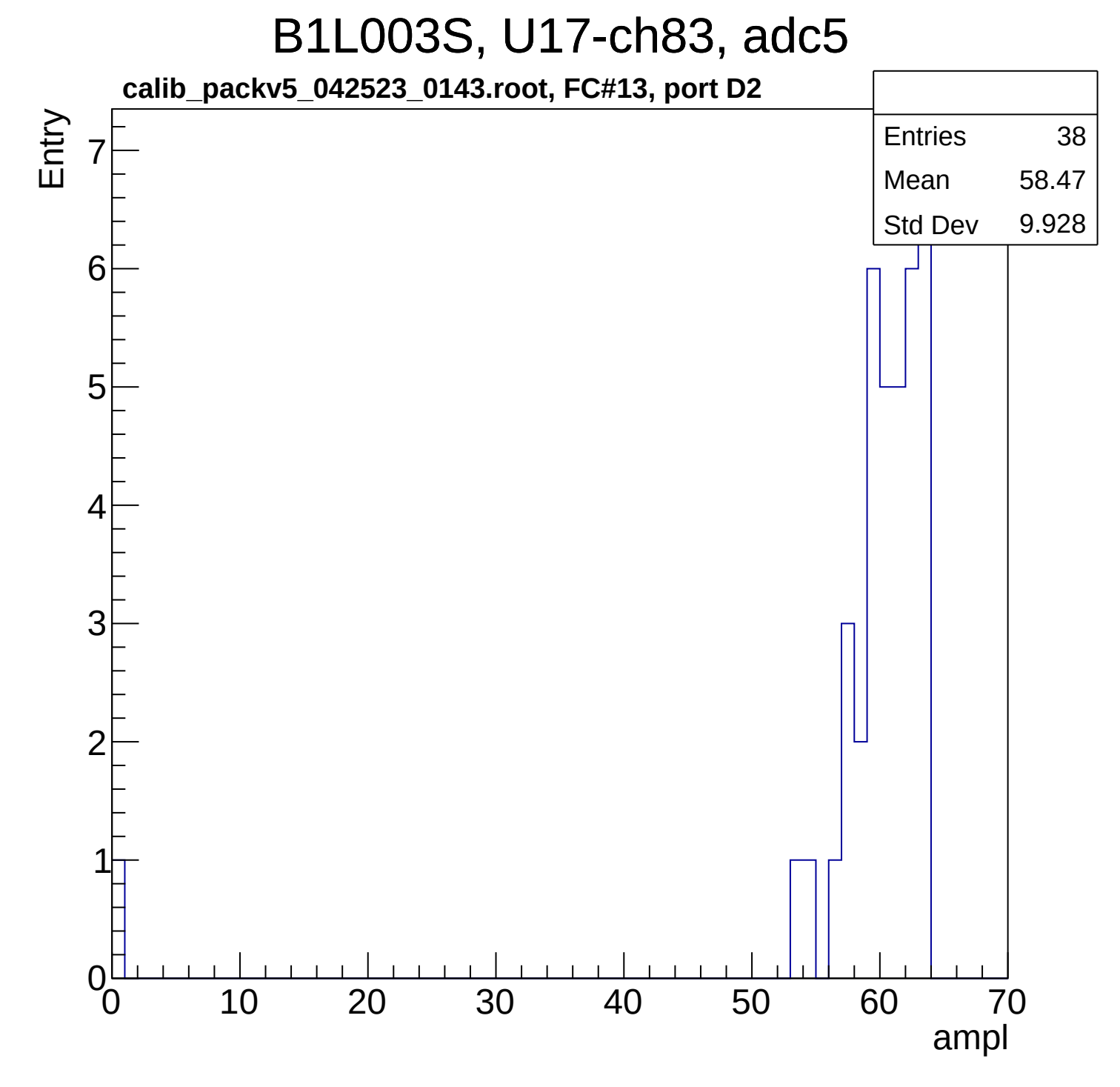

B1L003S, U17-ch83, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 56.34
Std Dev 3.364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch83, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 58.47
Std Dev 9.928
Entry
ampl
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2
3
4
5
6
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0
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20
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40
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60
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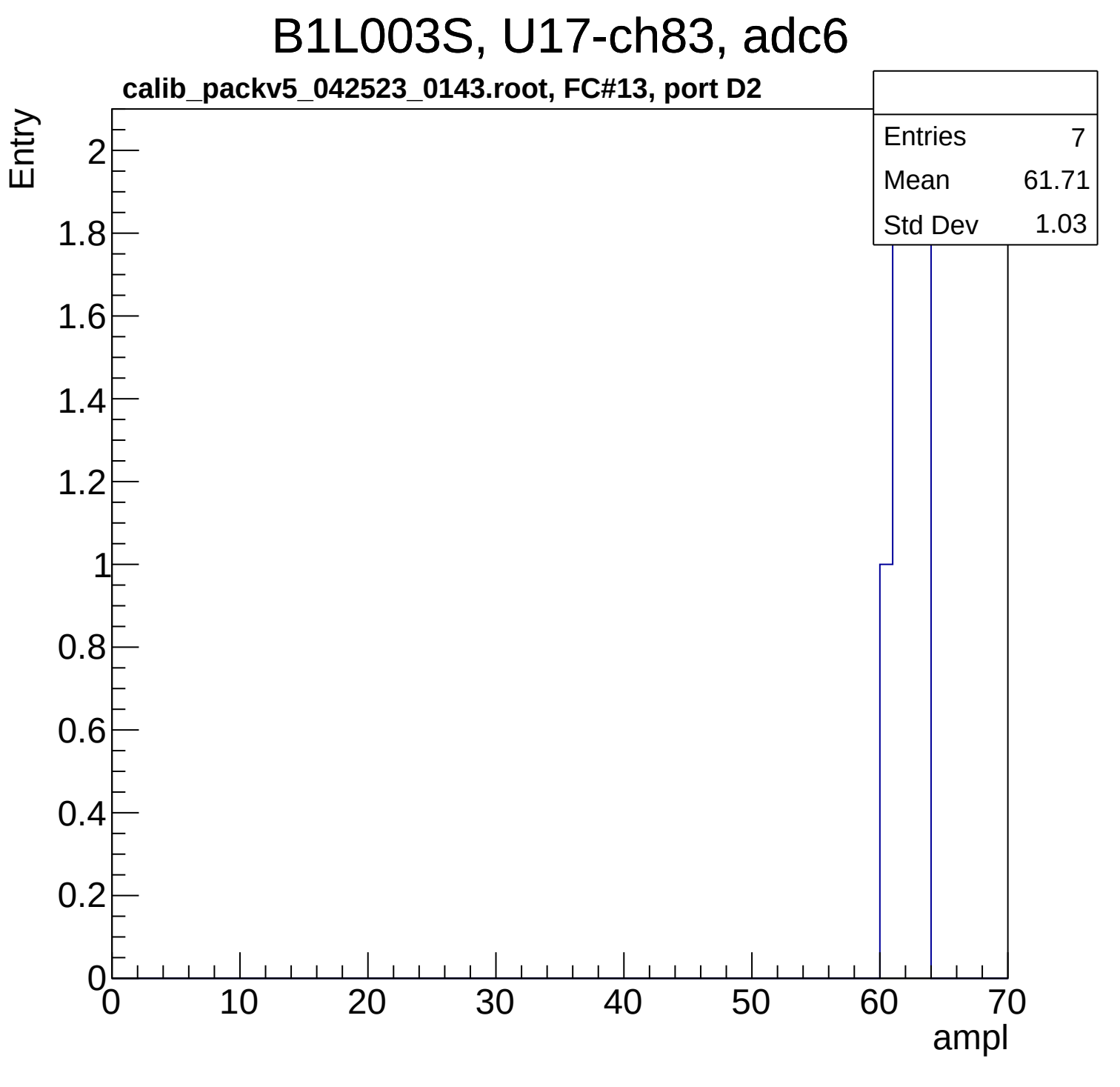
B1L003S, U17-ch83, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.71
Std Dev 1.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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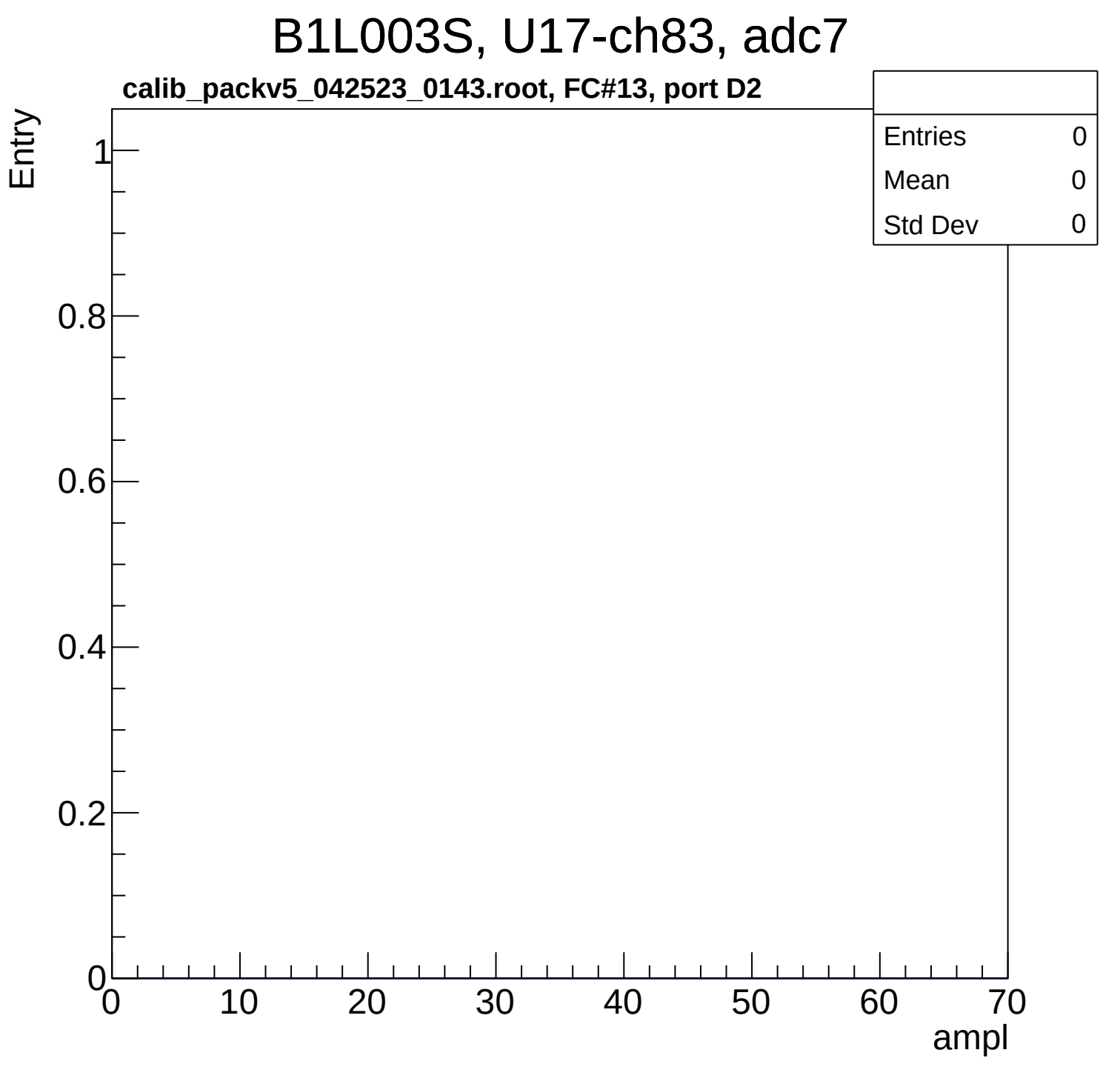
B1L003S, U17-ch83, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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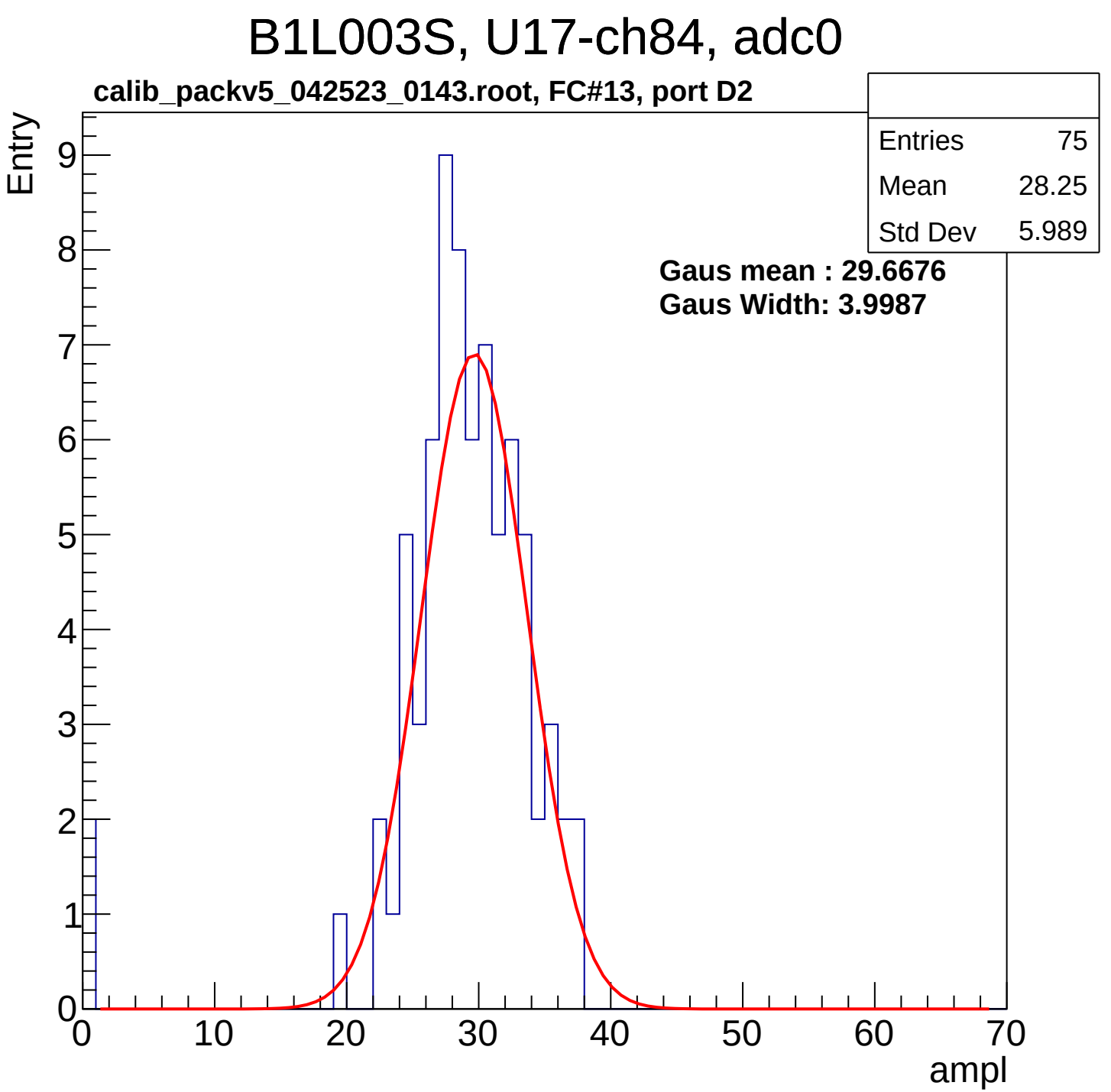
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch84, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.25
Std Dev 5.989
Gaus mean : 29.6676
Gaus Width: 3.9987
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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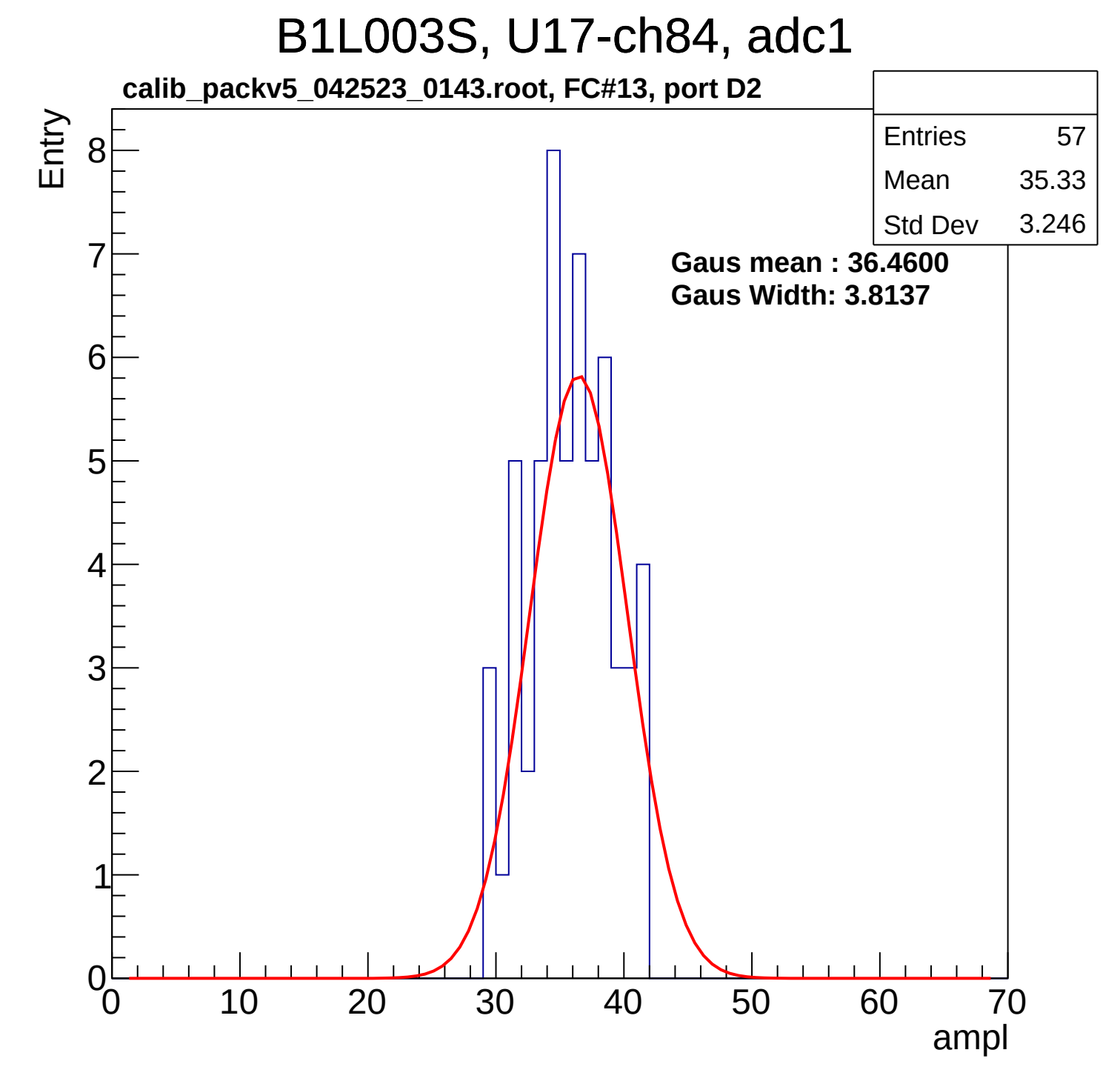
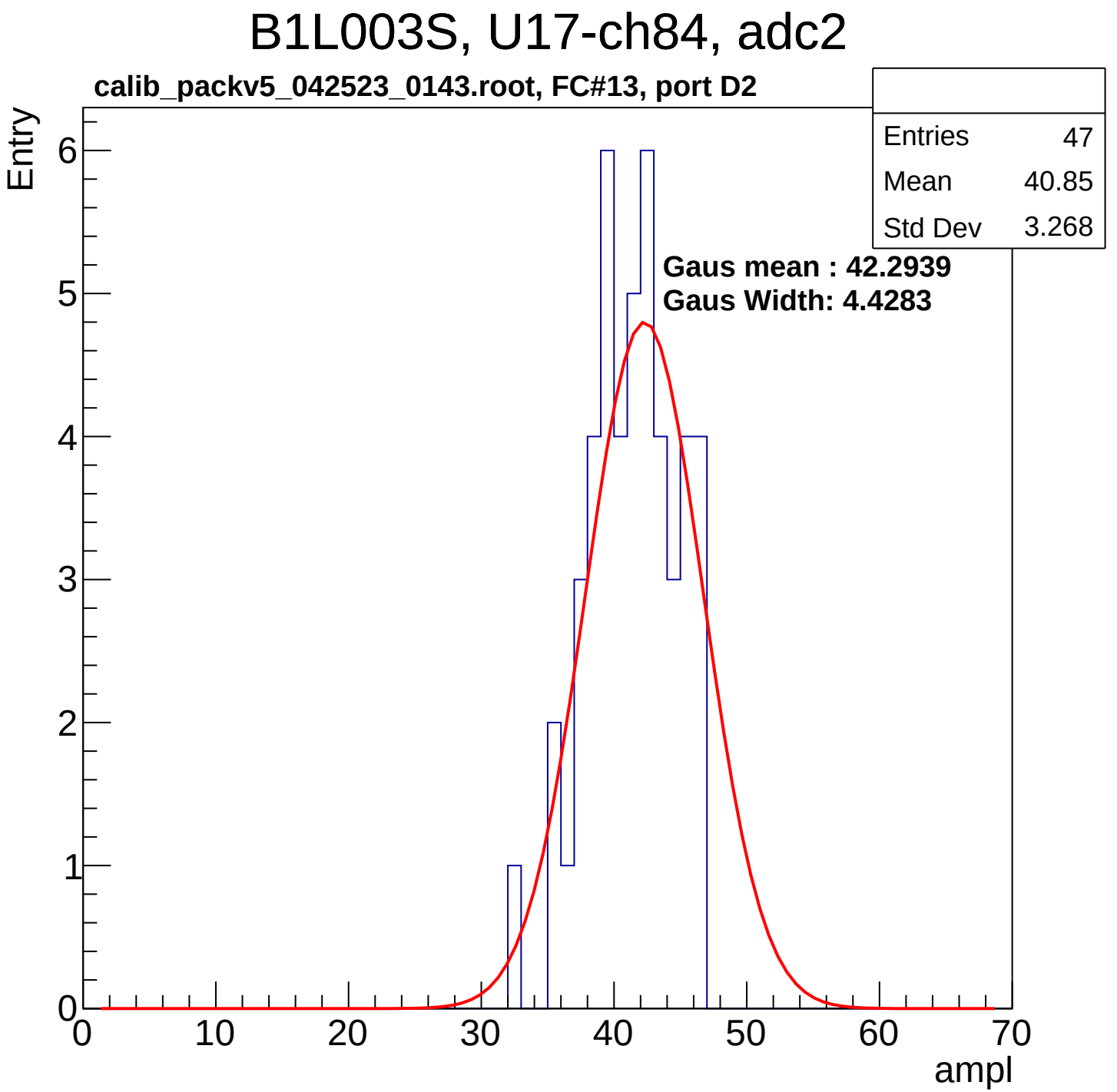
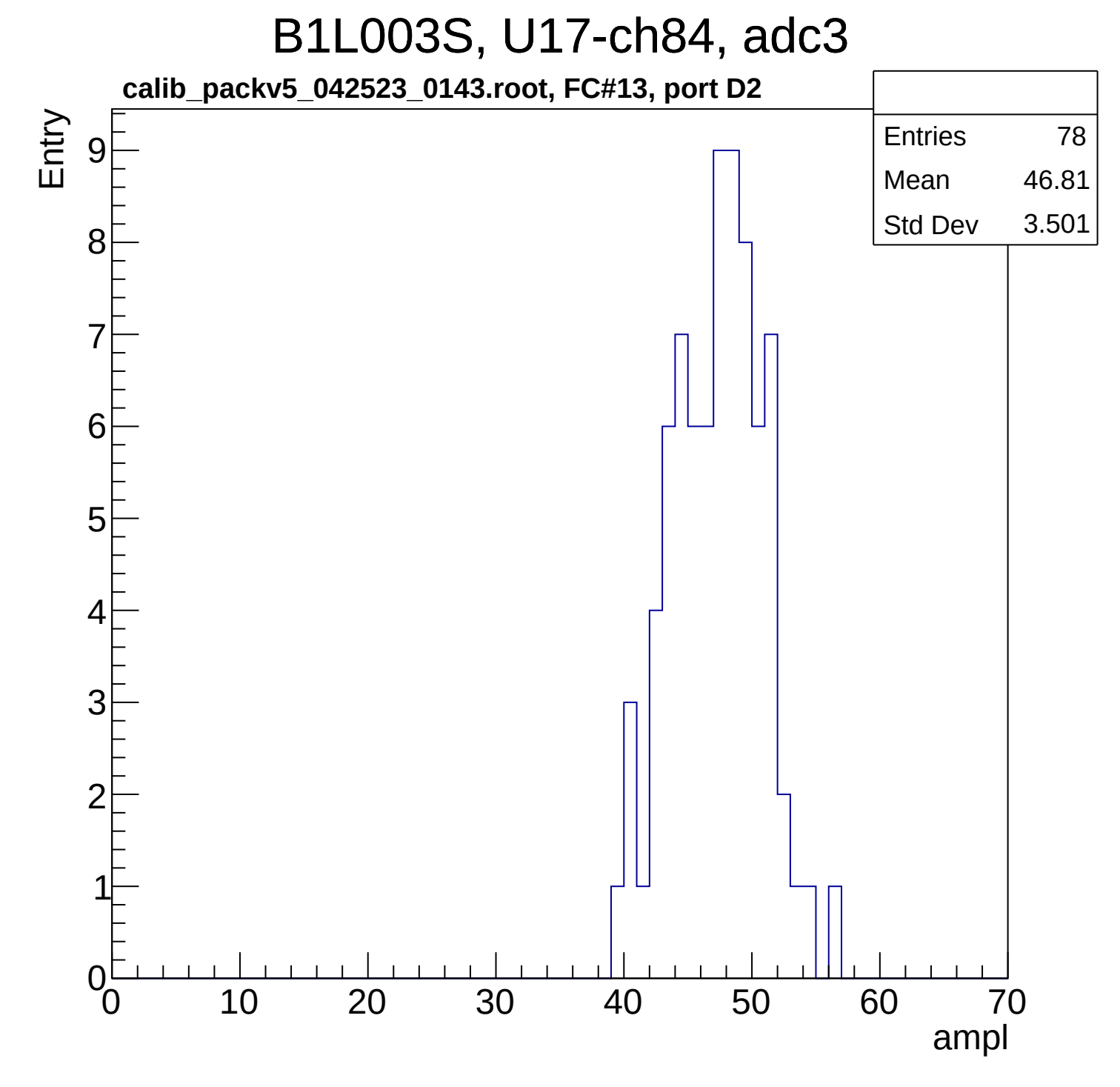
20
30
40
50
60
70


B1L003S, U17-ch84, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.33
Std Dev 3.246
Gaus mean : 36.4600
Gaus Width: 3.8137
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch84, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 40.85
Std Dev 3.268
Gaus mean : 42.2939
Gaus Width: 4.4283
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch84, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 46.81
Std Dev 3.501
Entry
ampl
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6
7
8
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0
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60
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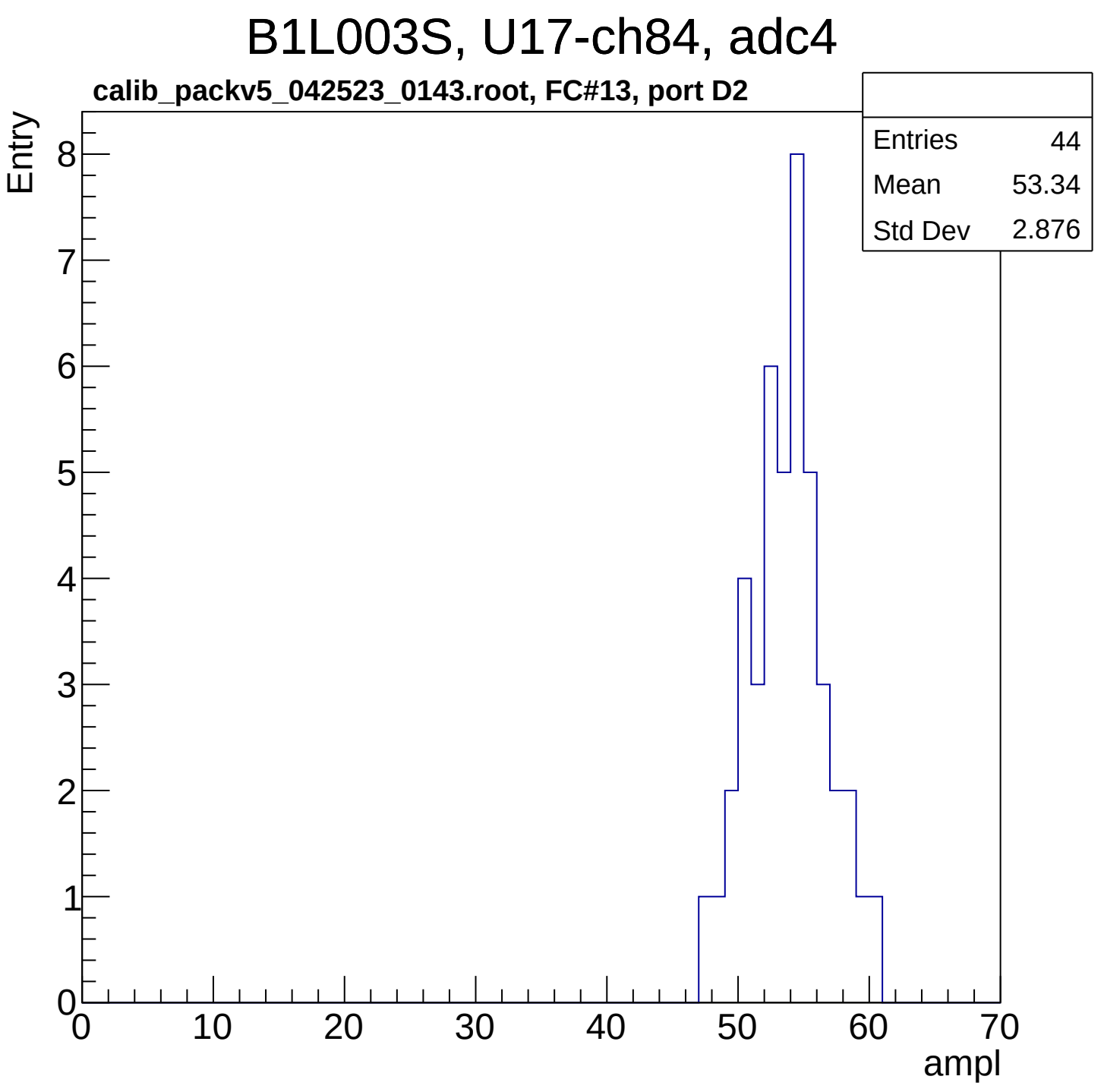
B1L003S, U17-ch84, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 53.34
Std Dev 2.876
Entry
ampl
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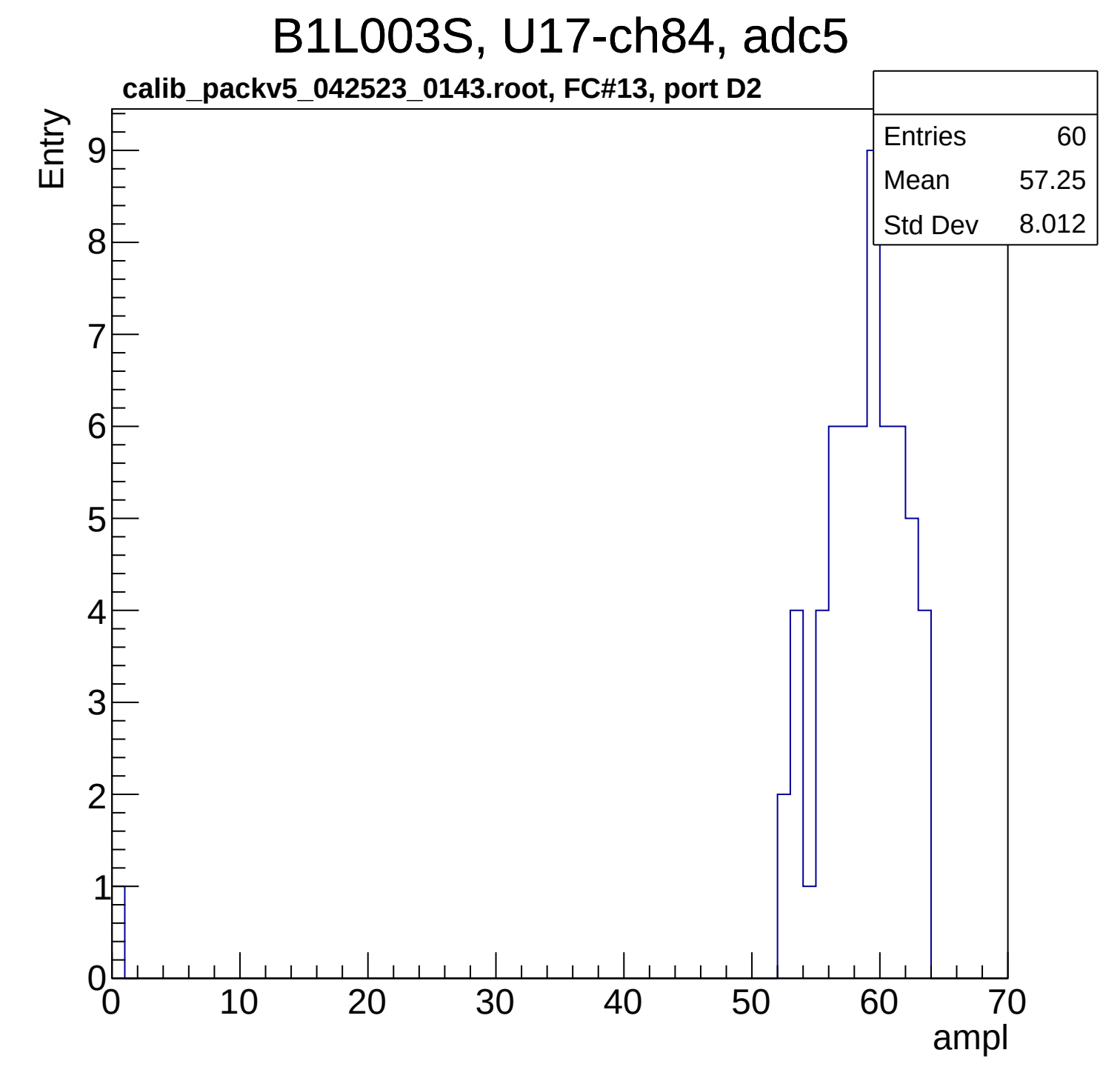
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch84, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 57.25
Std Dev 8.012
Entry
ampl
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2
3
4
5
6
7
8
9
0
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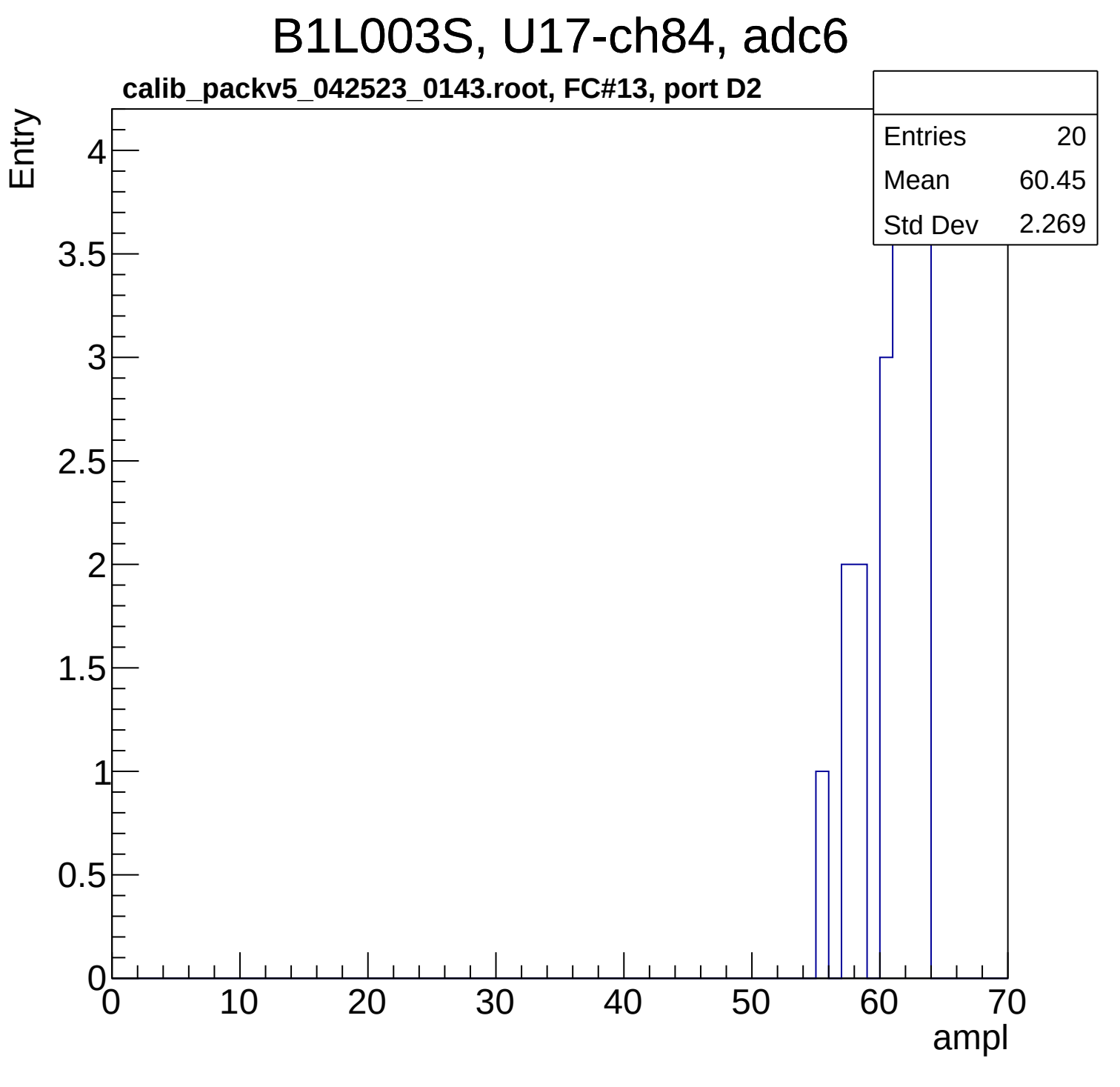
B1L003S, U17-ch84, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 60.45
Std Dev 2.269
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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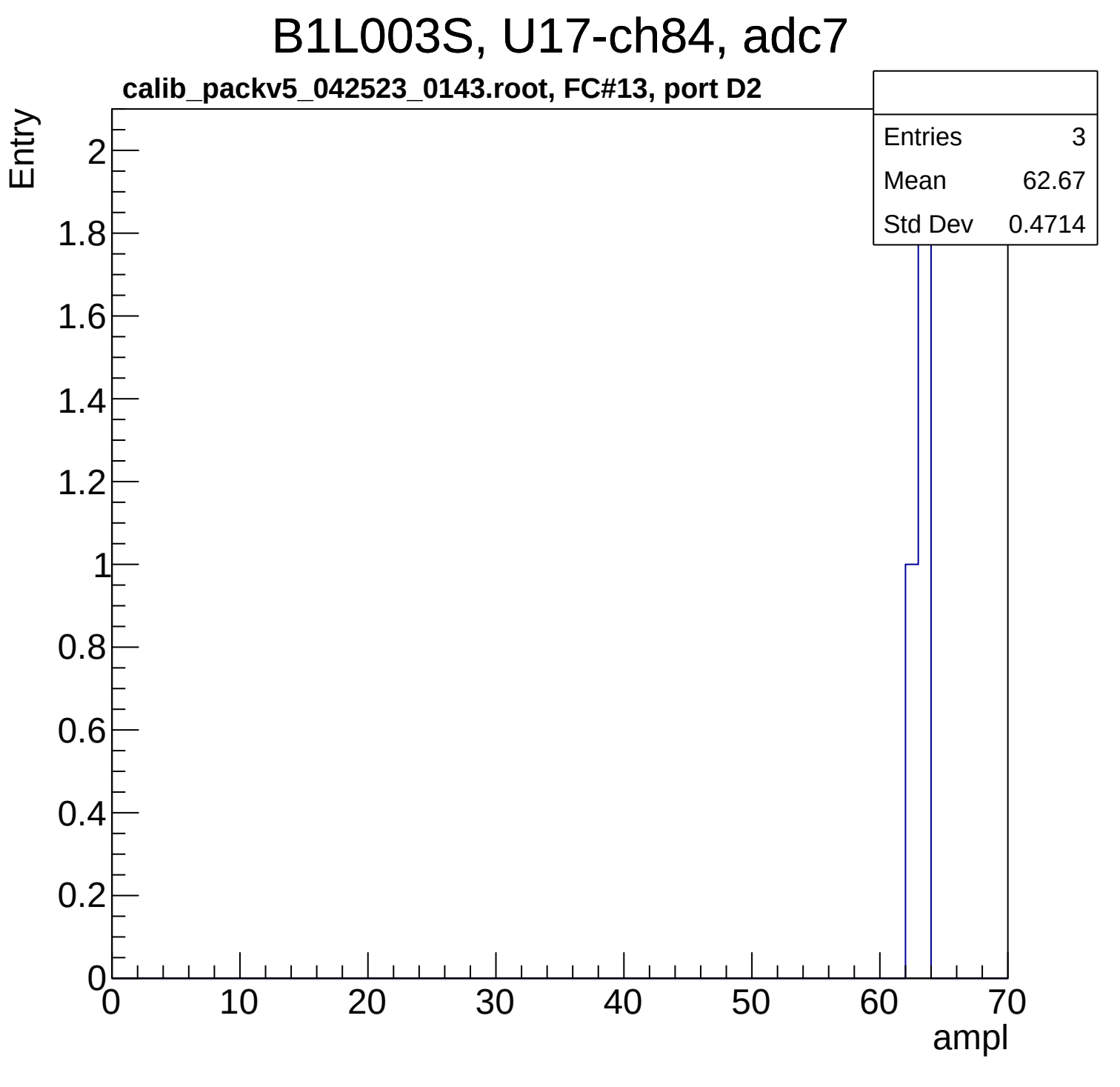
60
70


B1L003S, U17-ch84, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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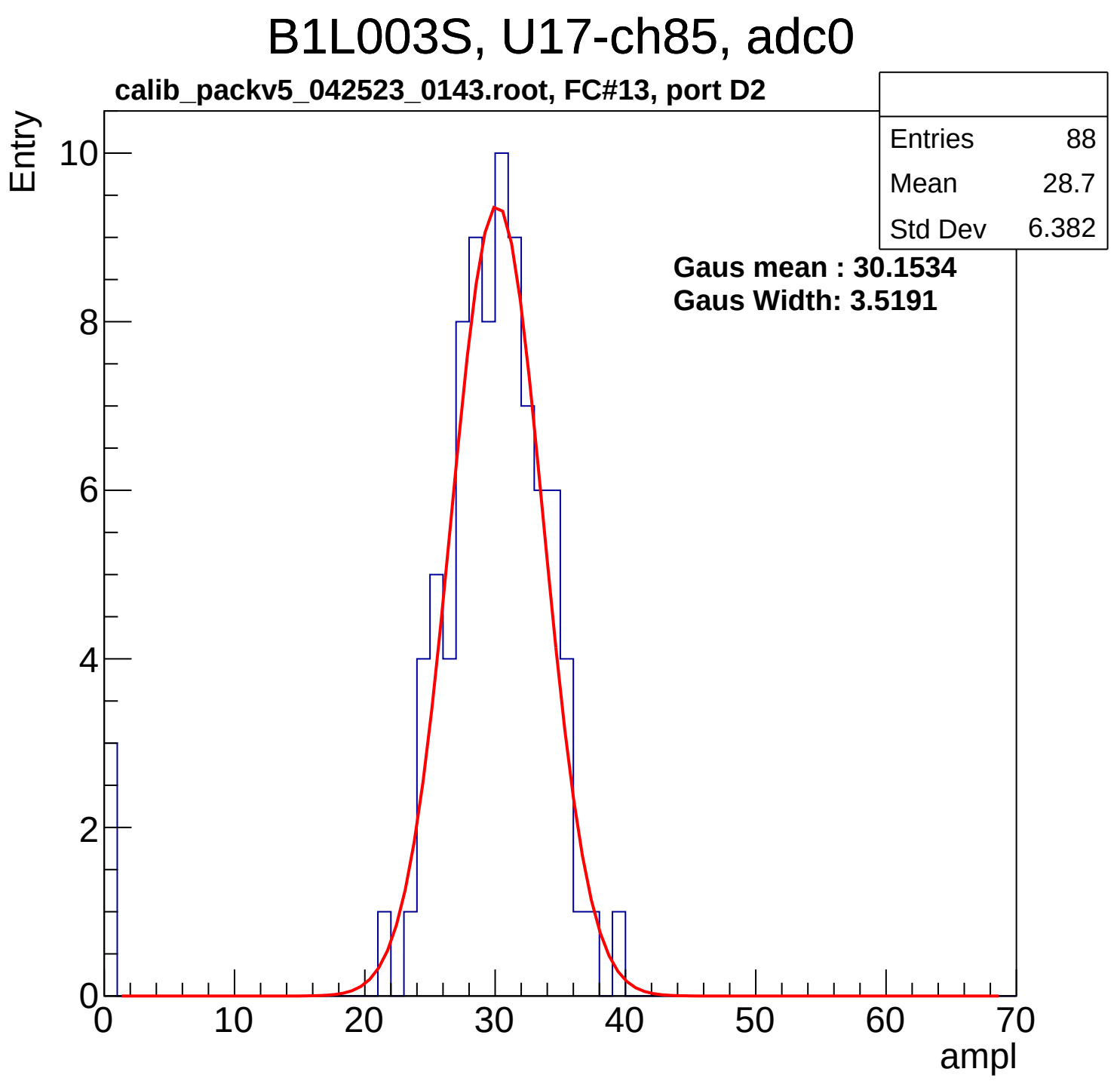
B1L003S, U17-ch85, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 88
Mean 28.7
Std Dev 6.382
Gaus mean : 30.1534
Gaus Width: 3.5191
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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60
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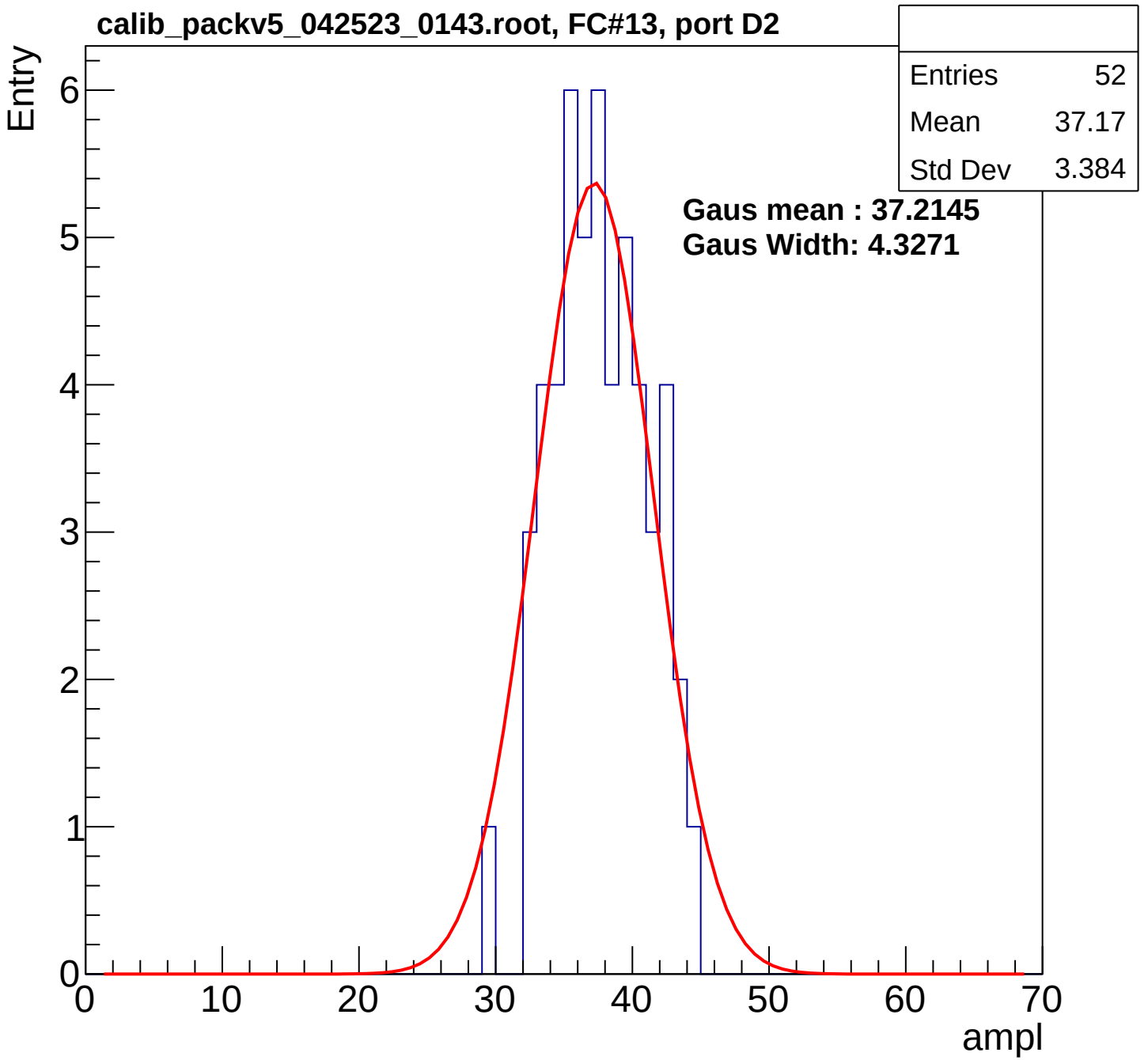
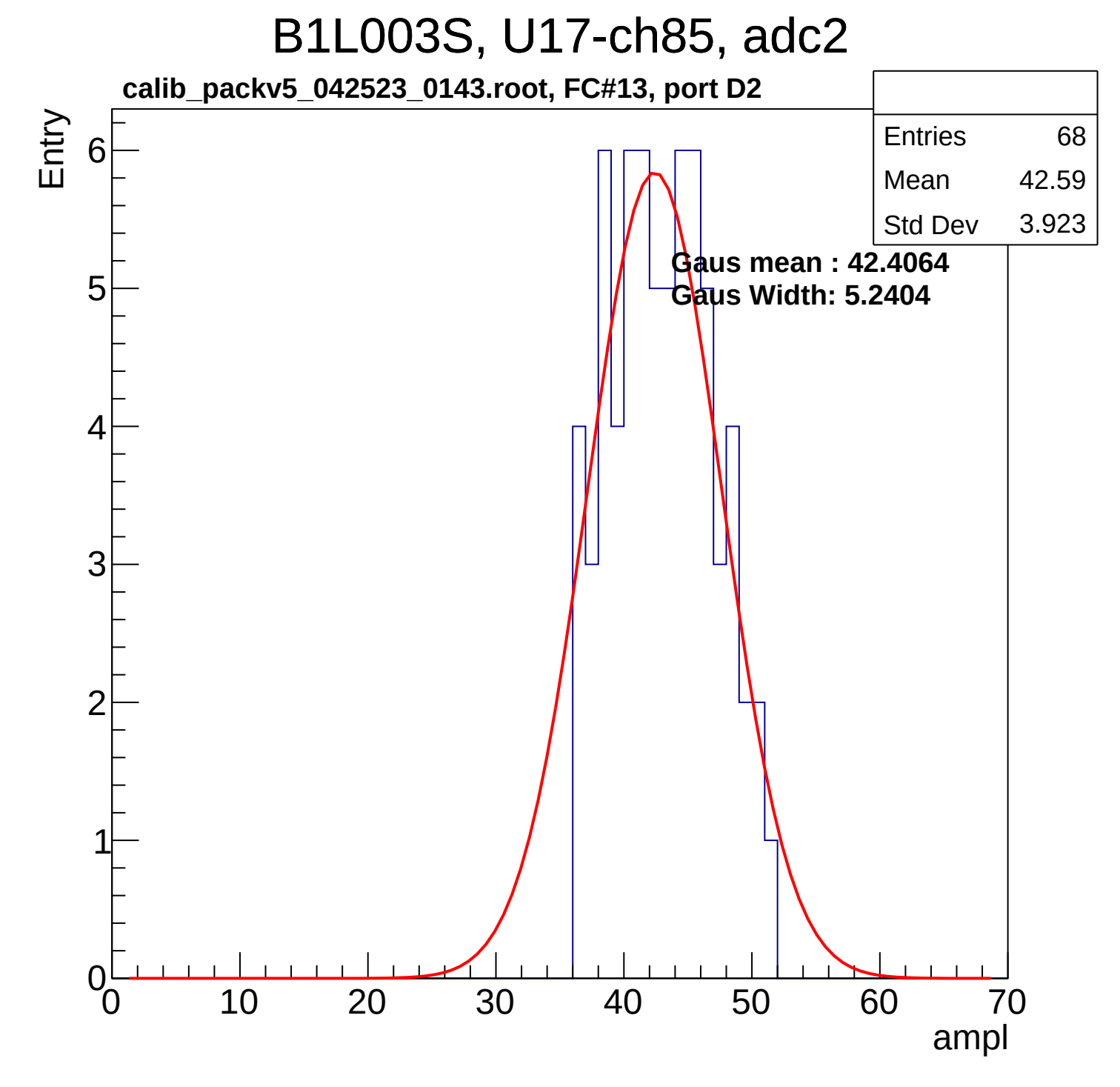
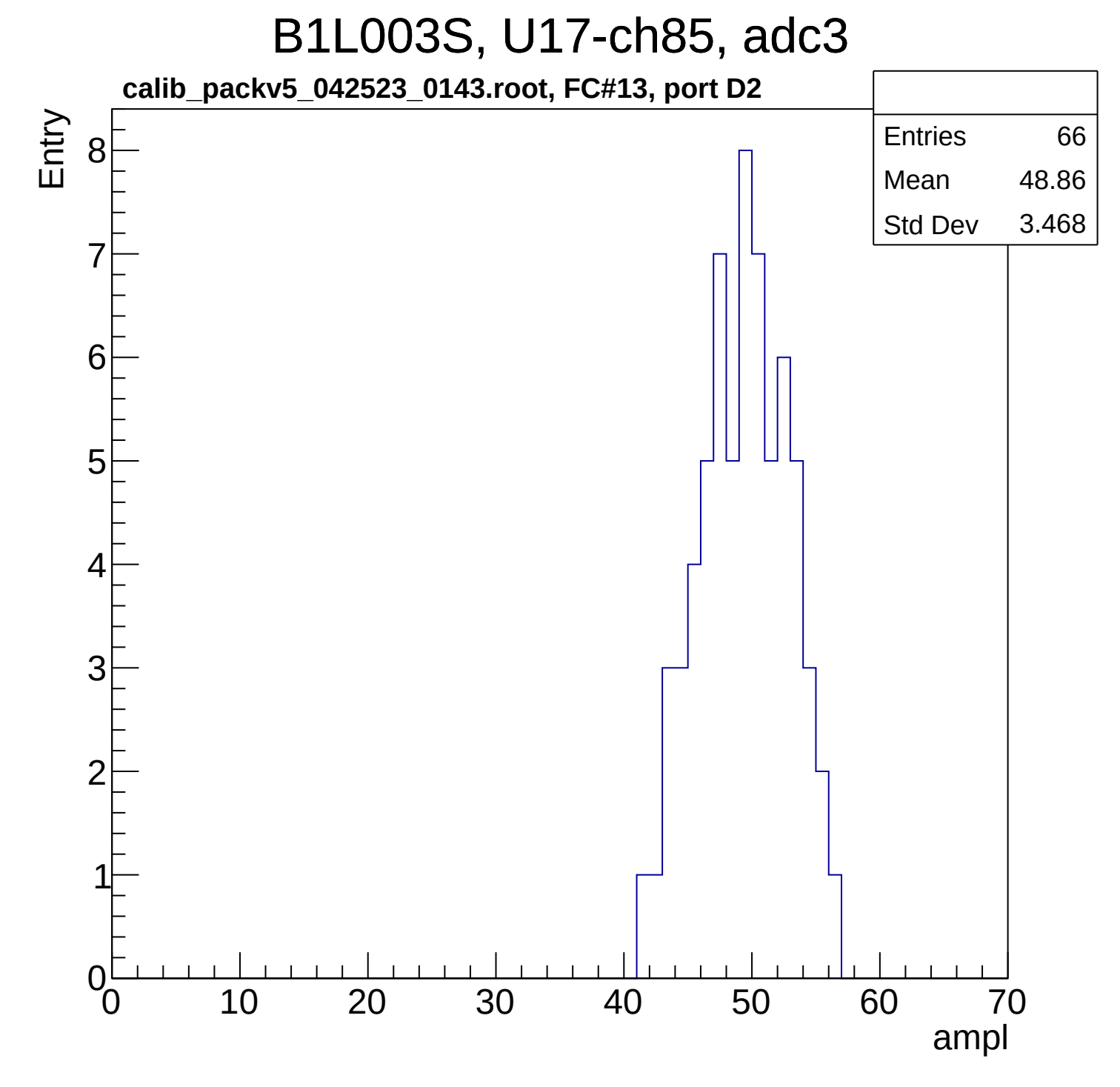

B1L003S, U17-ch85, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 37.17
Std Dev 3.384
Gaus mean : 37.2145
Gaus Width: 4.3271
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch85, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.59
Std Dev 3.923
Gaus mean : 42.4064
Gaus Width: 5.2404
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch85, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.86
Std Dev 3.468
Entry
ampl
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1
2
3
4
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0
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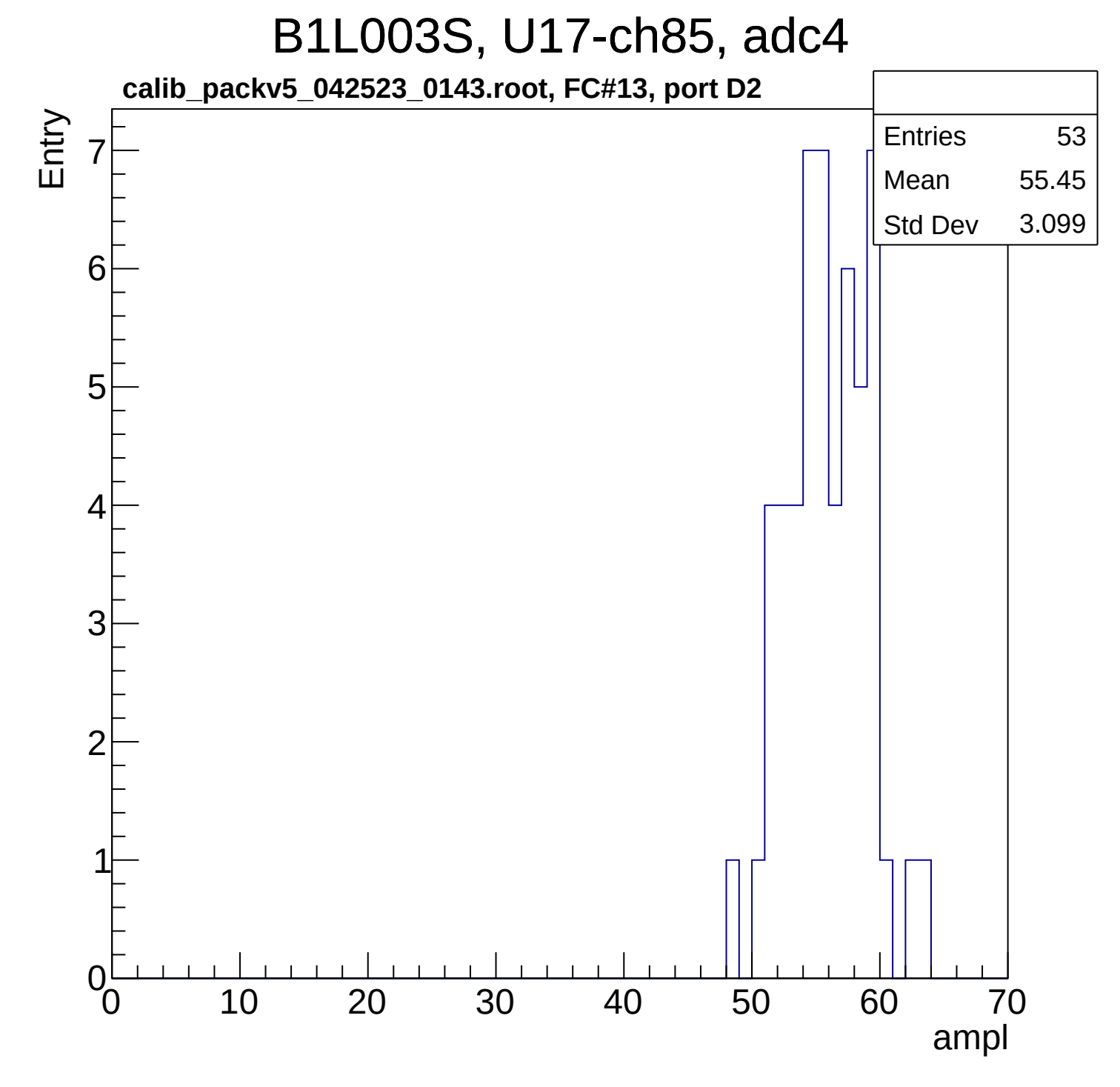
B1L003S, U17-ch85, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.45
Std Dev 3.099
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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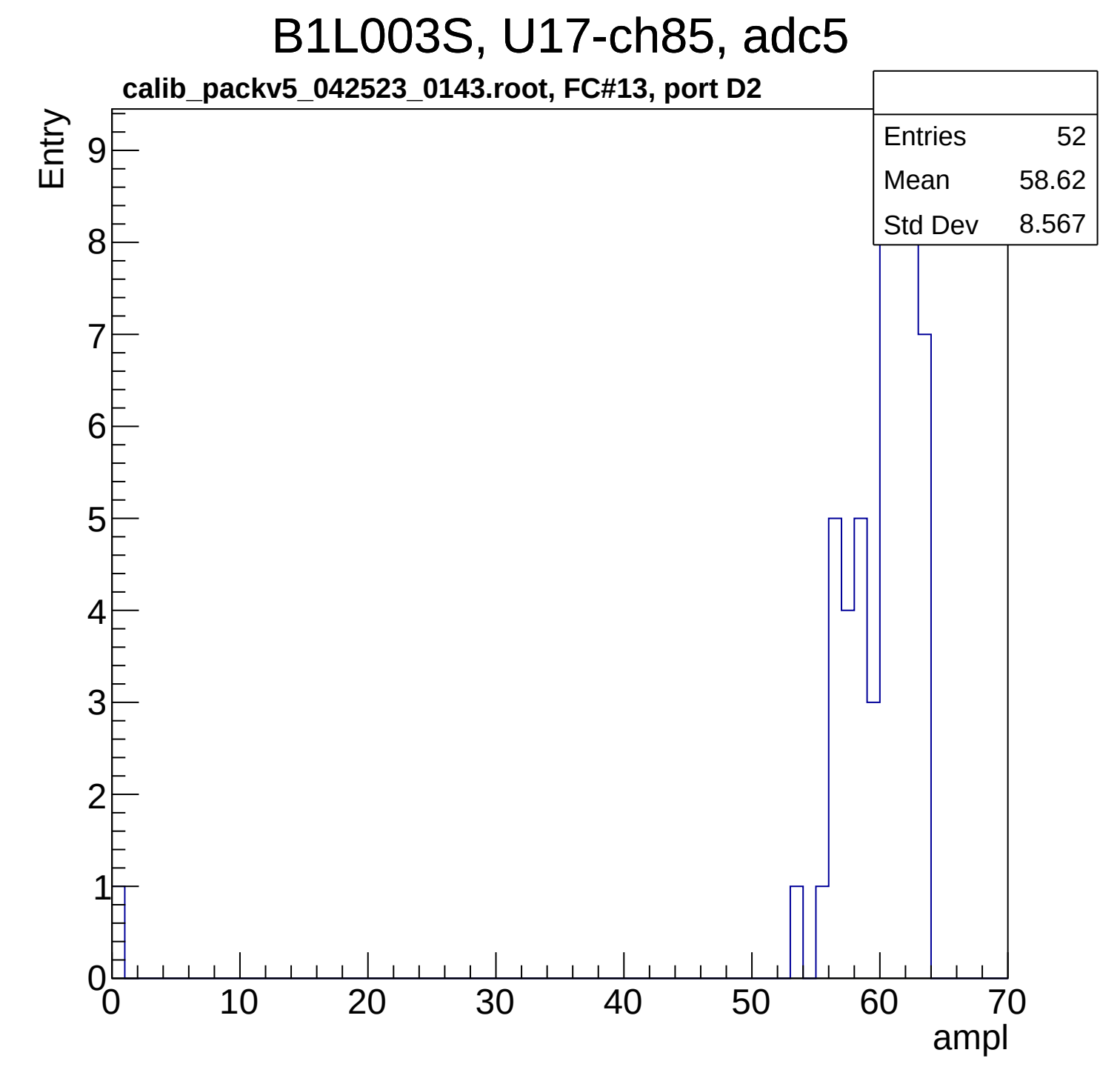
B1L003S, U17-ch85, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.62
Std Dev 8.567
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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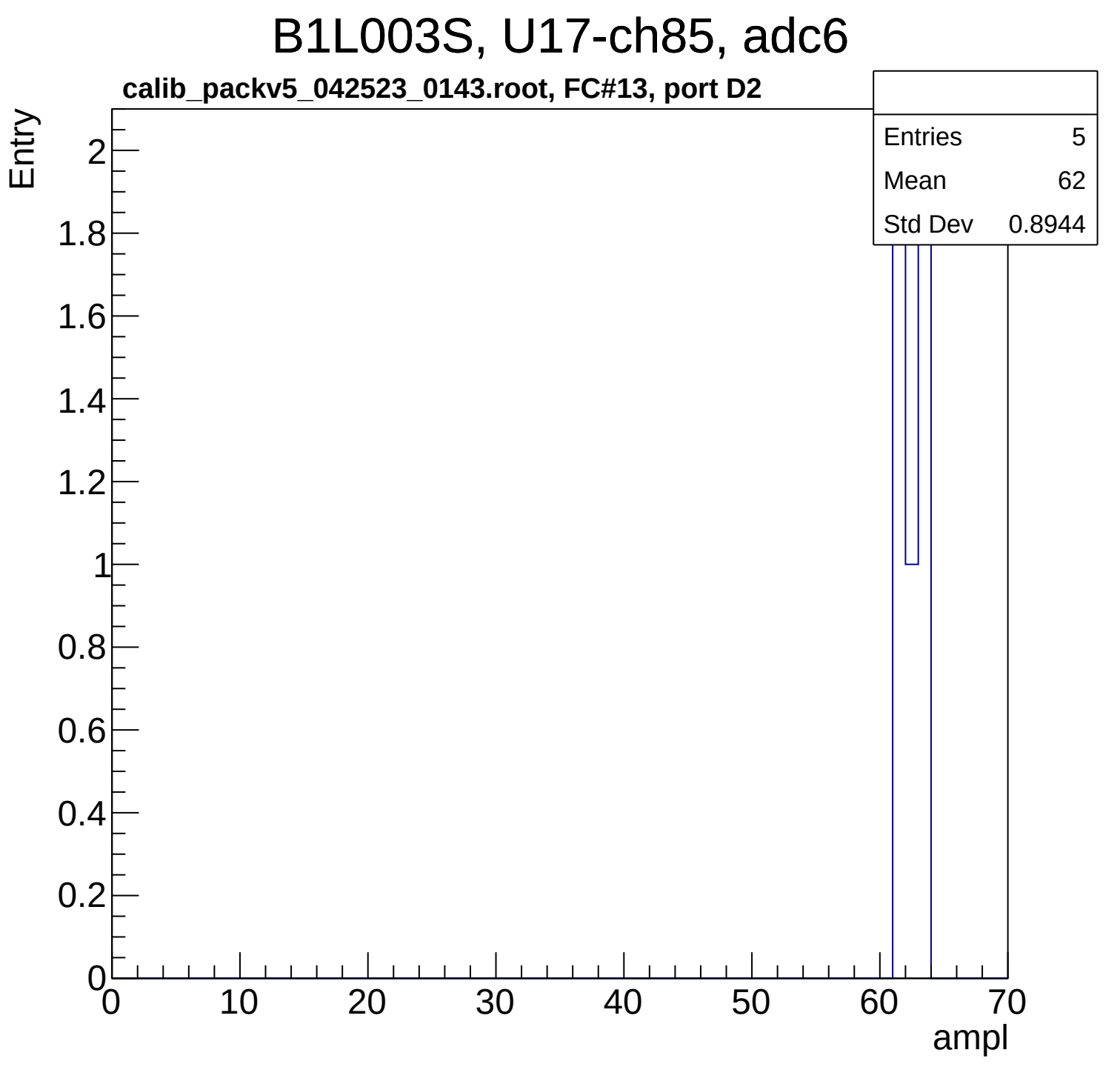
B1L003S, U17-ch85, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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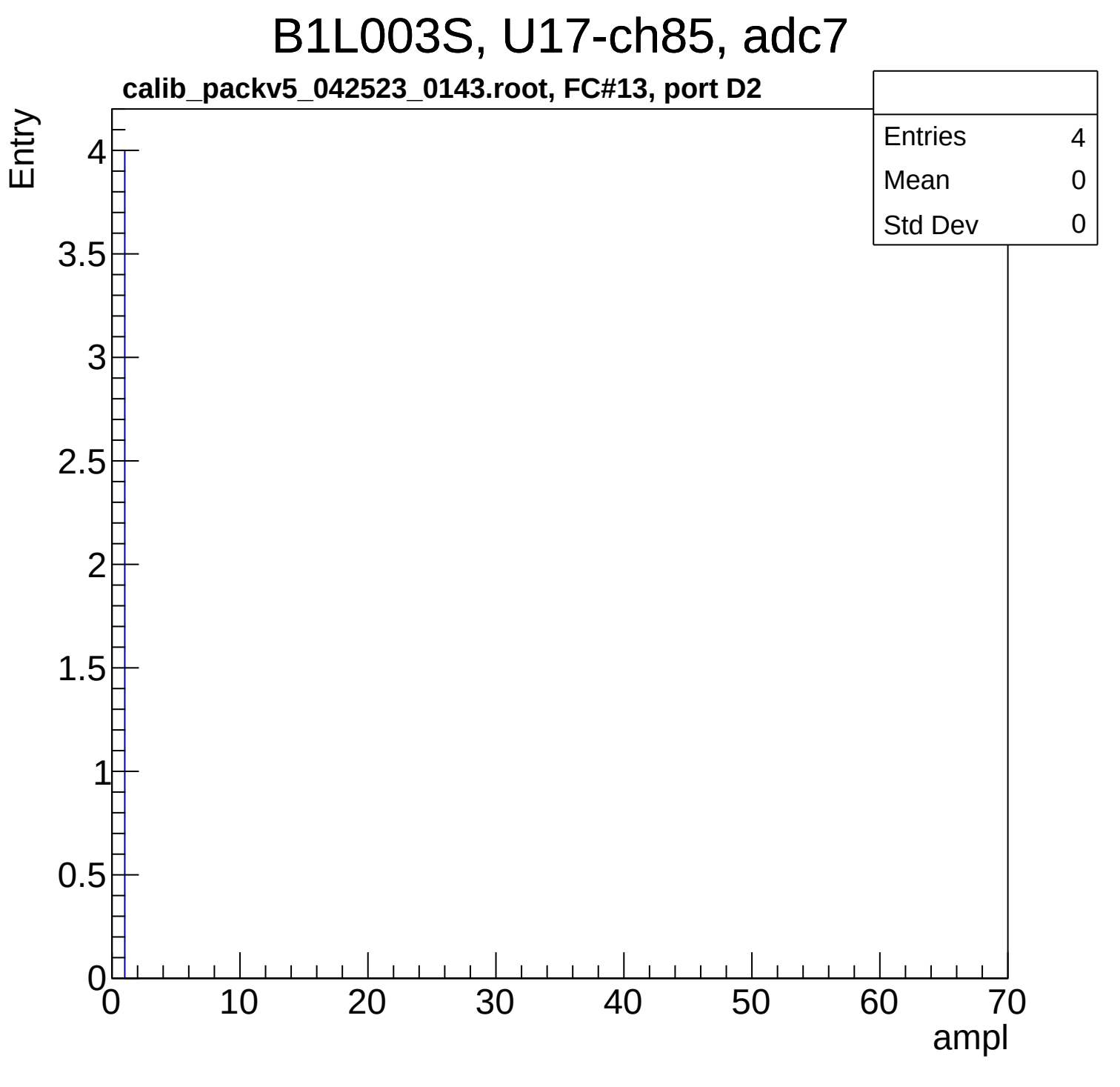
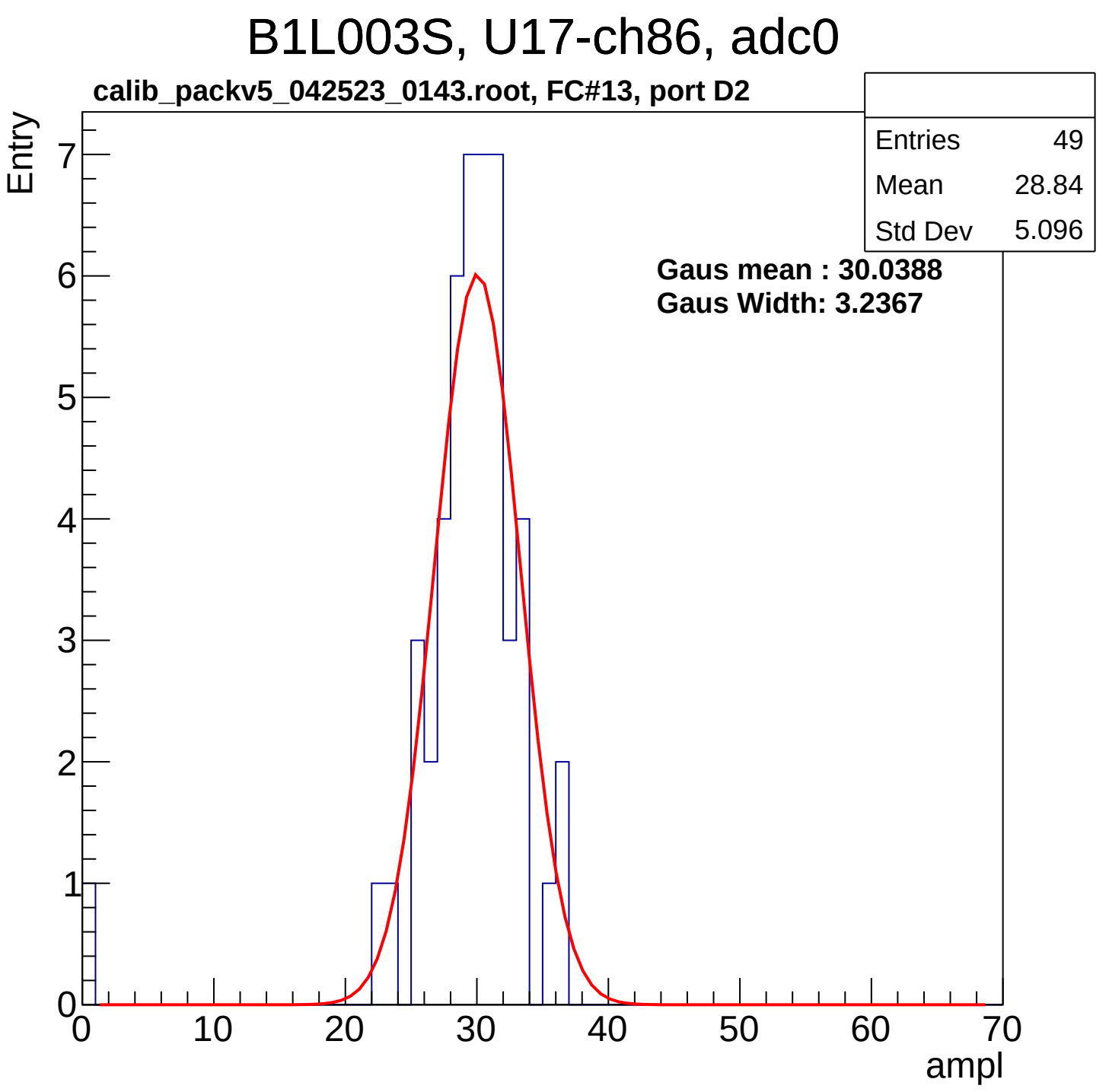

B1L003S, U17-ch85, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch86, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 28.84
Std Dev 5.096
Gaus mean : 30.0388
Gaus Width: 3.2367
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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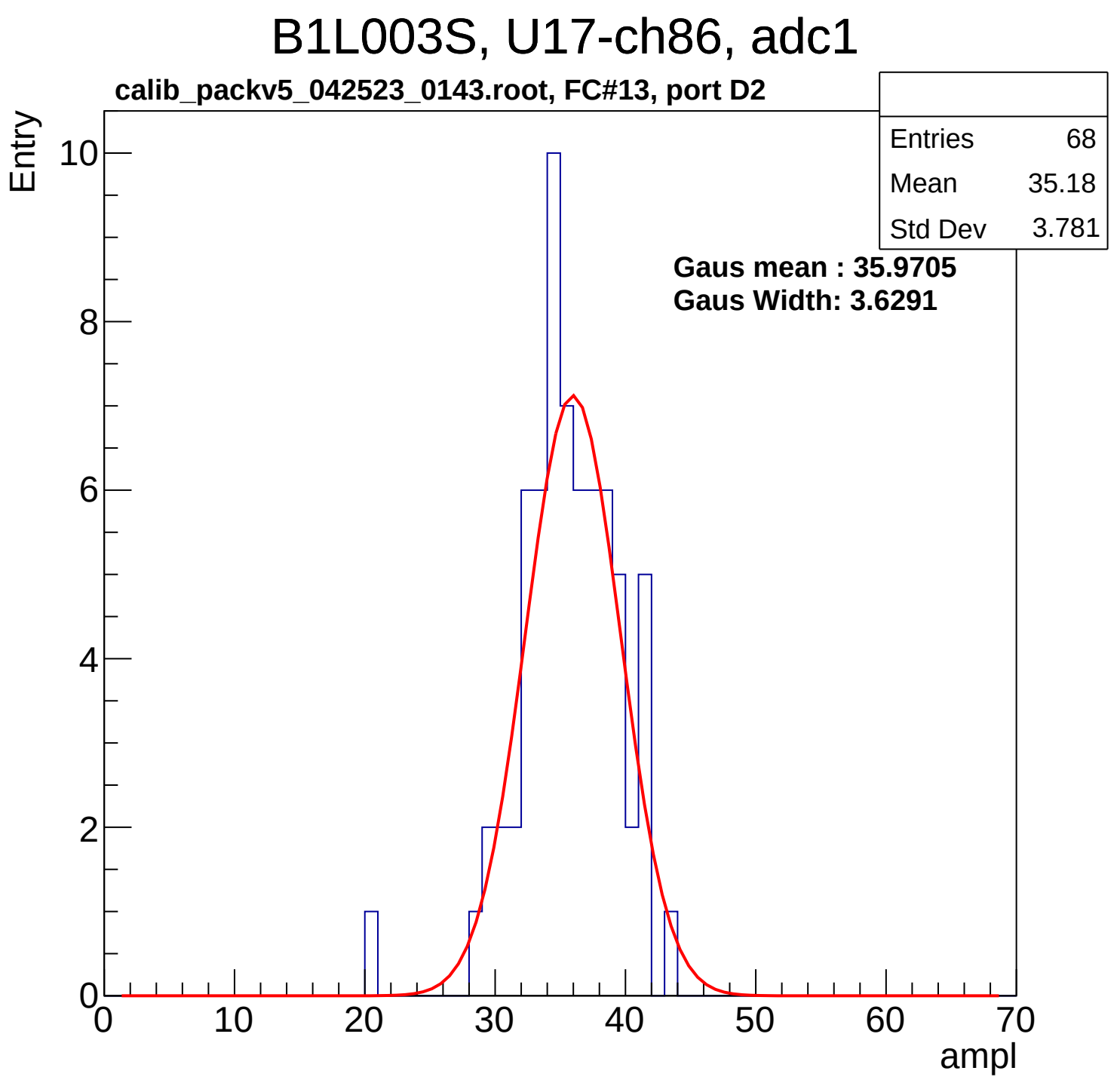
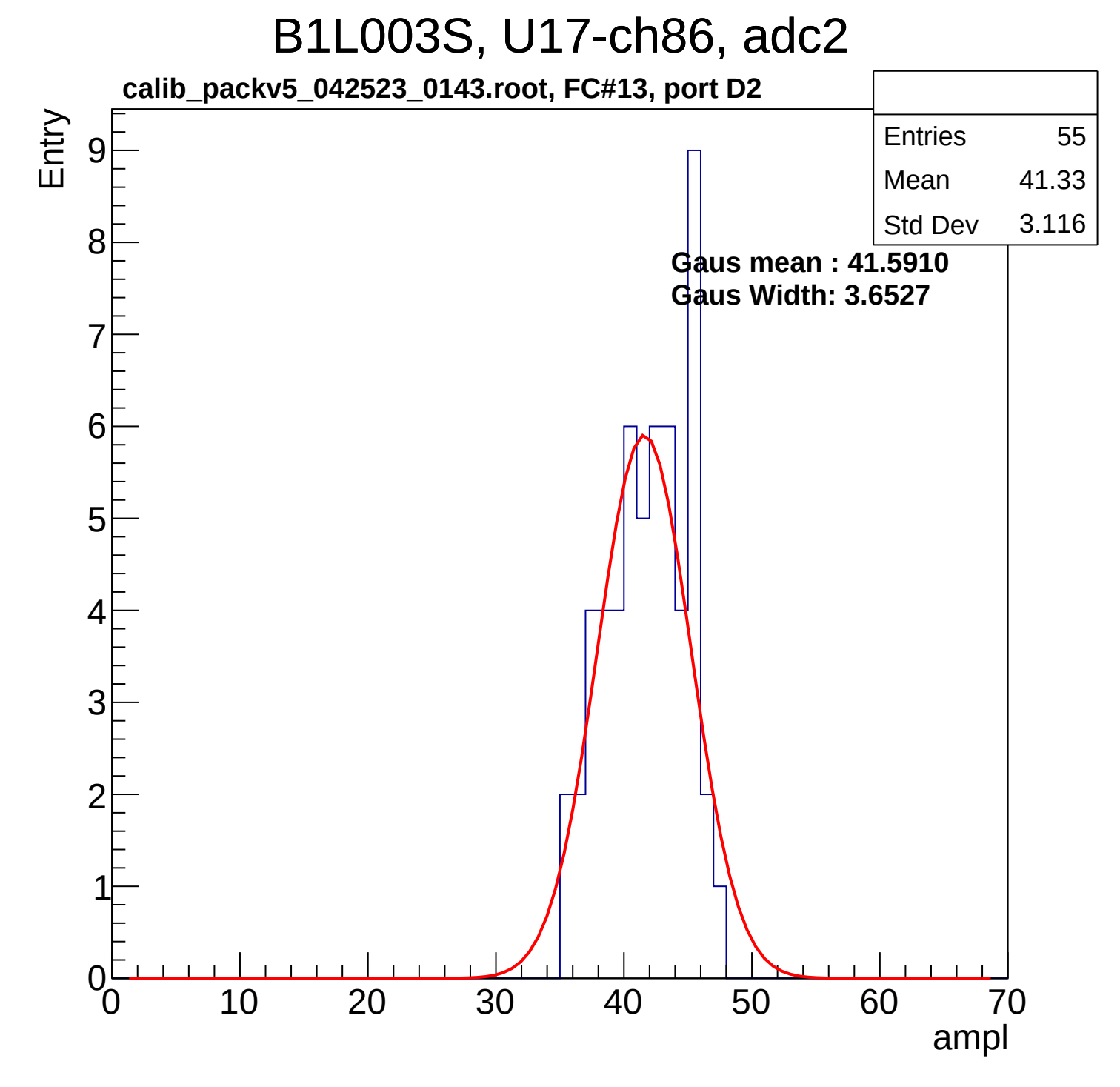

B1L003S, U17-ch86, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.18
Std Dev 3.781
Gaus mean : 35.9705
Gaus Width: 3.6291
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch86, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.33
Std Dev 3.116
Gaus mean : 41.5910
Gaus Width: 3.6527
Entry
ampl
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2
3
4
5
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7
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0
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60
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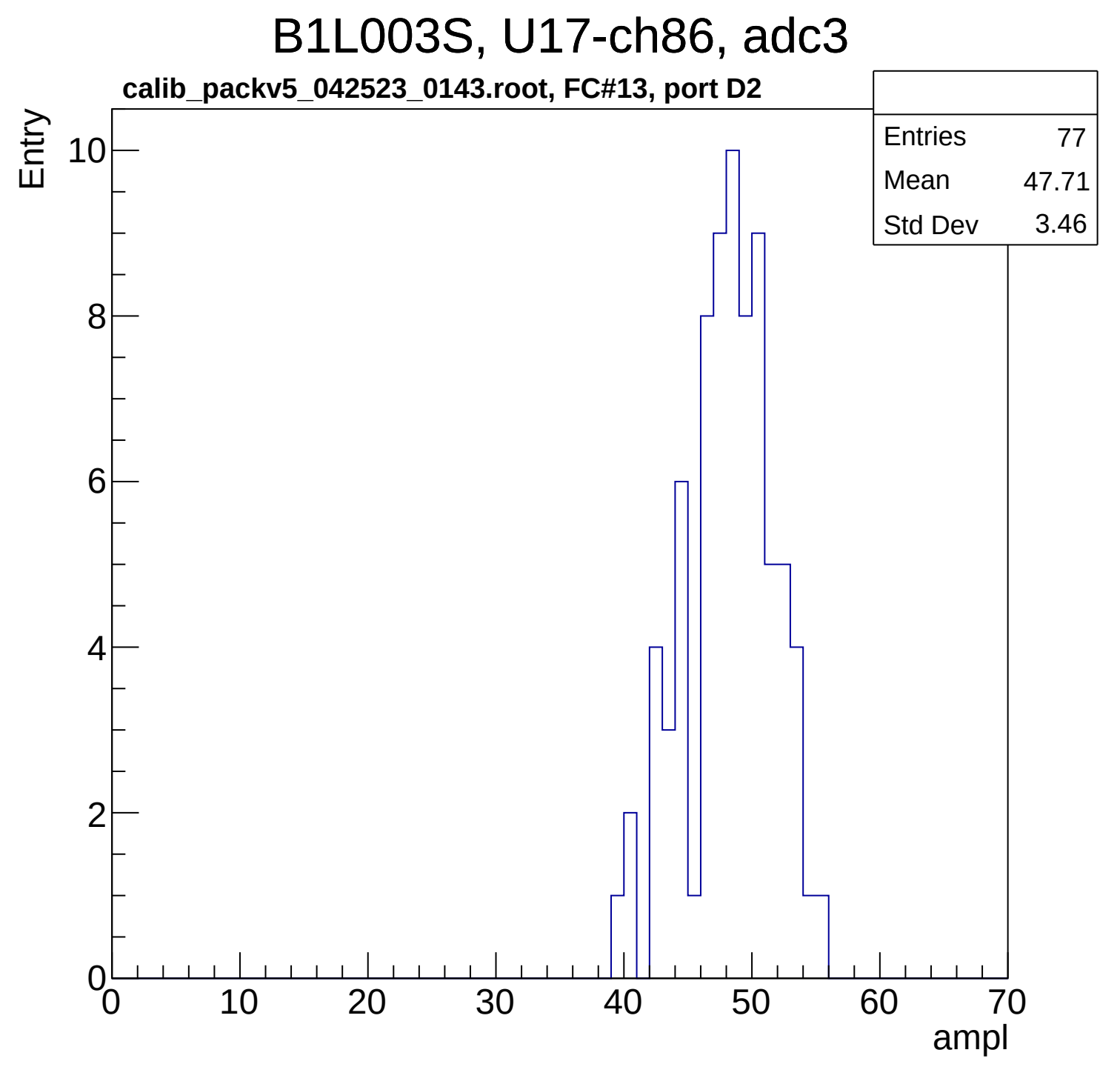
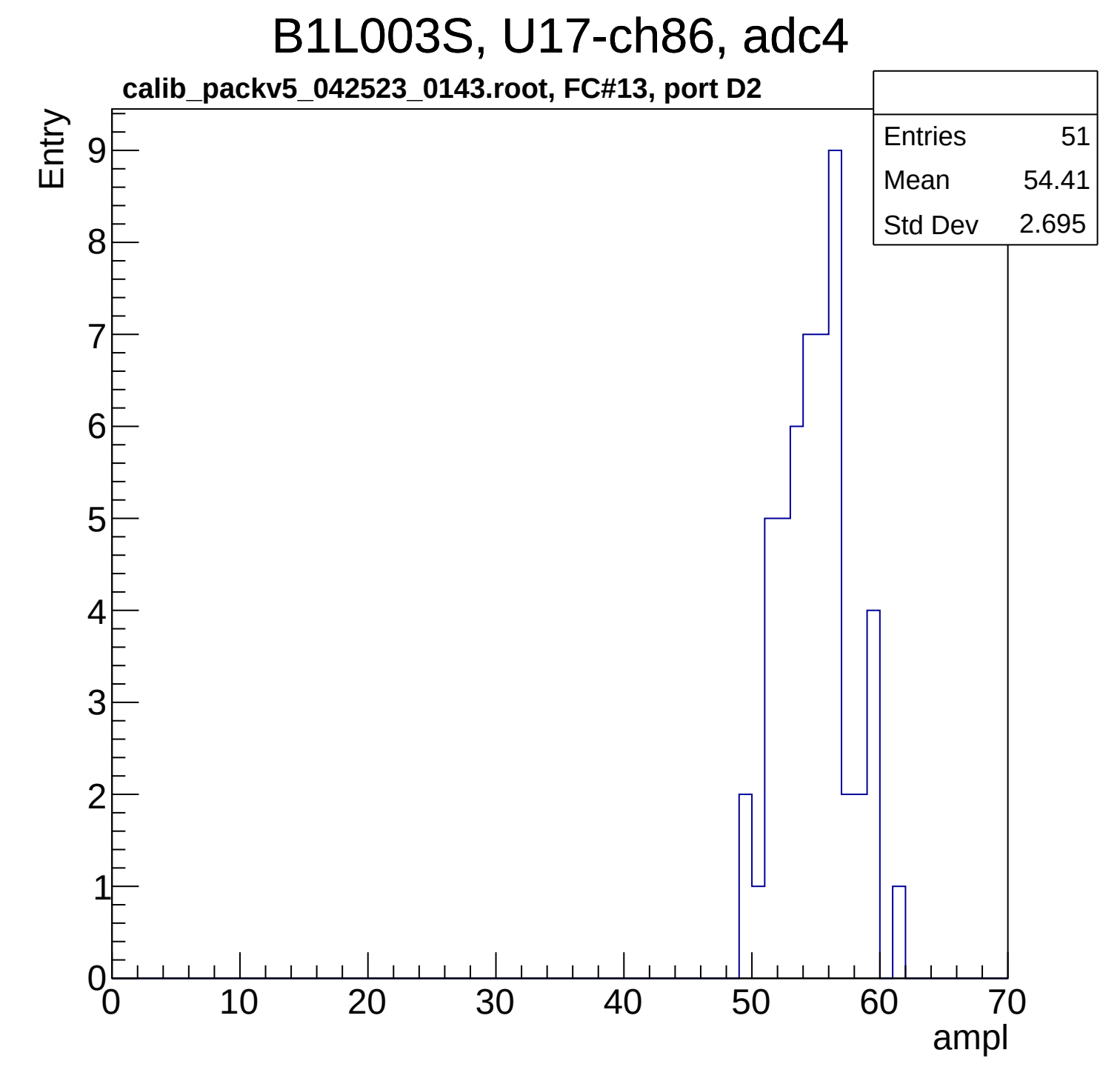
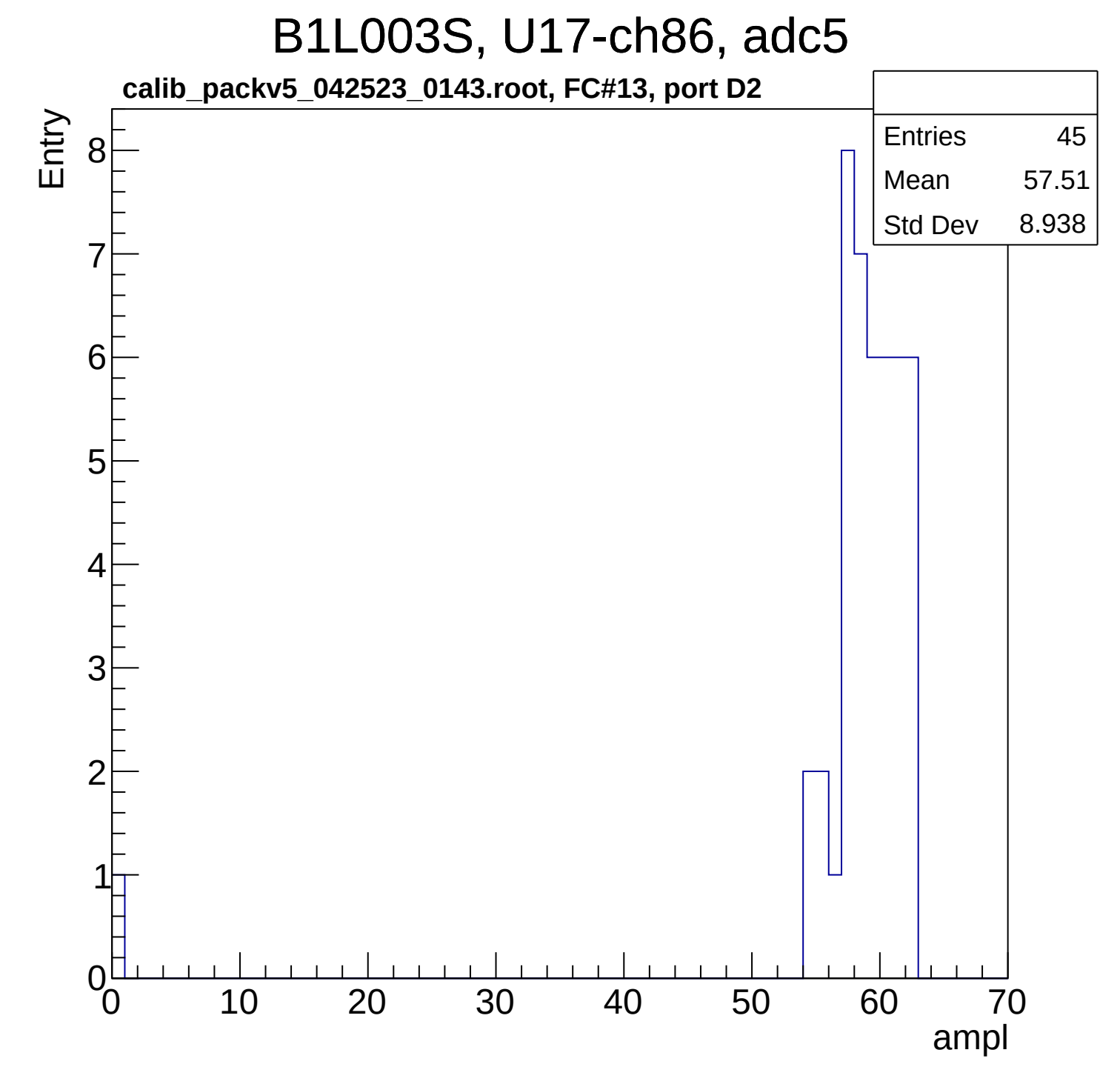

B1L003S, U17-ch86, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 47.71
Std Dev 3.46
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch86, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 54.41
Std Dev 2.695
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch86, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 57.51
Std Dev 8.938
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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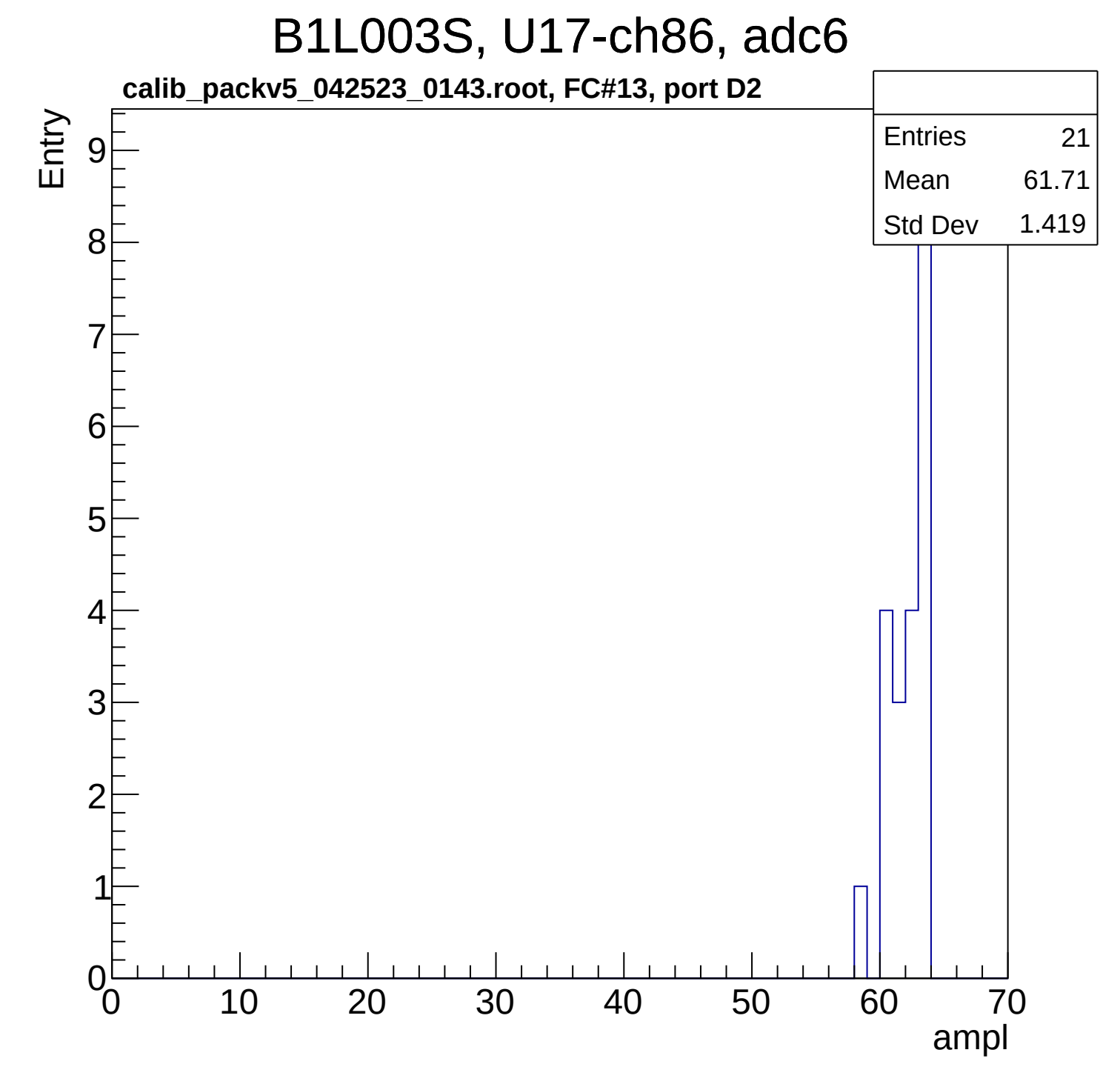
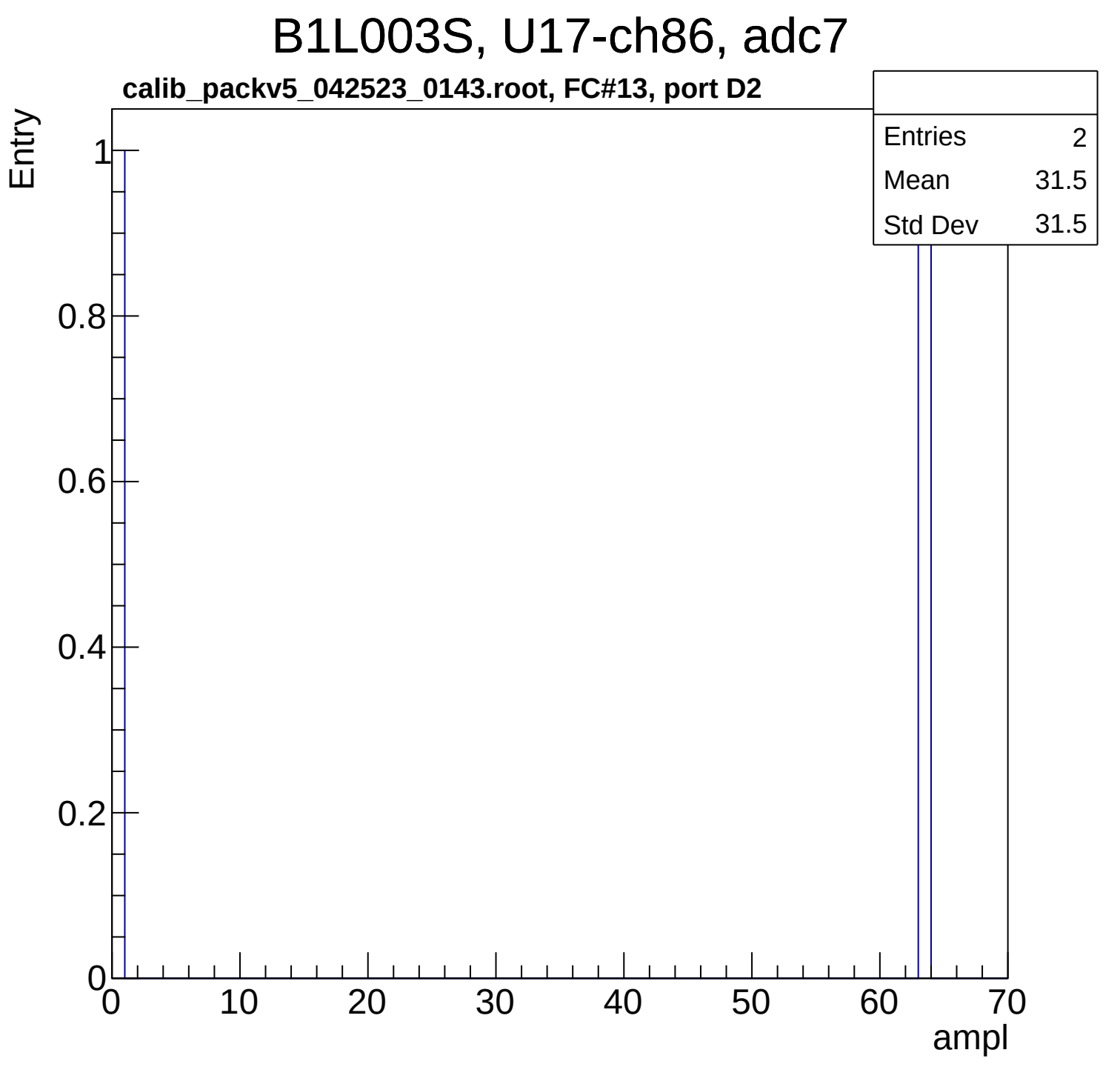
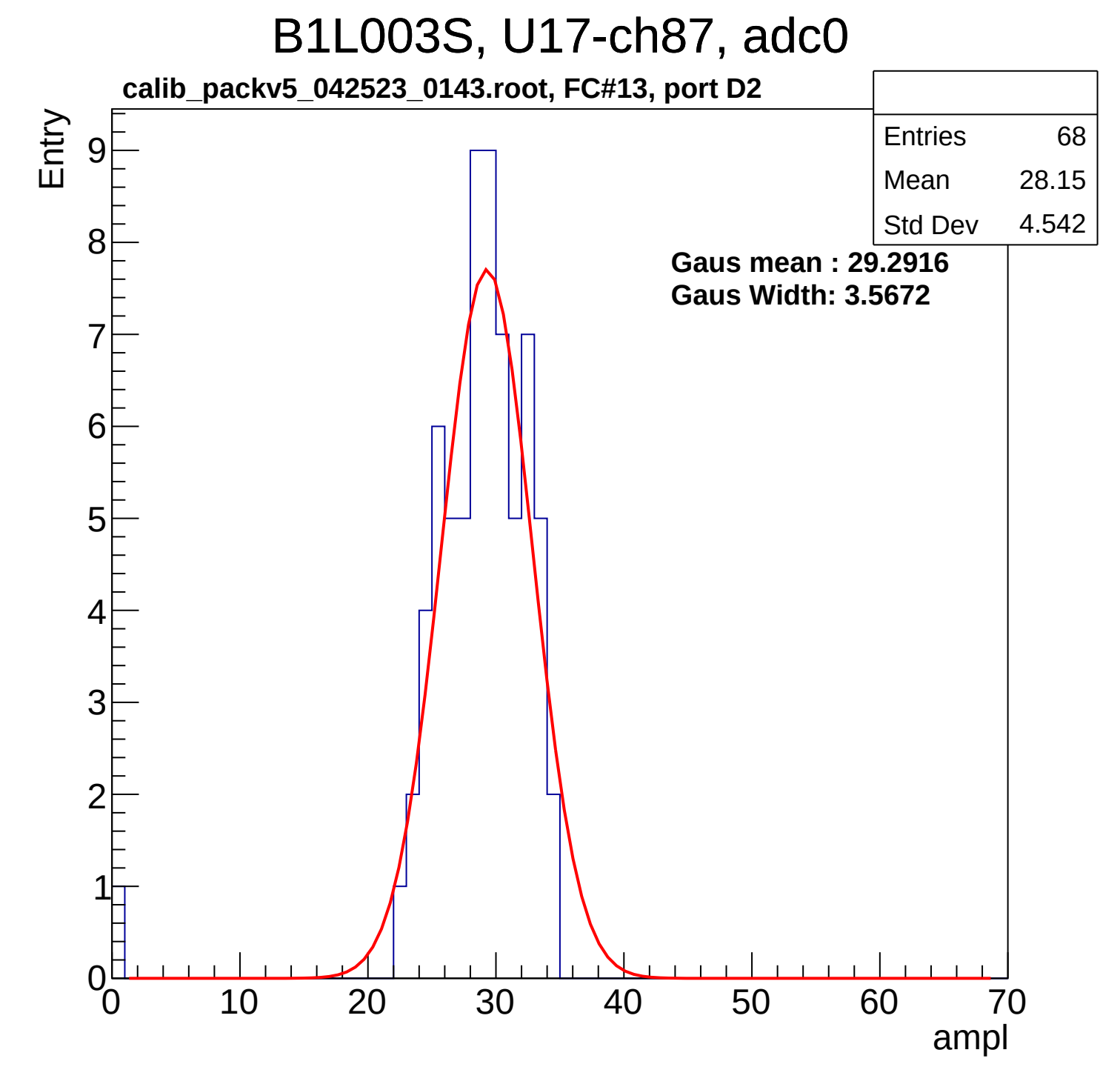
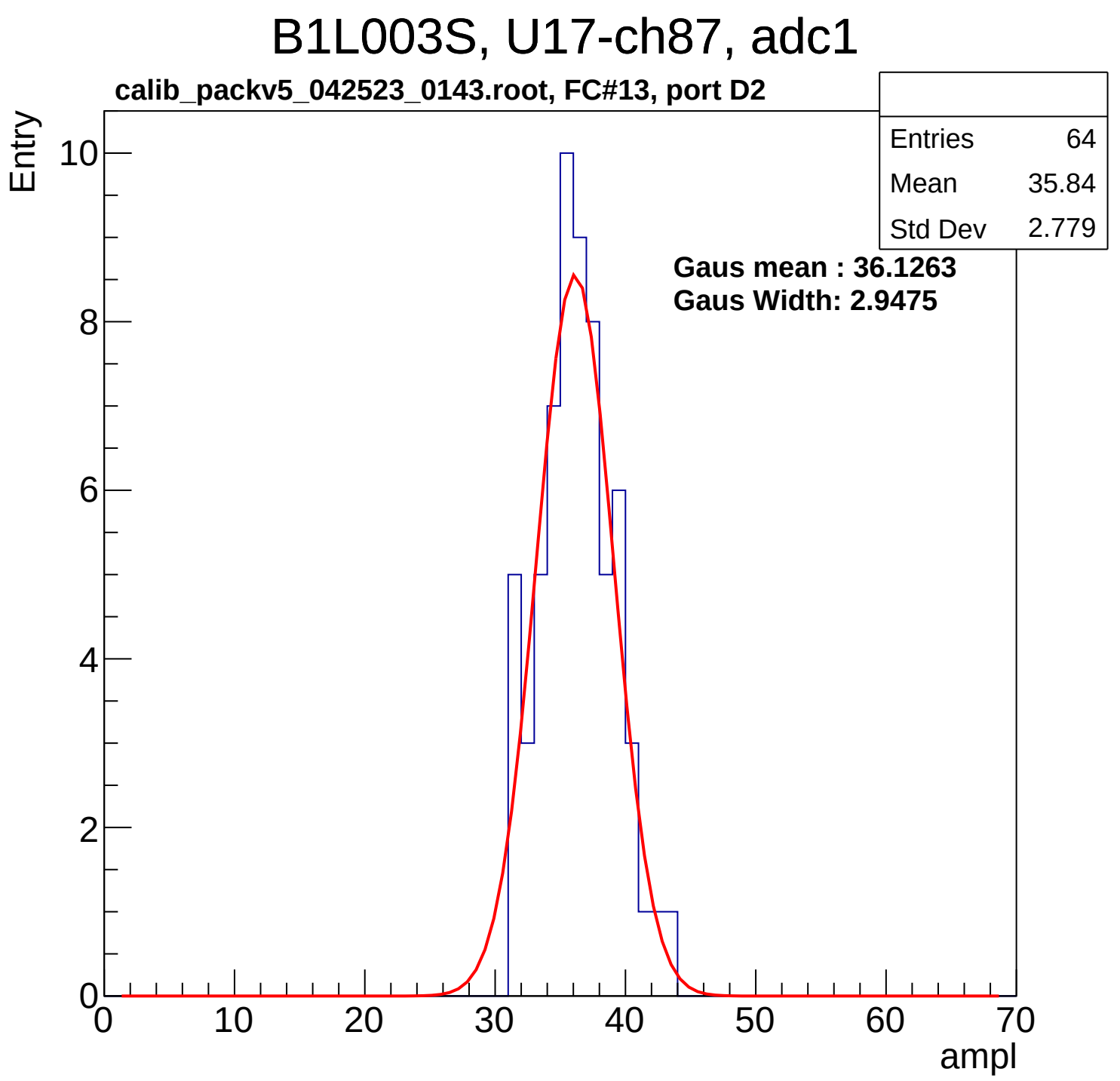
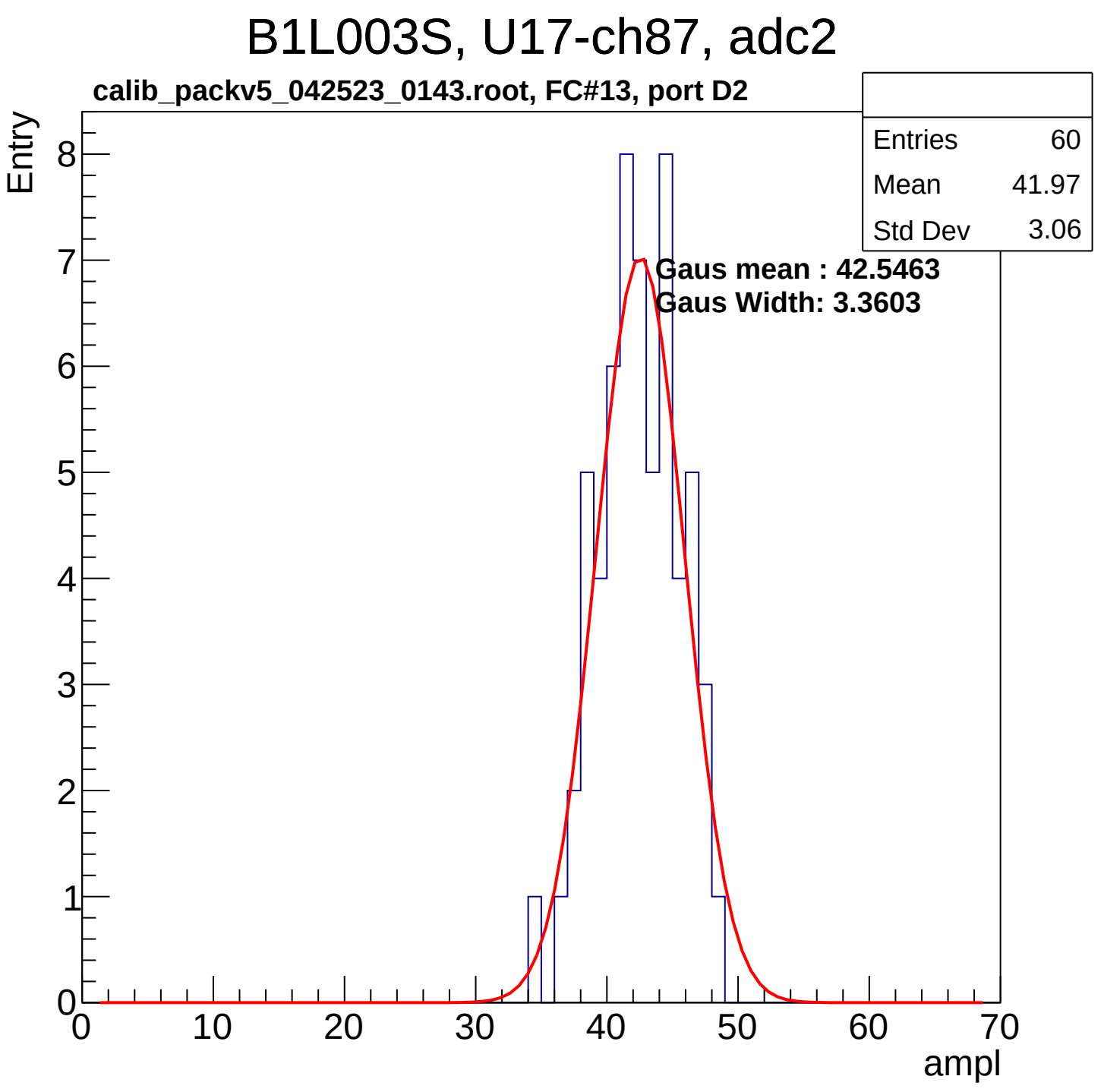
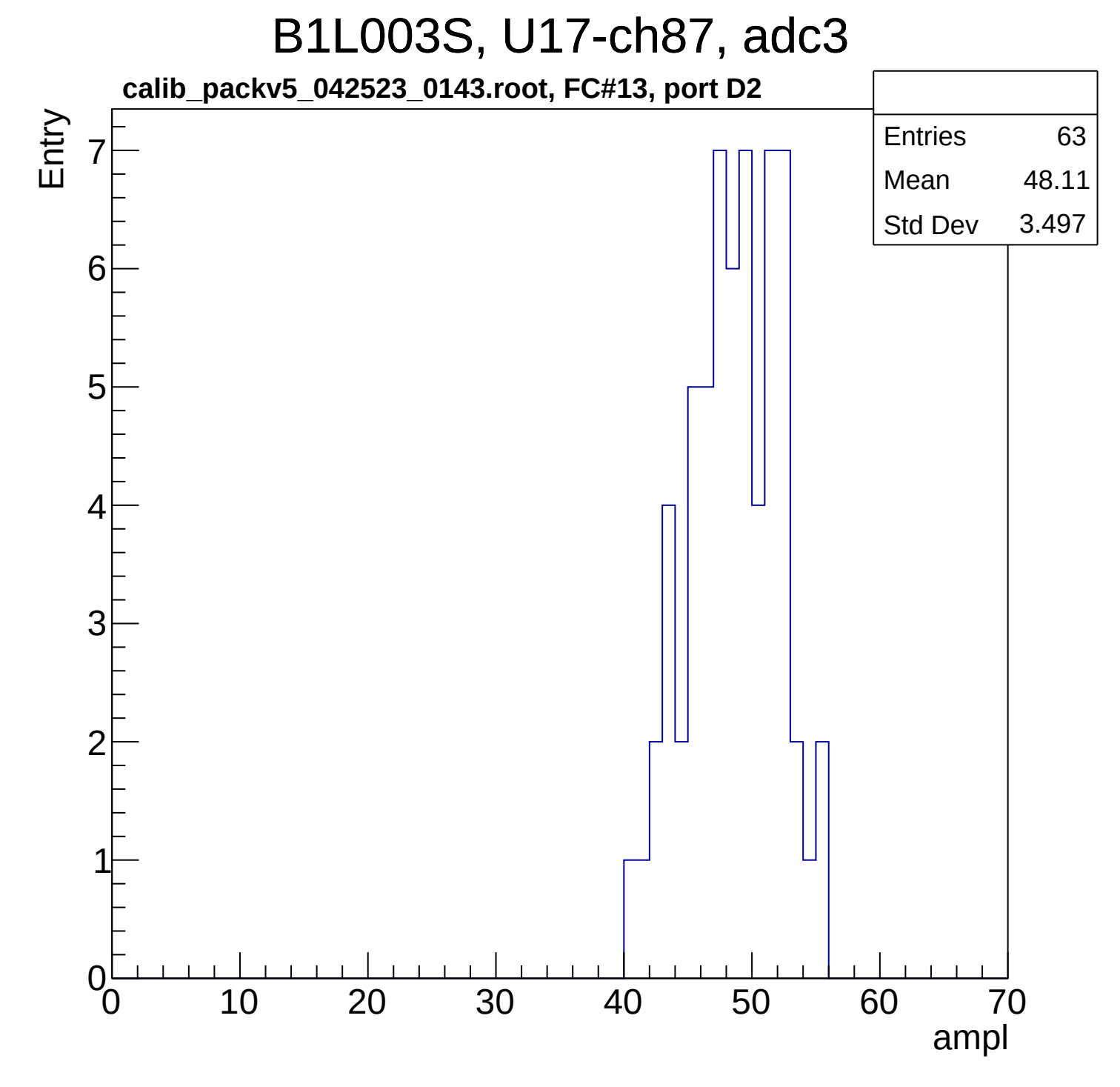

B1L003S, U17-ch86, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 61.71
Std Dev 1.419
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

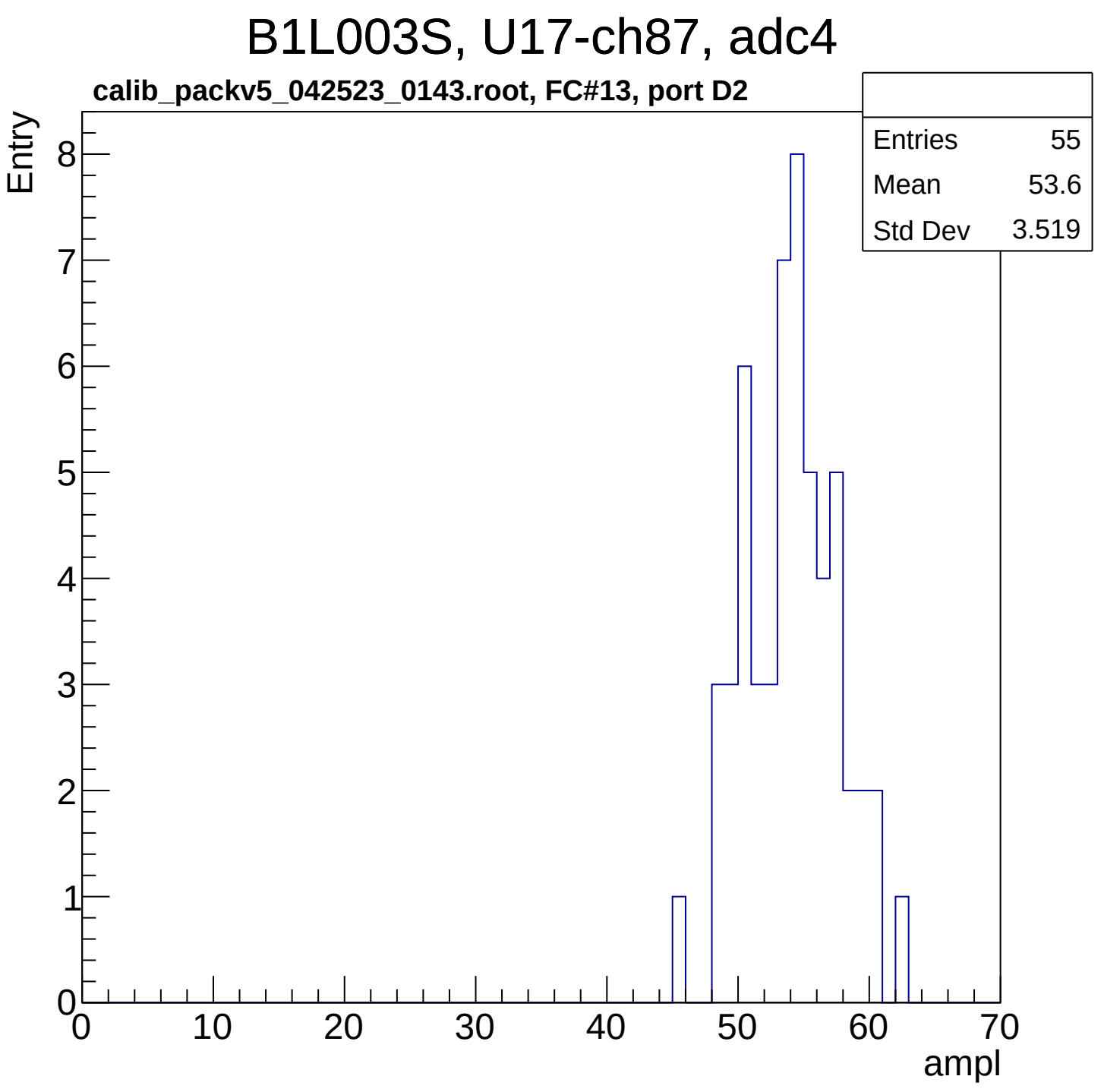

B1L003S, U17-ch86, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

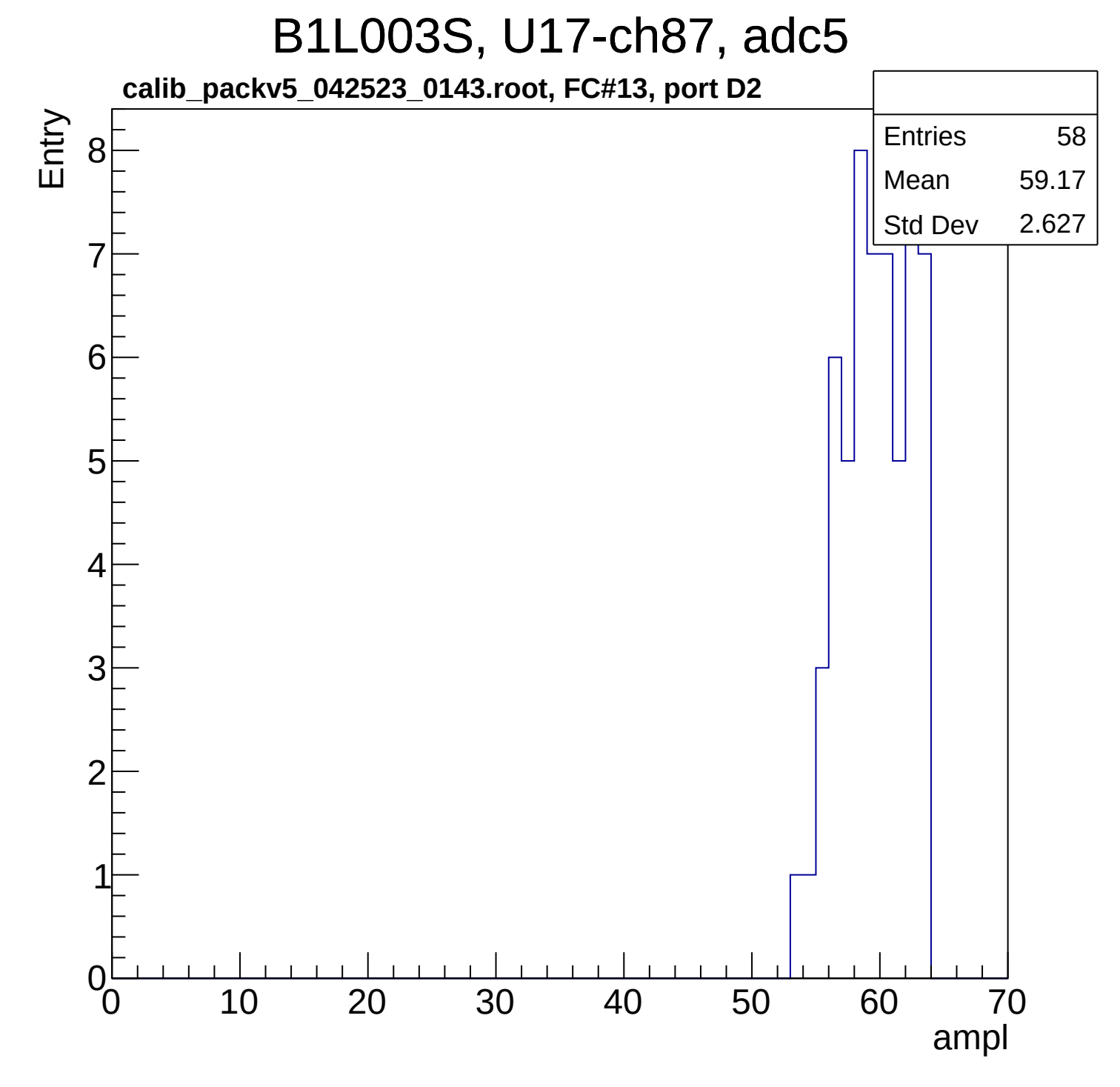

B1L003S, U17-ch87, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.15
Std Dev 4.542
Gaus mean : 29.2916
Gaus Width: 3.5672
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

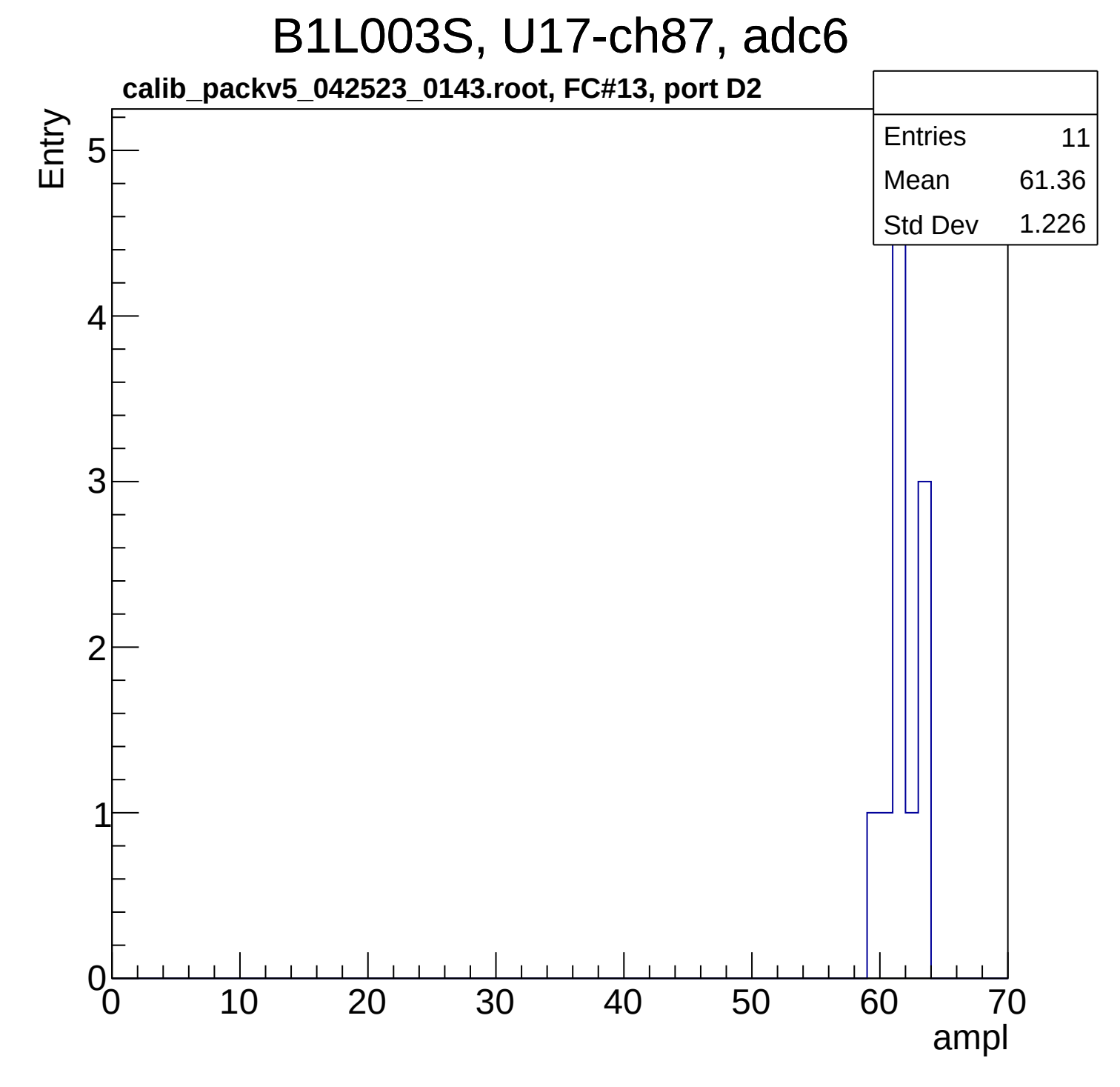

B1L003S, U17-ch87, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 35.84
Std Dev 2.779
Gaus mean : 36.1263
Gaus Width: 2.9475
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

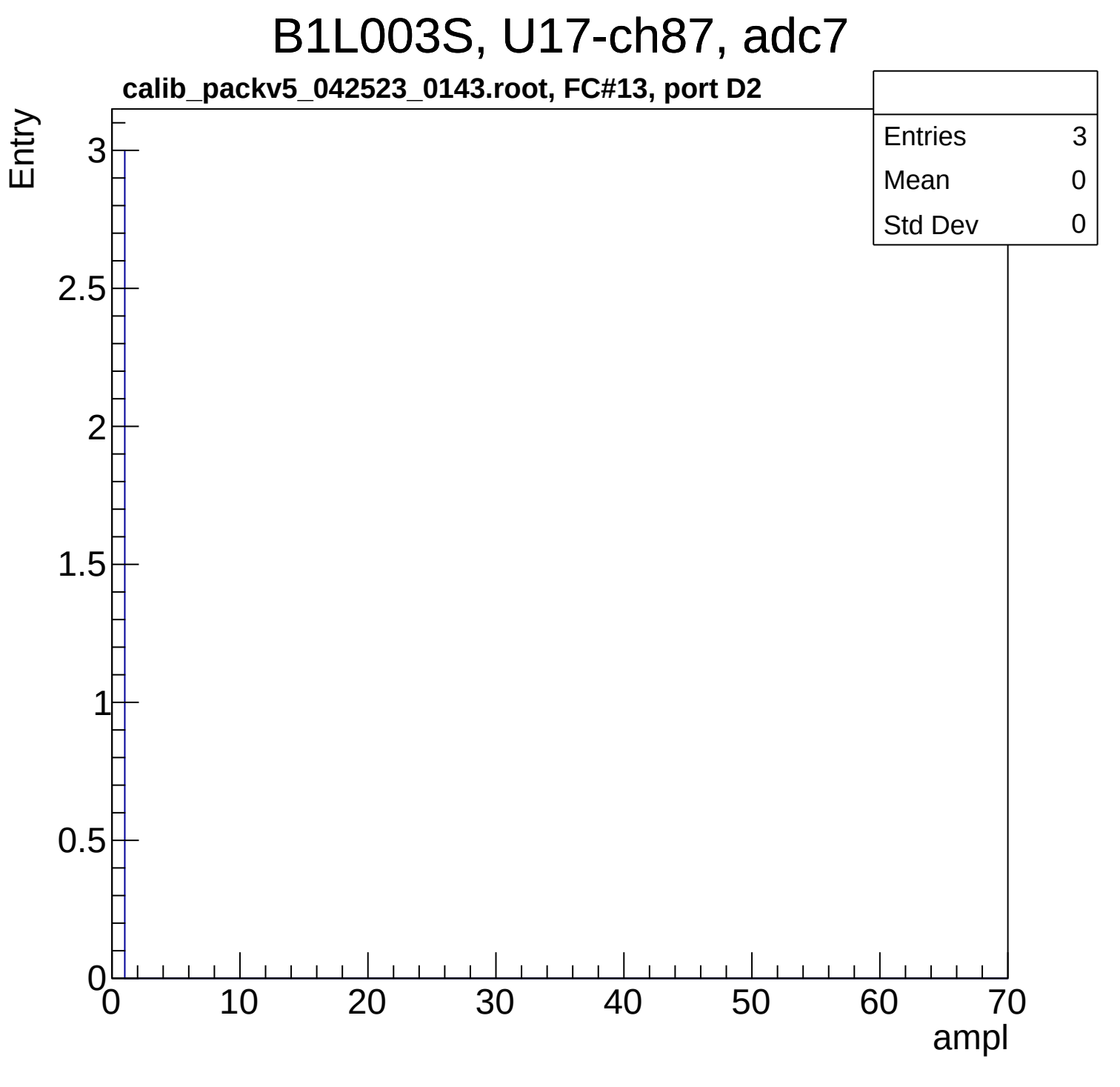

B1L003S, U17-ch87, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.97
Std Dev 3.06
Gaus mean : 42.5463
Gaus Width: 3.3603
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

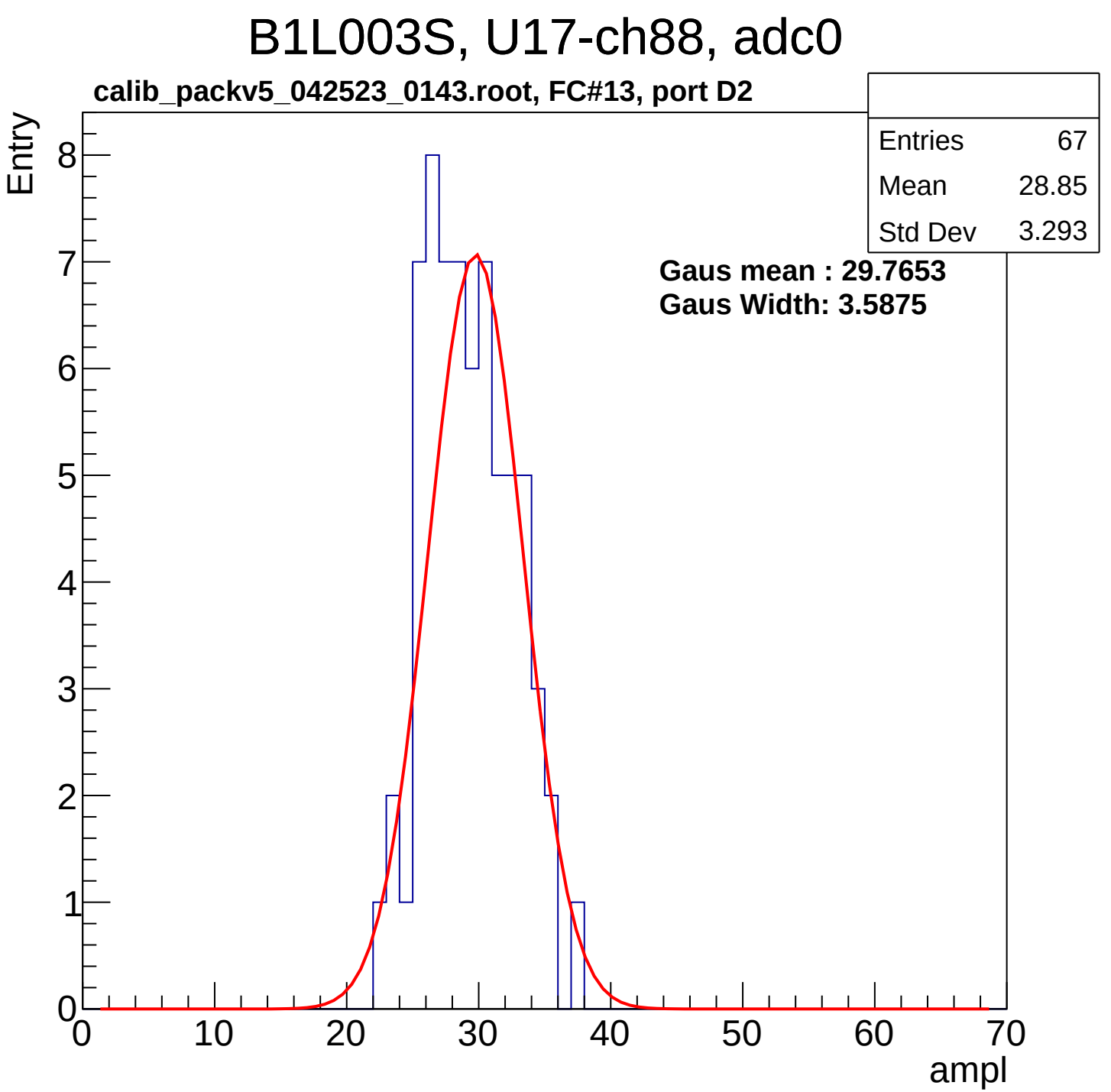

B1L003S, U17-ch87, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.11
Std Dev 3.497
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

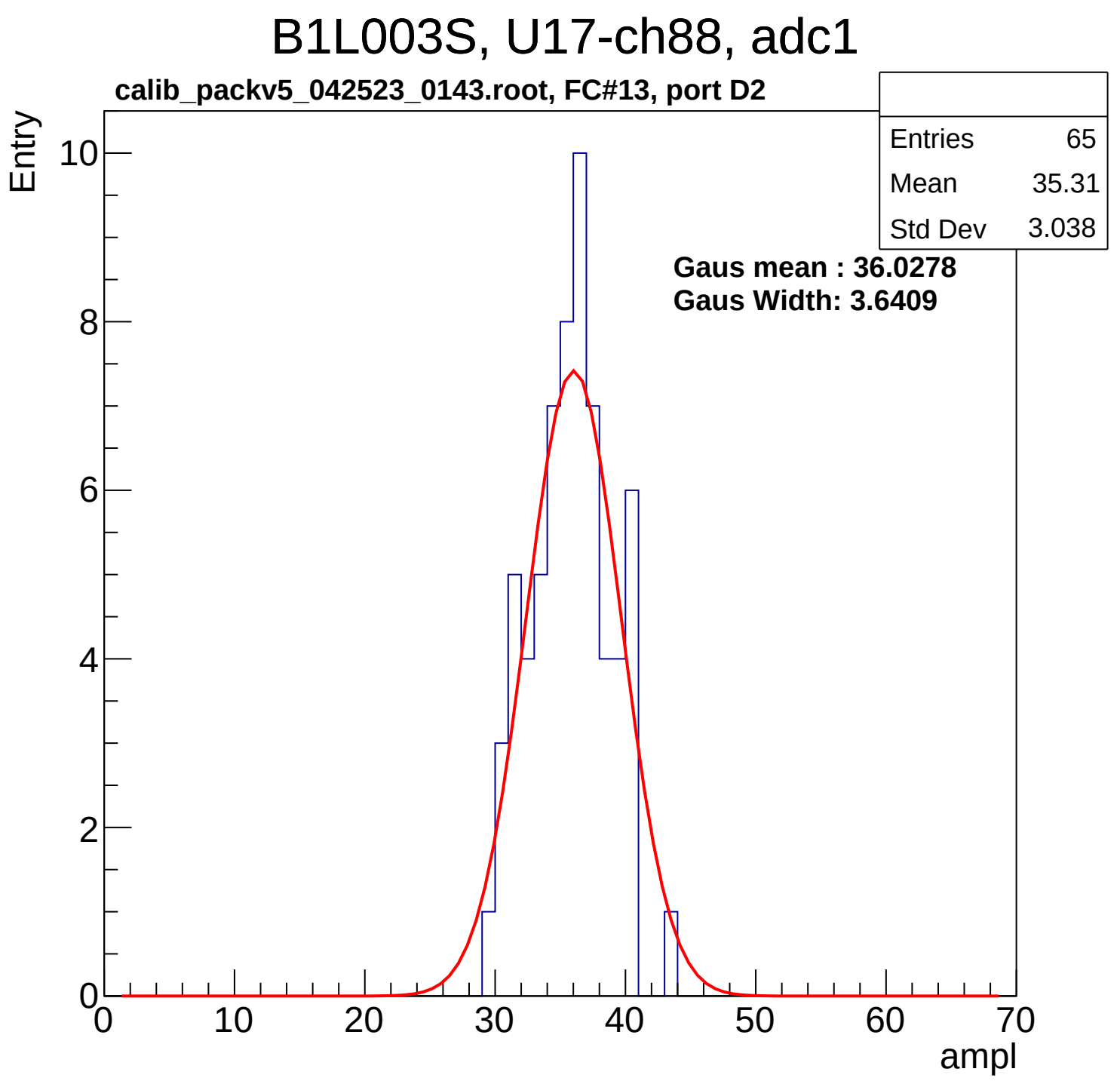

B1L003S, U17-ch87, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.6
Std Dev 3.519
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

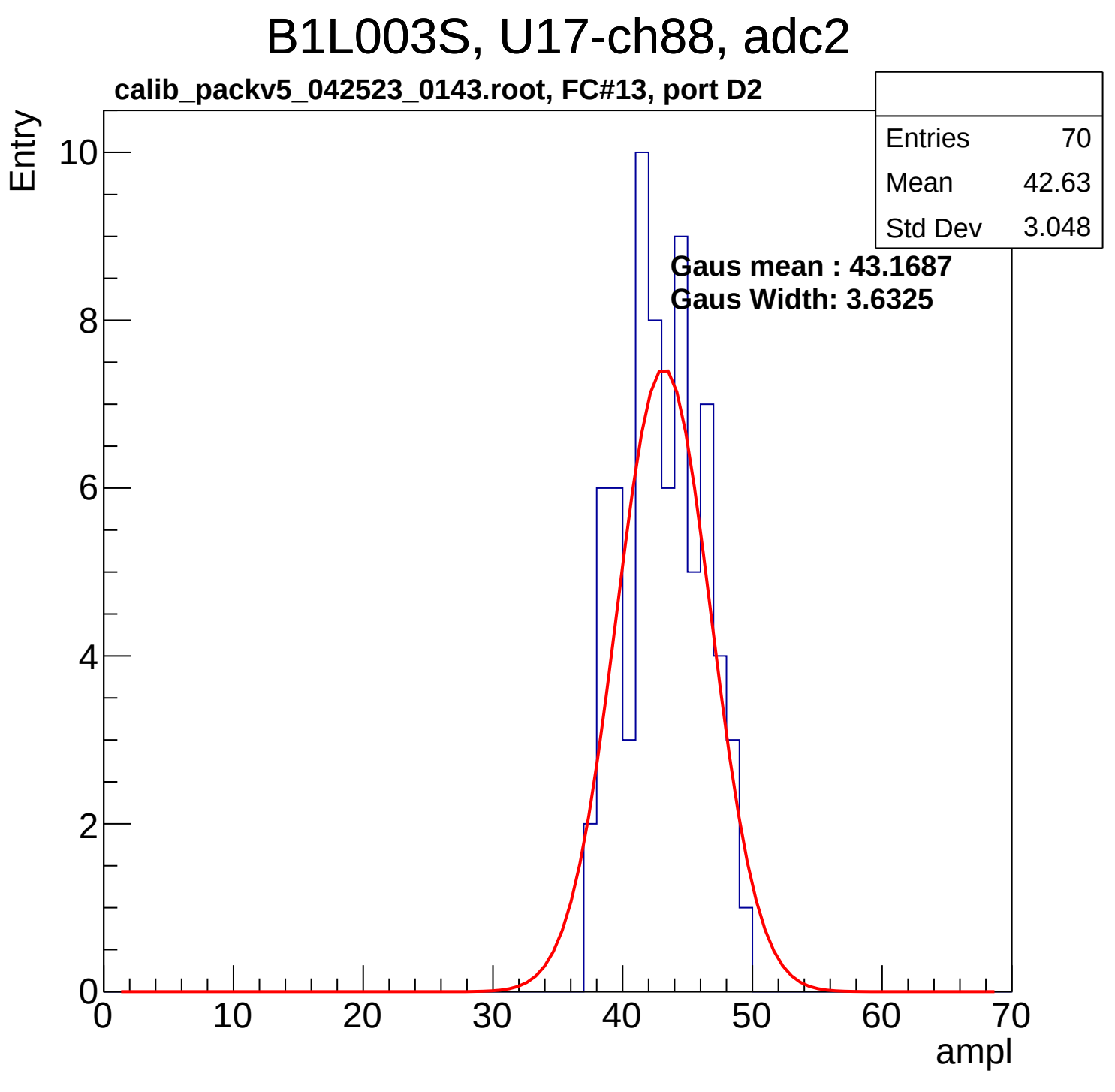

B1L003S, U17-ch87, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 59.17
Std Dev 2.627
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

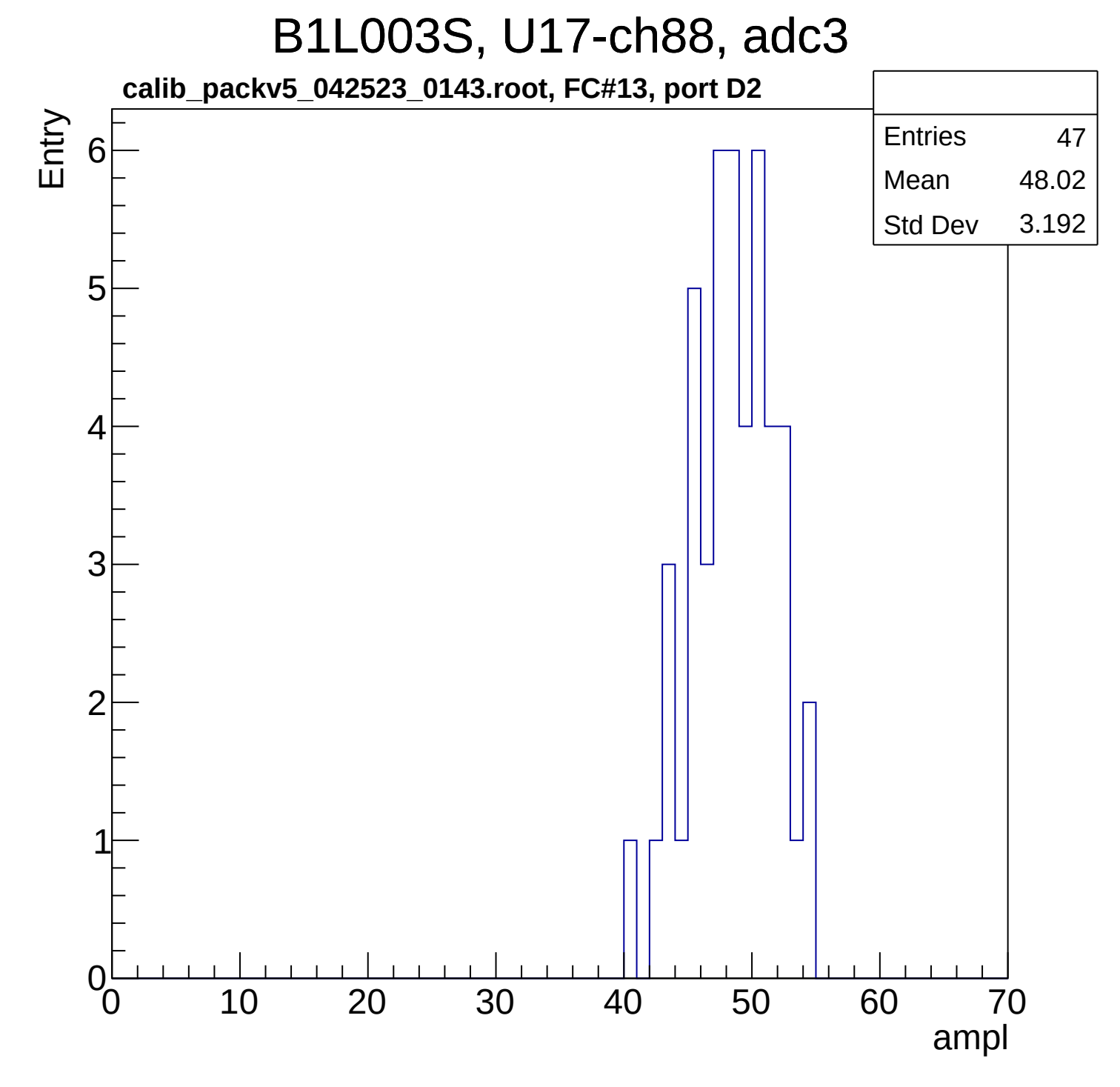

B1L003S, U17-ch87, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.36
Std Dev 1.226
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

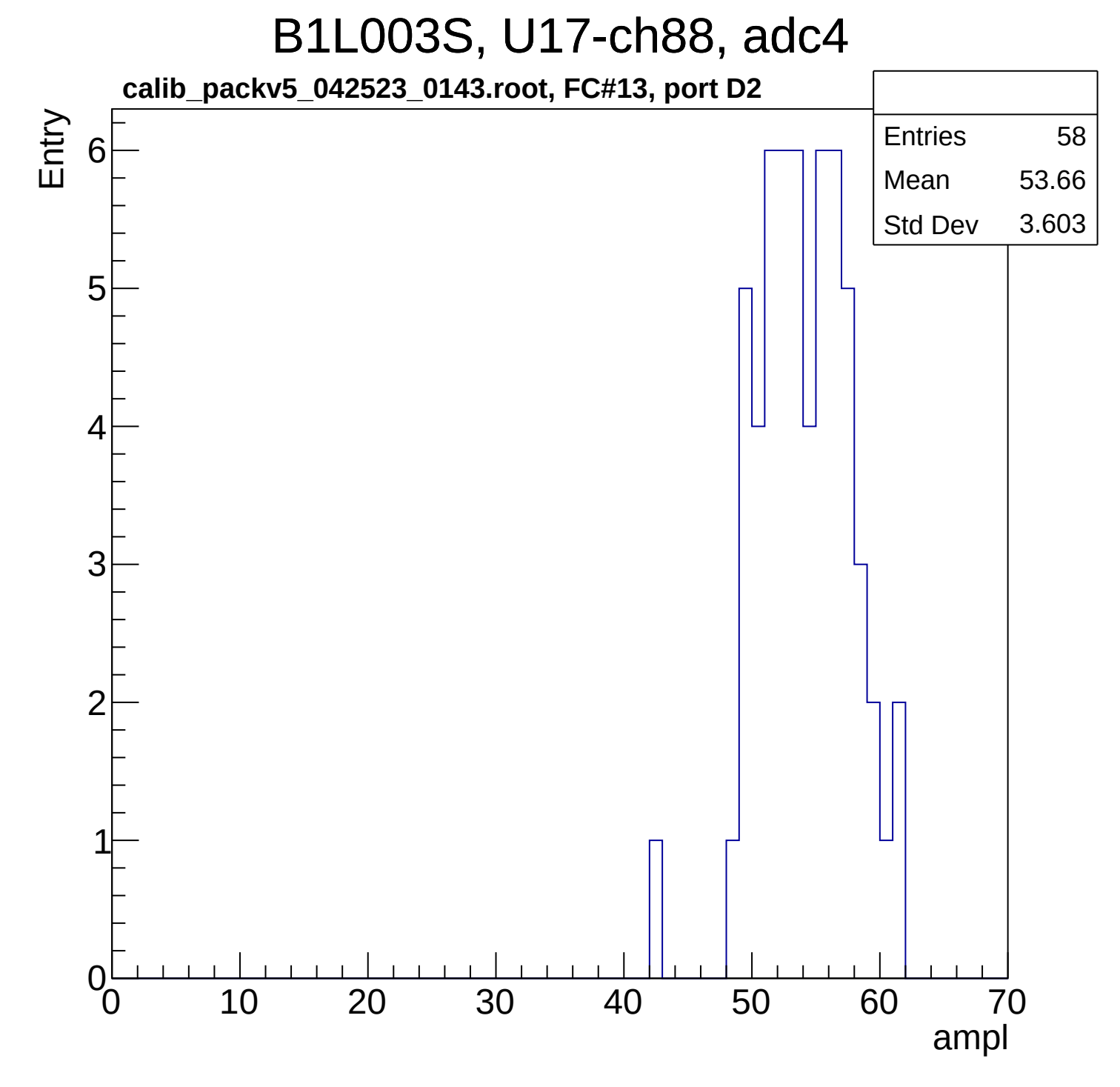

B1L003S, U17-ch87, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

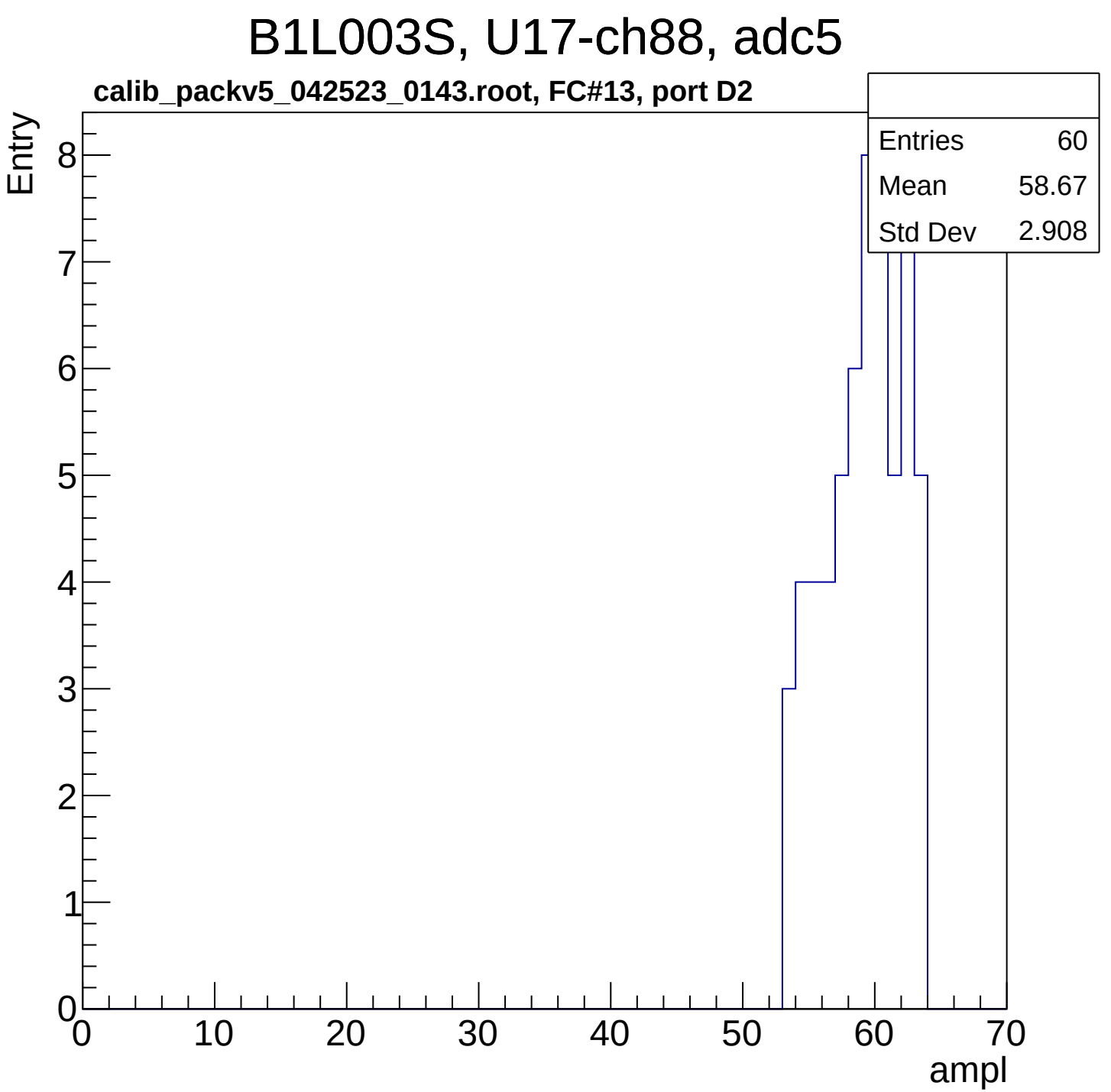

B1L003S, U17-ch88, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 28.85
Std Dev 3.293
Gaus mean : 29.7653
Gaus Width: 3.5875
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

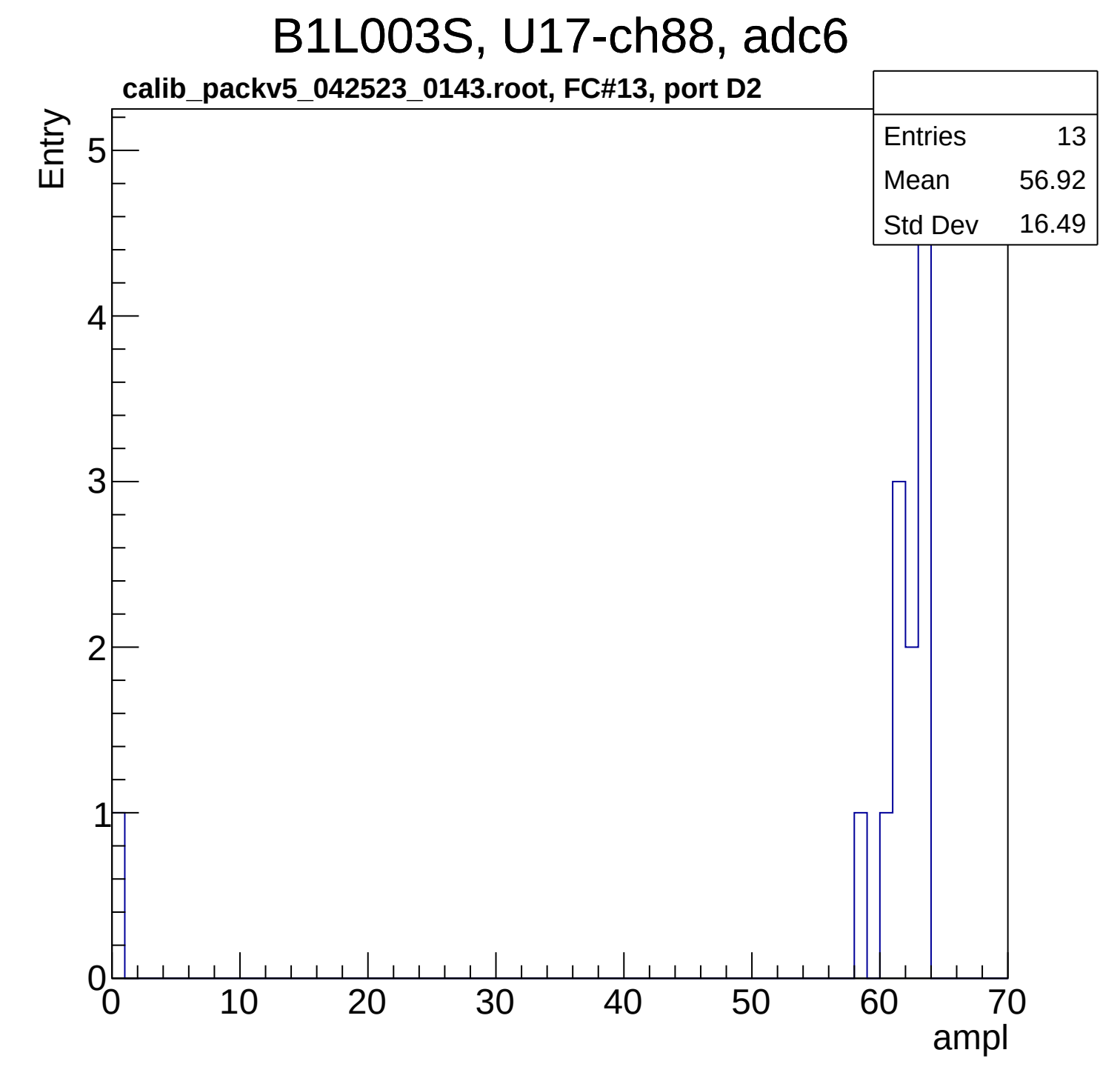

B1L003S, U17-ch88, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.31
Std Dev 3.038
Gaus mean : 36.0278
Gaus Width: 3.6409
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

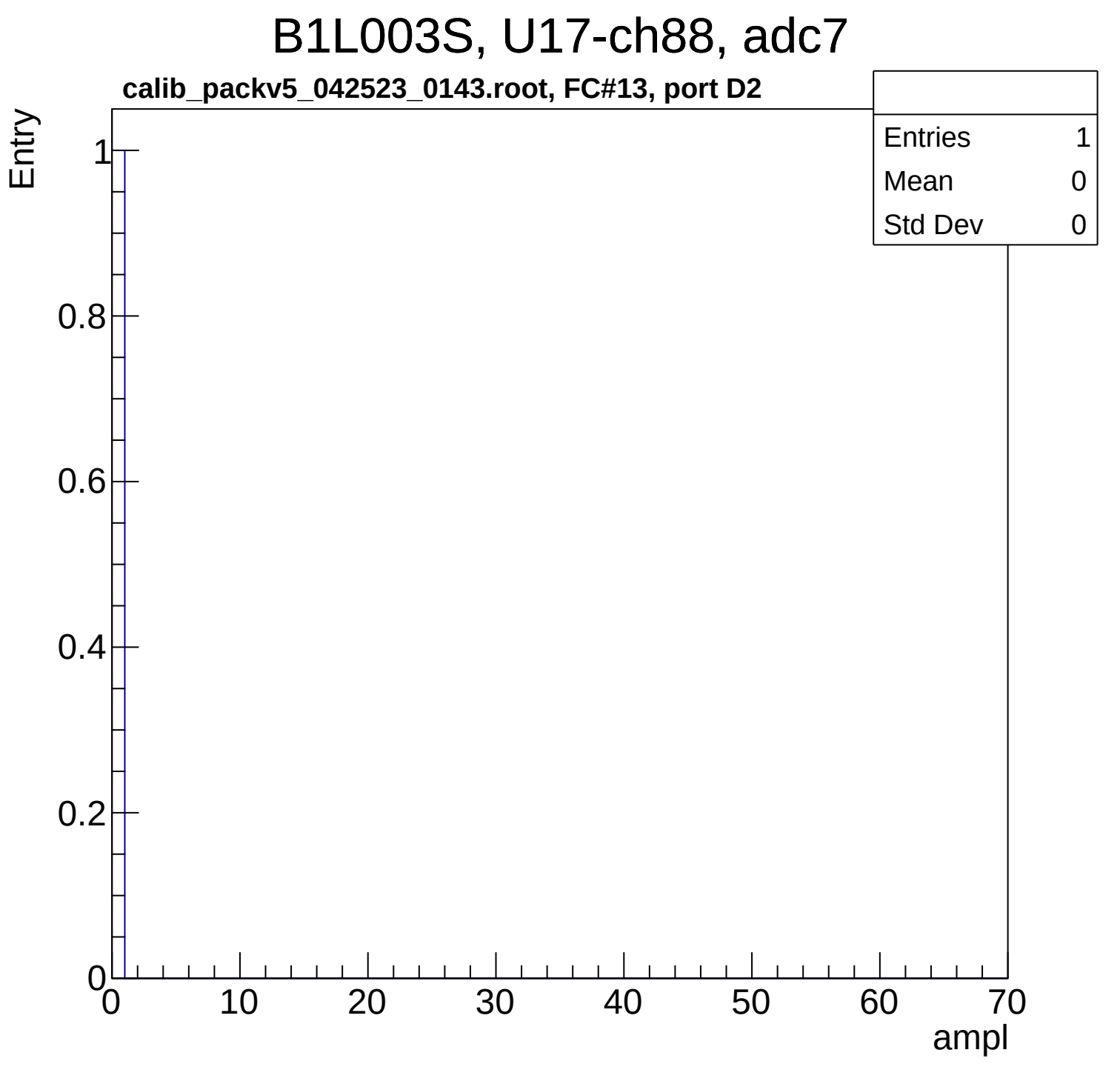

B1L003S, U17-ch88, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.63
Std Dev 3.048
Gaus mean : 43.1687
Gaus Width: 3.6325
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch88, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 48.02
Std Dev 3.192
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch88, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 53.66
Std Dev 3.603
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch88, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.67
Std Dev 2.908
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch88, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.92
Std Dev 16.49
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch88, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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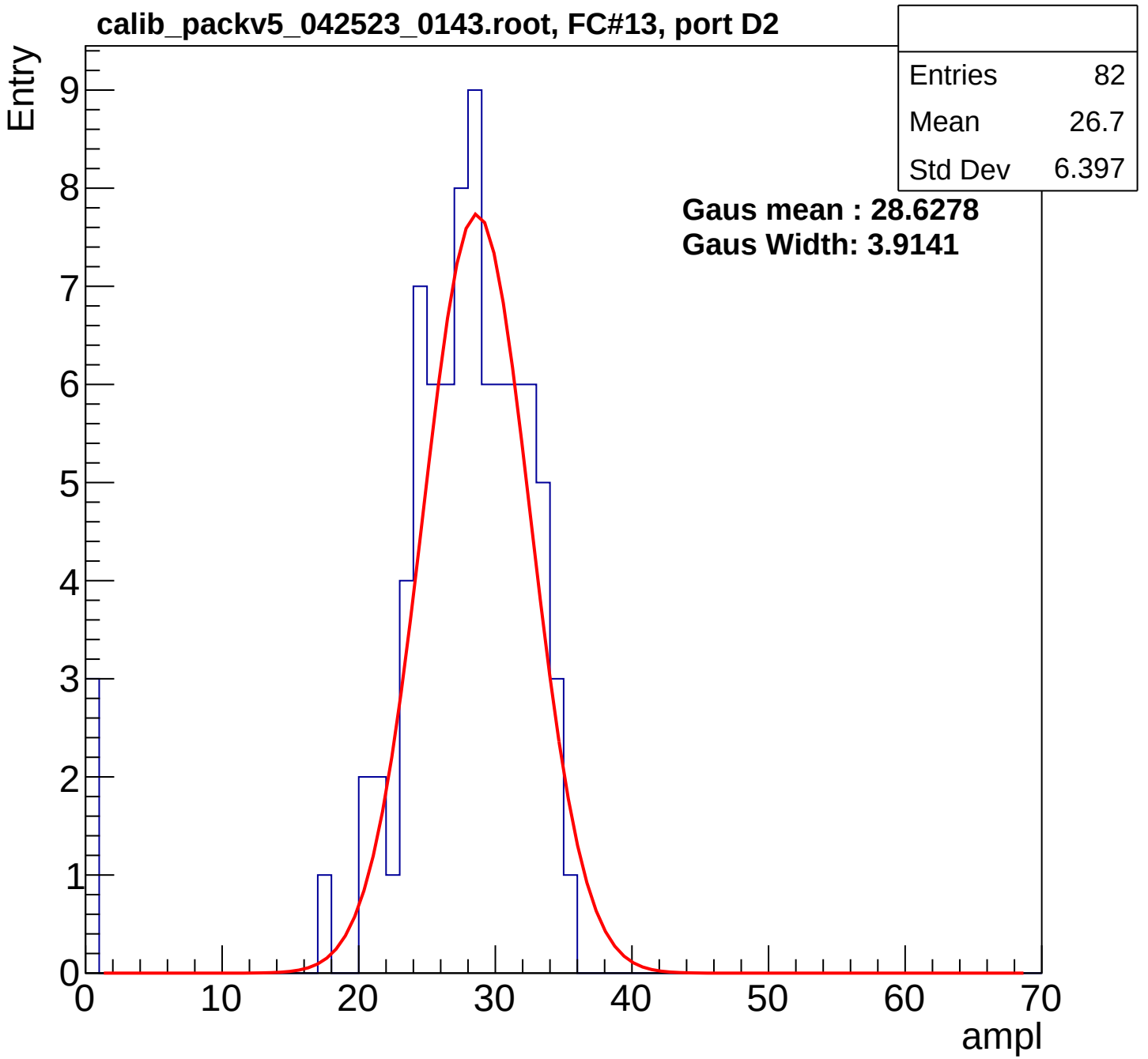
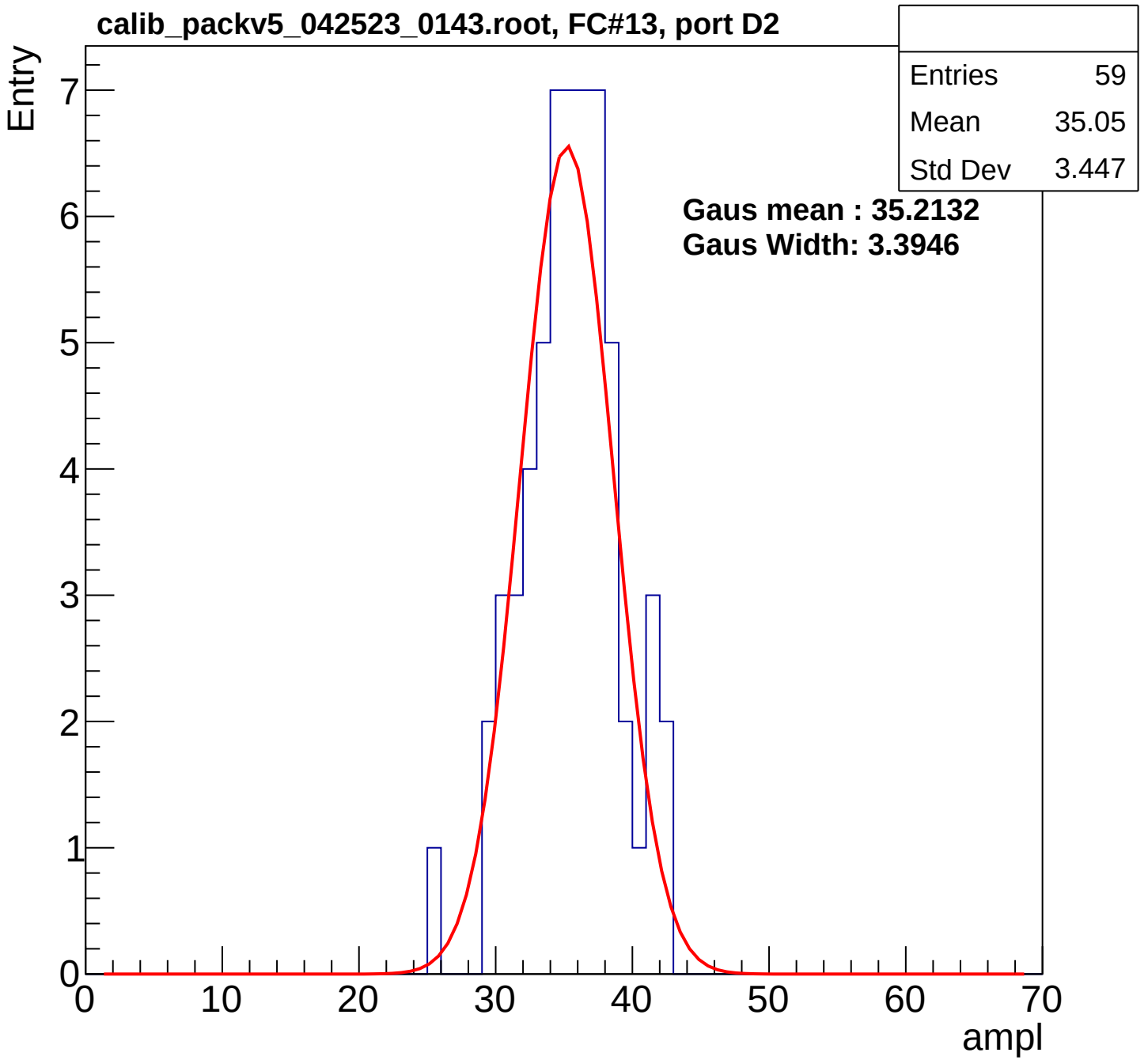
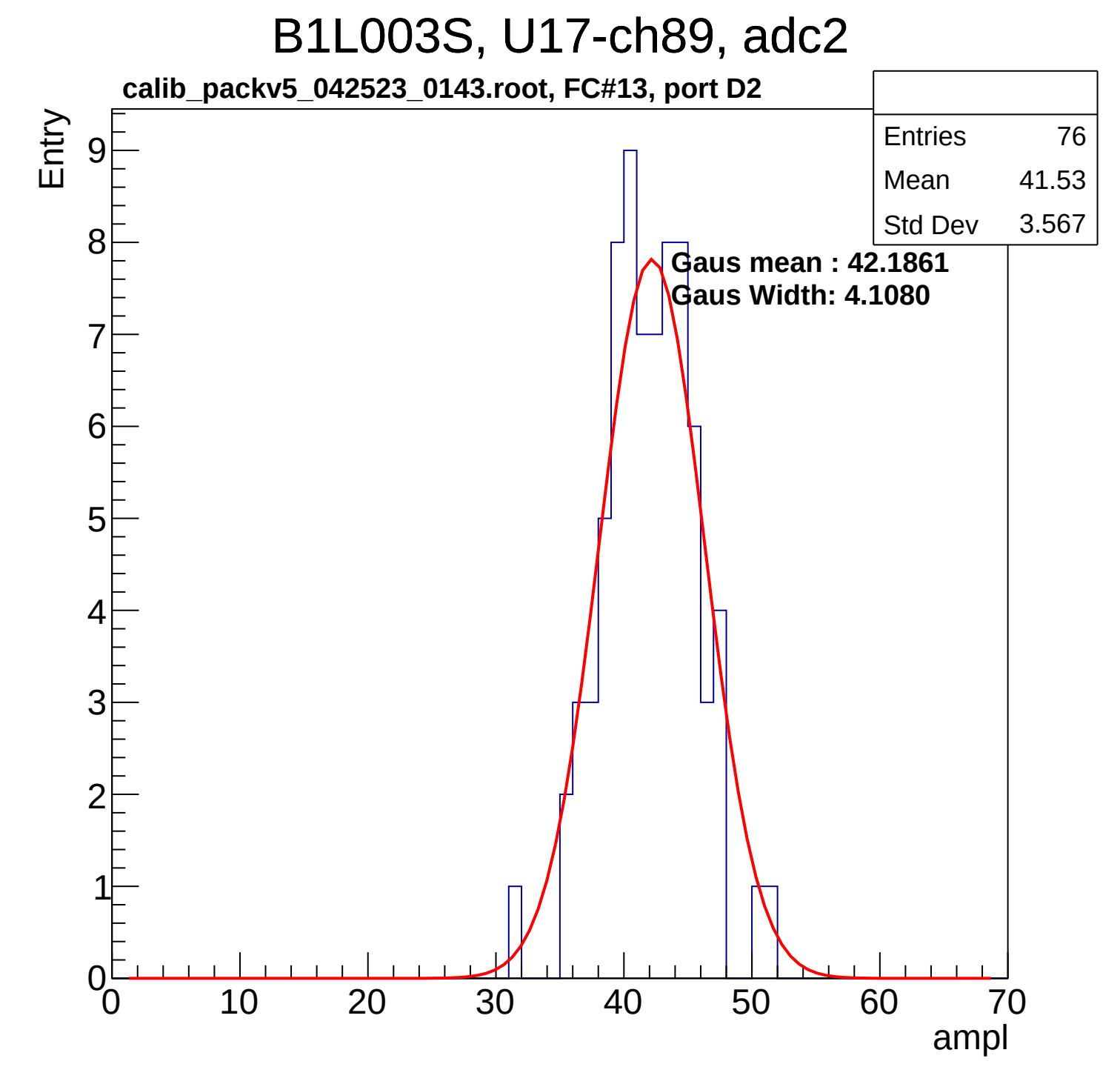
50
60
70
ampl


B1L003S, U17-ch89, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 26.7
Std Dev 6.397
Gaus mean : 28.6278
Gaus Width: 3.9141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch89, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 35.05
Std Dev 3.447
Gaus mean : 35.2132
Gaus Width: 3.3946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch89, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 41.53
Std Dev 3.567
Gaus mean : 42.1861
Gaus Width: 4.1080
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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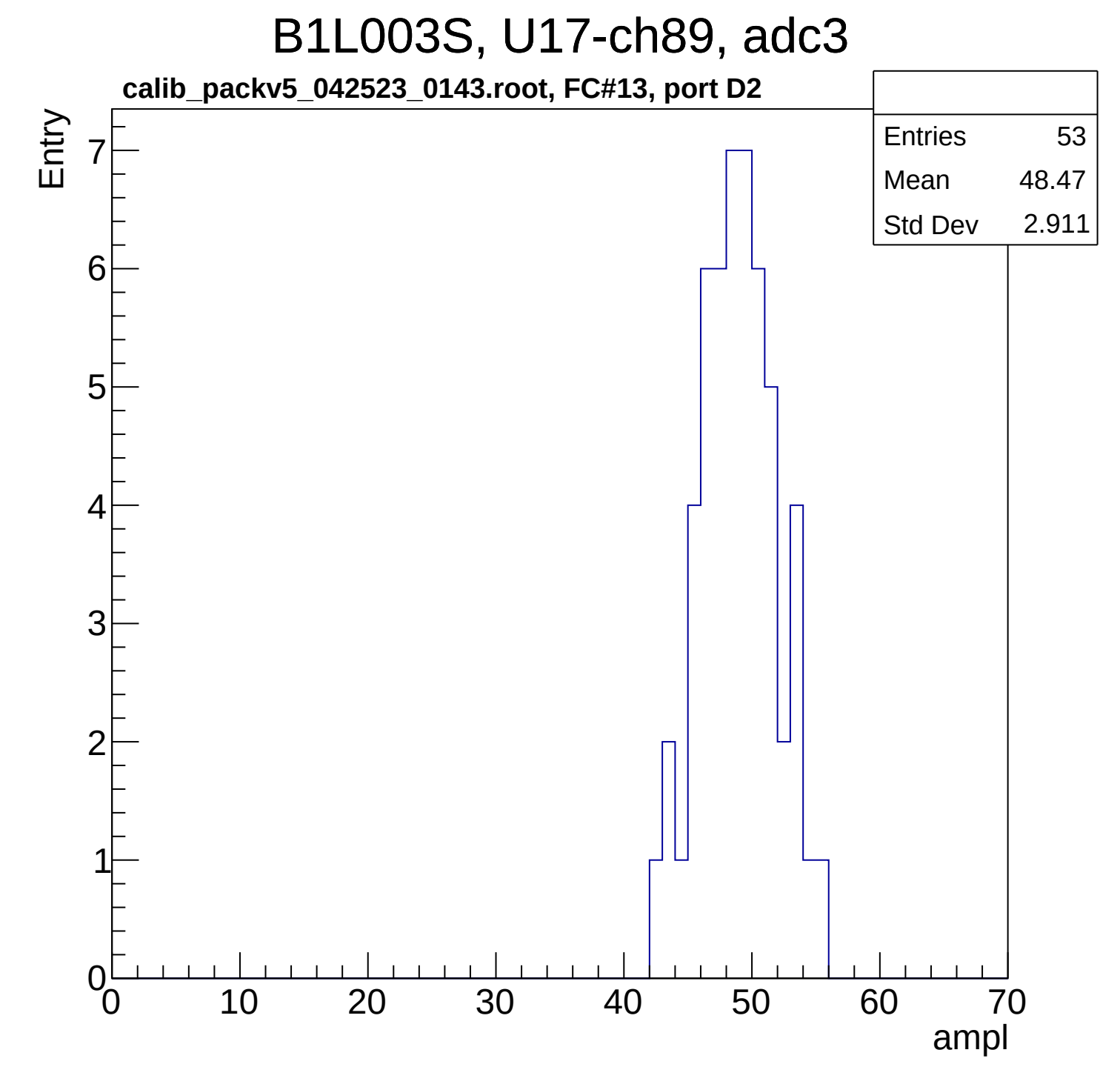
B1L003S, U17-ch89, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.47
Std Dev 2.911
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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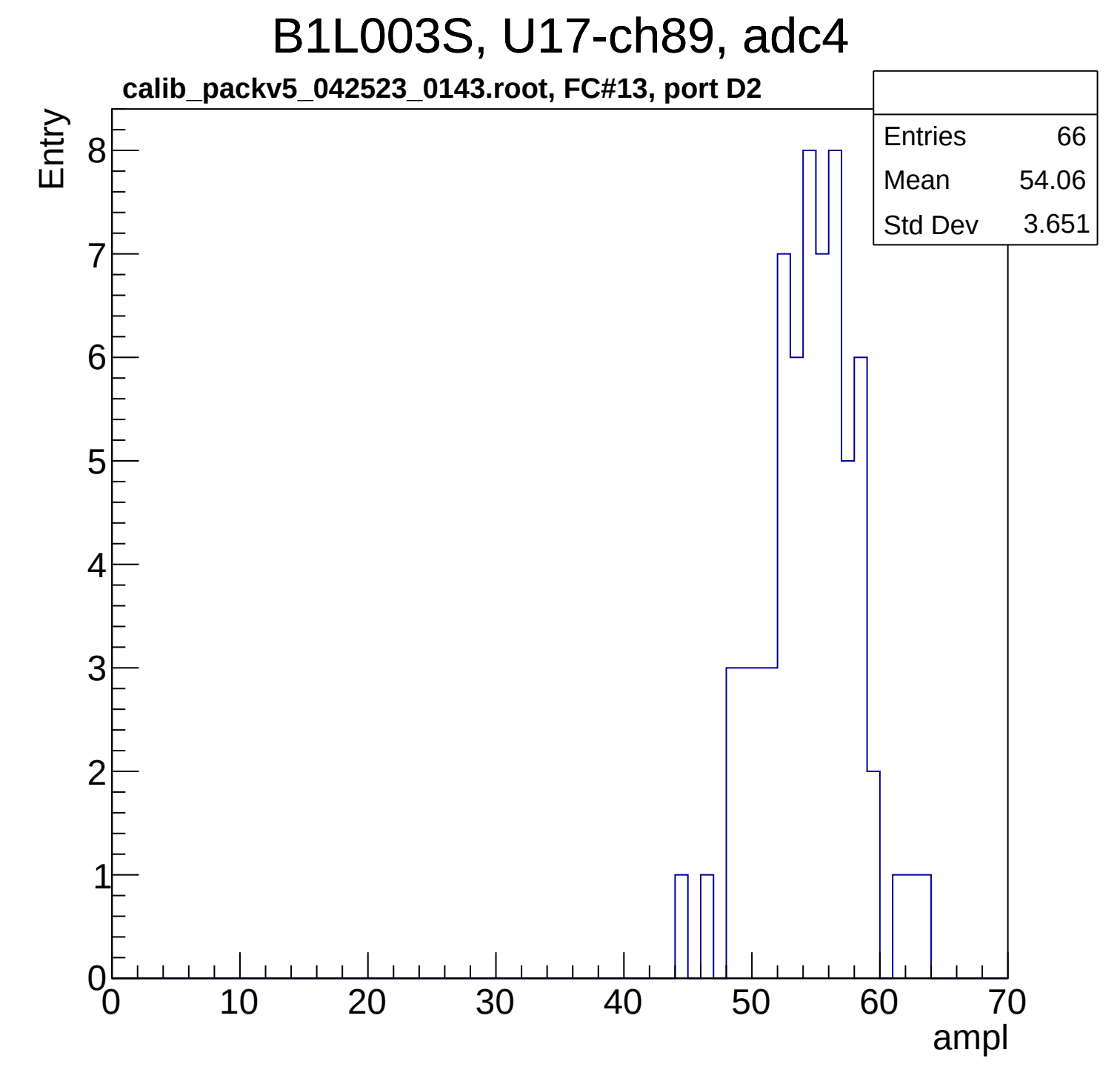
B1L003S, U17-ch89, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 54.06
Std Dev 3.651
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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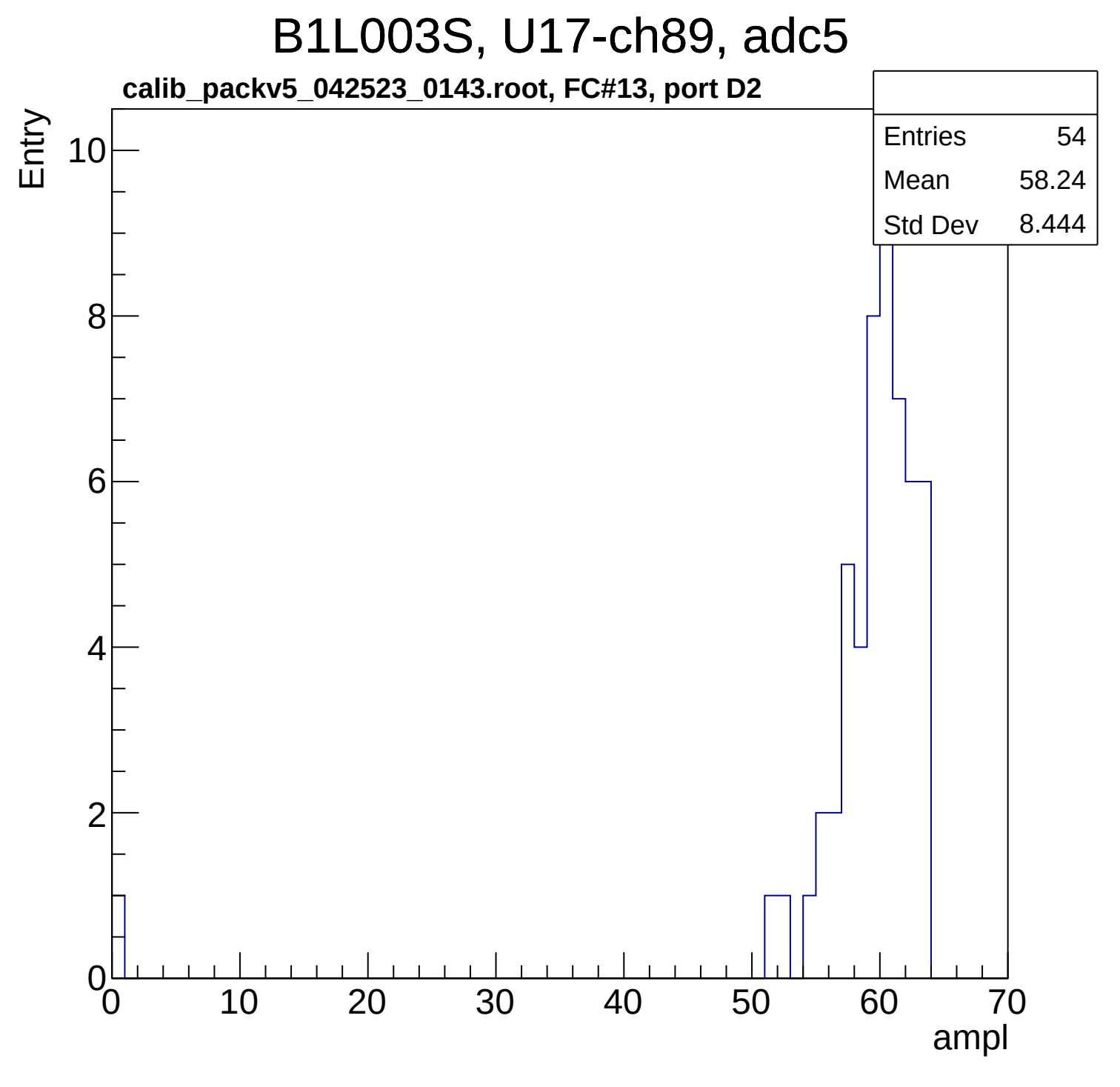
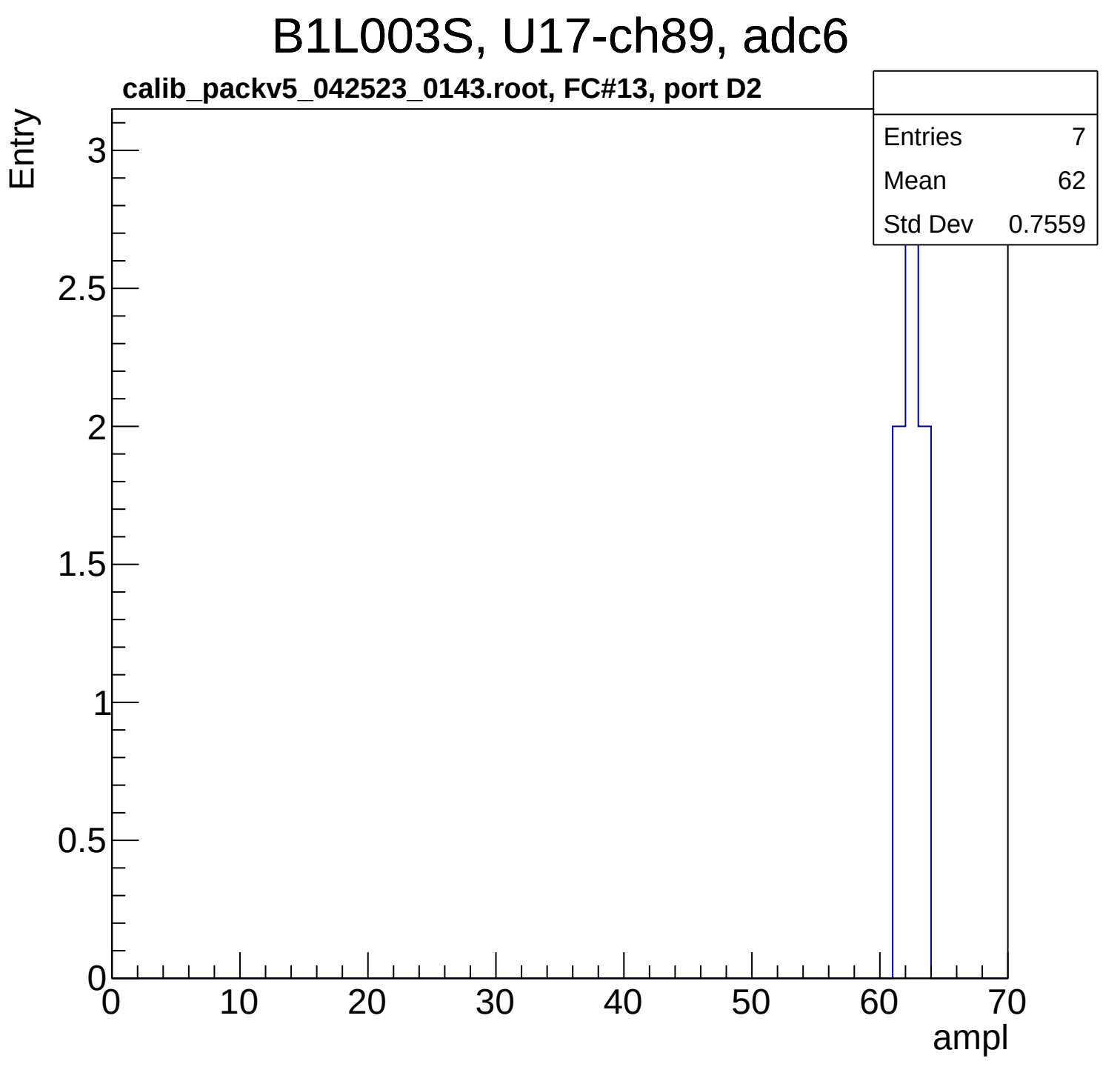
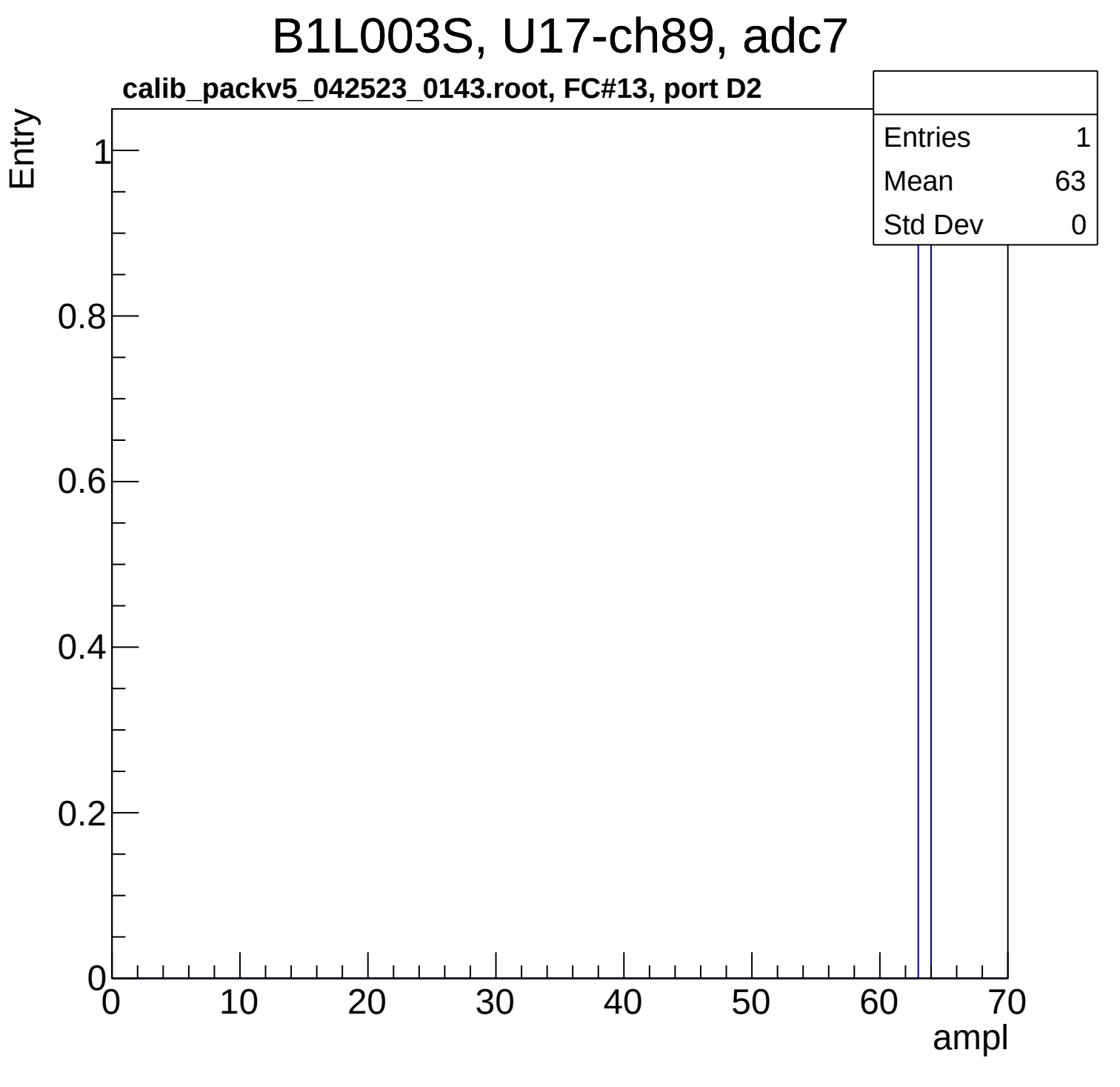
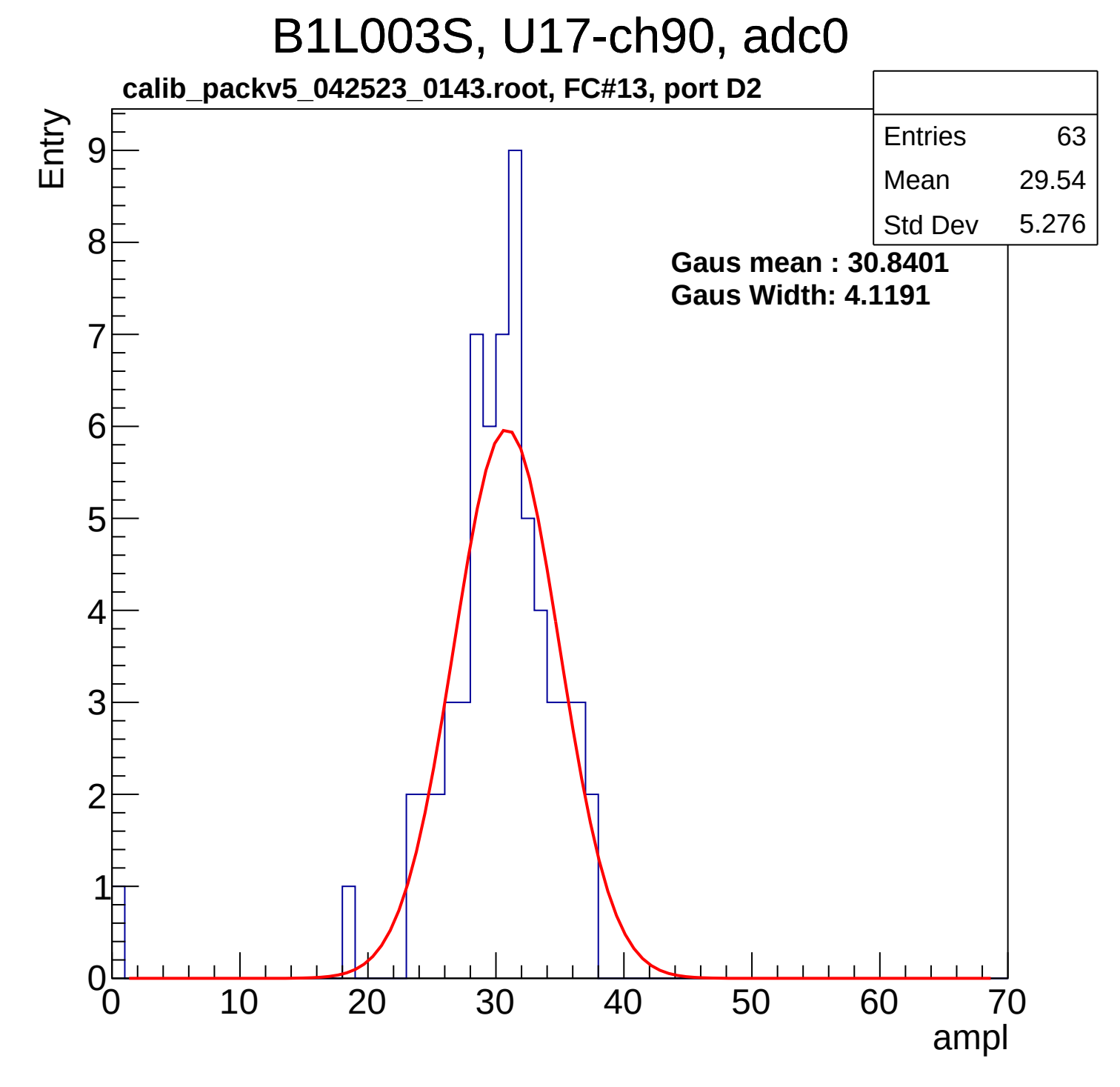
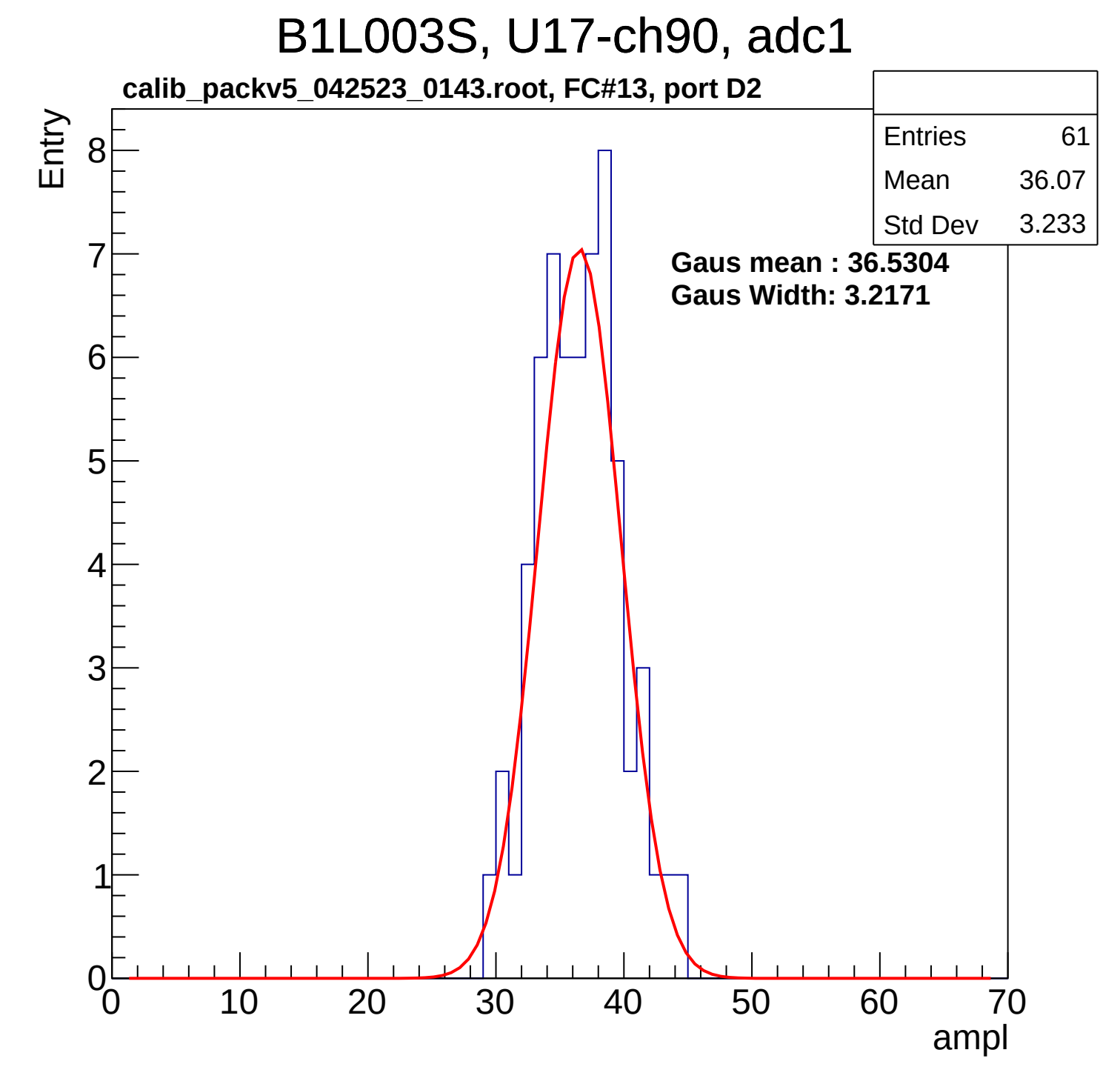
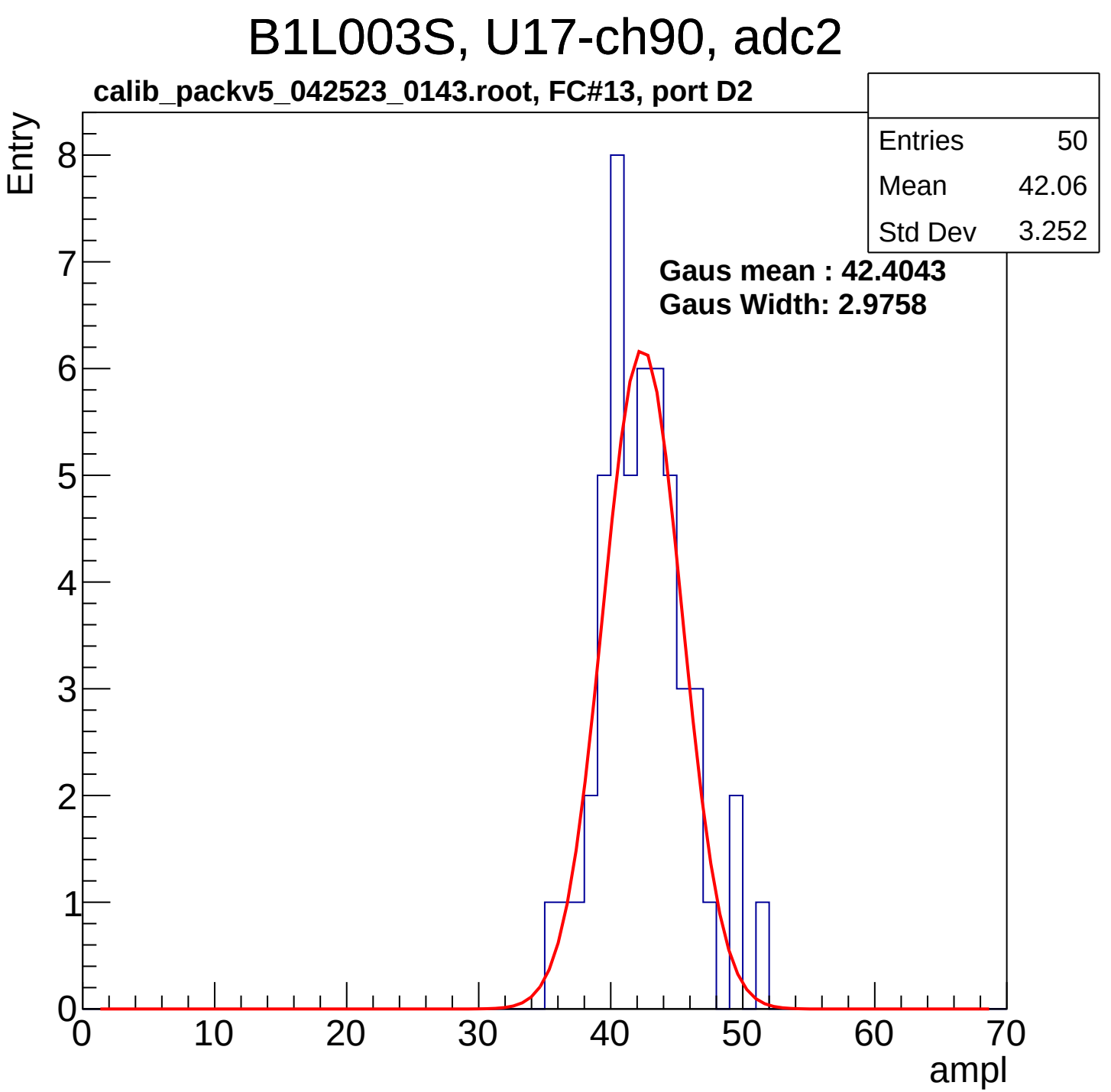

B1L003S, U17-ch89, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.24
Std Dev 8.444
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

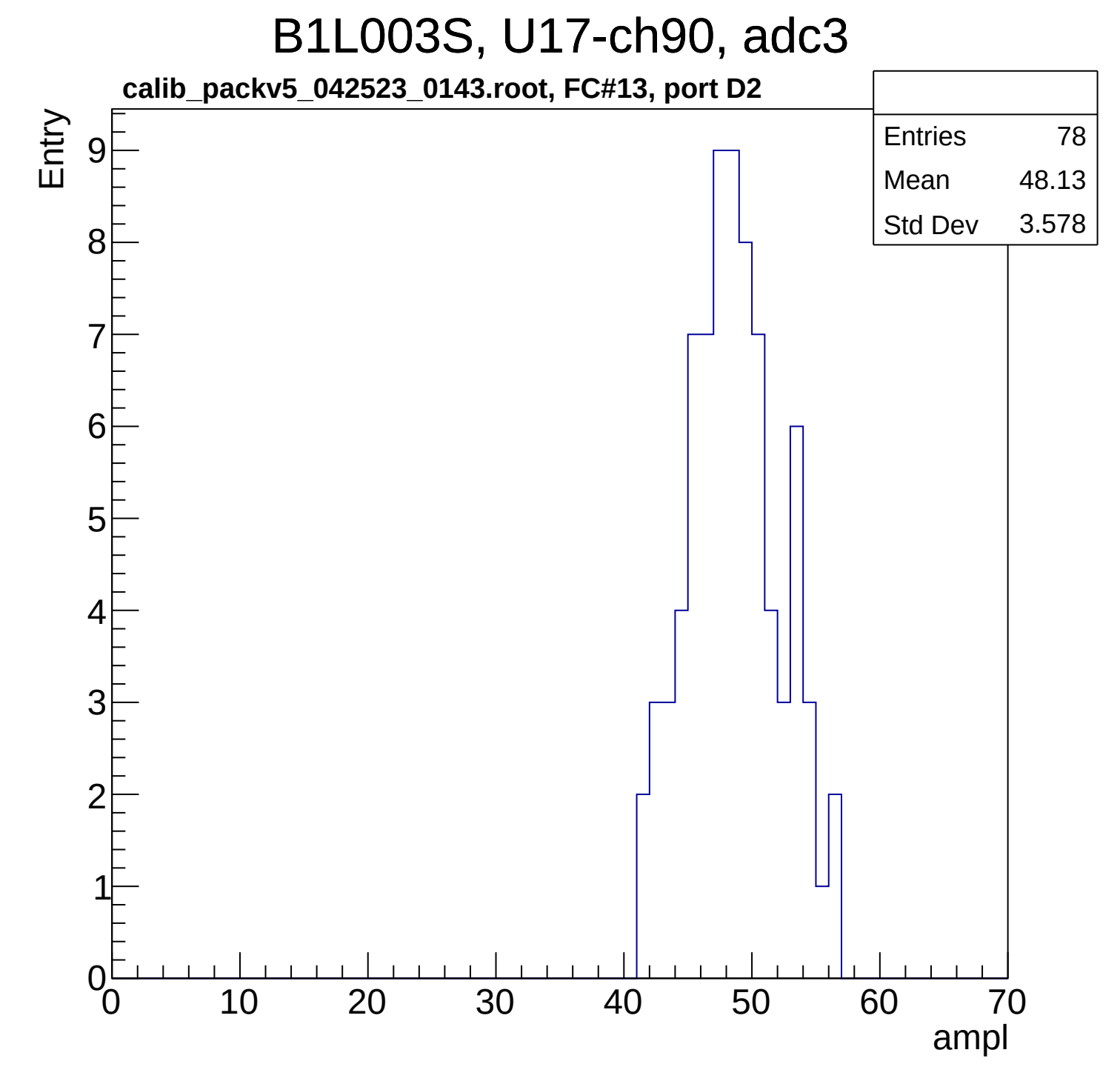

B1L003S, U17-ch89, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62
Std Dev 0.7559
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

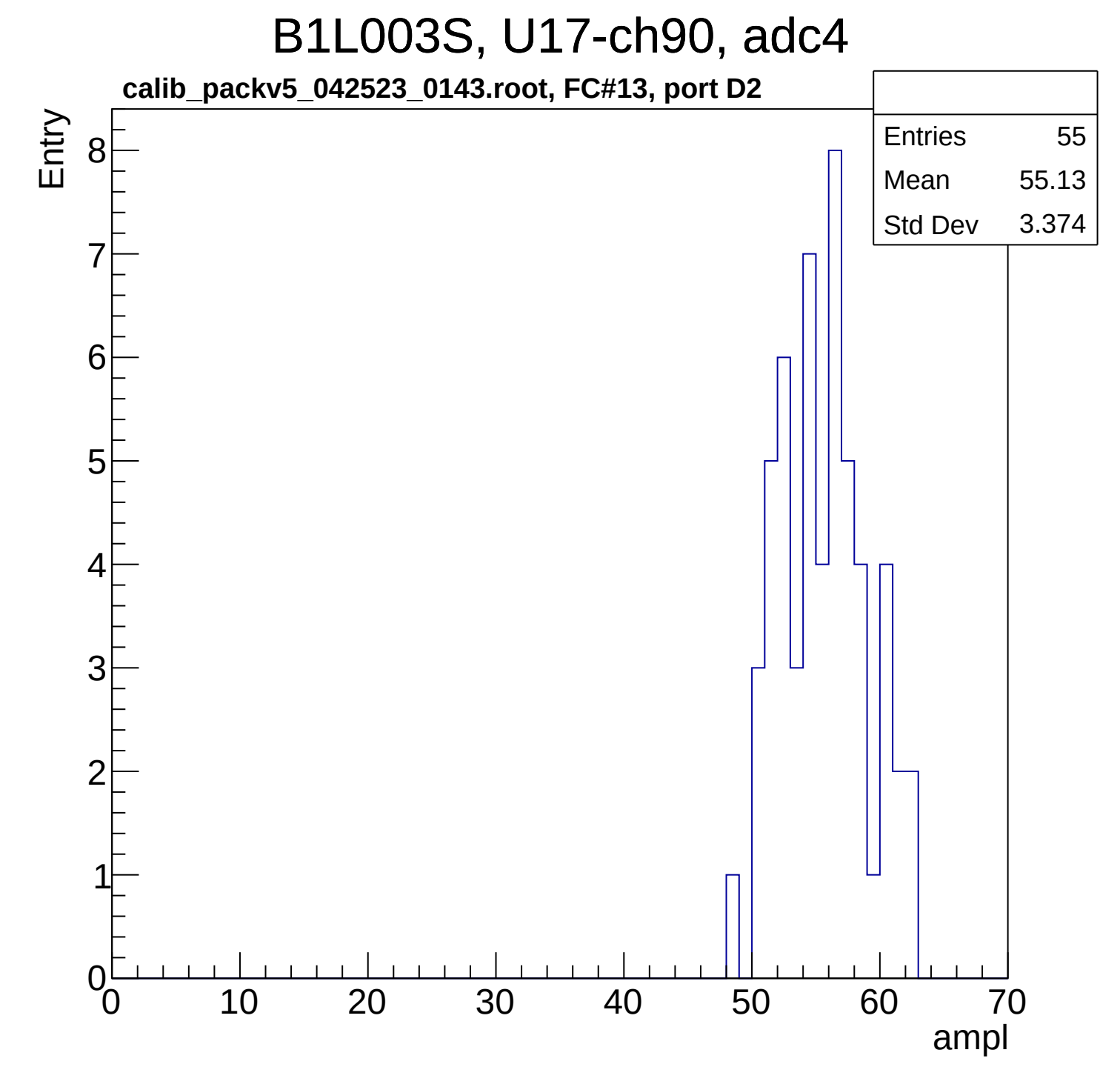

B1L003S, U17-ch89, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch90, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29.54
Std Dev 5.276
Gaus mean : 30.8401
Gaus Width: 4.1191
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch90, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.07
Std Dev 3.233
Gaus mean : 36.5304
Gaus Width: 3.2171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch90, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 42.06
Std Dev 3.252
Gaus mean : 42.4043
Gaus Width: 2.9758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch90, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 48.13
Std Dev 3.578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch90, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55.13
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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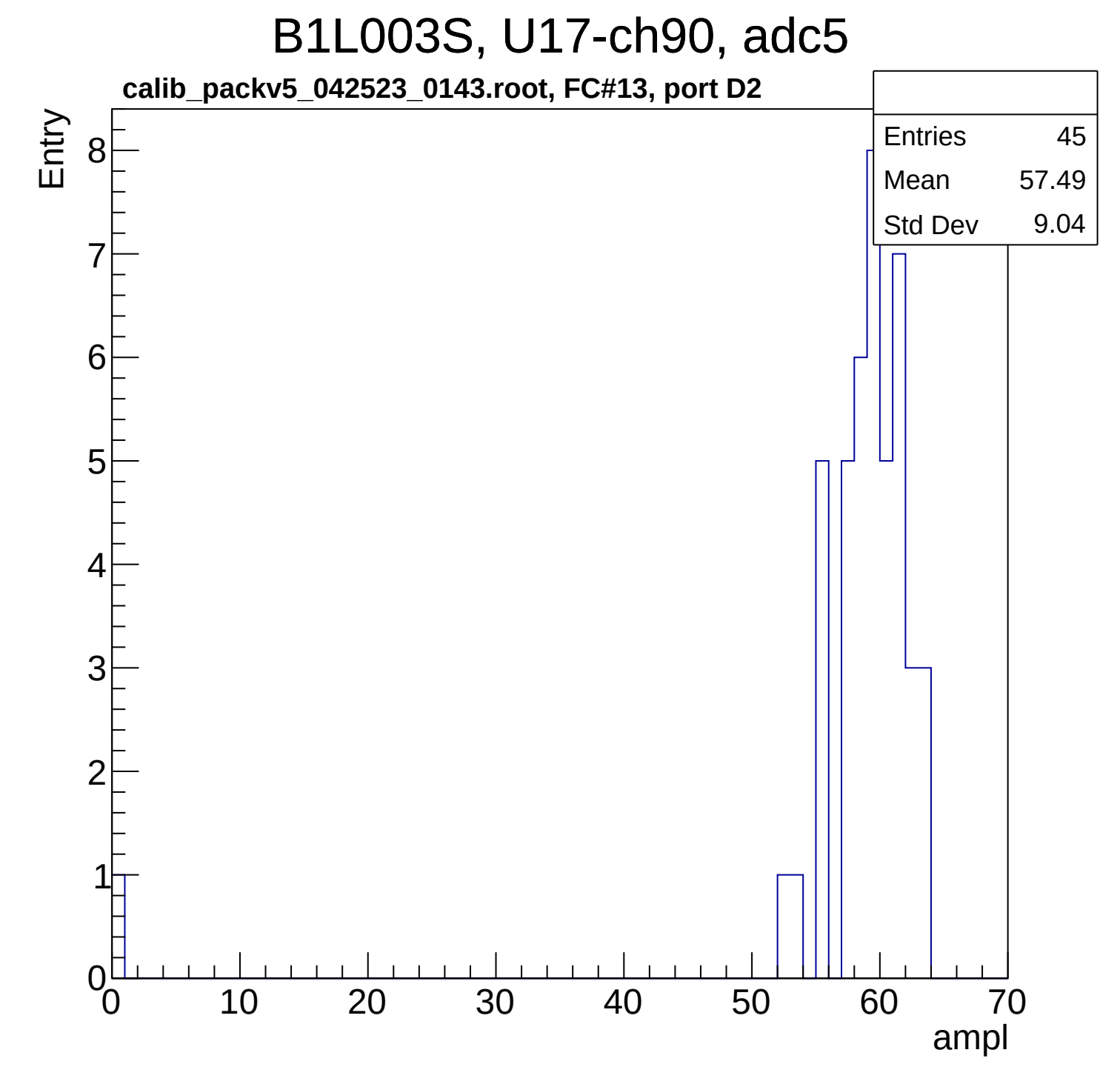
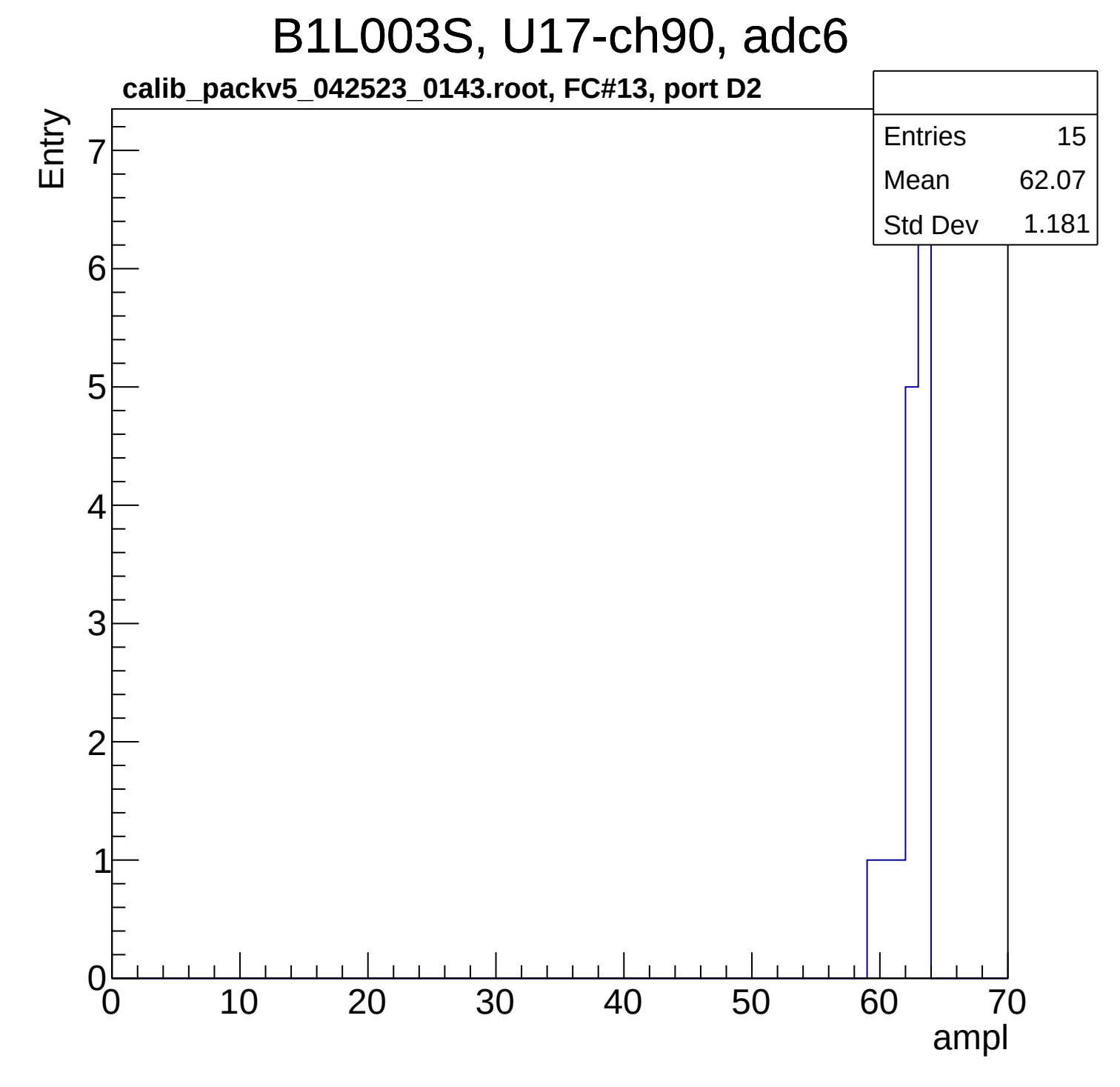
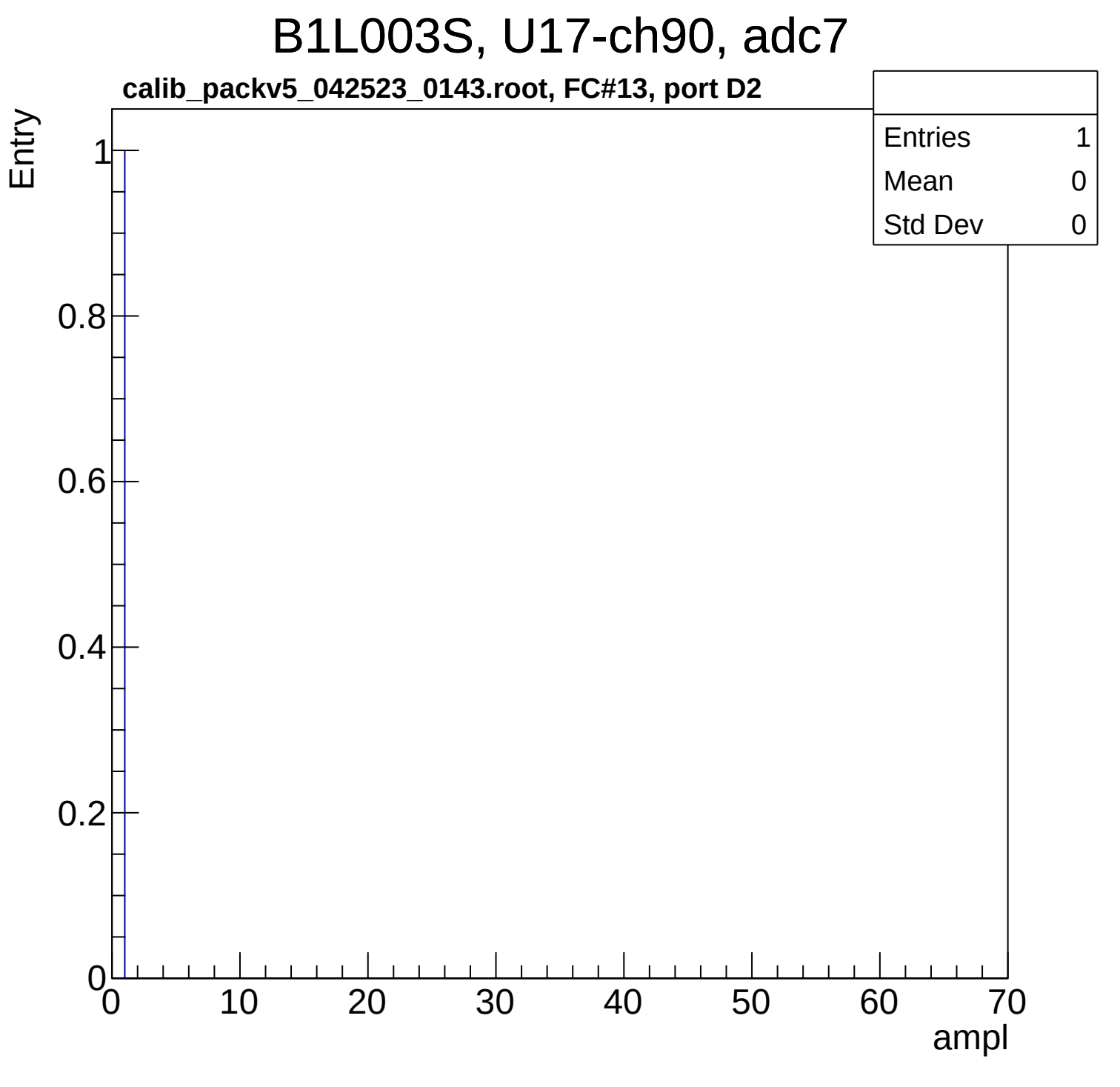
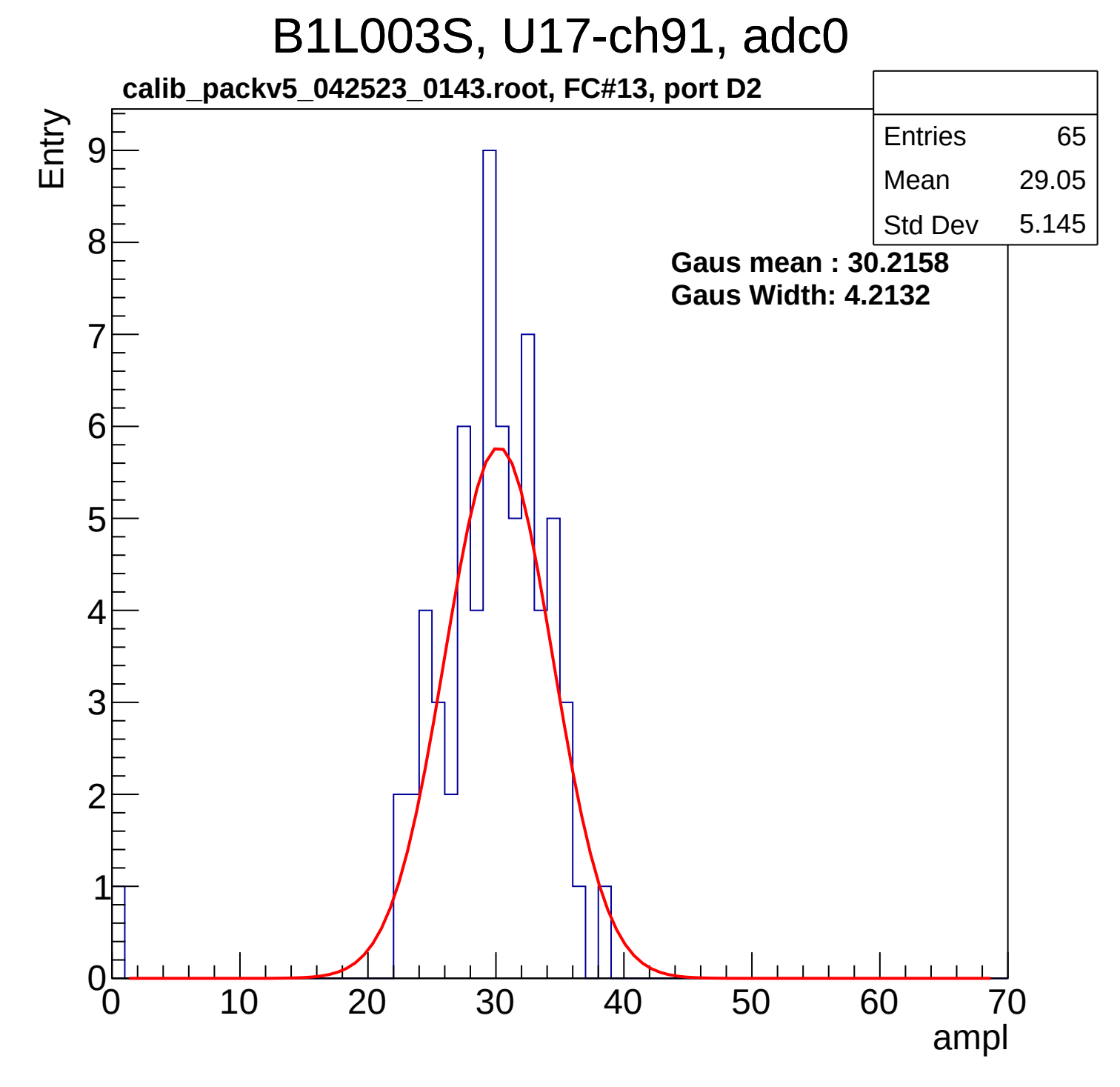
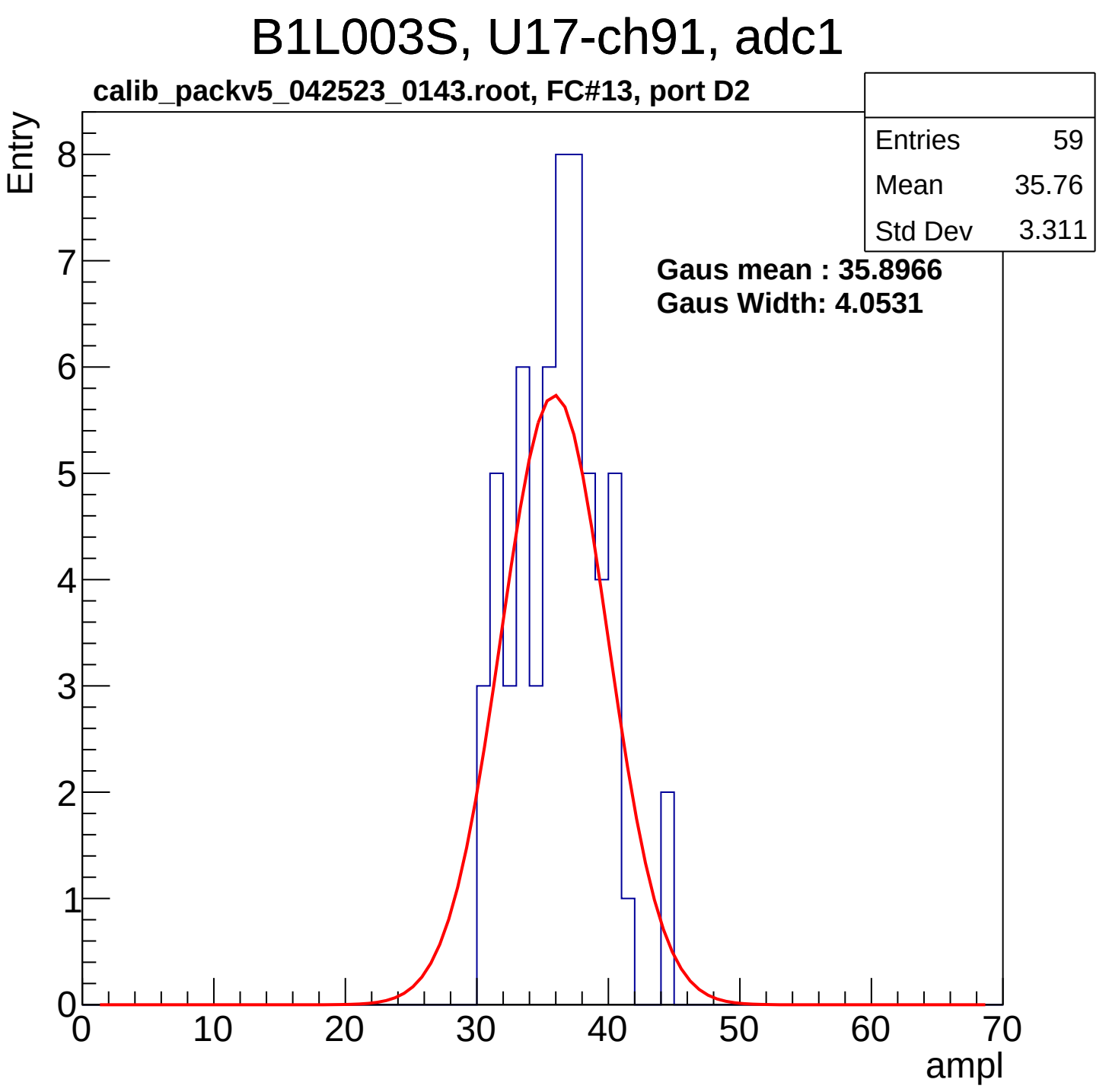
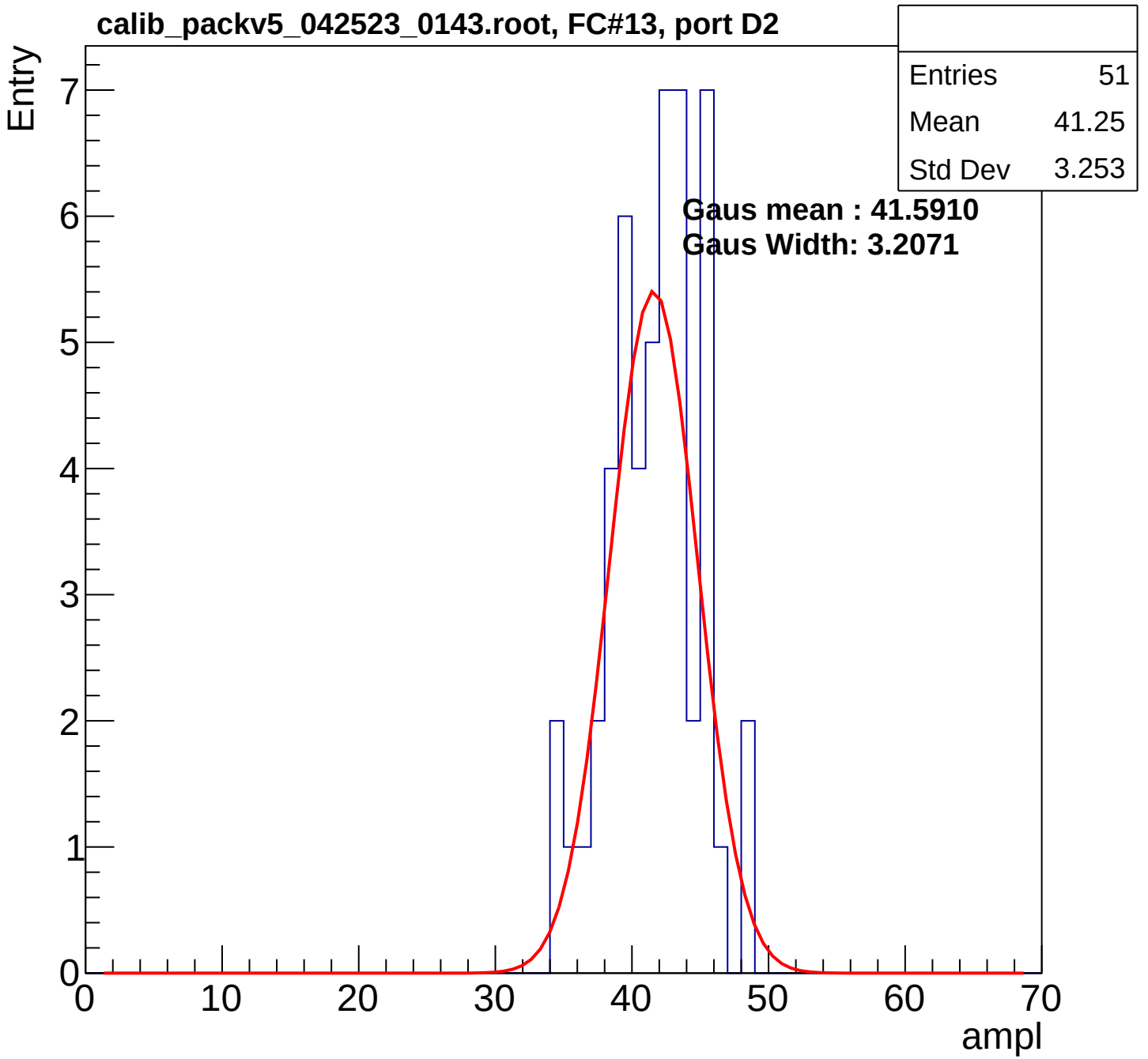

B1L003S, U17-ch90, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 57.49
Std Dev 9.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

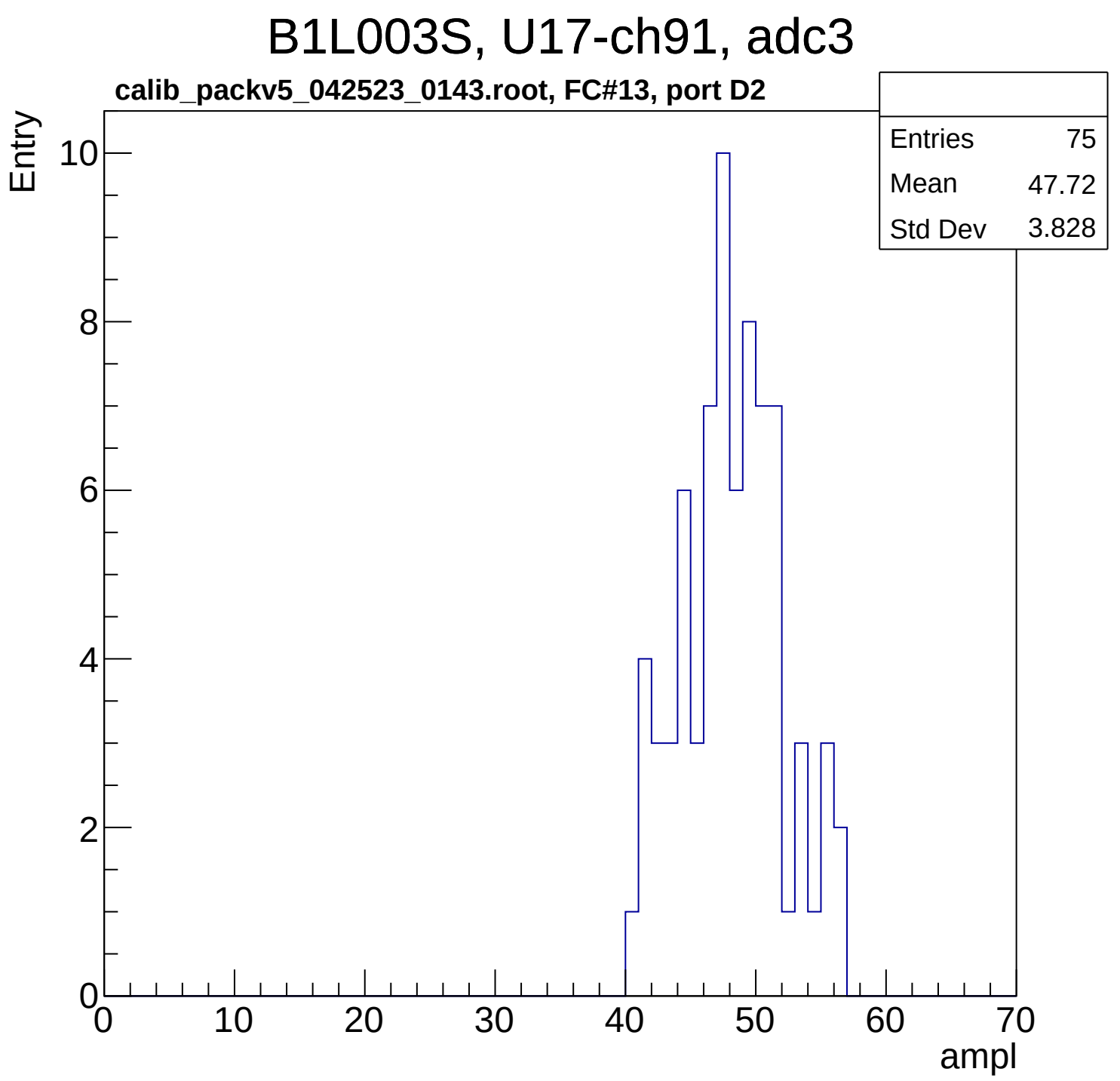

B1L003S, U17-ch90, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 62.07
Std Dev 1.181
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

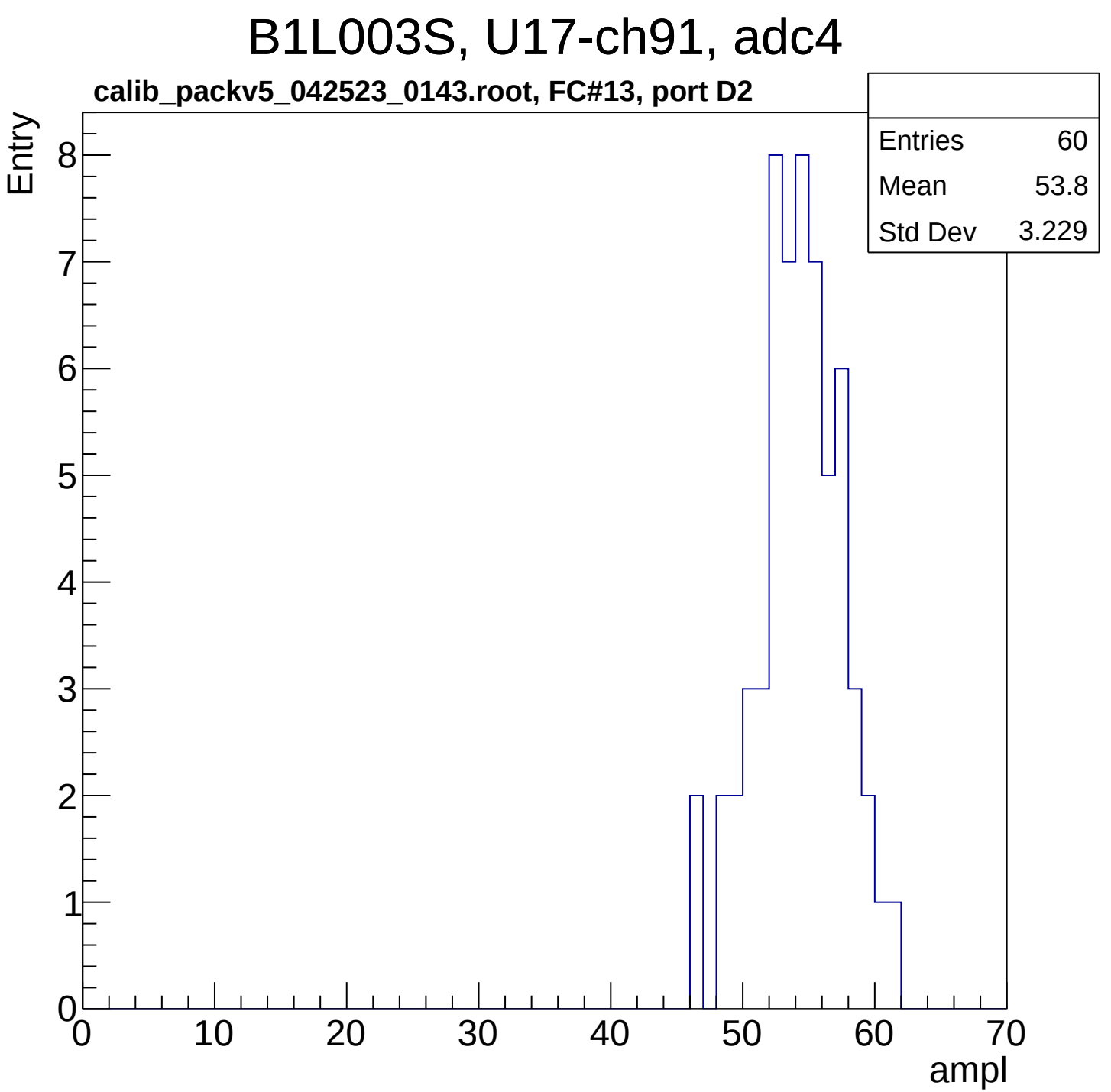

B1L003S, U17-ch90, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

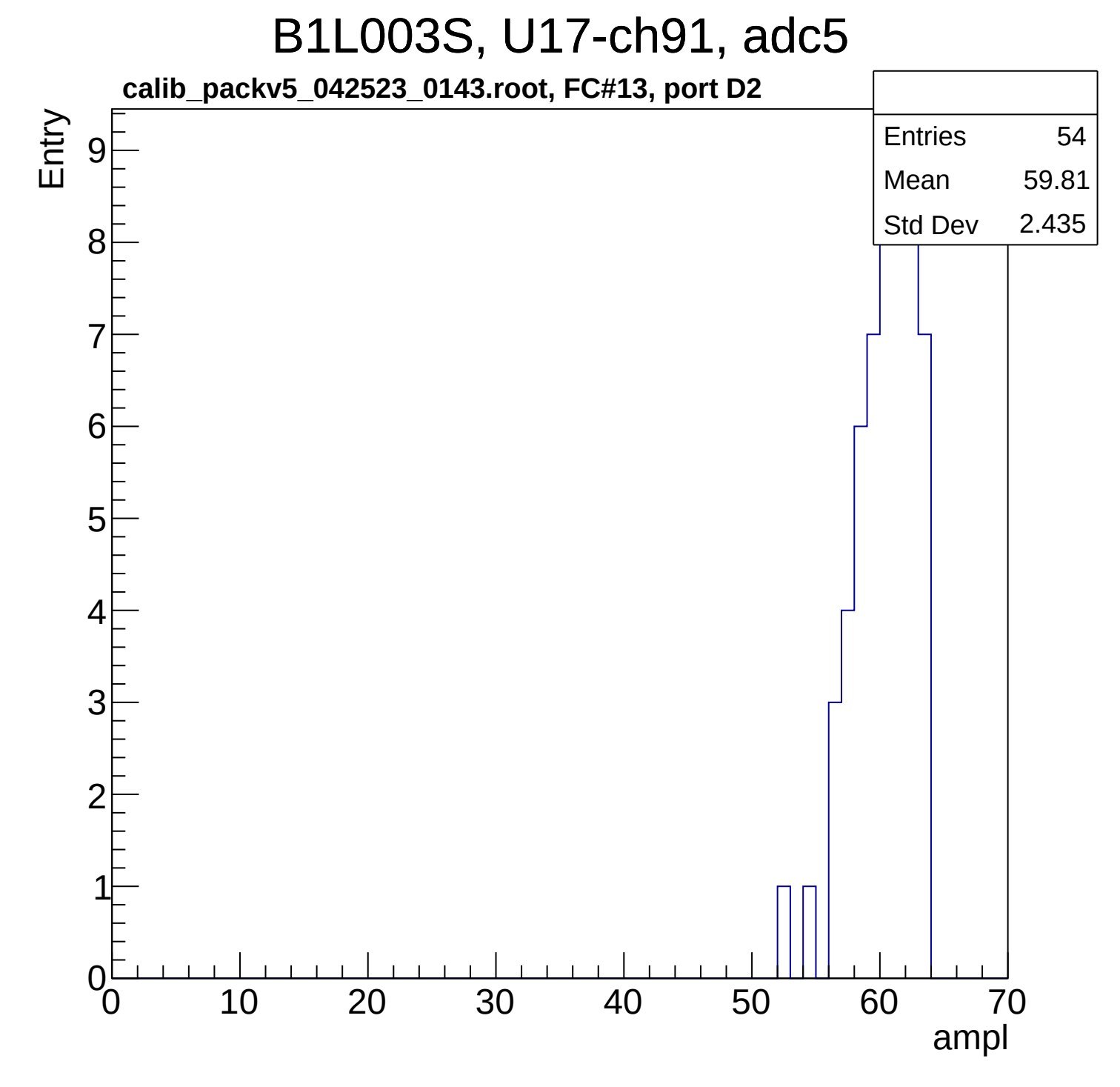

B1L003S, U17-ch91, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.05
Std Dev 5.145
Gaus mean : 30.2158
Gaus Width: 4.2132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch91, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 35.76
Std Dev 3.311
Gaus mean : 35.8966
Gaus Width: 4.0531
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch91, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 41.25
Std Dev 3.253
Gaus mean : 41.5910
Gaus Width: 3.2071
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch91, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 47.72
Std Dev 3.828
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch91, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 53.8
Std Dev 3.229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch91, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.81
Std Dev 2.435
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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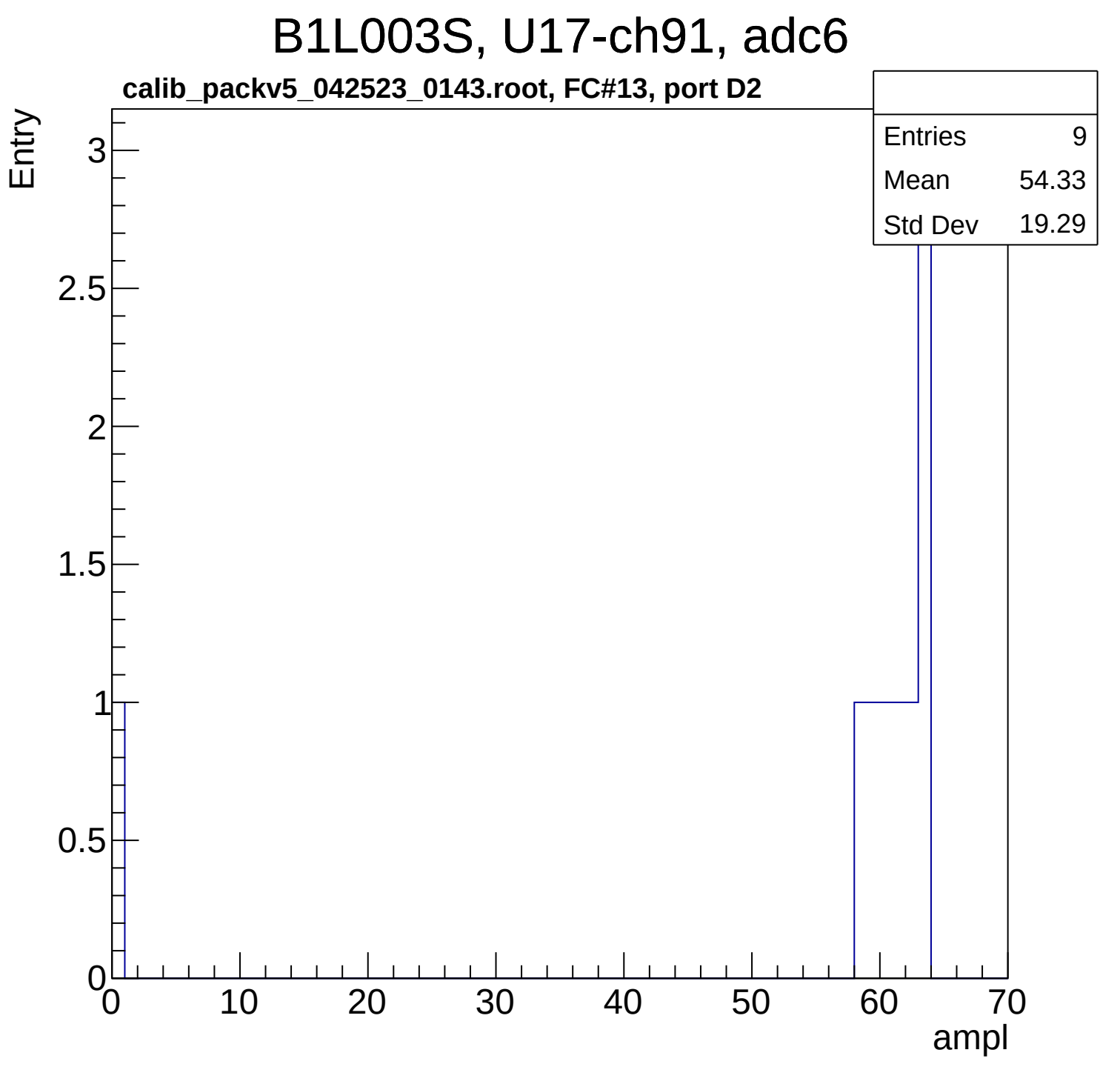
B1L003S, U17-ch91, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 54.33
Std Dev 19.29
Entry
ampl
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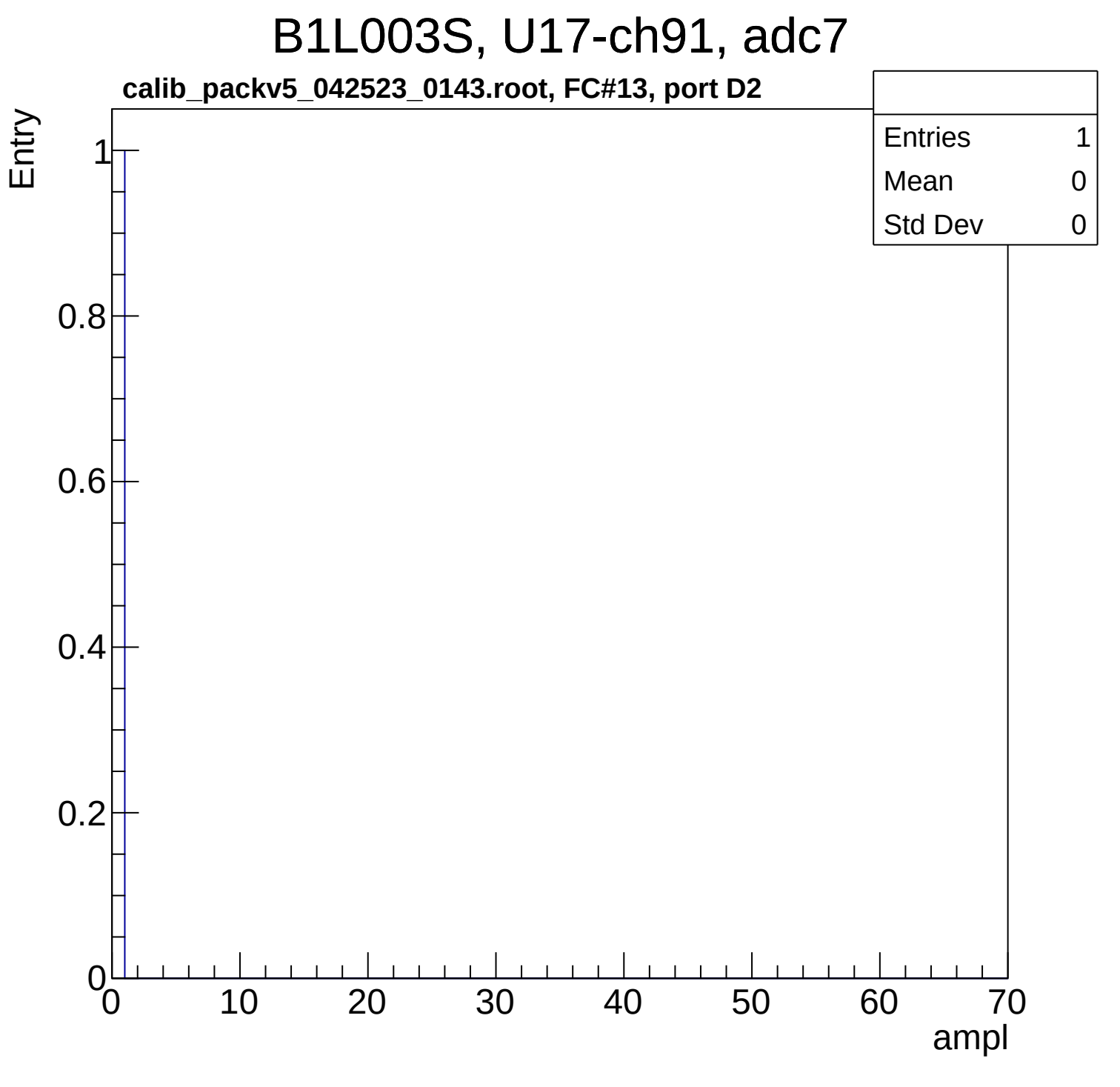
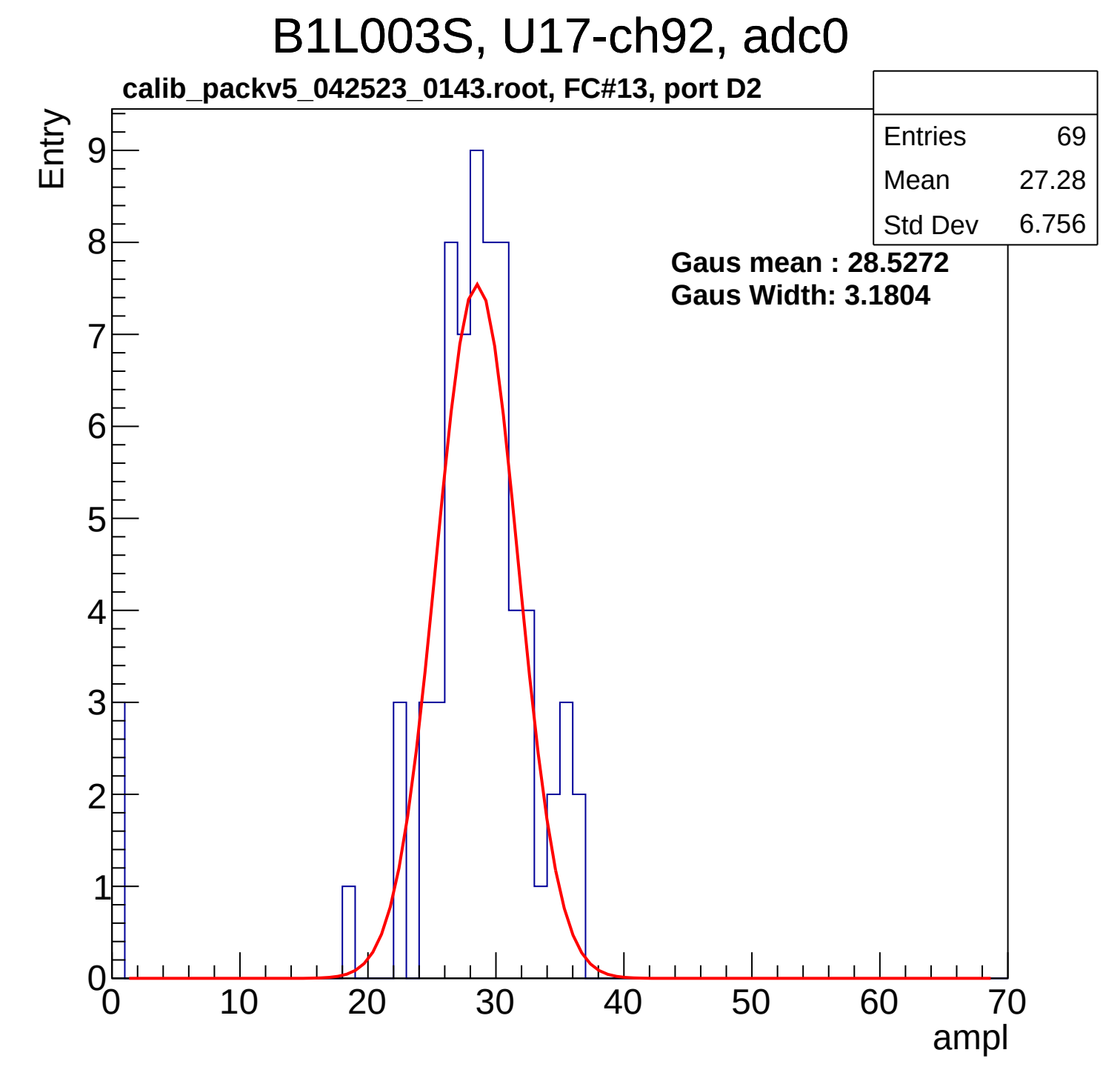
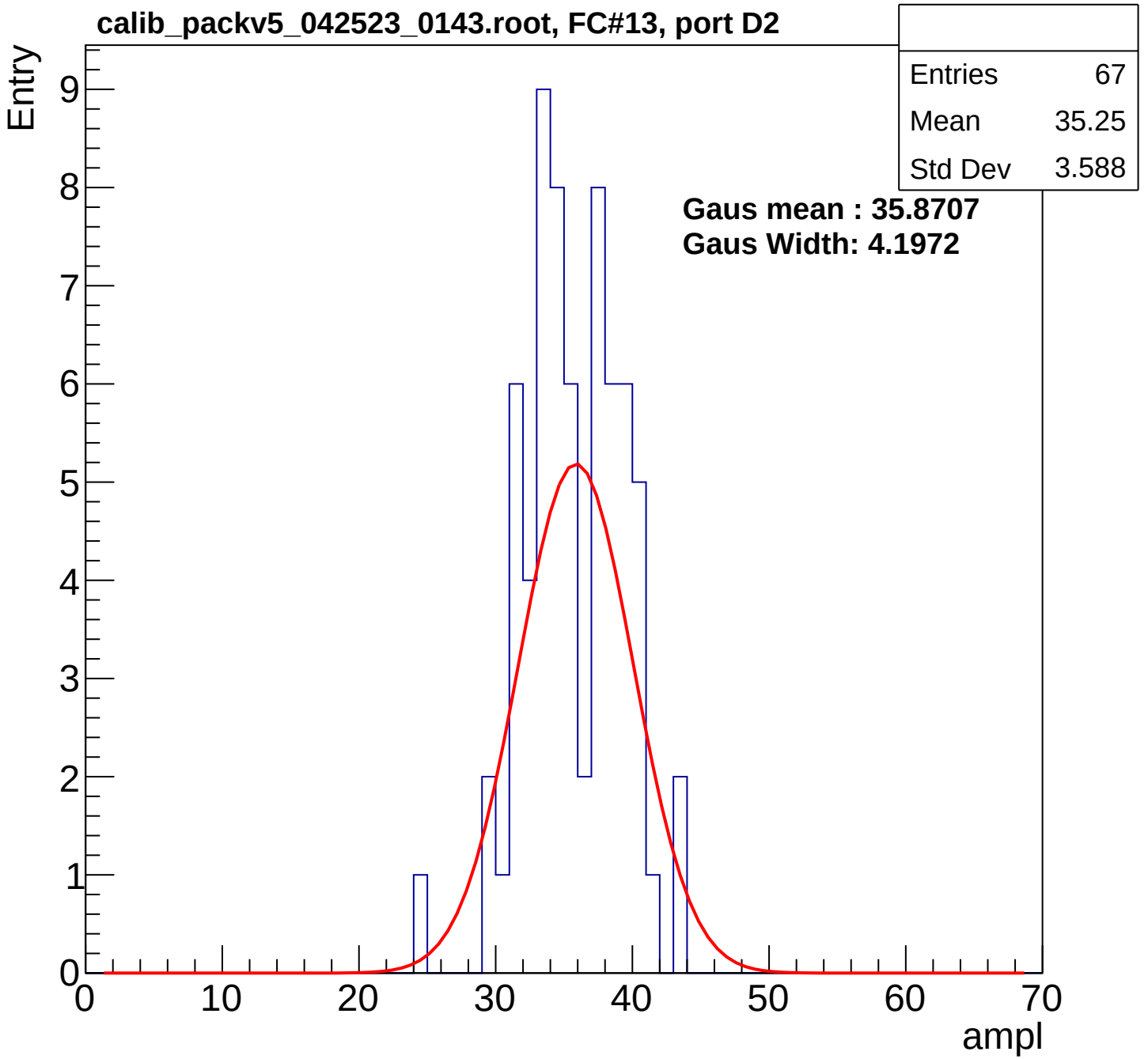
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch91, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch92, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 27.28
Std Dev 6.756
Gaus mean : 28.5272
Gaus Width: 3.1804
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch92, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 35.25
Std Dev 3.588
Gaus mean : 35.8707
Gaus Width: 4.1972
Entry
ampl
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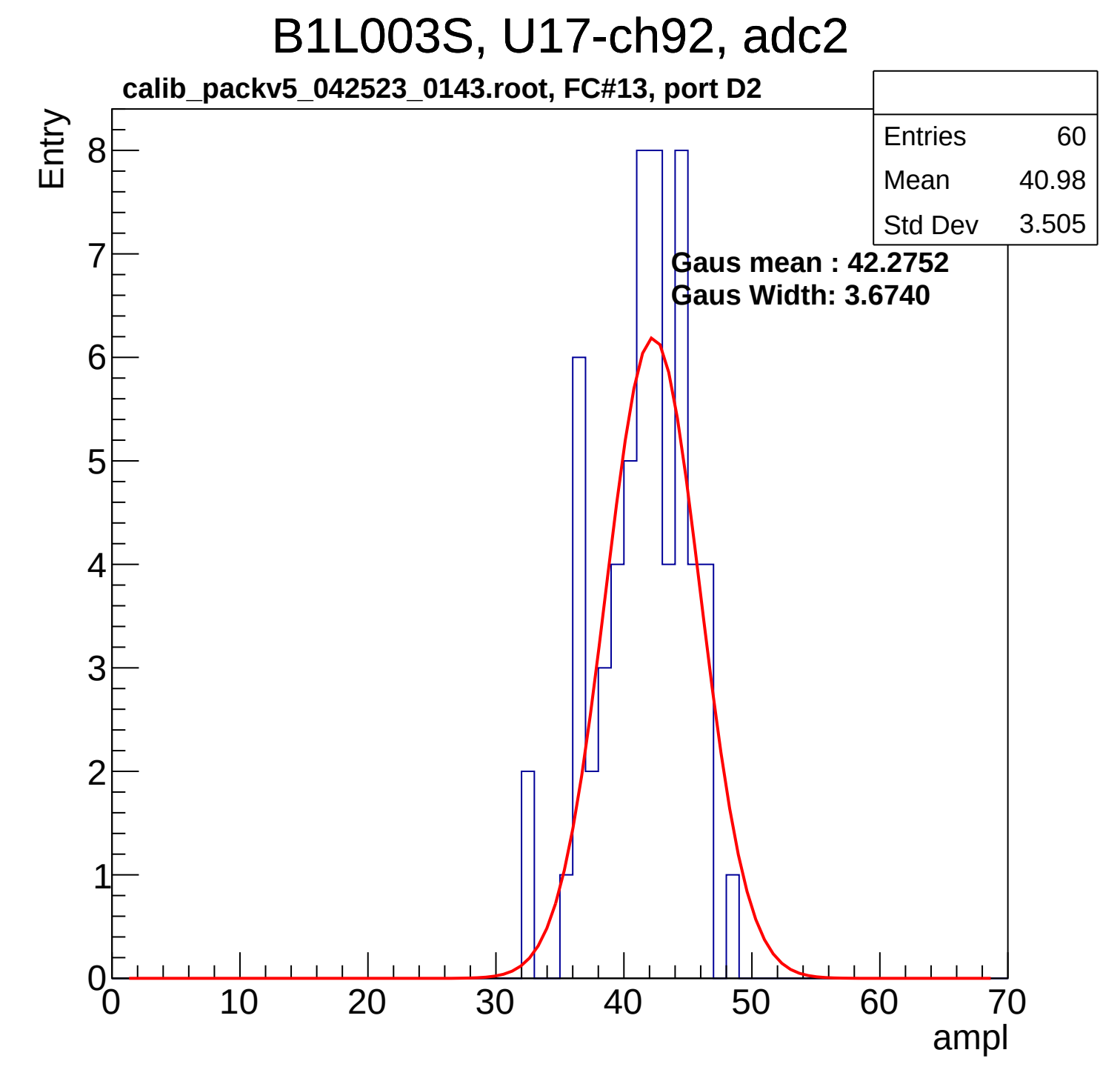
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch92, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 40.98
Std Dev 3.505
Gaus mean : 42.2752
Gaus Width: 3.6740
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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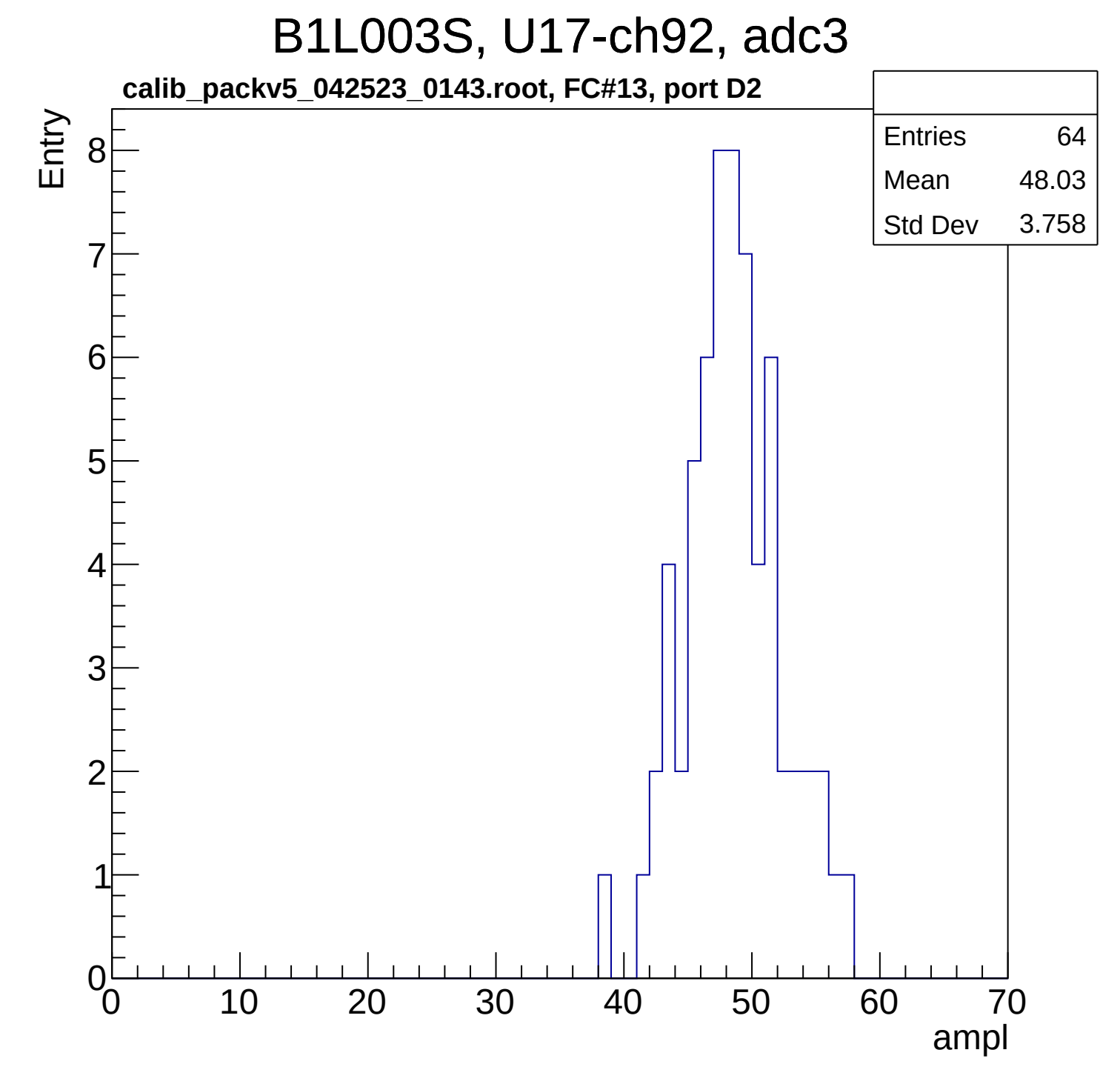
B1L003S, U17-ch92, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 48.03
Std Dev 3.758
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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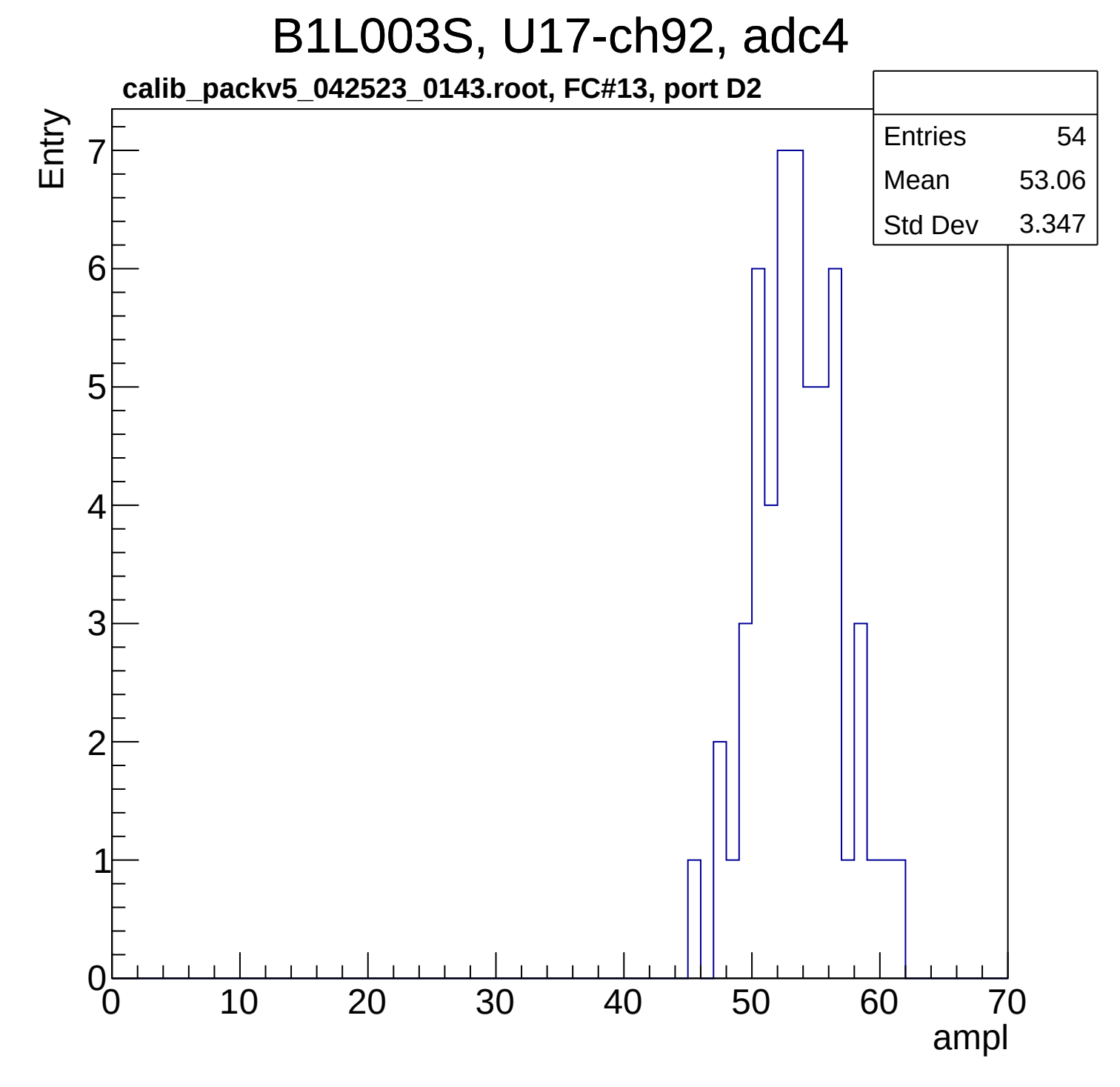
B1L003S, U17-ch92, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.06
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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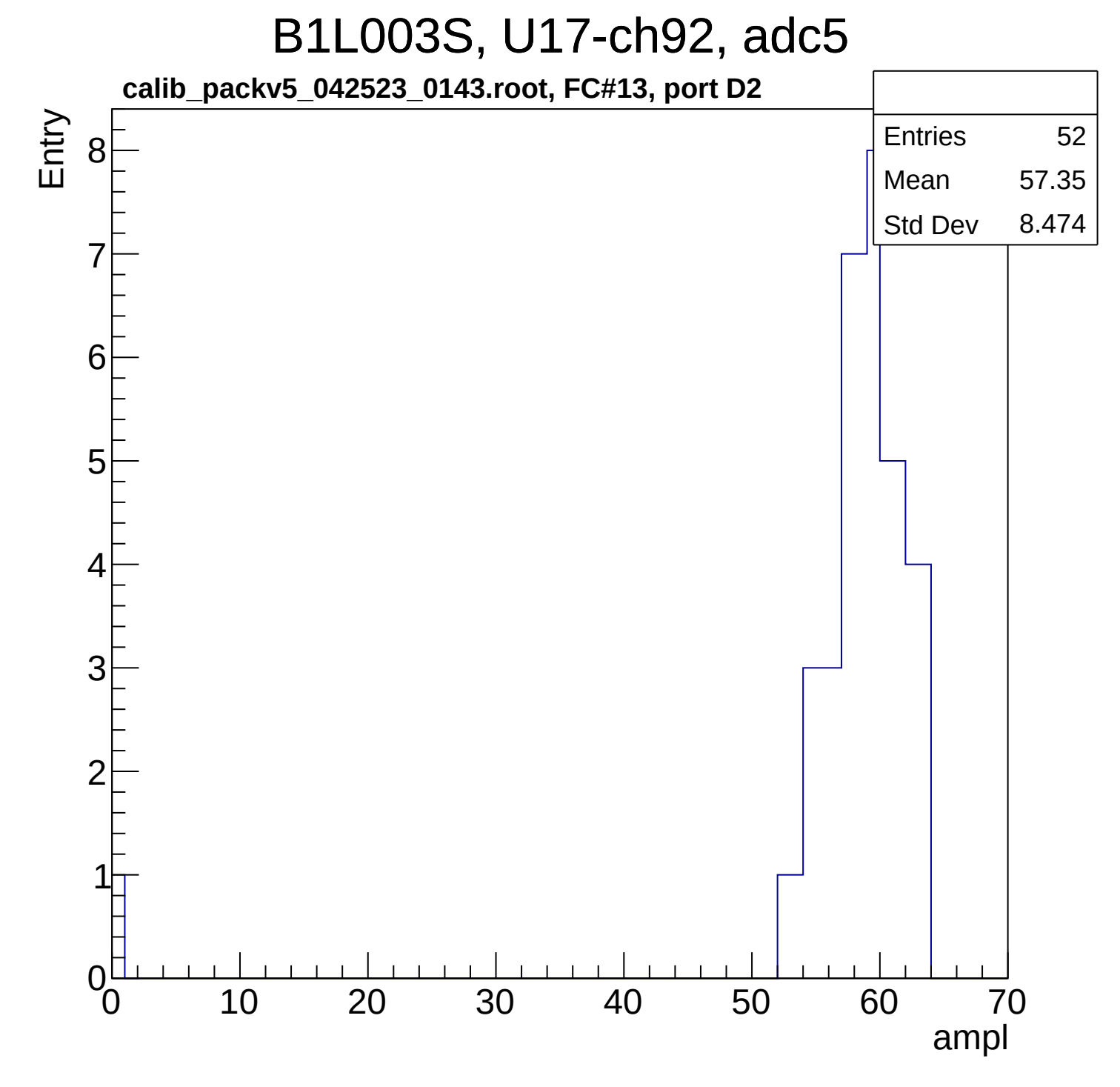
B1L003S, U17-ch92, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 57.35
Std Dev 8.474
Entry
ampl
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1
2
3
4
5
6
7
8
0
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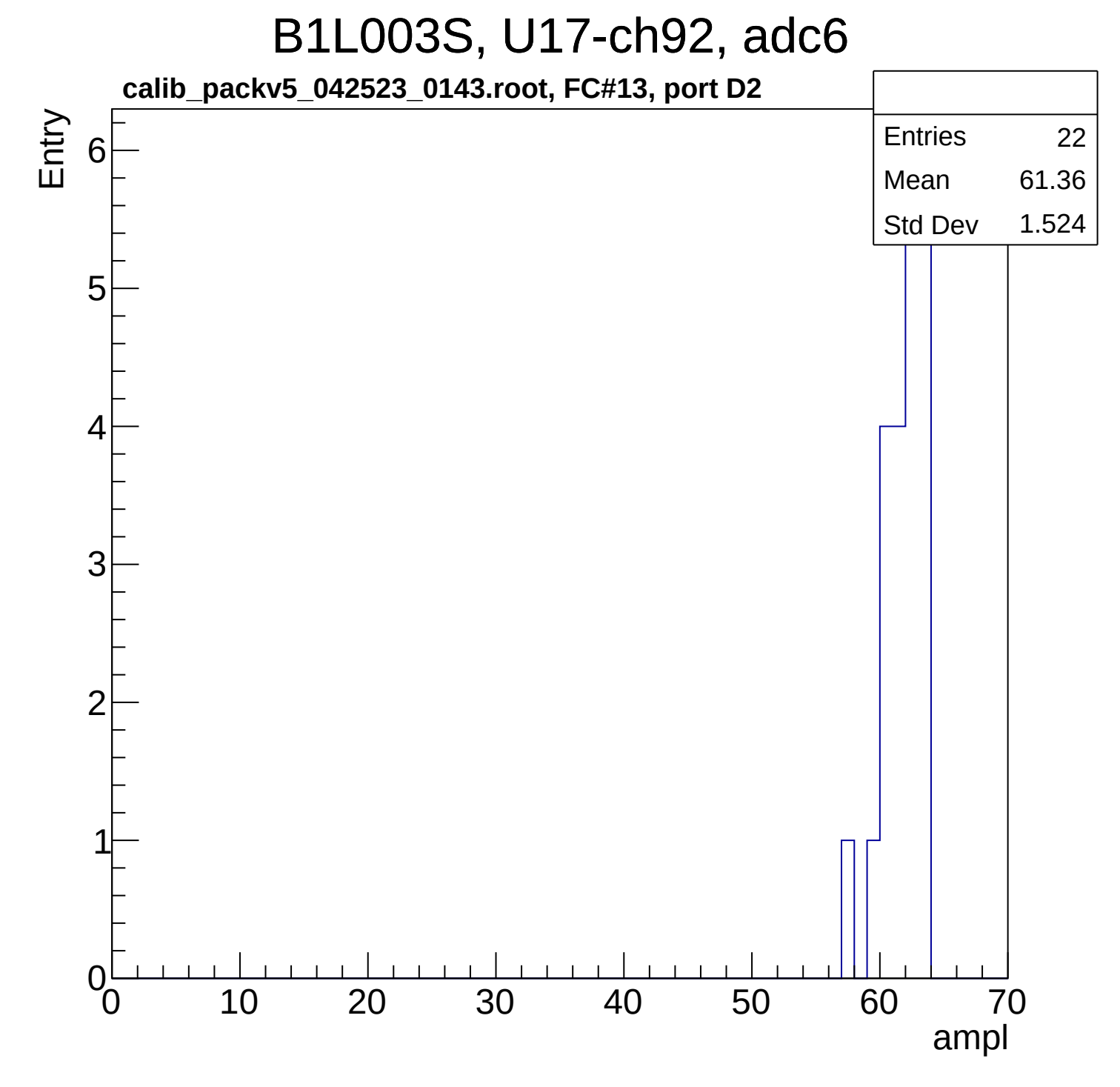
B1L003S, U17-ch92, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 61.36
Std Dev 1.524
Entry
ampl
0
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3
4
5
6
0
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60
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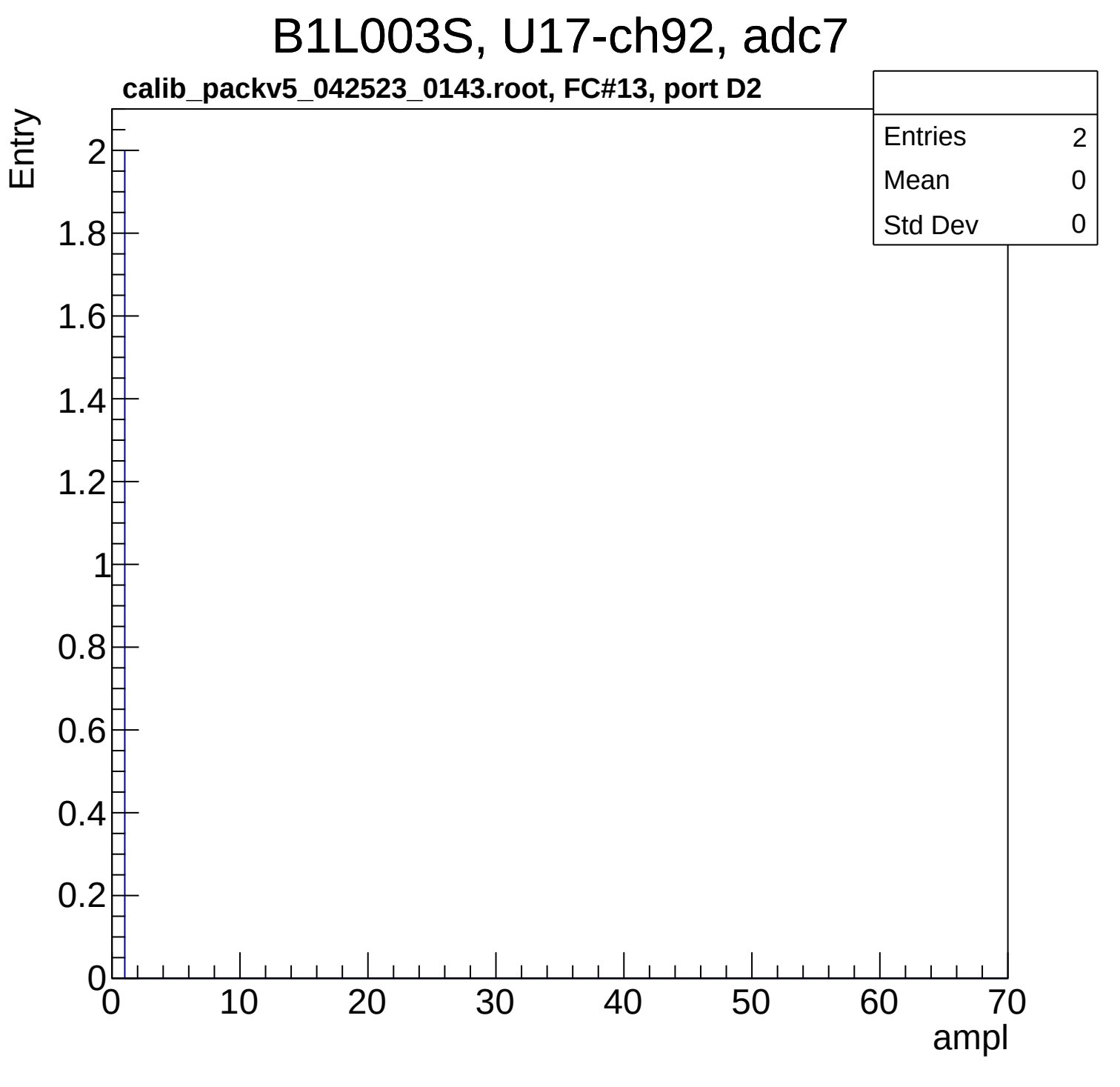
B1L003S, U17-ch92, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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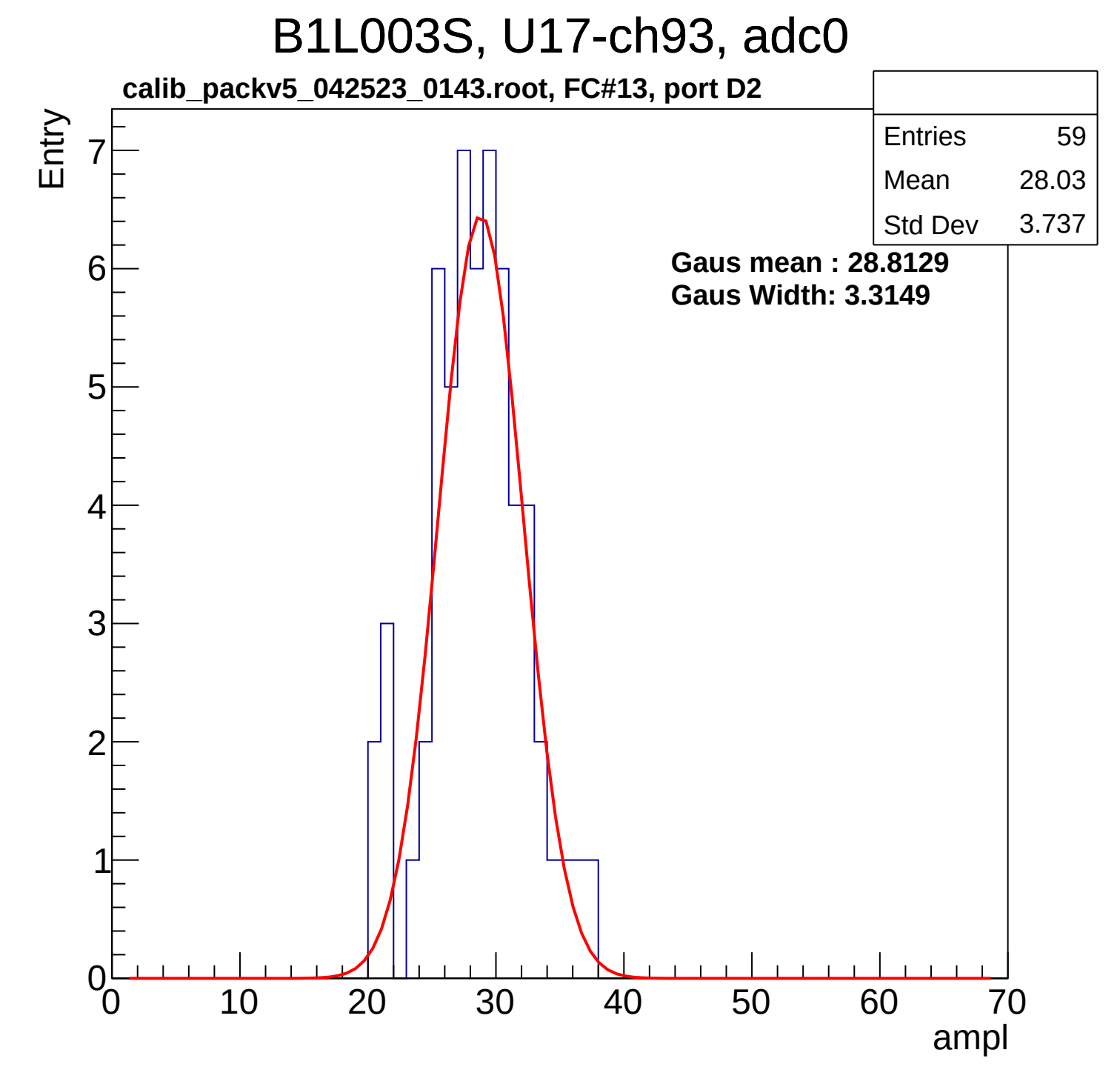
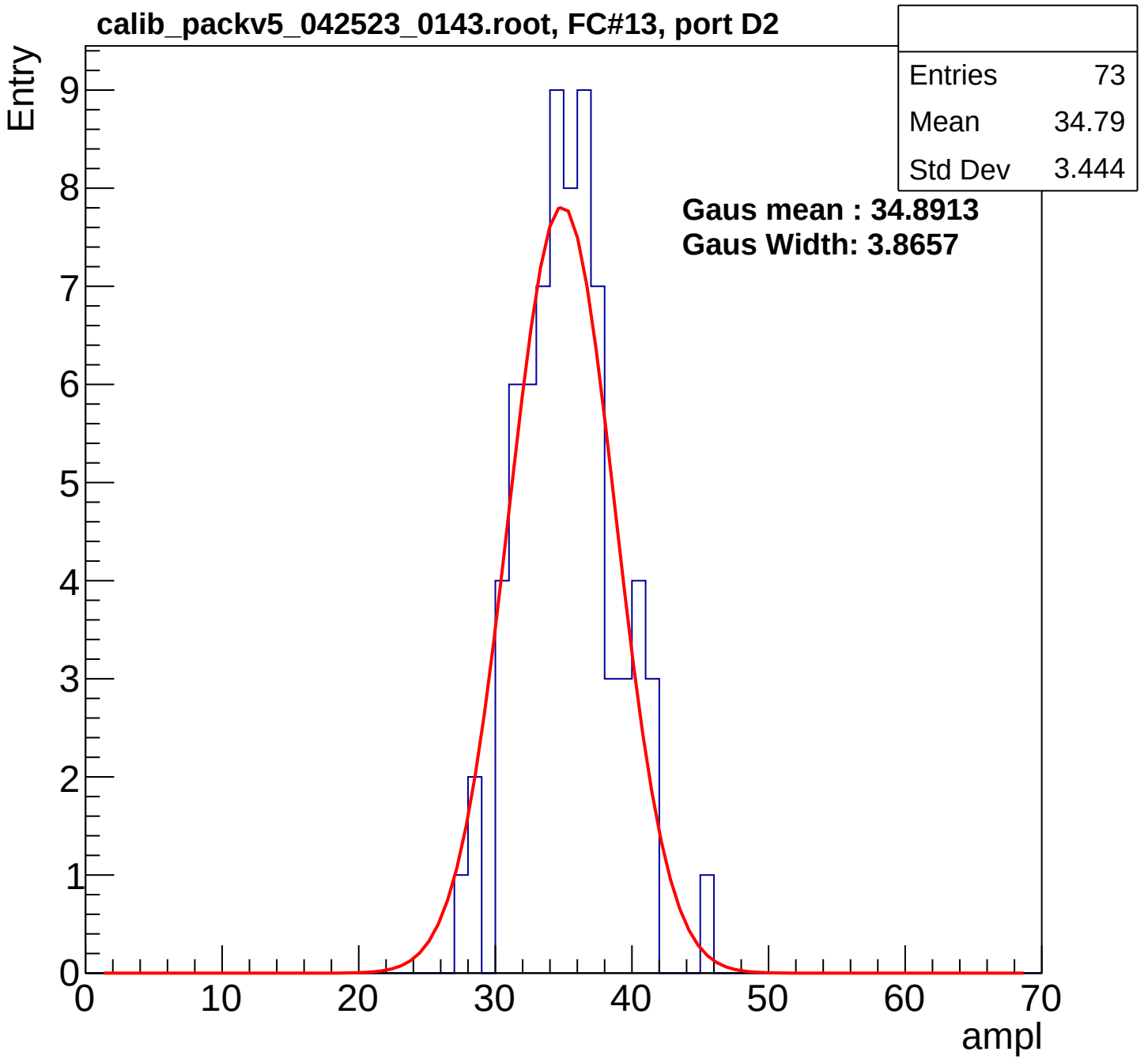
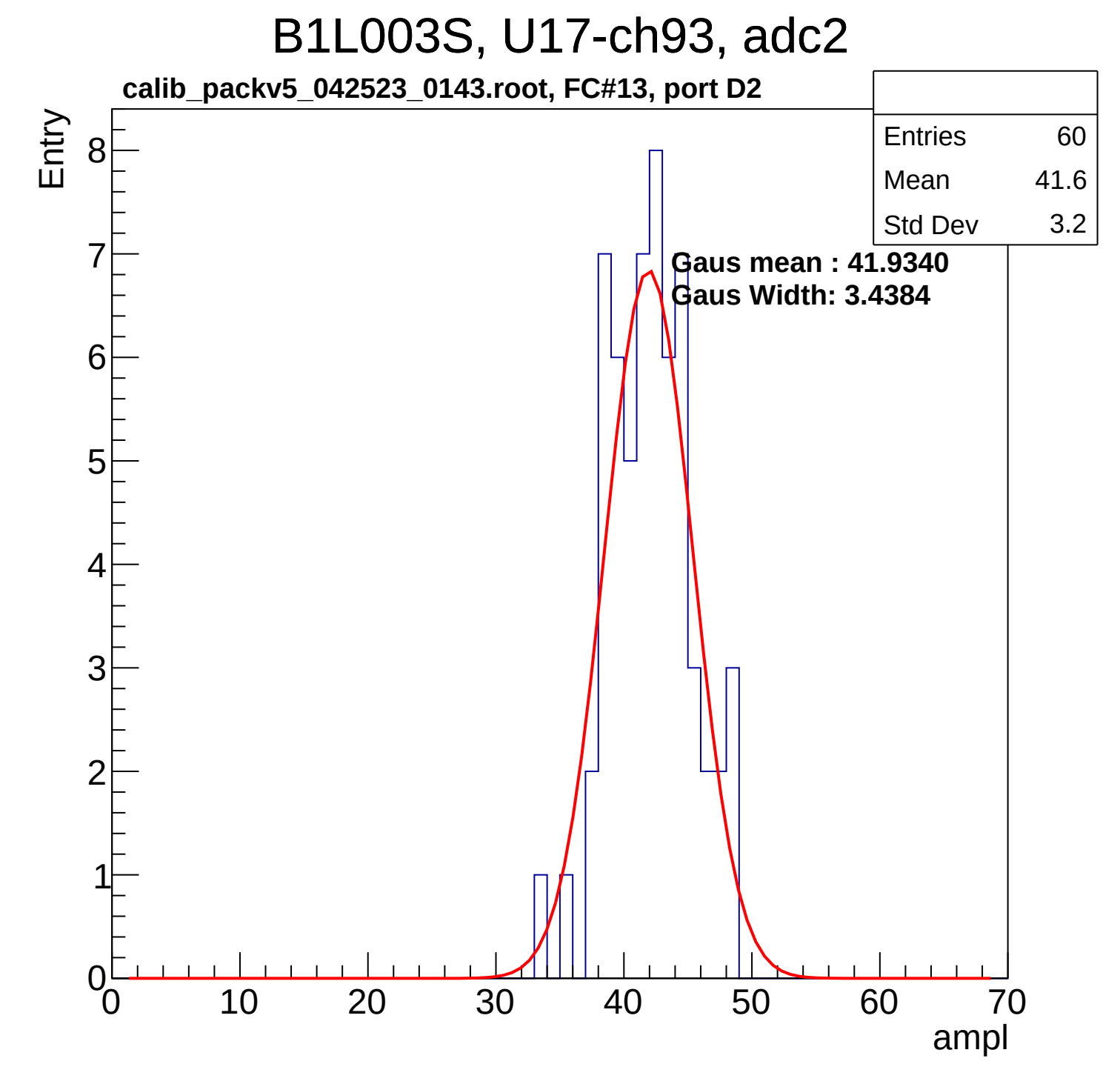

B1L003S, U17-ch93, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.03
Std Dev 3.737
Gaus mean : 28.8129
Gaus Width: 3.3149
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch93, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 34.79
Std Dev 3.444
Gaus mean : 34.8913
Gaus Width: 3.8657
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch93, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.6
Std Dev 3.2
Gaus mean : 41.9340
Gaus Width: 3.4384
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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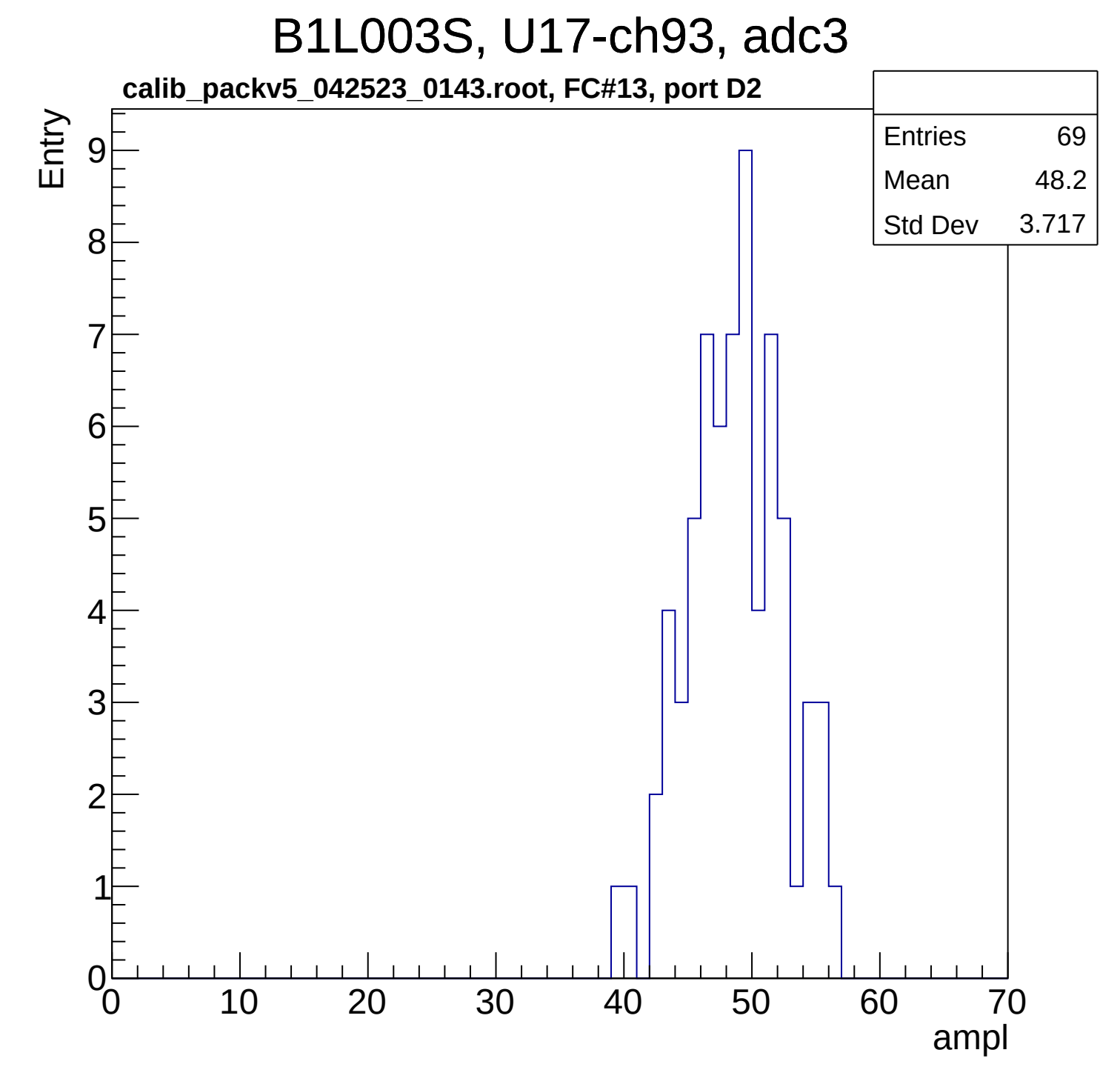
B1L003S, U17-ch93, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.2
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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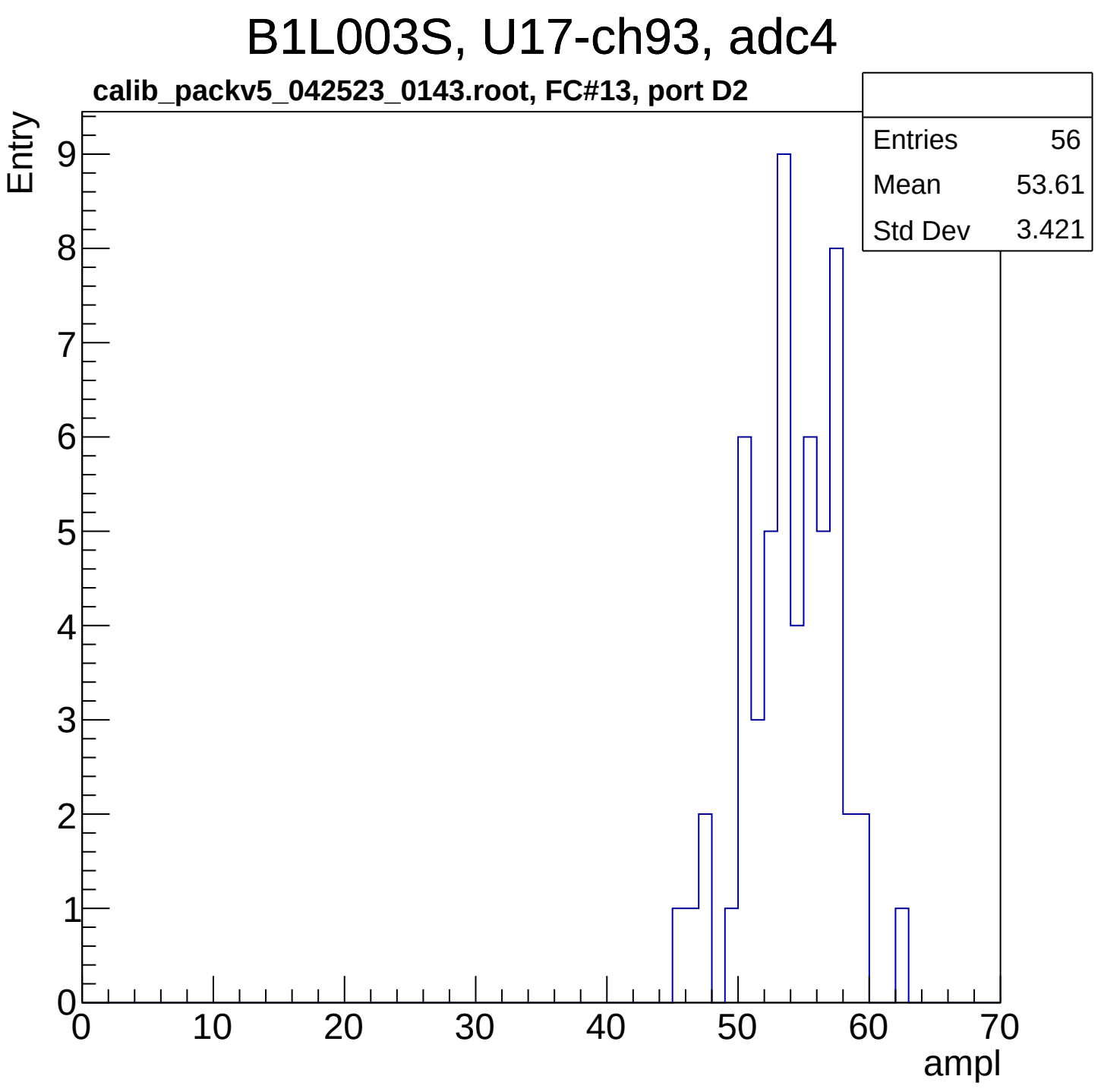
B1L003S, U17-ch93, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 53.61
Std Dev 3.421
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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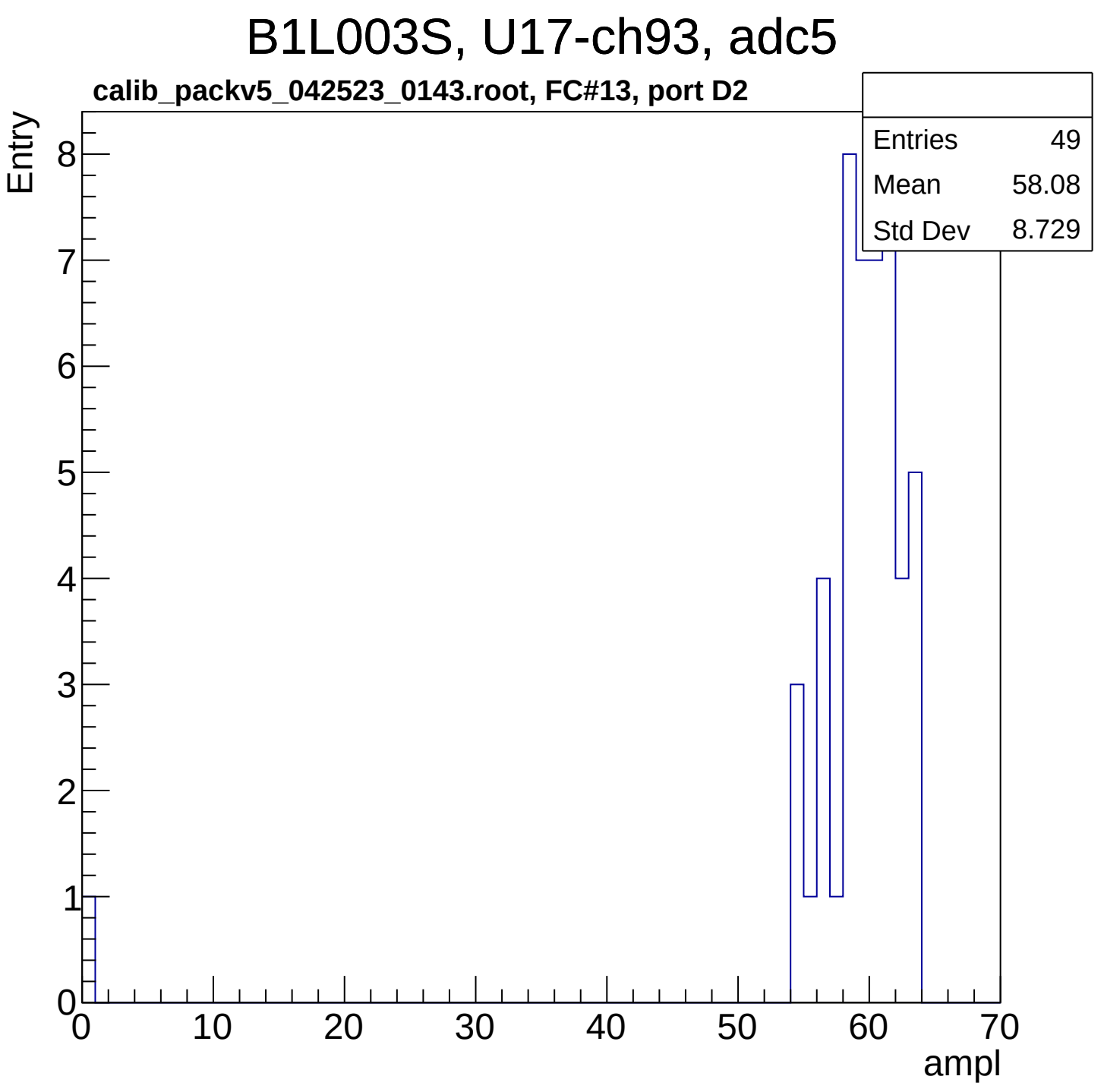
B1L003S, U17-ch93, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.08
Std Dev 8.729
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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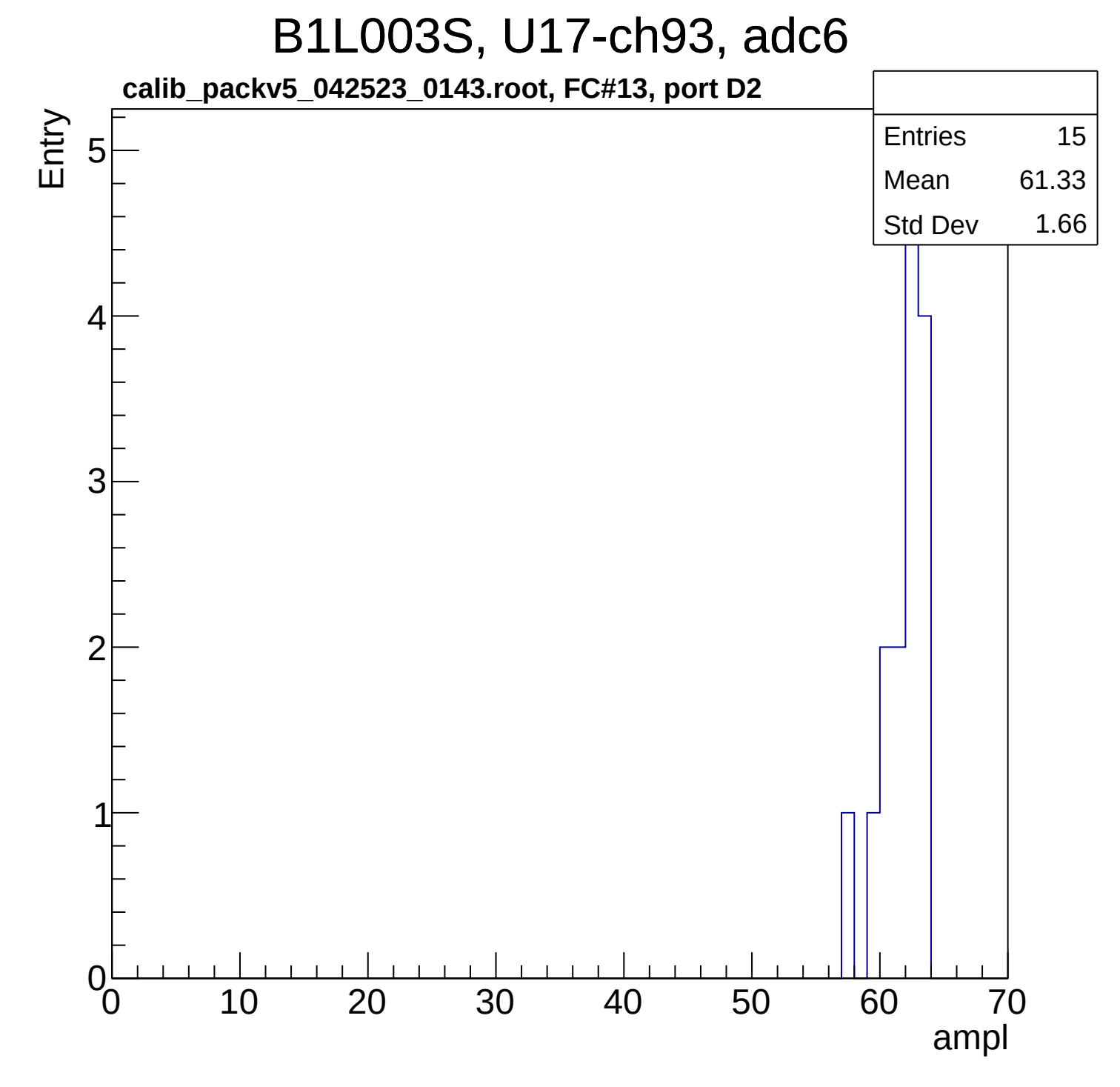
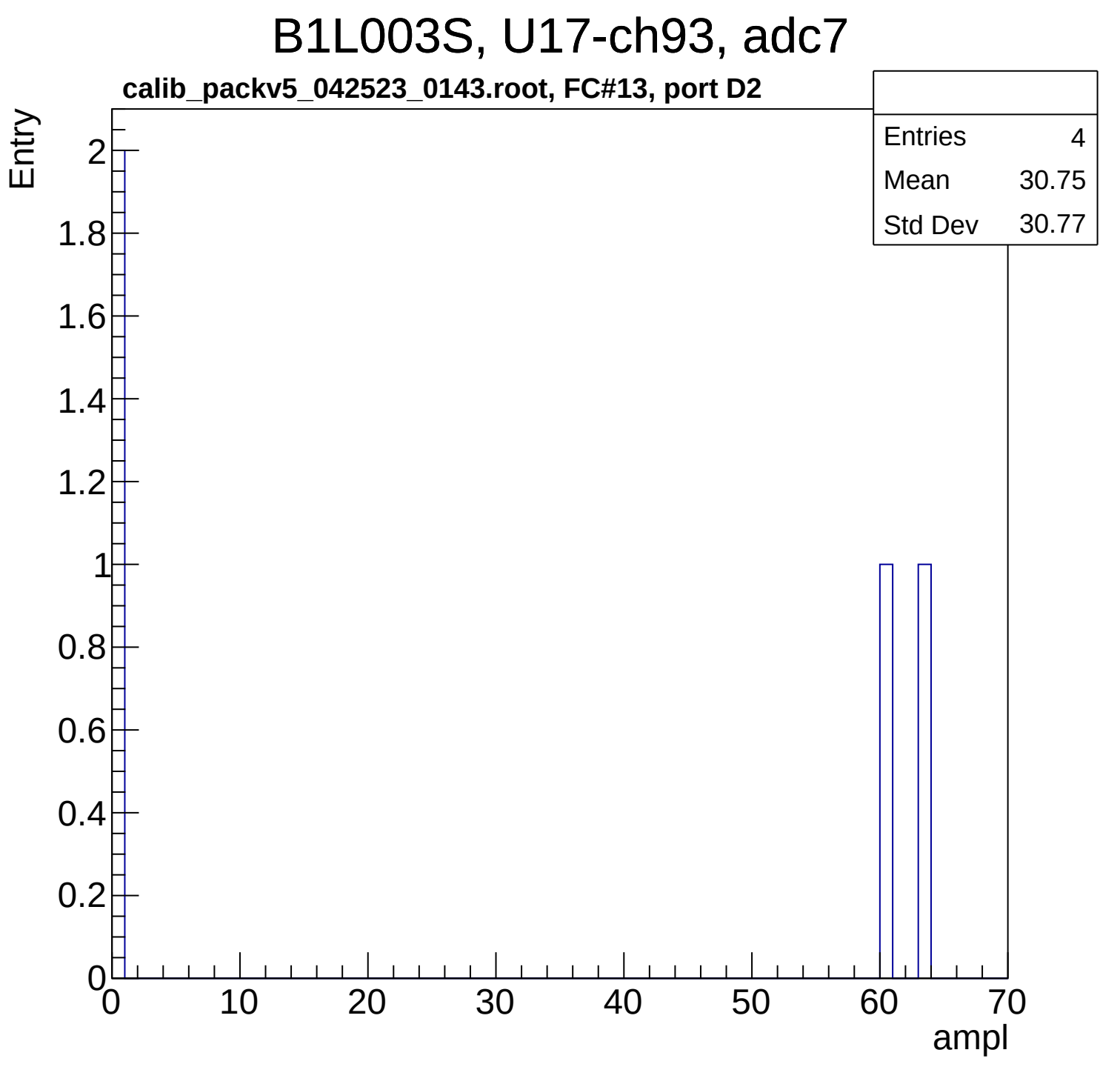

B1L003S, U17-ch93, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.33
Std Dev 1.66
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch93, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 30.75
Std Dev 30.77
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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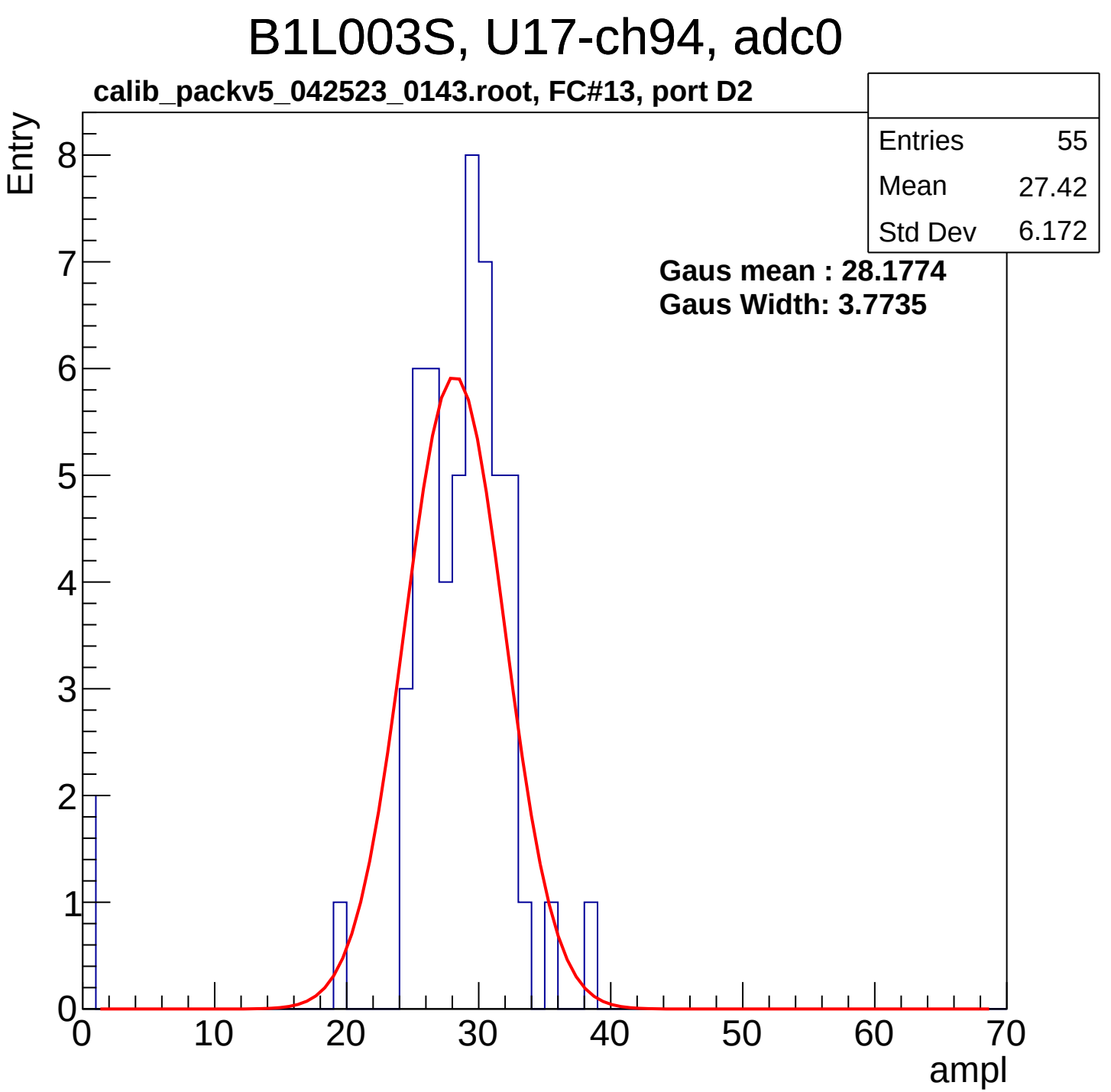
B1L003S, U17-ch94, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 27.42
Std Dev 6.172
Gaus mean : 28.1774
Gaus Width: 3.7735
Entry
ampl
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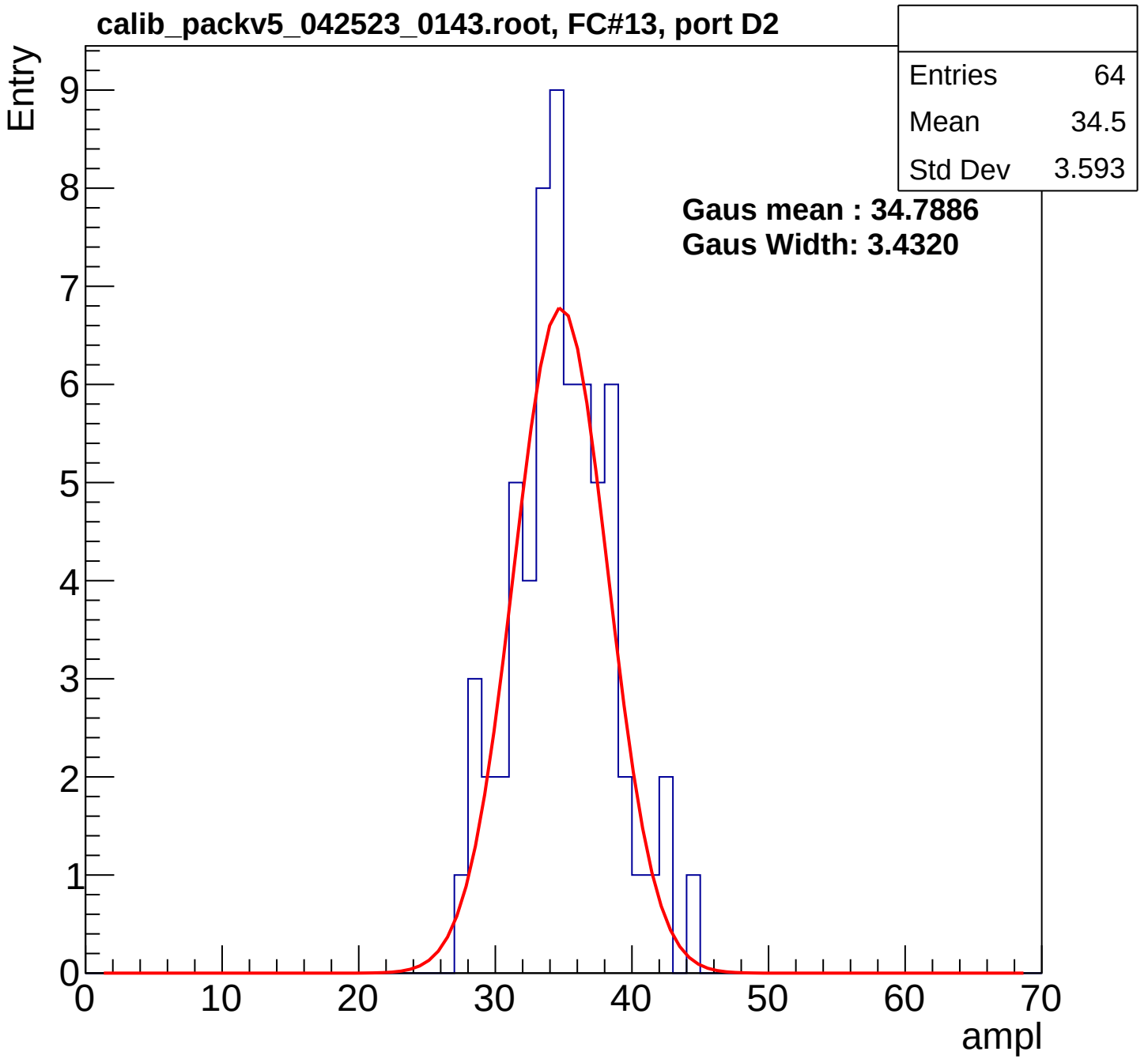
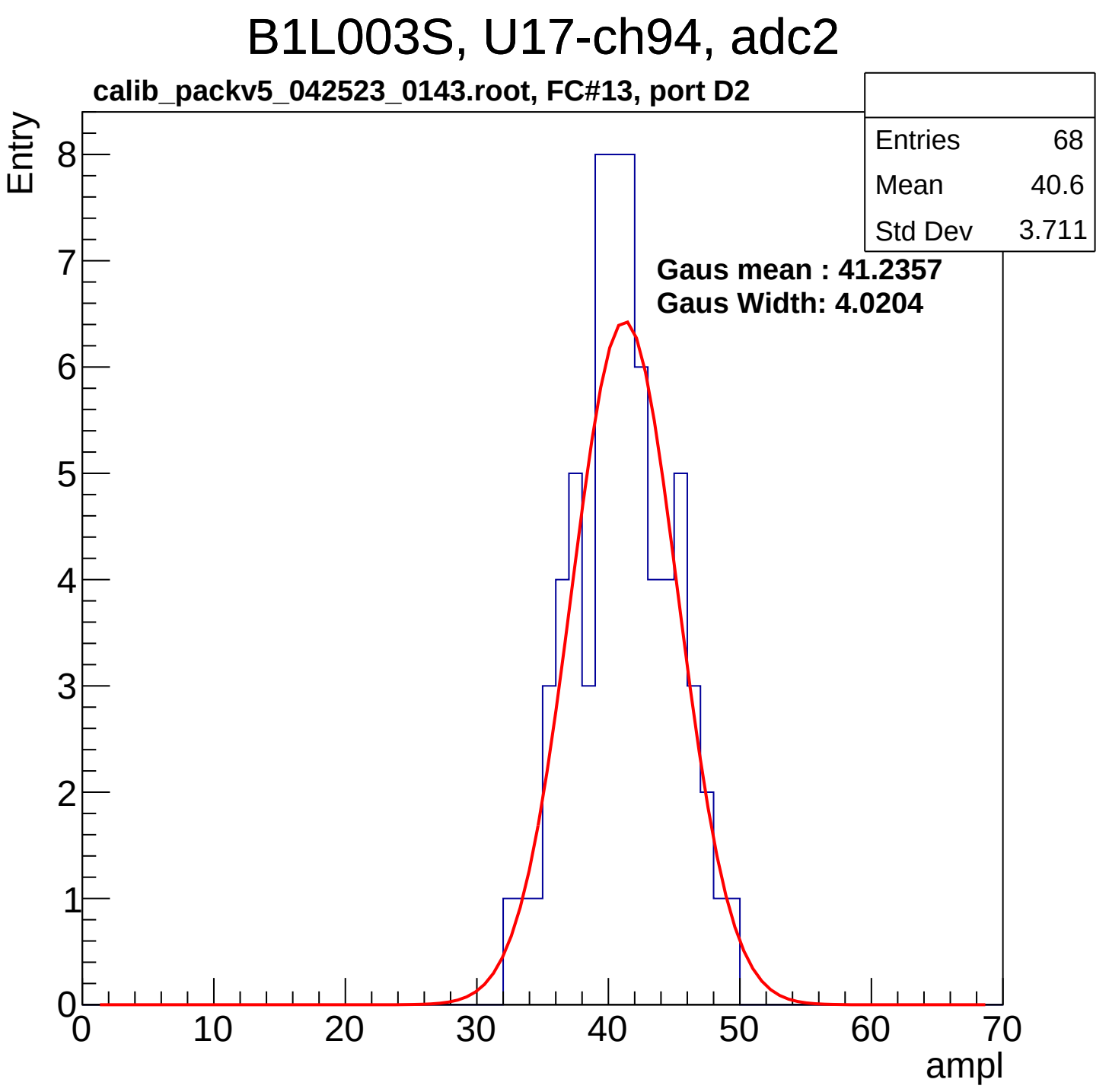
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch94, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 34.5
Std Dev 3.593
Gaus mean : 34.7886
Gaus Width: 3.4320
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch94, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 40.6
Std Dev 3.711
Gaus mean : 41.2357
Gaus Width: 4.0204
Entry
ampl
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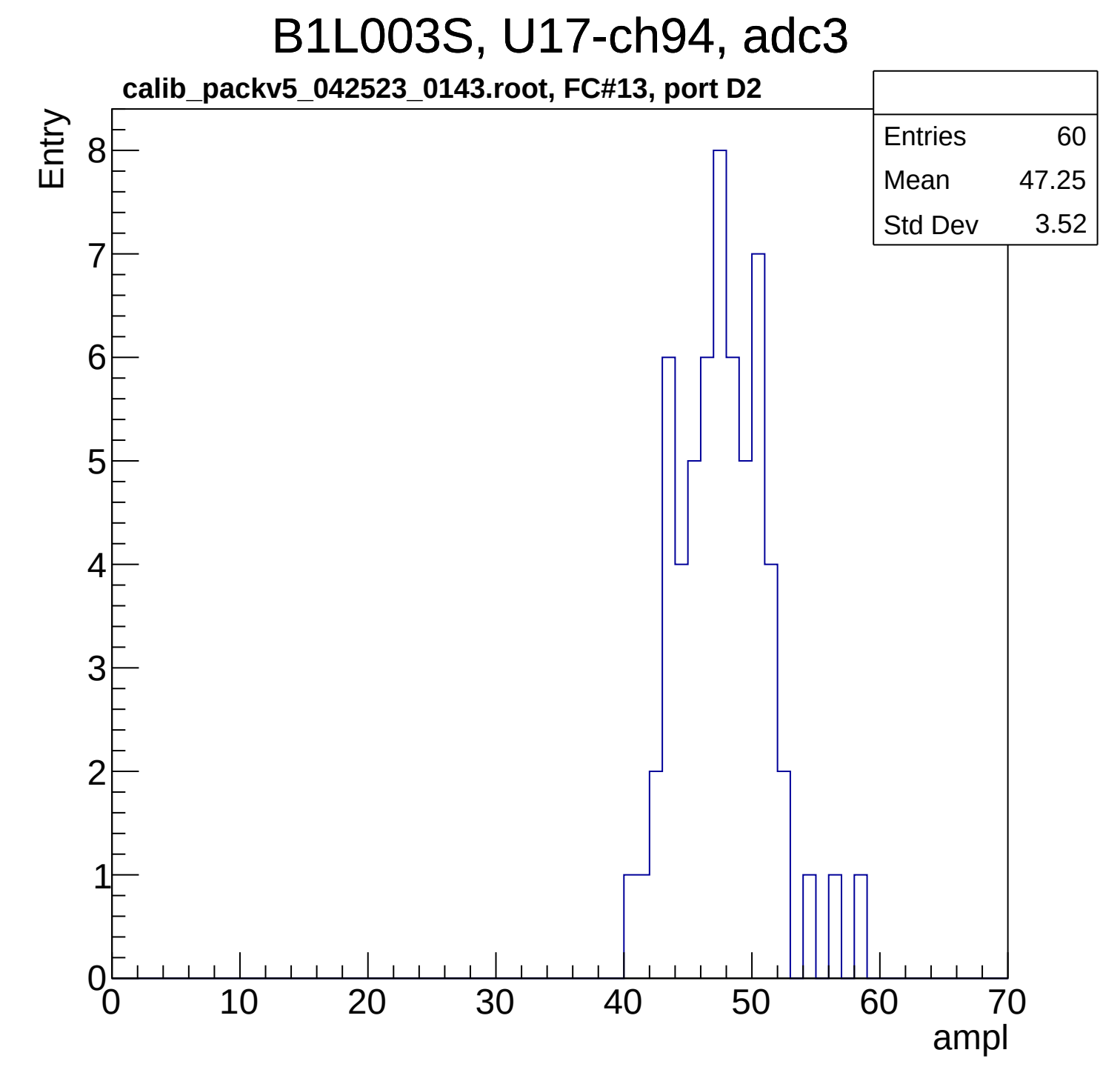
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch94, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 47.25
Std Dev 3.52
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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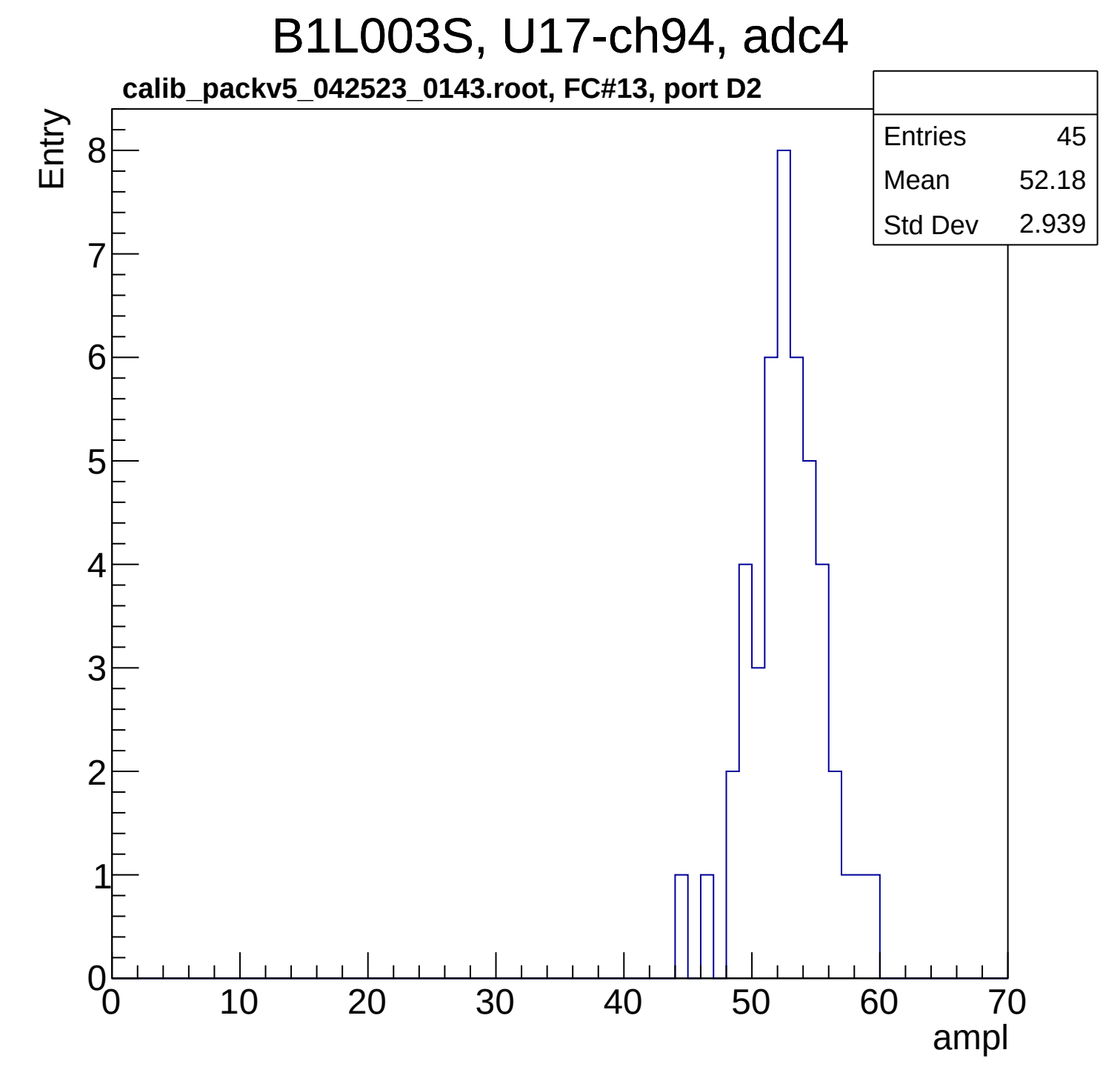
B1L003S, U17-ch94, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 52.18
Std Dev 2.939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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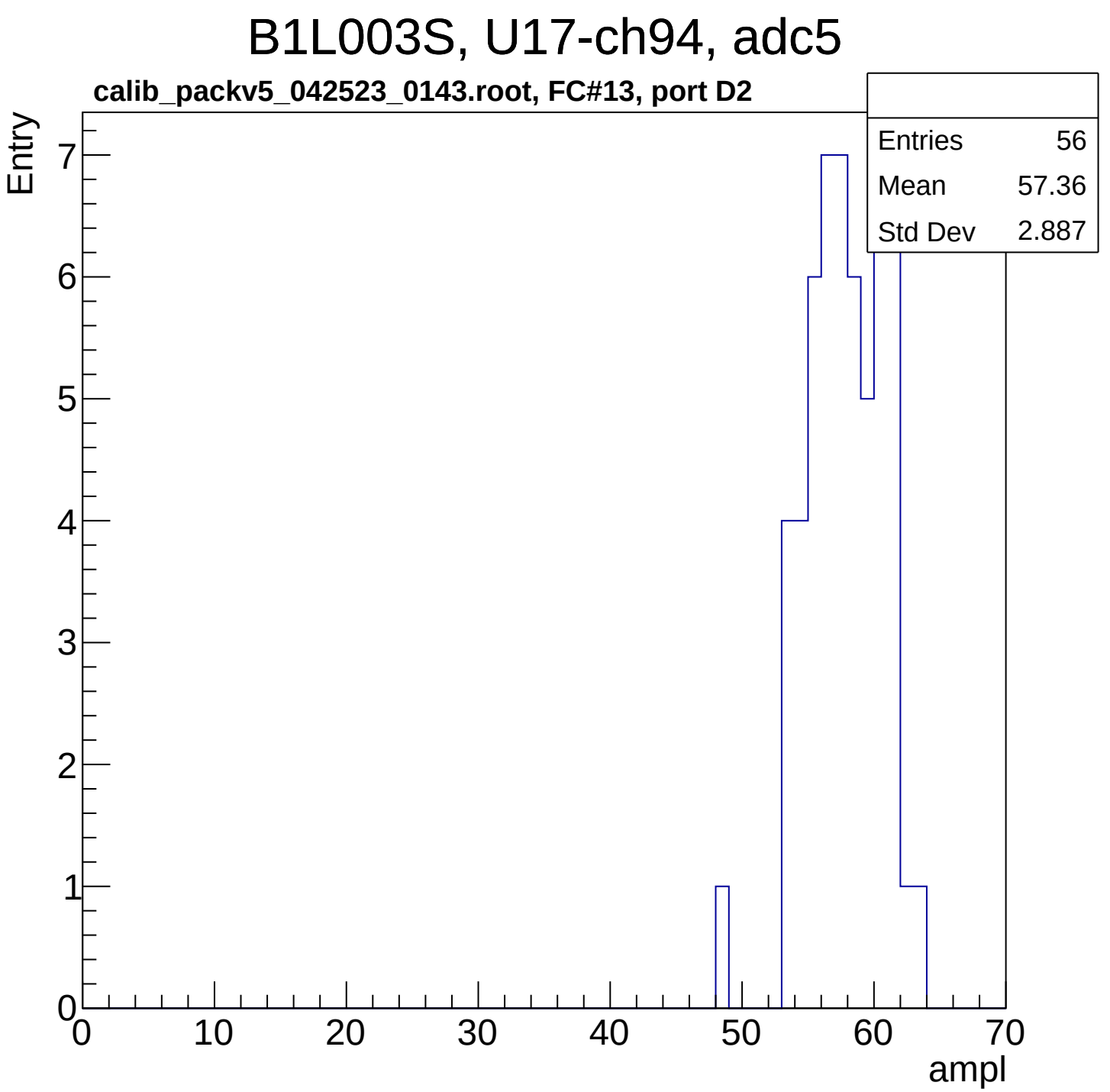
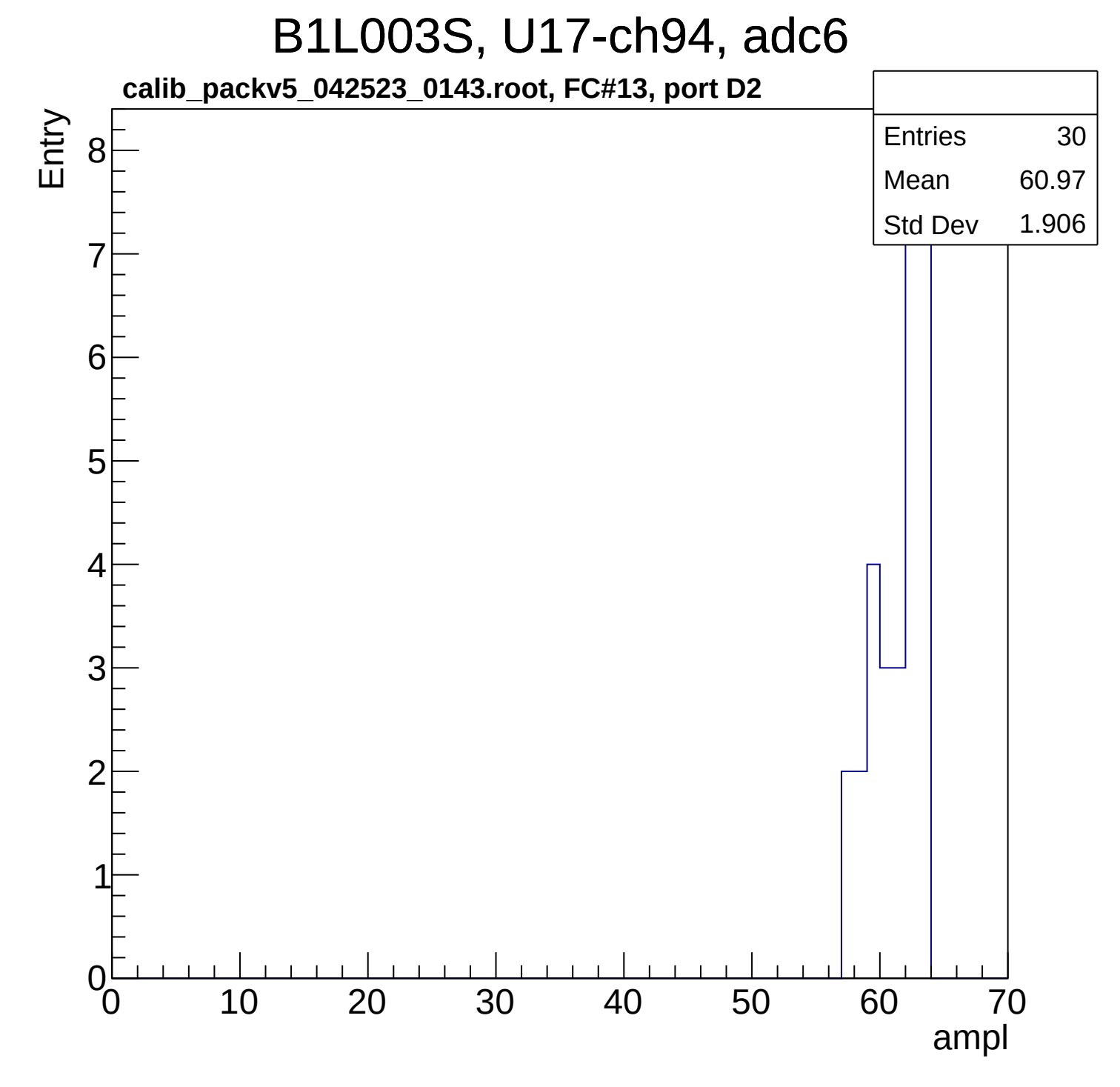

B1L003S, U17-ch94, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 57.36
Std Dev 2.887
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch94, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 60.97
Std Dev 1.906
Entry
ampl
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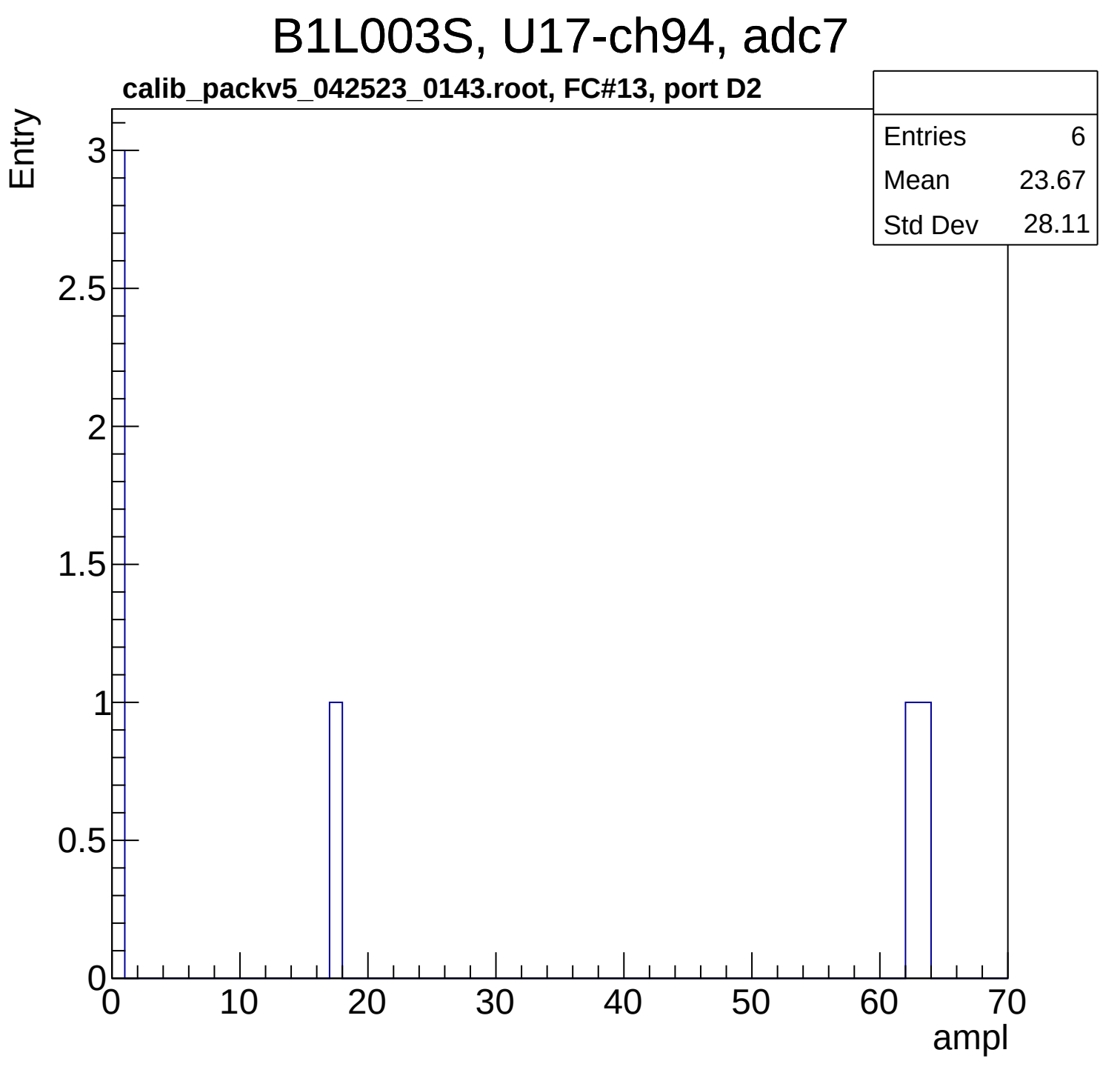
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch94, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 23.67
Std Dev 28.11
Entry
ampl
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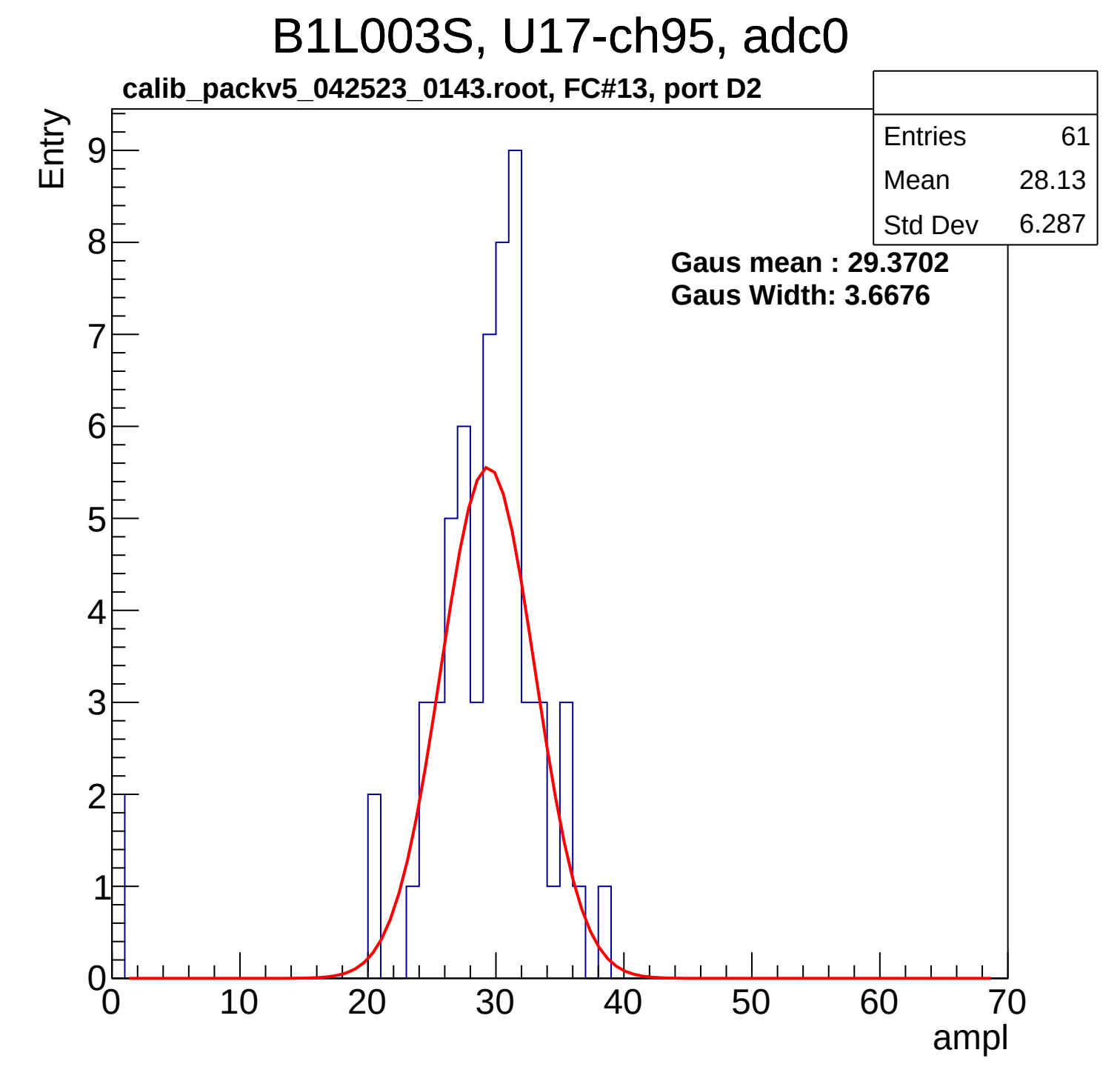
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch95, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 28.13
Std Dev 6.287
Gaus mean : 29.3702
Gaus Width: 3.6676
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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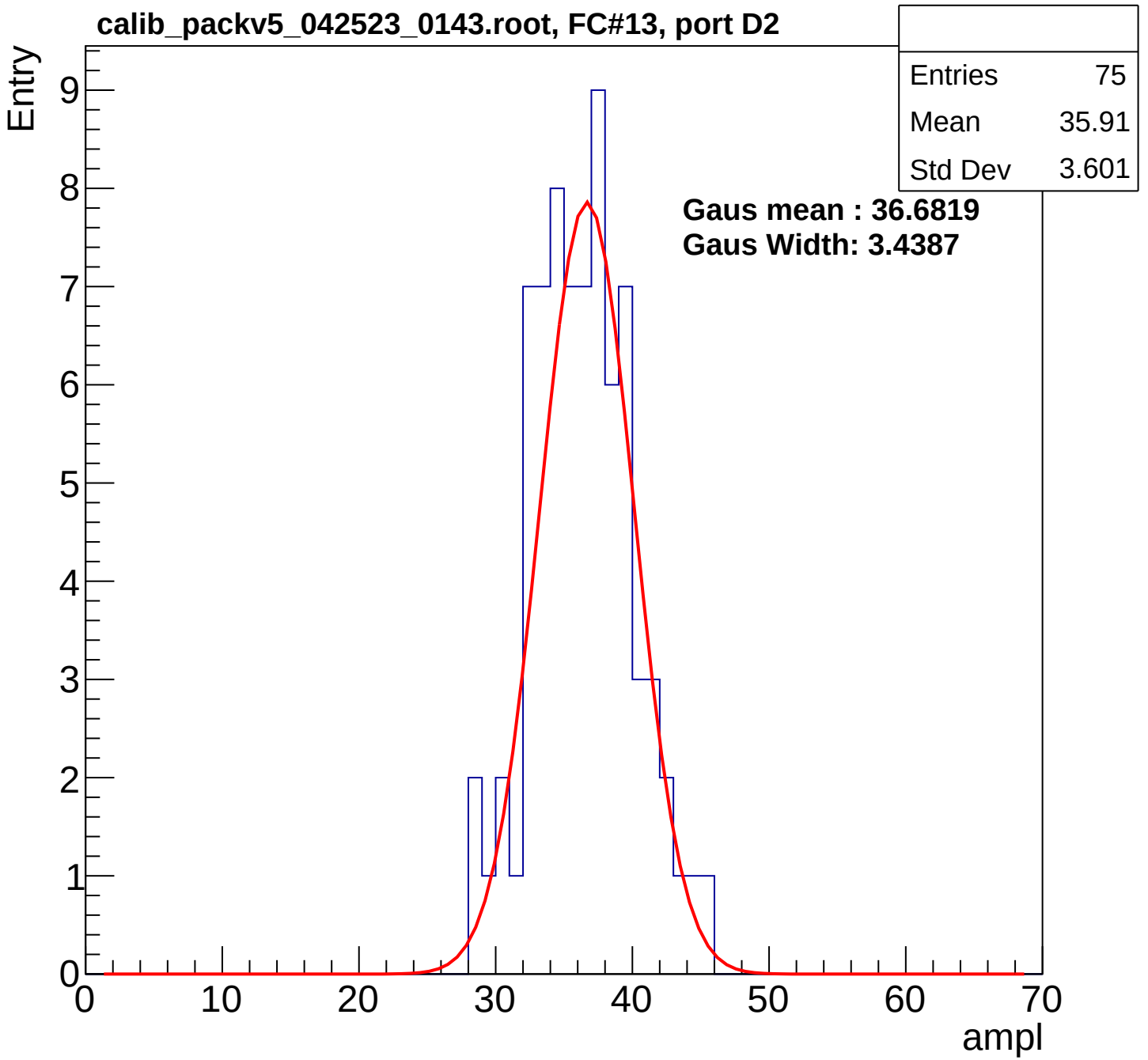
B1L003S, U17-ch95, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 35.91
Std Dev 3.601
Gaus mean : 36.6819
Gaus Width: 3.4387
Entry
ampl
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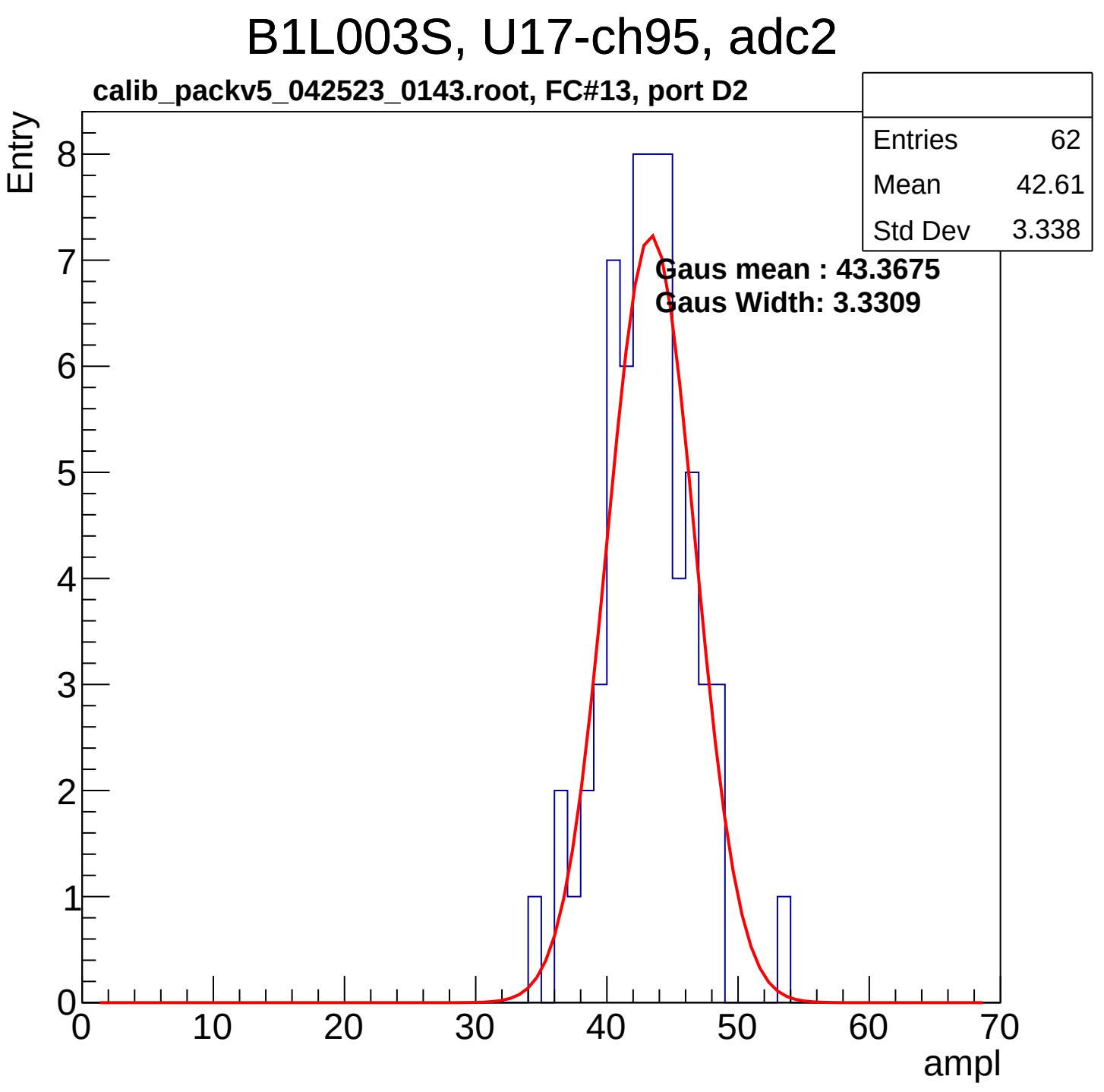
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U17-ch95, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.61
Std Dev 3.338
Gaus mean : 43.3675
Gaus Width: 3.3309
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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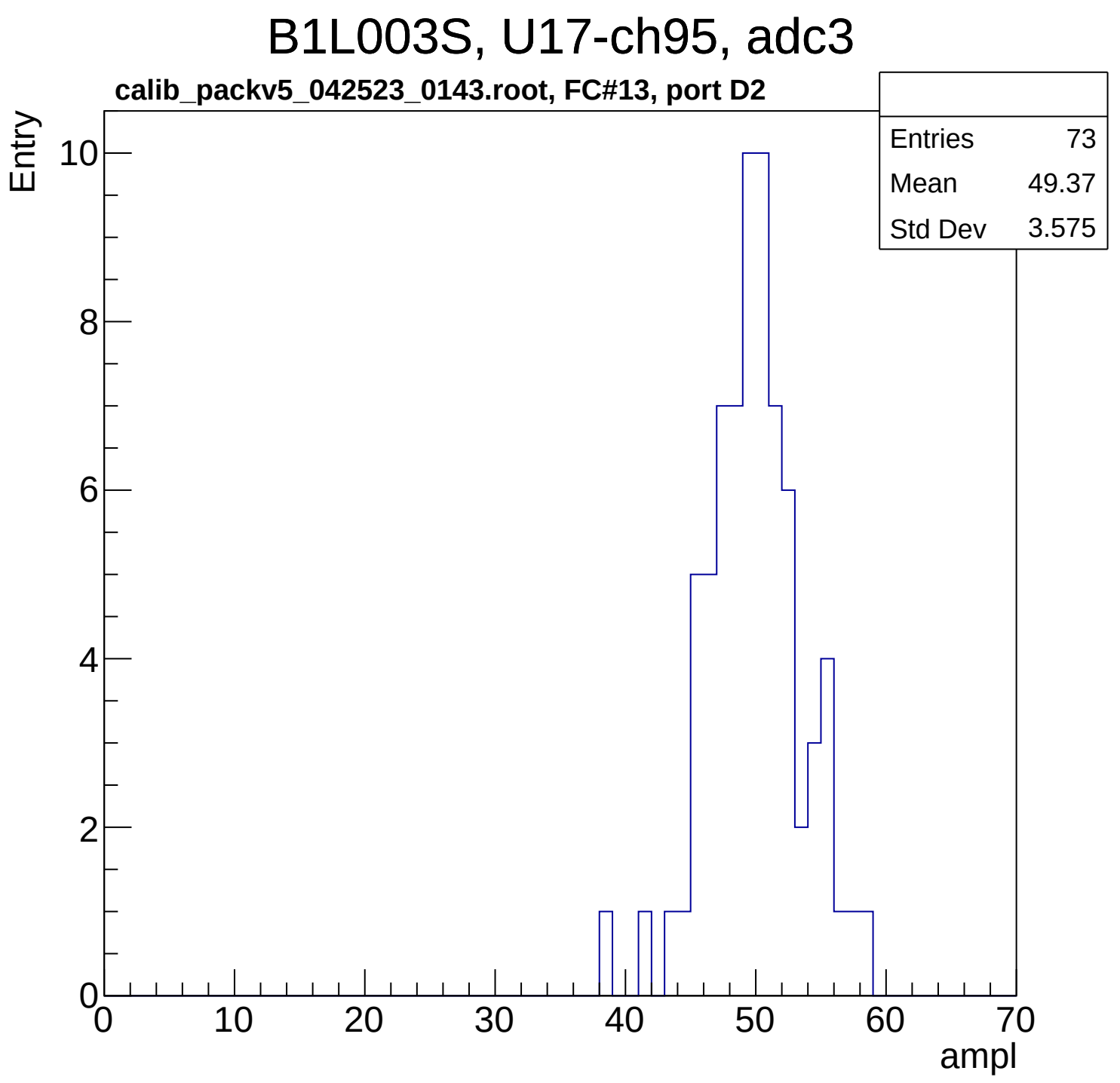
B1L003S, U17-ch95, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.37
Std Dev 3.575
Entry
ampl
0
2
4
6
8
10
0
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60
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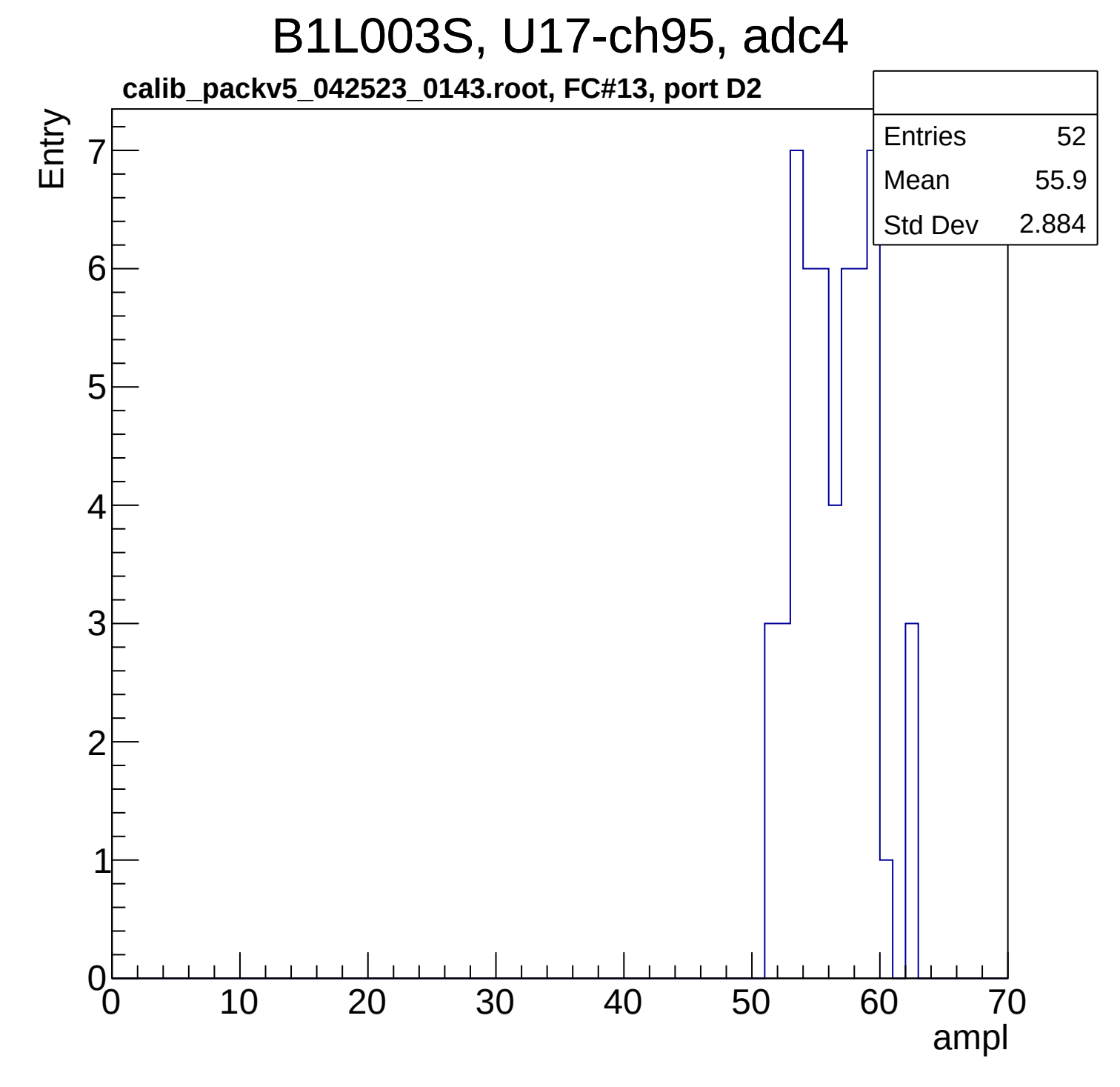
B1L003S, U17-ch95, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.9
Std Dev 2.884
Entry
ampl
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0
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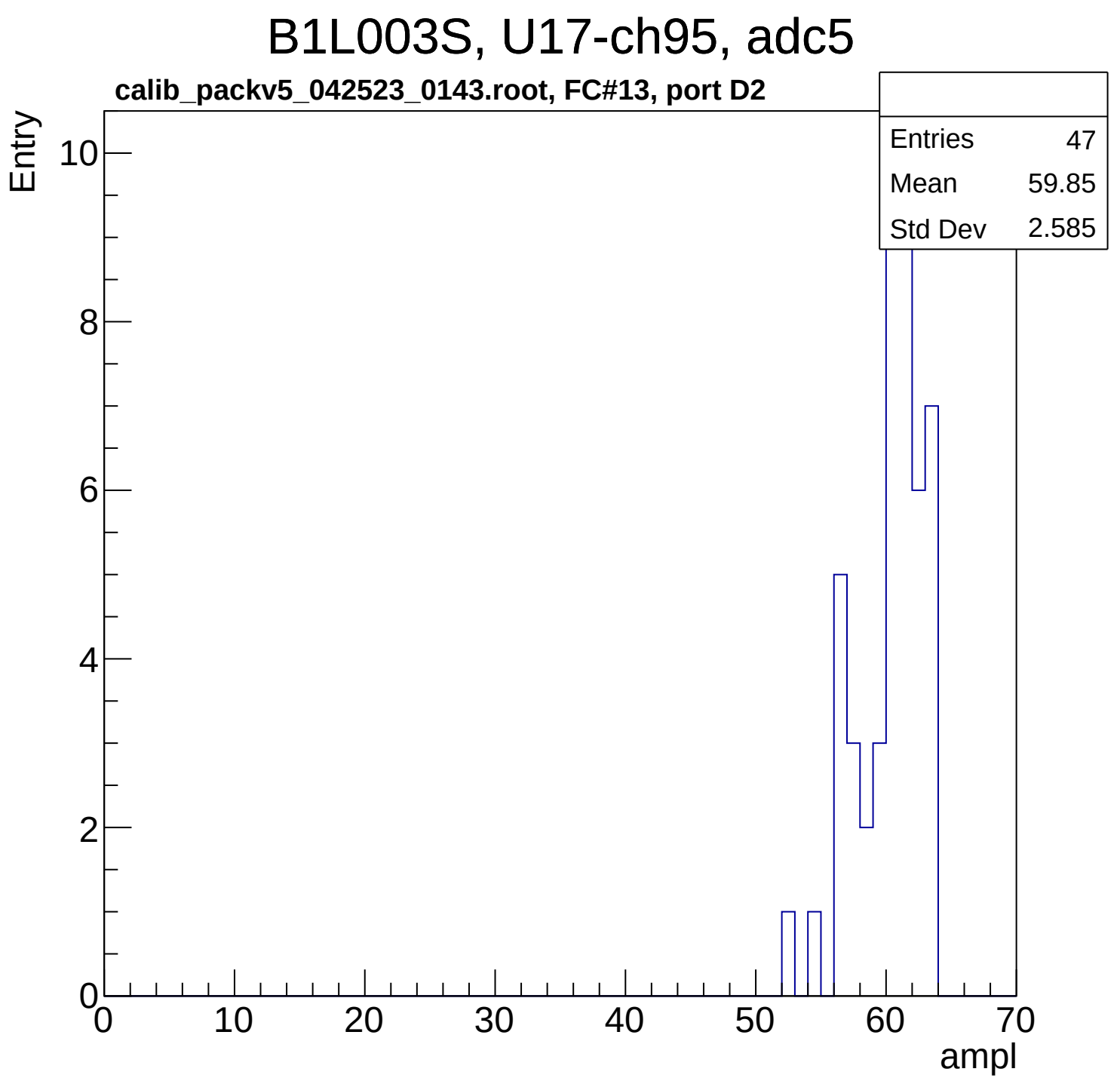
B1L003S, U17-ch95, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59.85
Std Dev 2.585
Entry
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2
4
6
8
10
0
10
20
30
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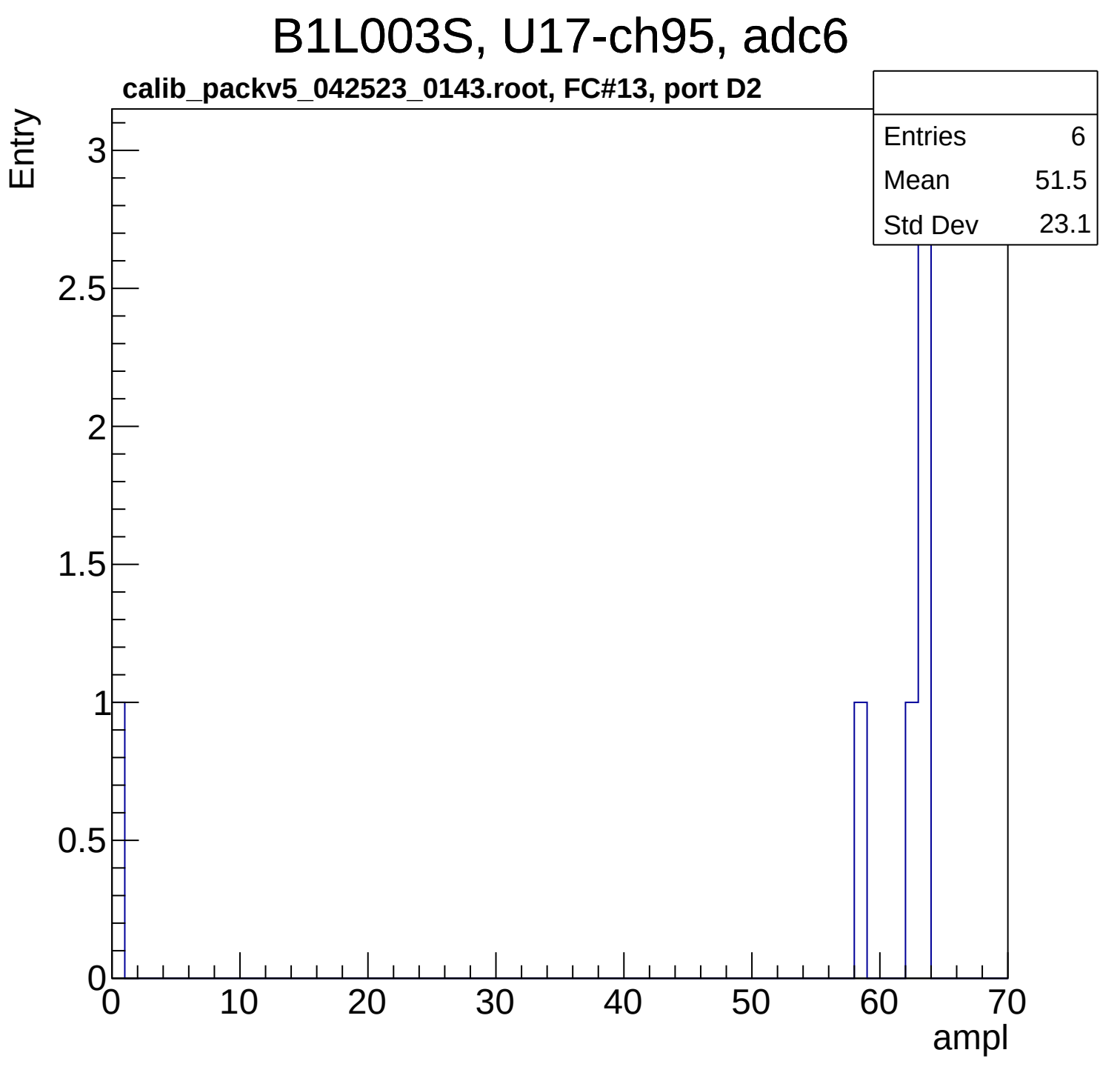
50
60
70
ampl


B1L003S, U17-ch95, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 51.5
Std Dev 23.1
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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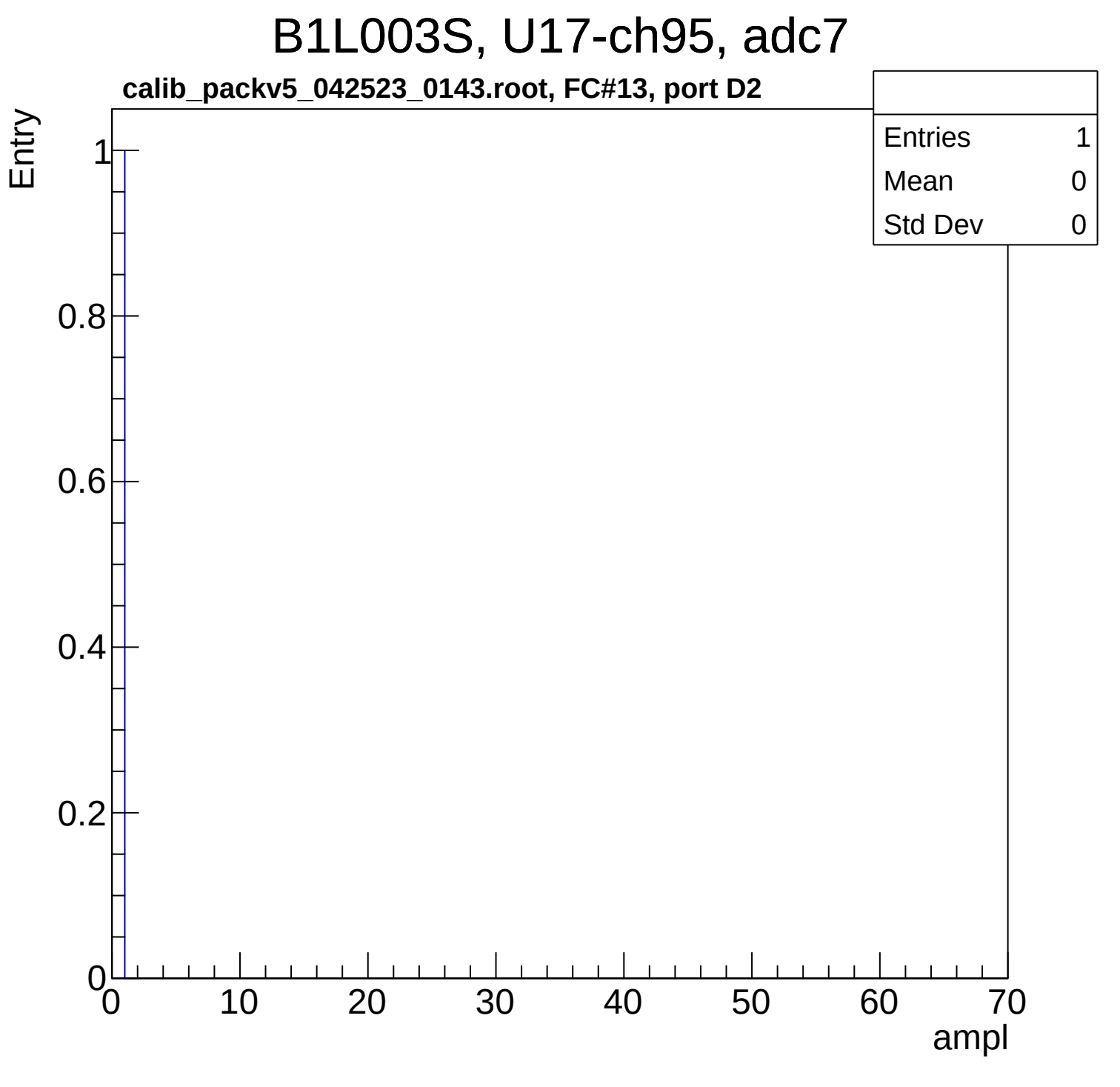
B1L003S, U17-ch95, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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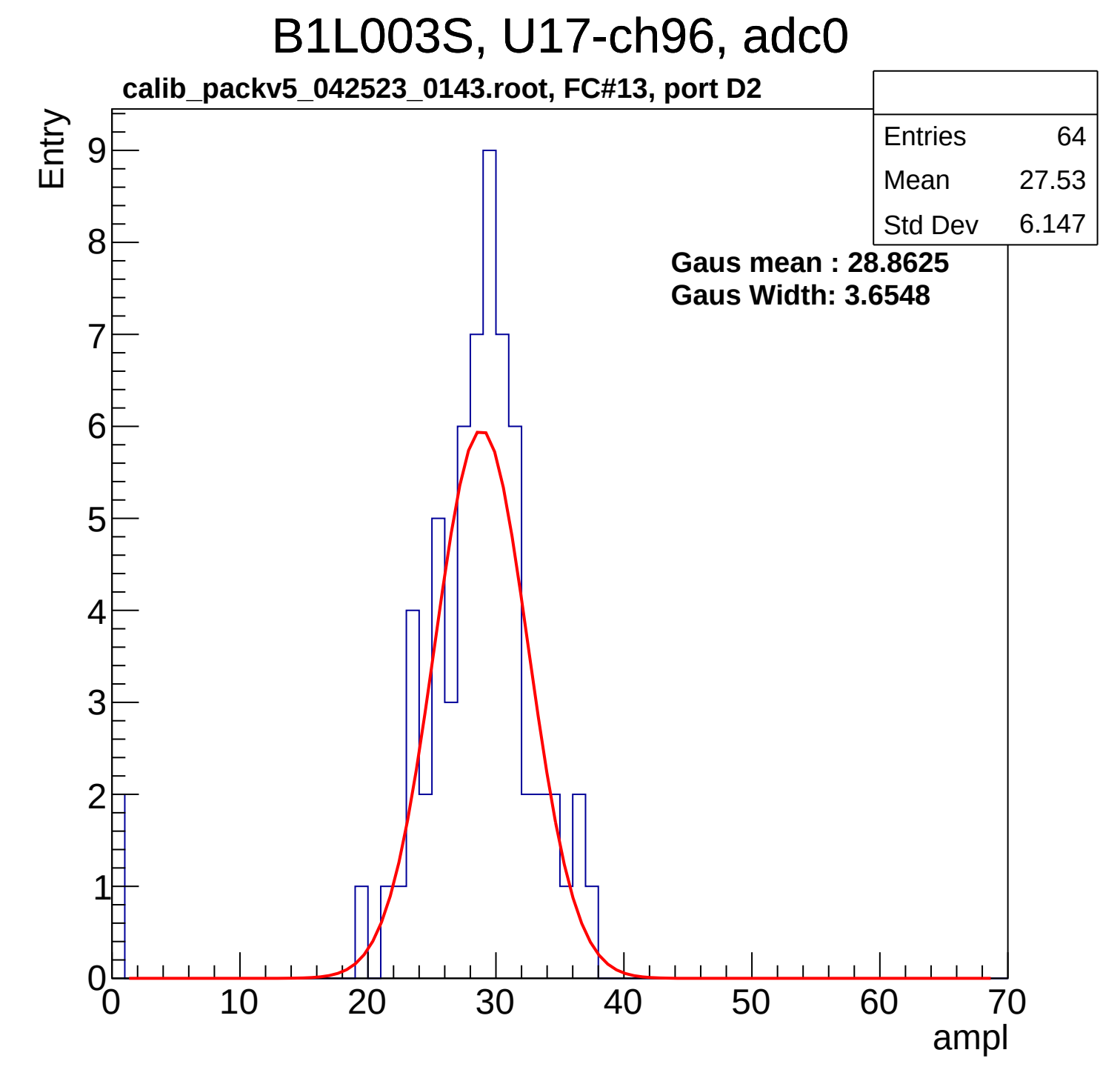
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch96, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 27.53
Std Dev 6.147
Gaus mean : 28.8625
Gaus Width: 3.6548
Entry
ampl
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2
3
4
5
6
7
8
9
0
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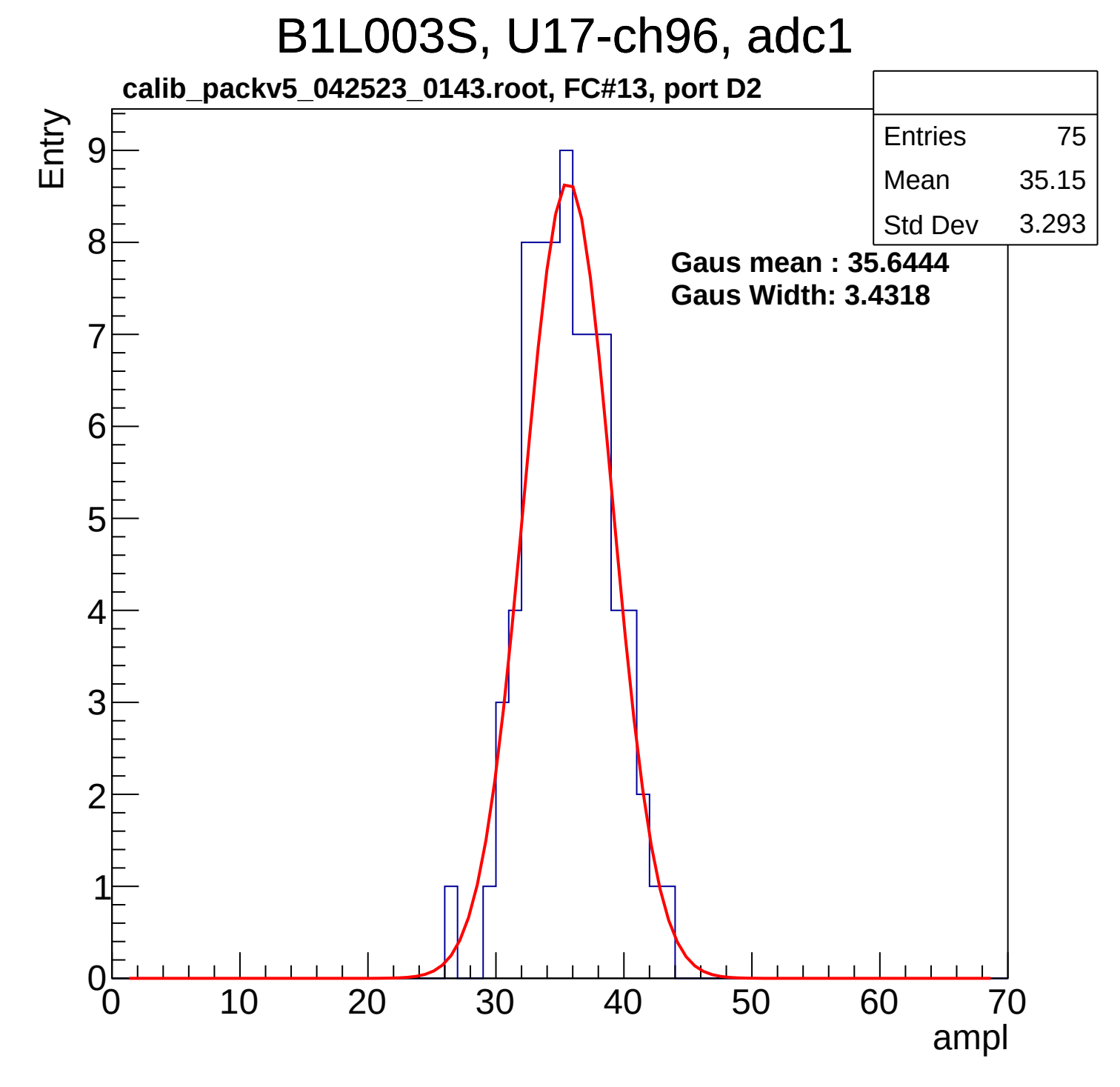
B1L003S, U17-ch96, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 35.15
Std Dev 3.293
Gaus mean : 35.6444
Gaus Width: 3.4318
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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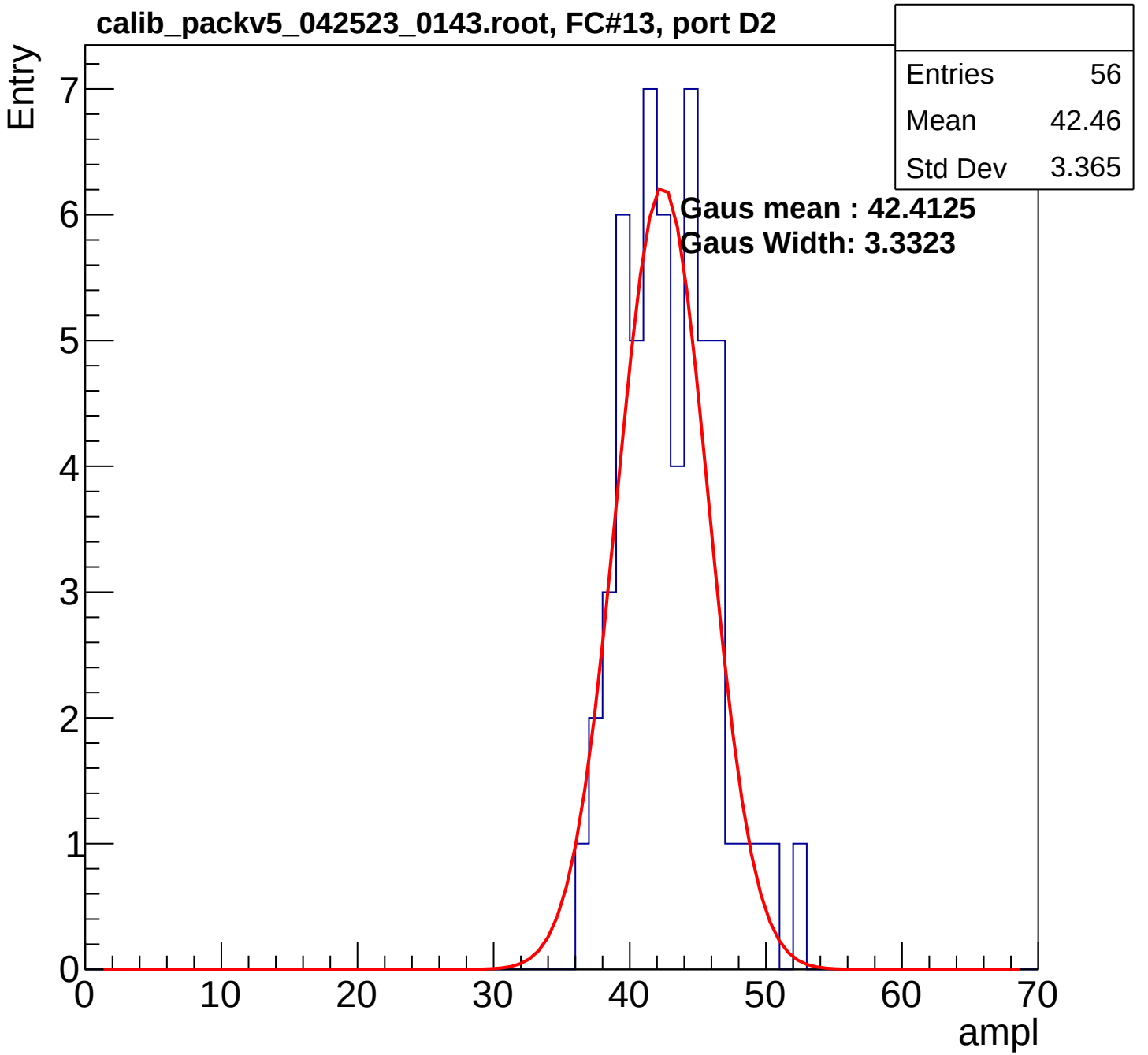
B1L003S, U17-ch96, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.46
Std Dev 3.365
Gaus mean : 42.4125
Gaus Width: 3.3323
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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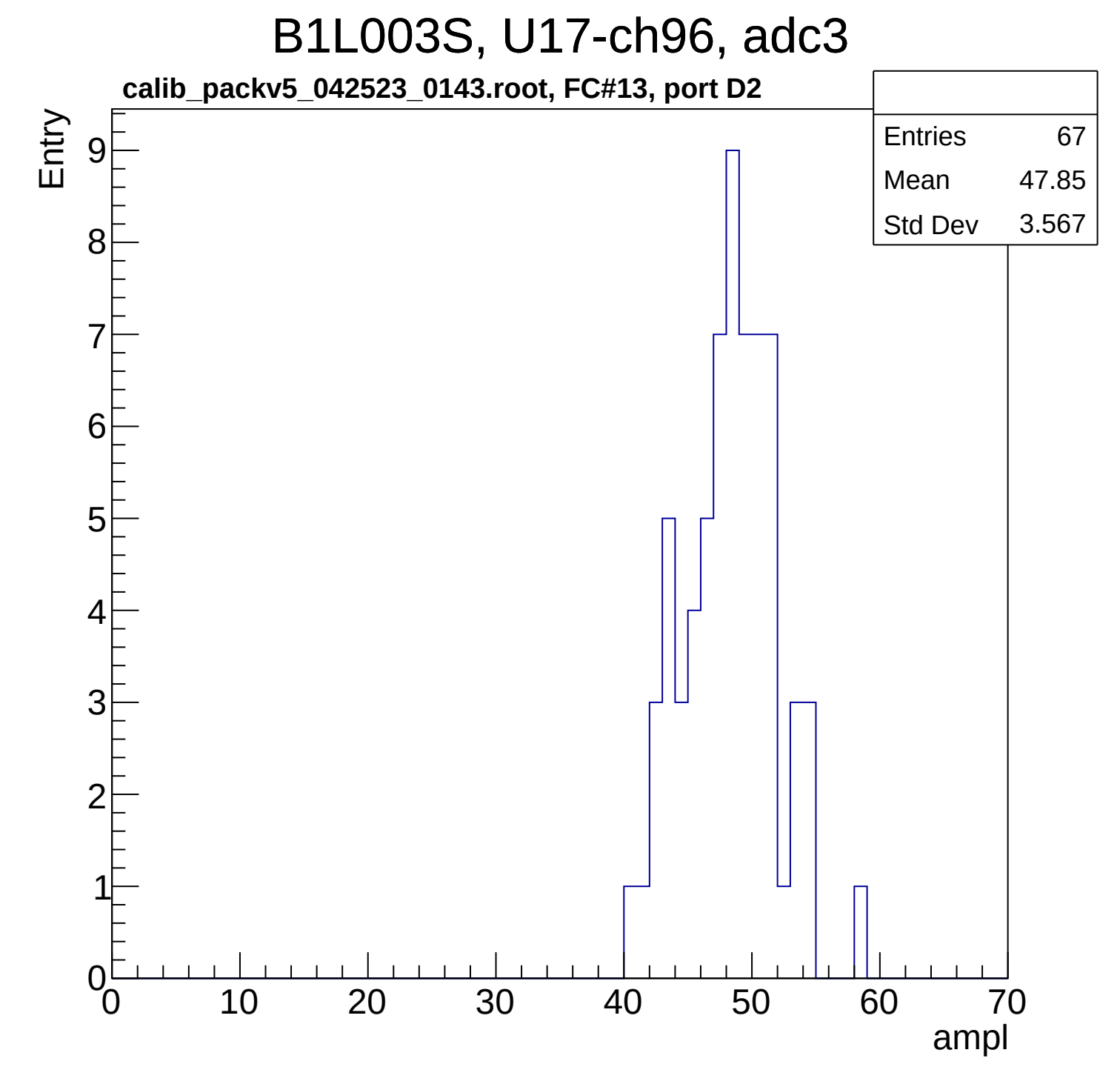
B1L003S, U17-ch96, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 47.85
Std Dev 3.567
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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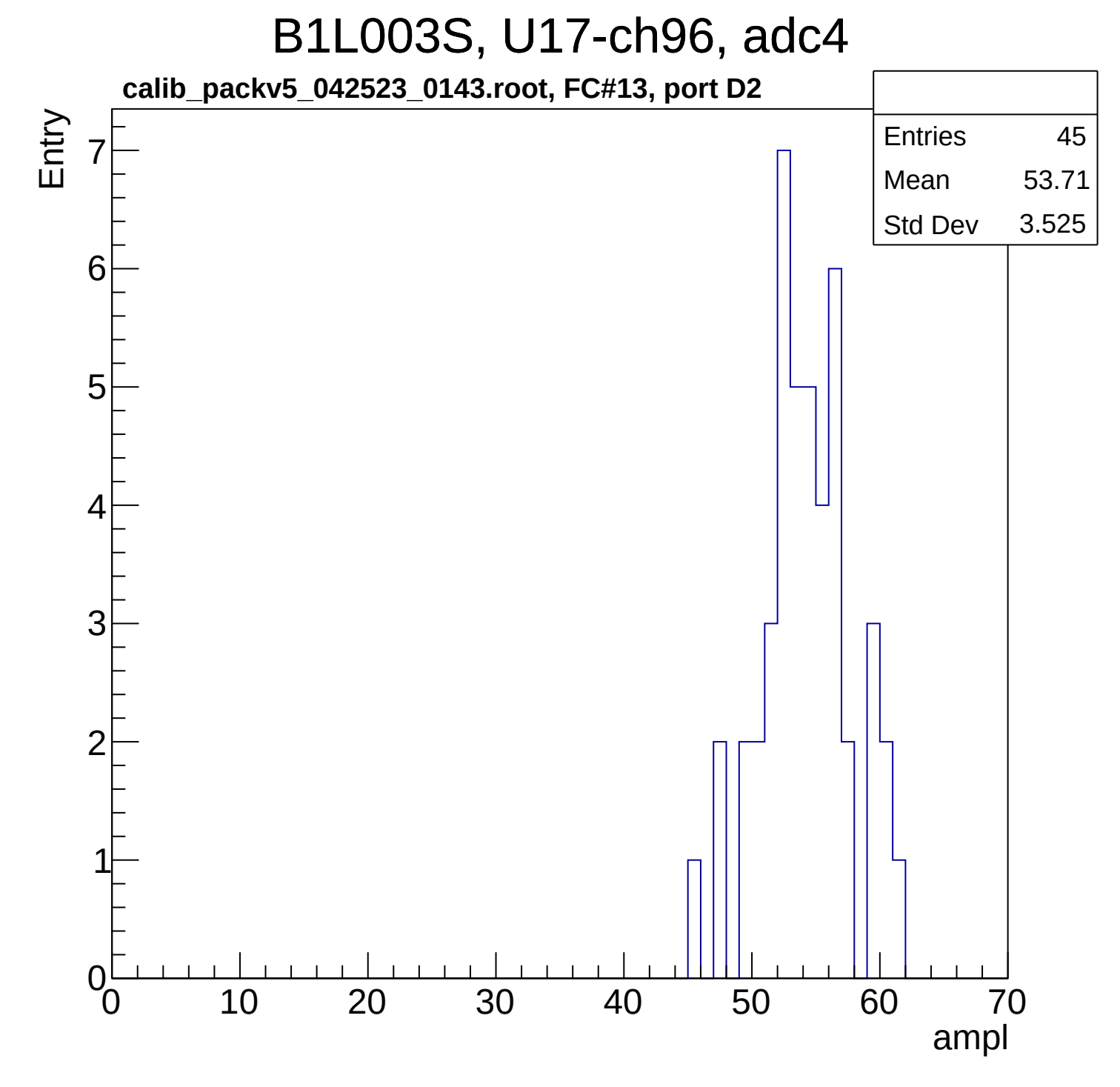
40
50
60
70


B1L003S, U17-ch96, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 53.71
Std Dev 3.525
Entry
ampl
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1
2
3
4
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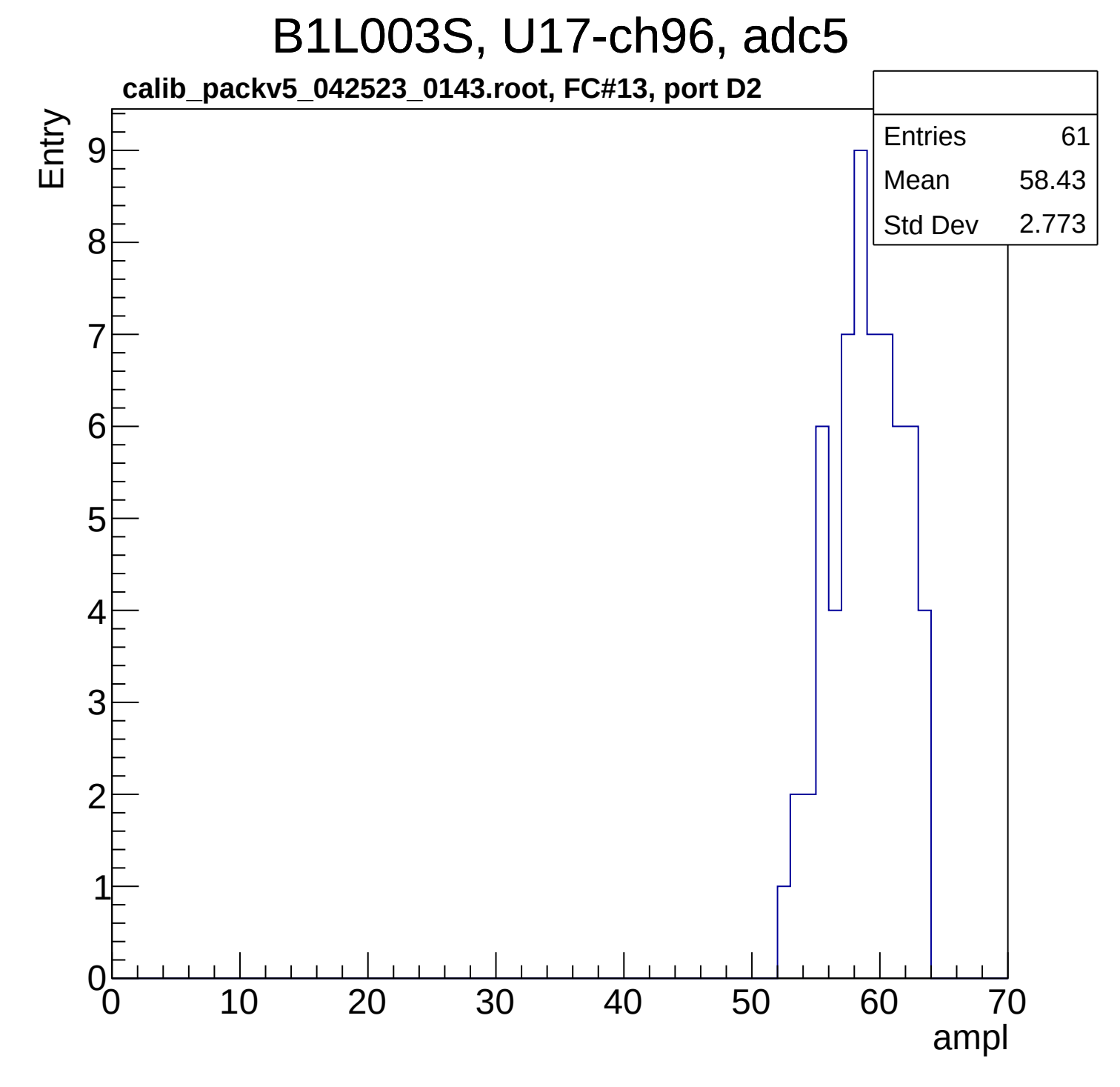
B1L003S, U17-ch96, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58.43
Std Dev 2.773
Entry
ampl
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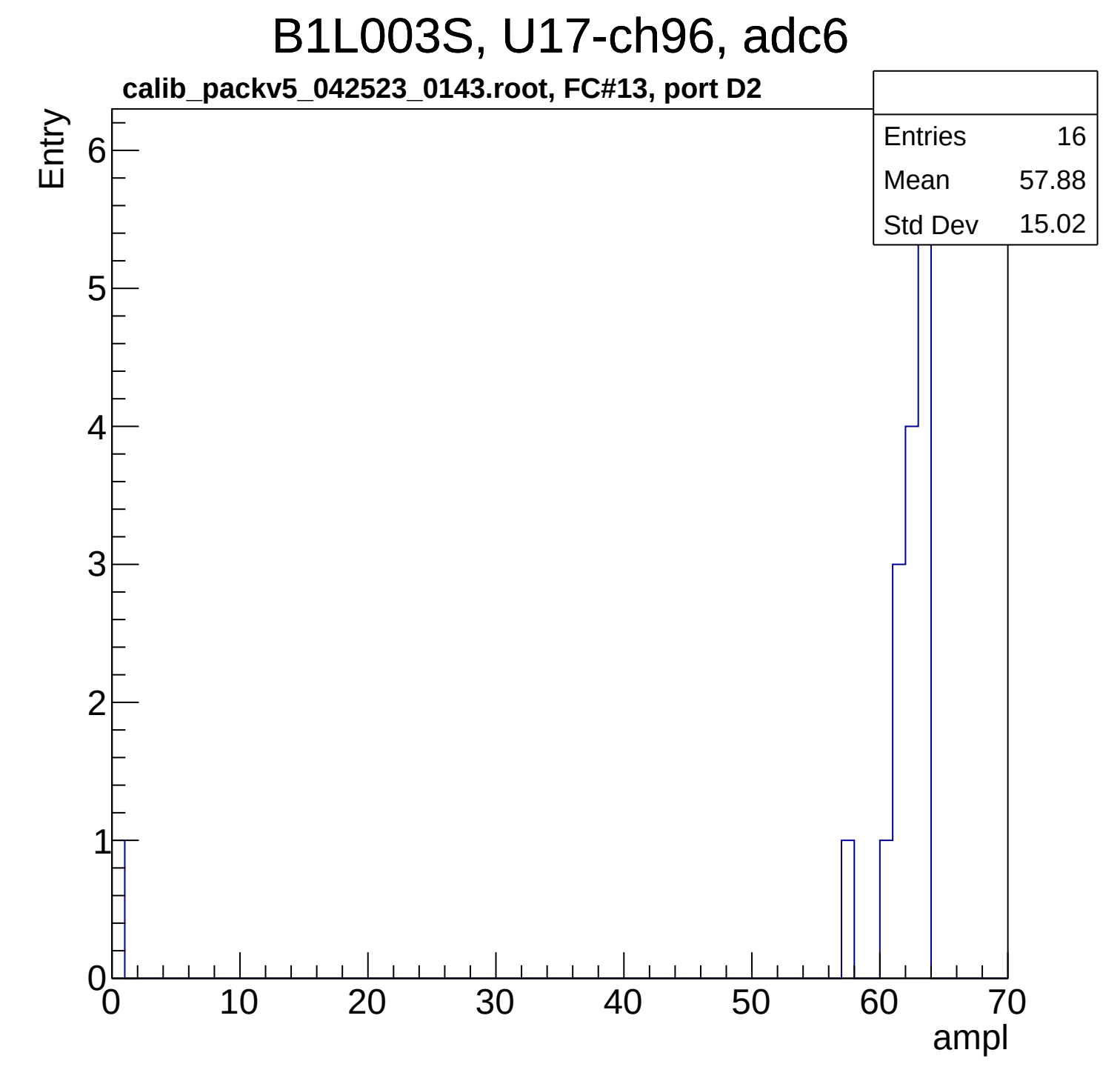
B1L003S, U17-ch96, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 57.88
Std Dev 15.02
Entry
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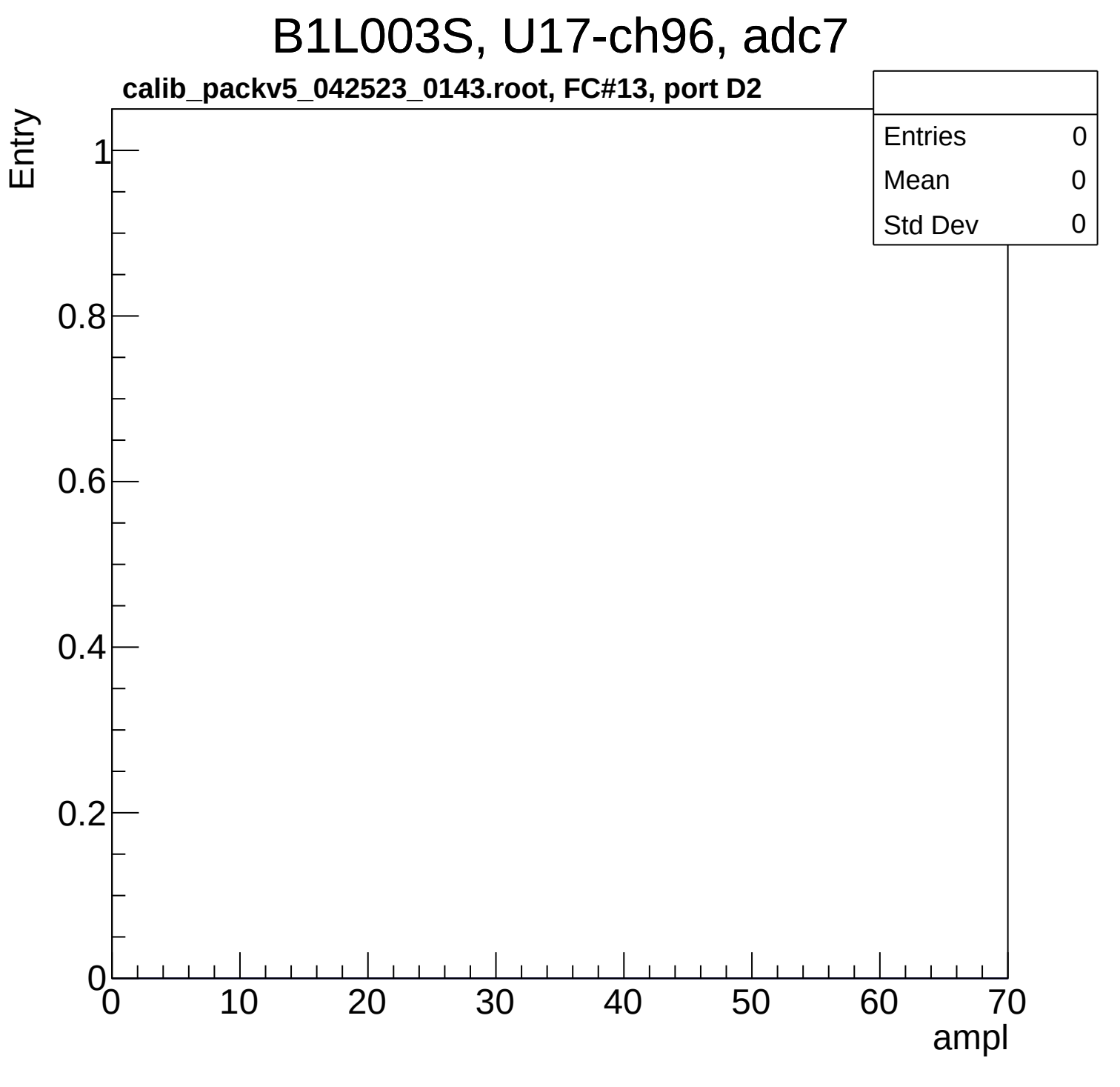
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch96, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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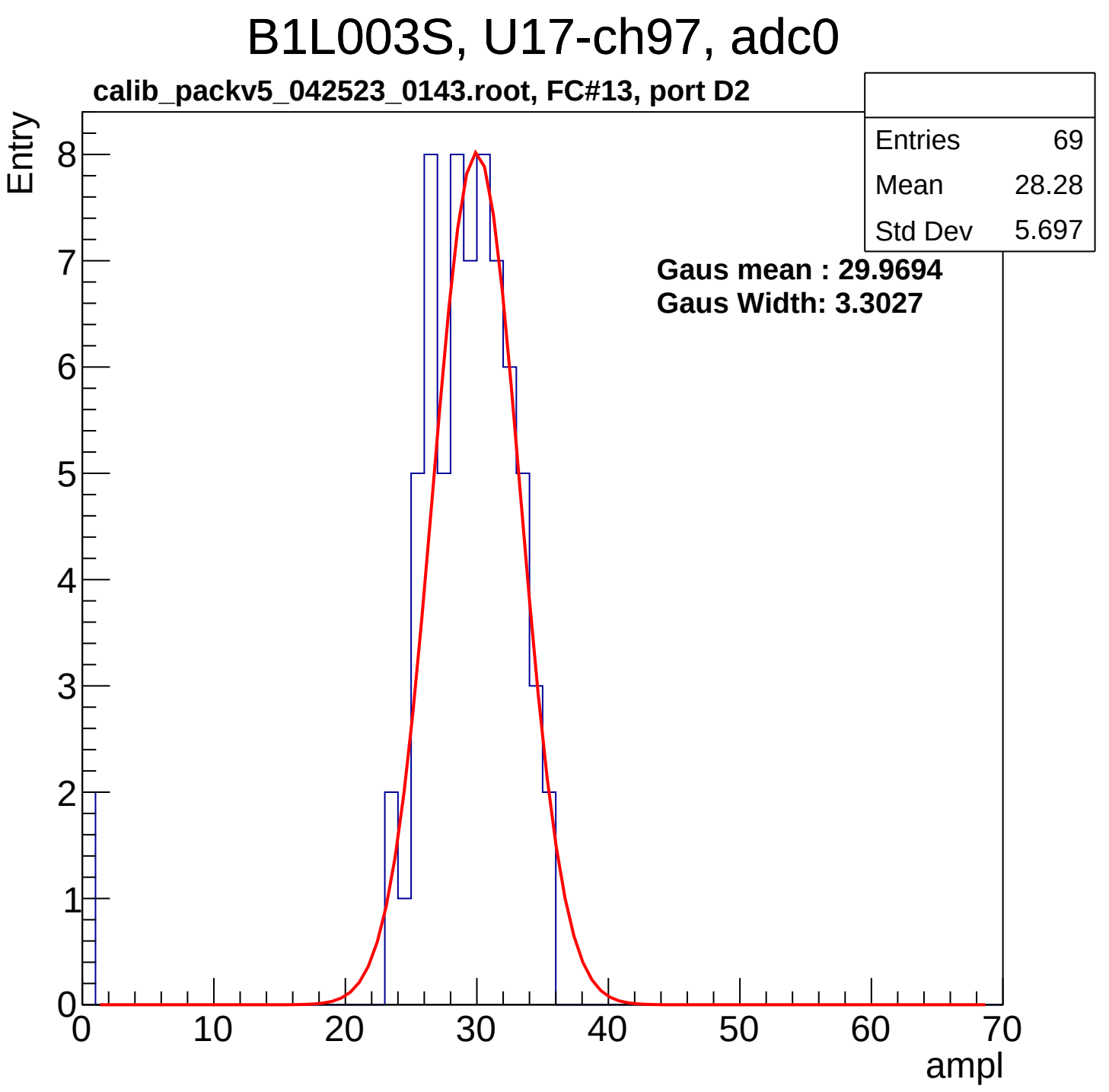
40
50
60
70
ampl


B1L003S, U17-ch97, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 28.28
Std Dev 5.697
Gaus mean : 29.9694
Gaus Width: 3.3027
Entry
ampl
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1
2
3
4
5
6
7
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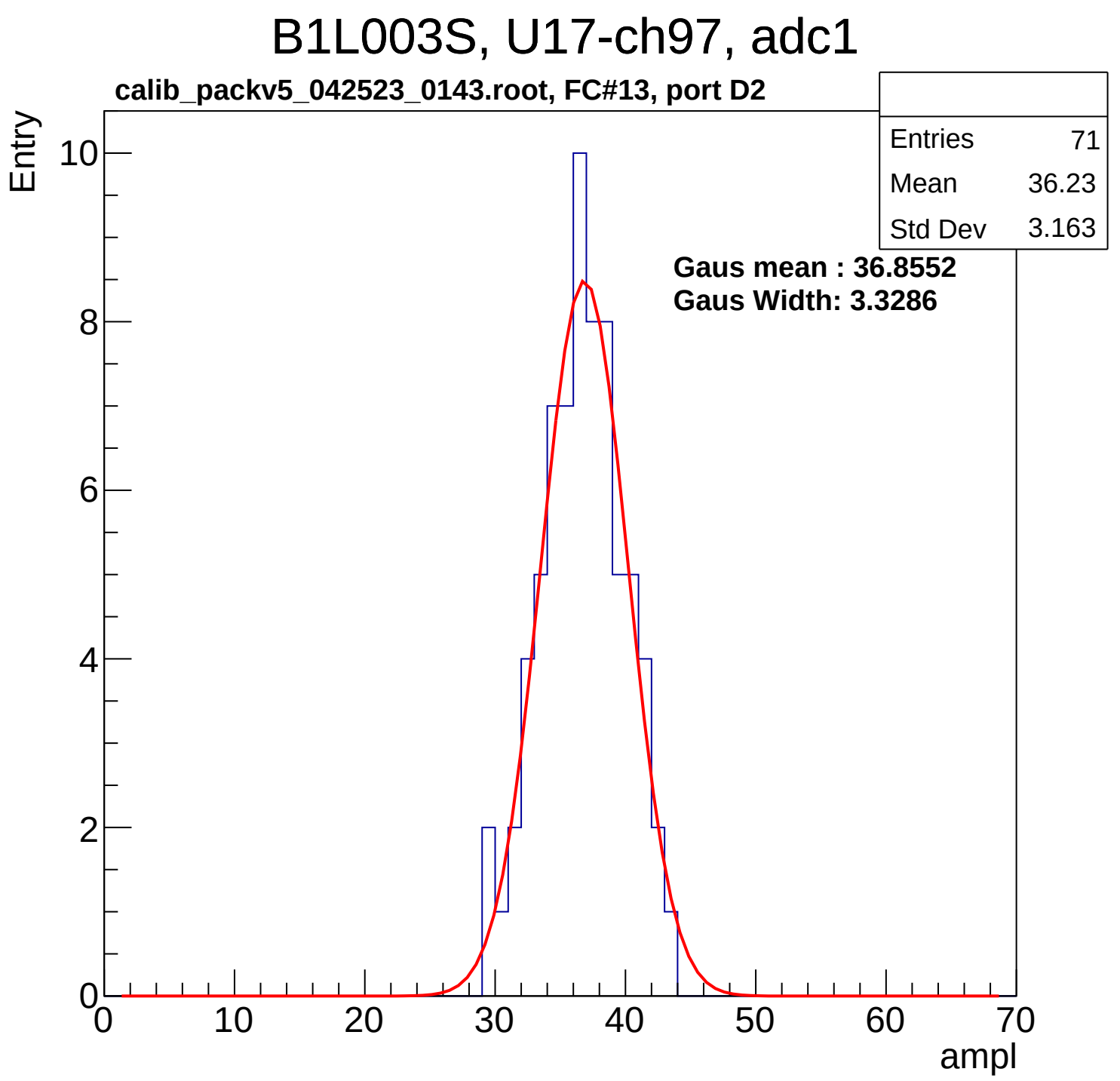
B1L003S, U17-ch97, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.23
Std Dev 3.163
Gaus mean : 36.8552
Gaus Width: 3.3286
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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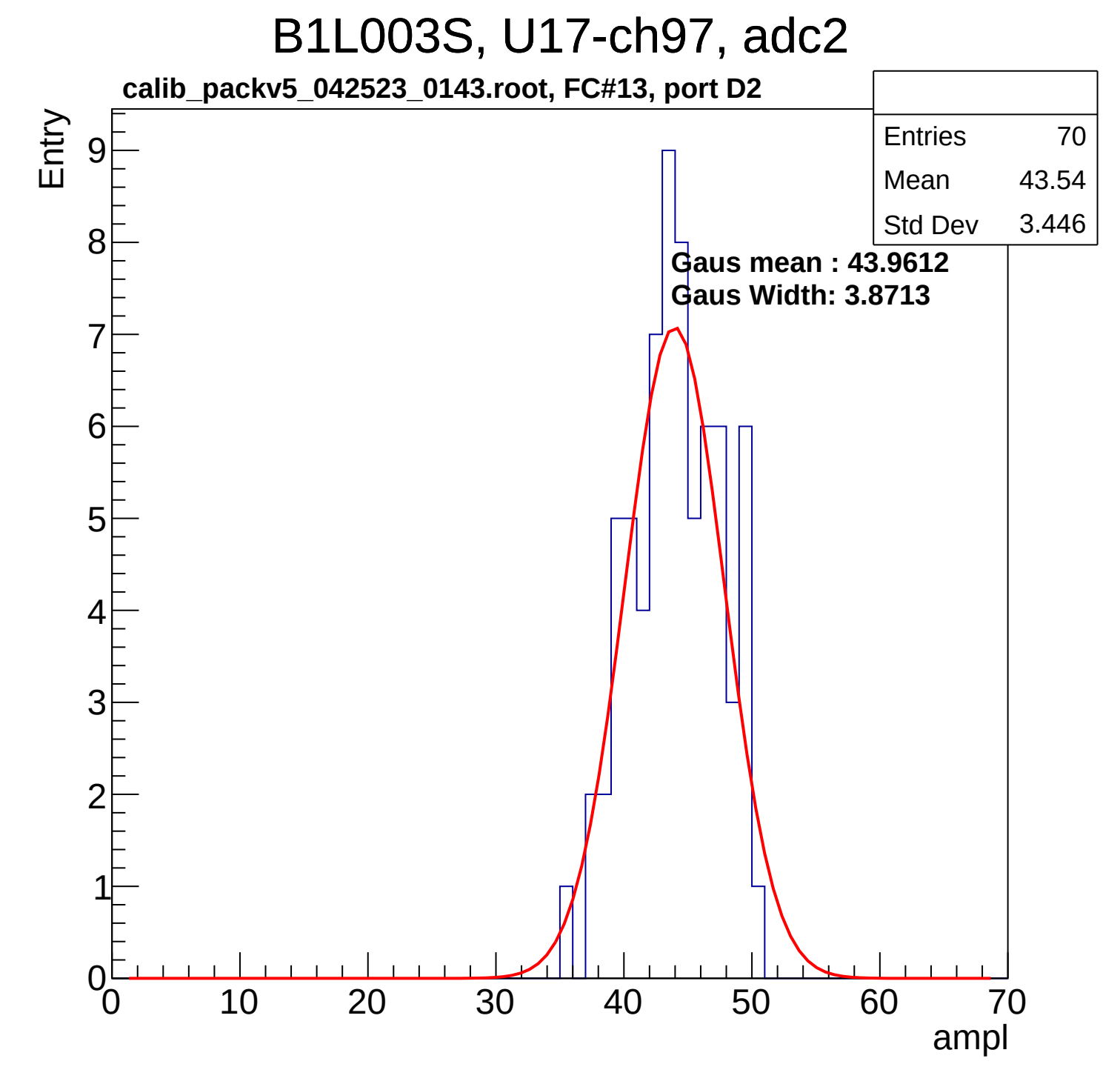
B1L003S, U17-ch97, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 43.54
Std Dev 3.446
Gaus mean : 43.9612
Gaus Width: 3.8713
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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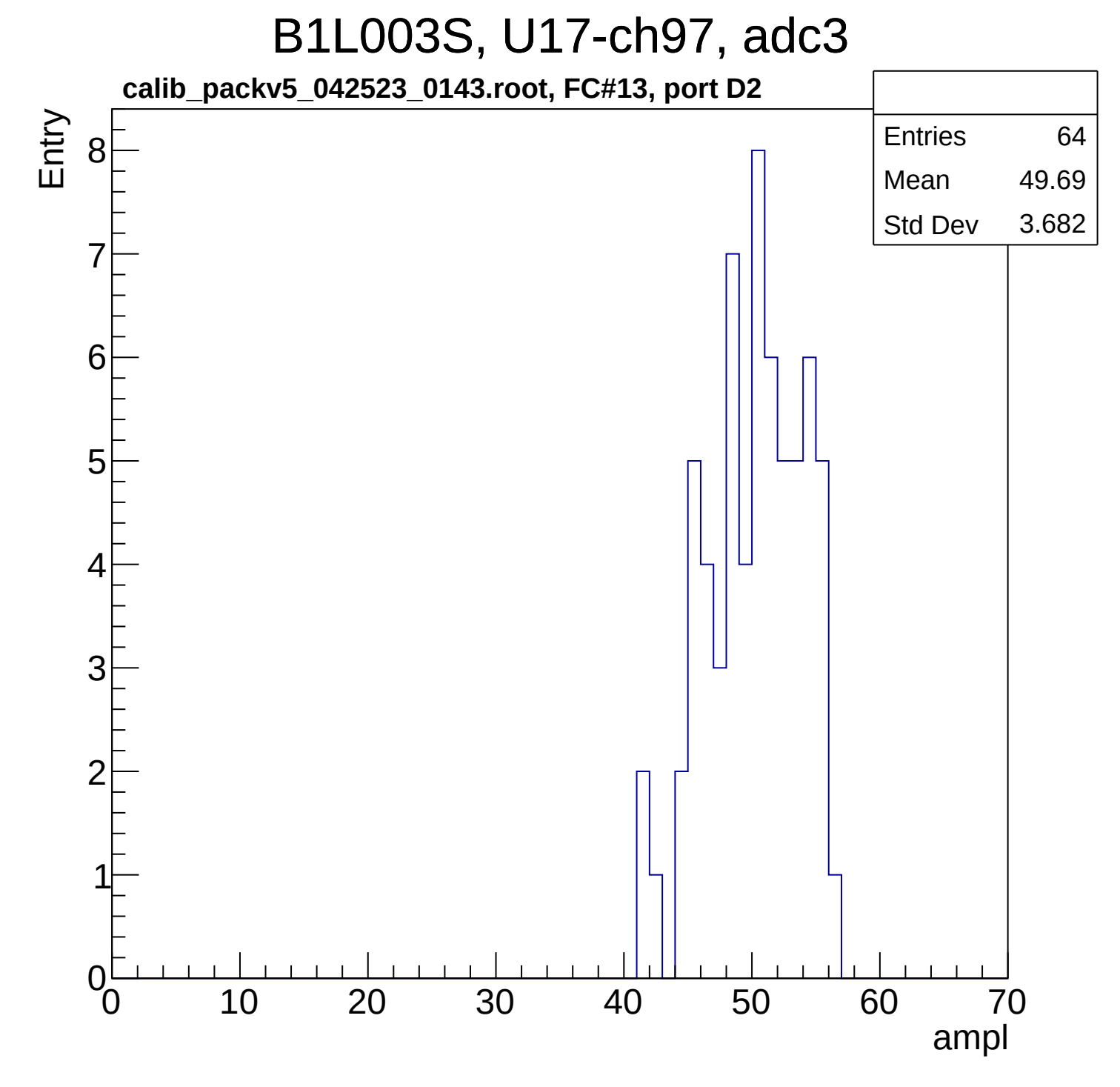
B1L003S, U17-ch97, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49.69
Std Dev 3.682
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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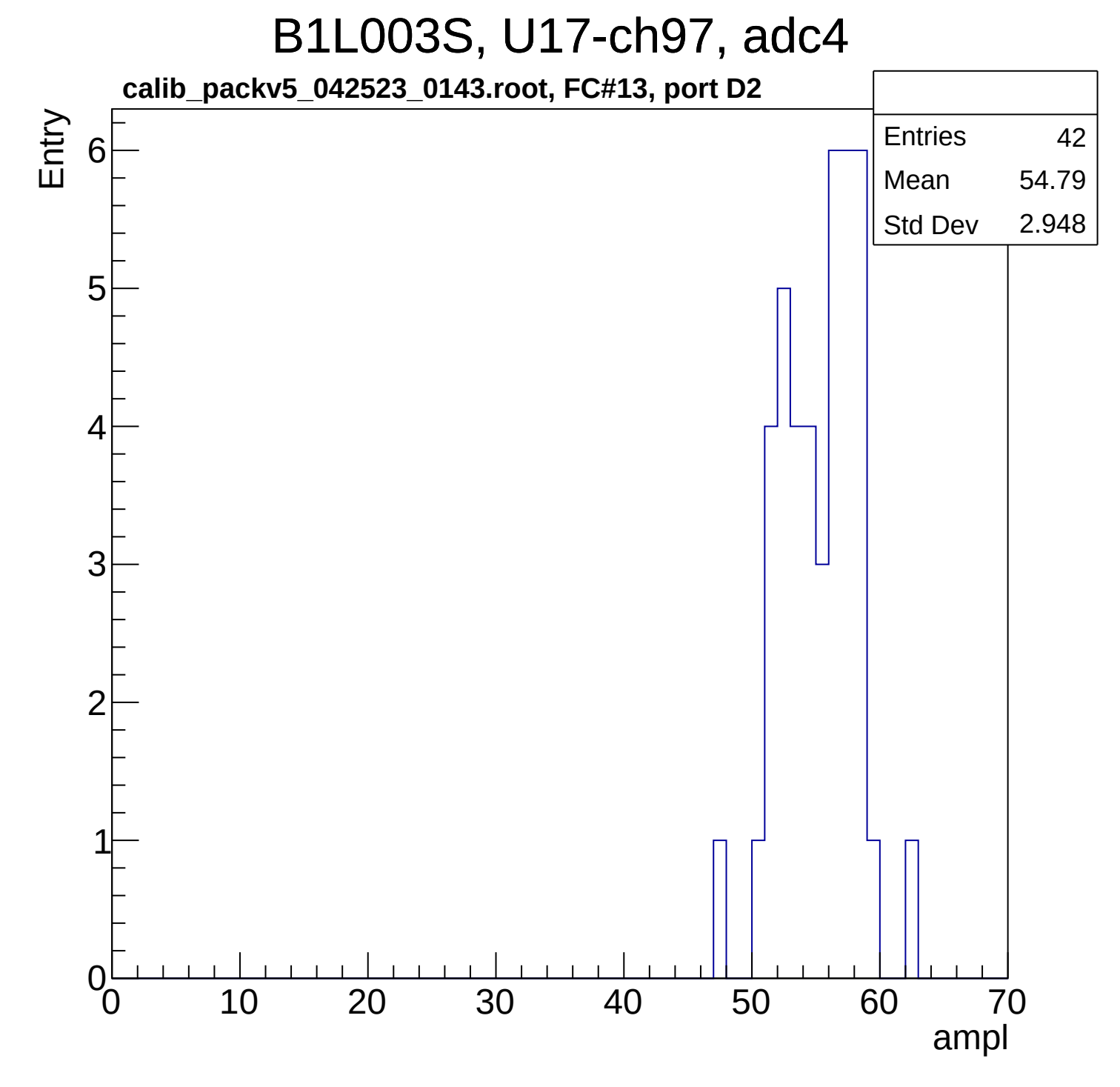
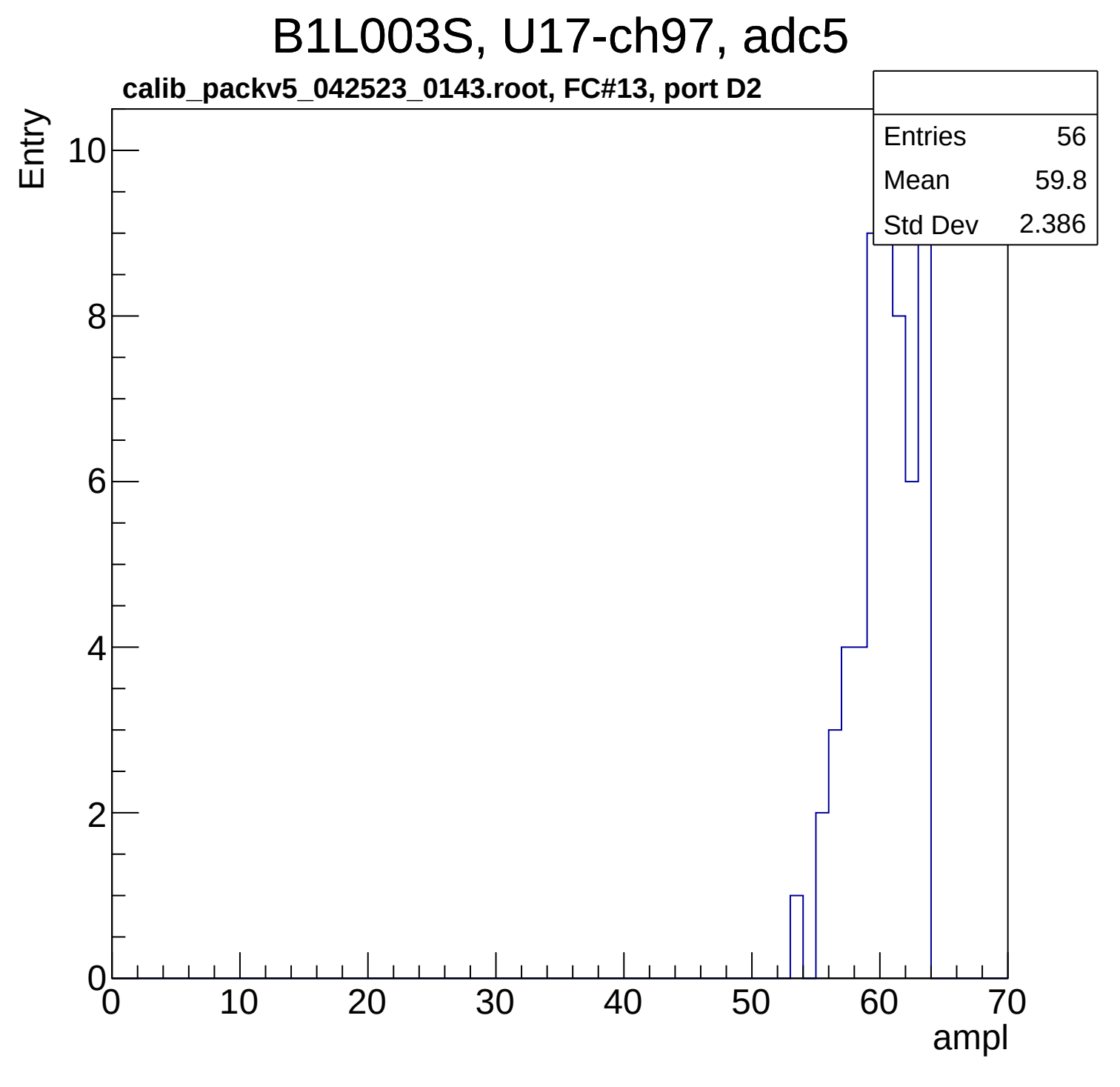

B1L003S, U17-ch97, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 54.79
Std Dev 2.948
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch97, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.8
Std Dev 2.386
Entry
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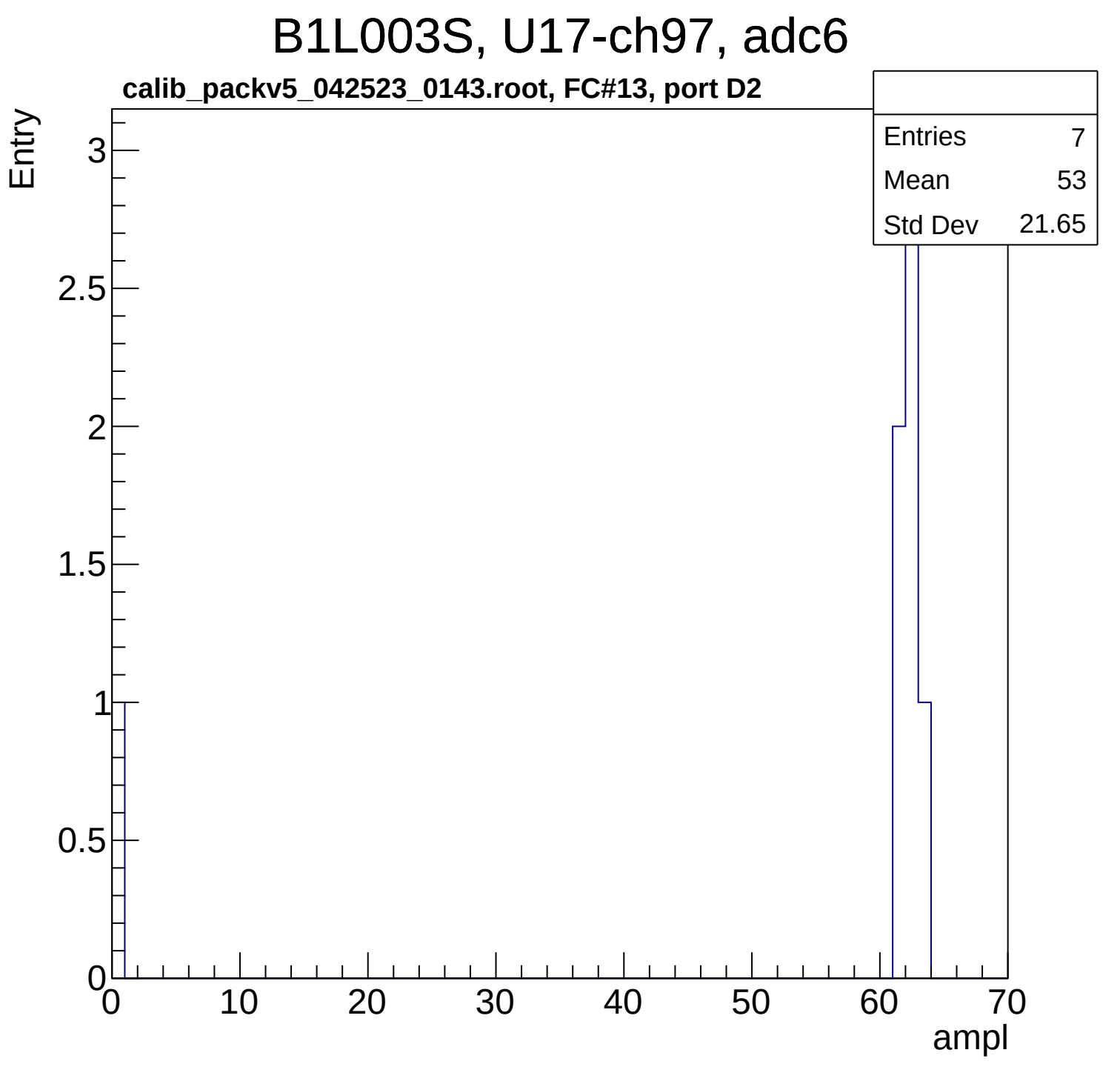
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch97, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53
Std Dev 21.65
Entry
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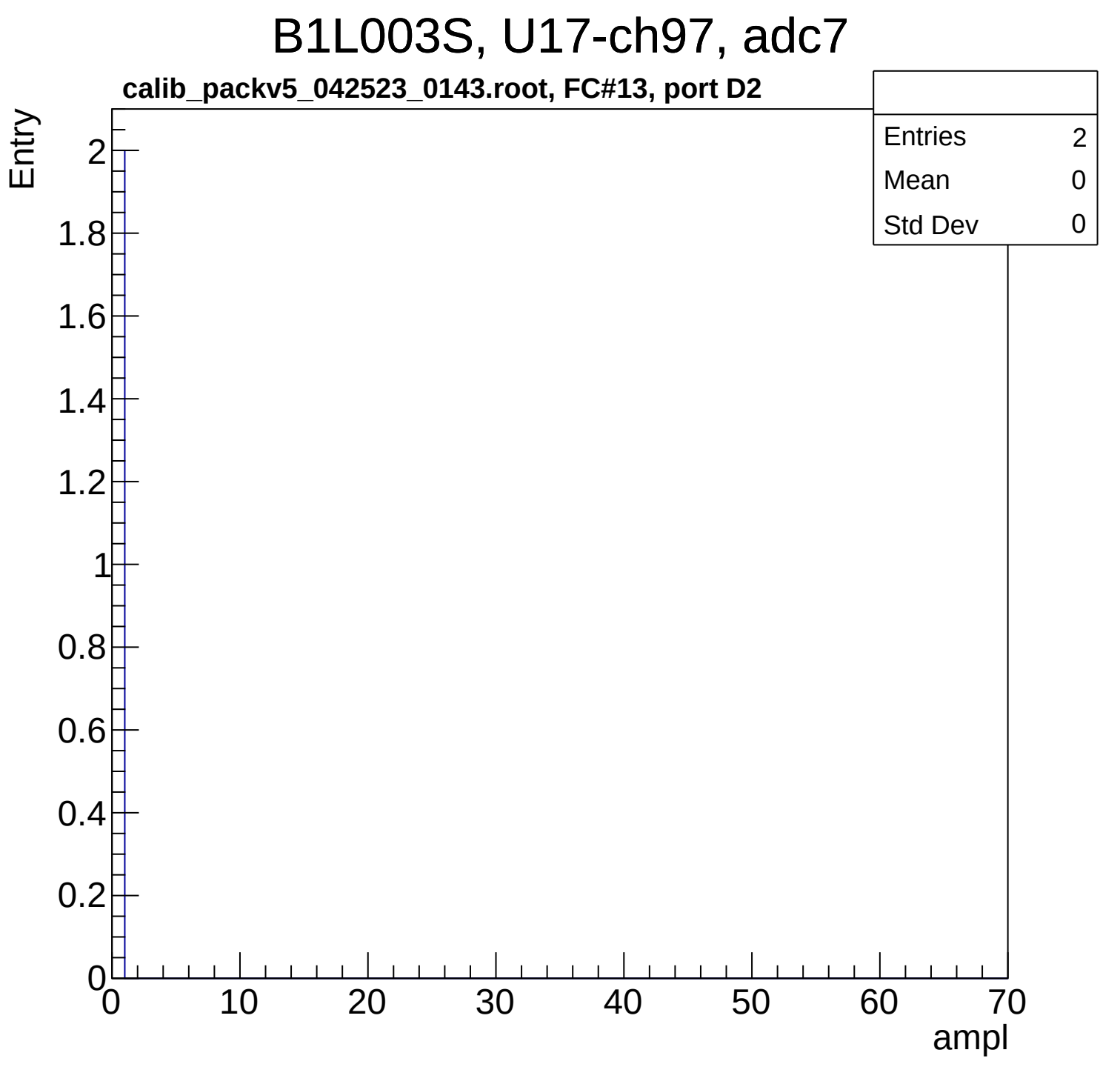
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch97, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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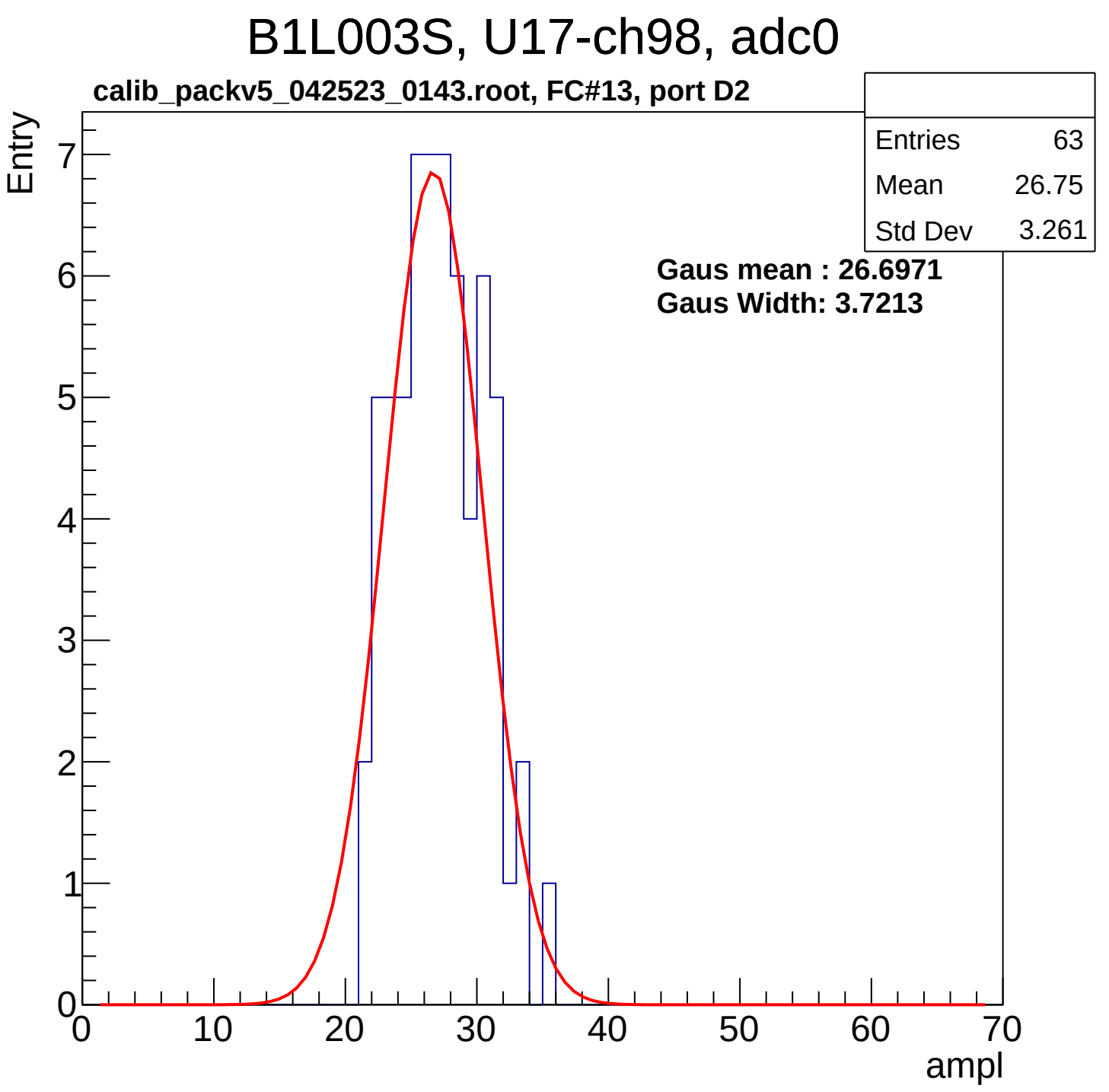
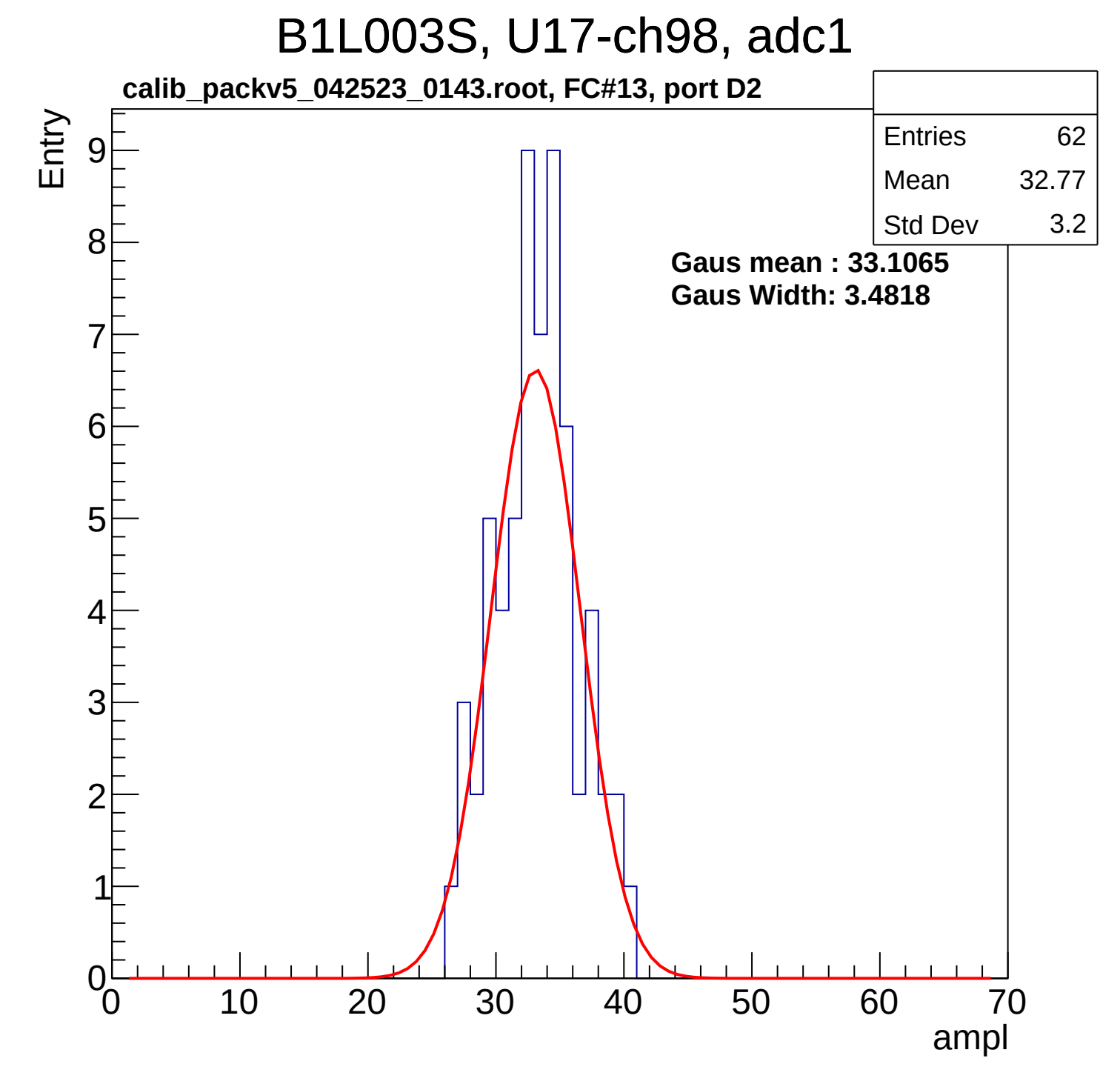

B1L003S, U17-ch98, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 26.75
Std Dev 3.261
Gaus mean : 26.6971
Gaus Width: 3.7213
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch98, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 32.77
Std Dev 3.2
Gaus mean : 33.1065
Gaus Width: 3.4818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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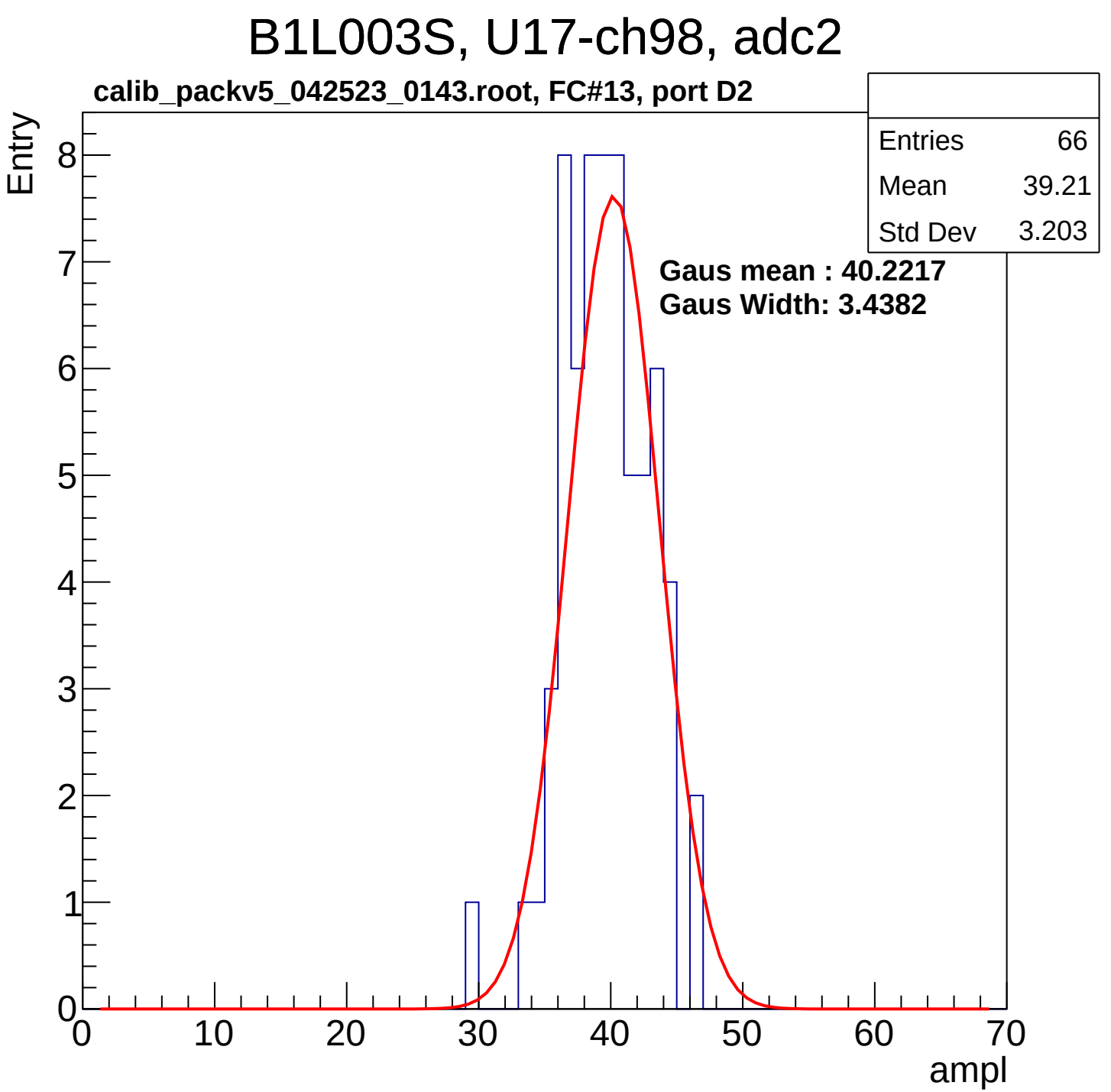
B1L003S, U17-ch98, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 39.21
Std Dev 3.203
Gaus mean : 40.2217
Gaus Width: 3.4382
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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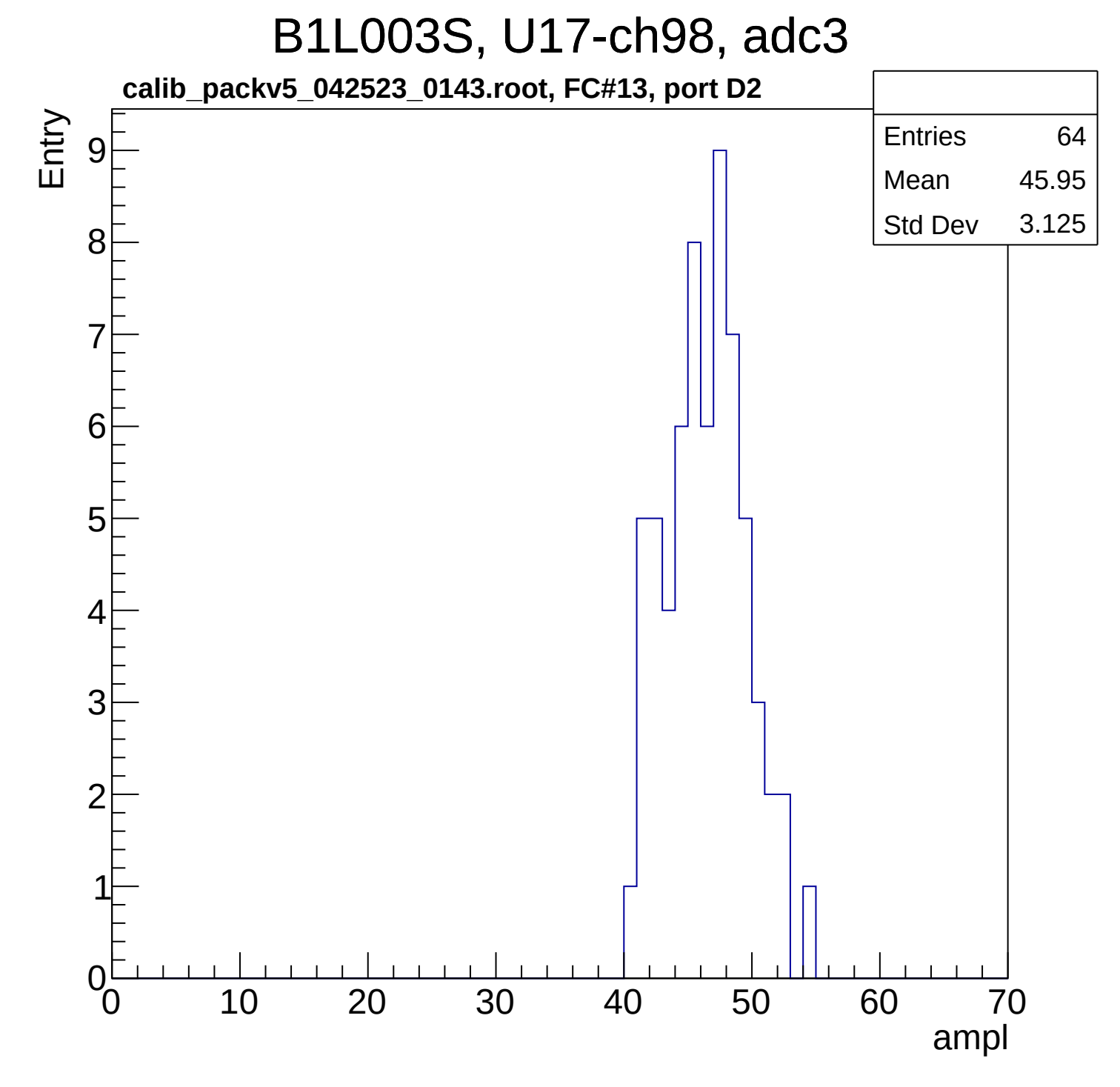
B1L003S, U17-ch98, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 45.95
Std Dev 3.125
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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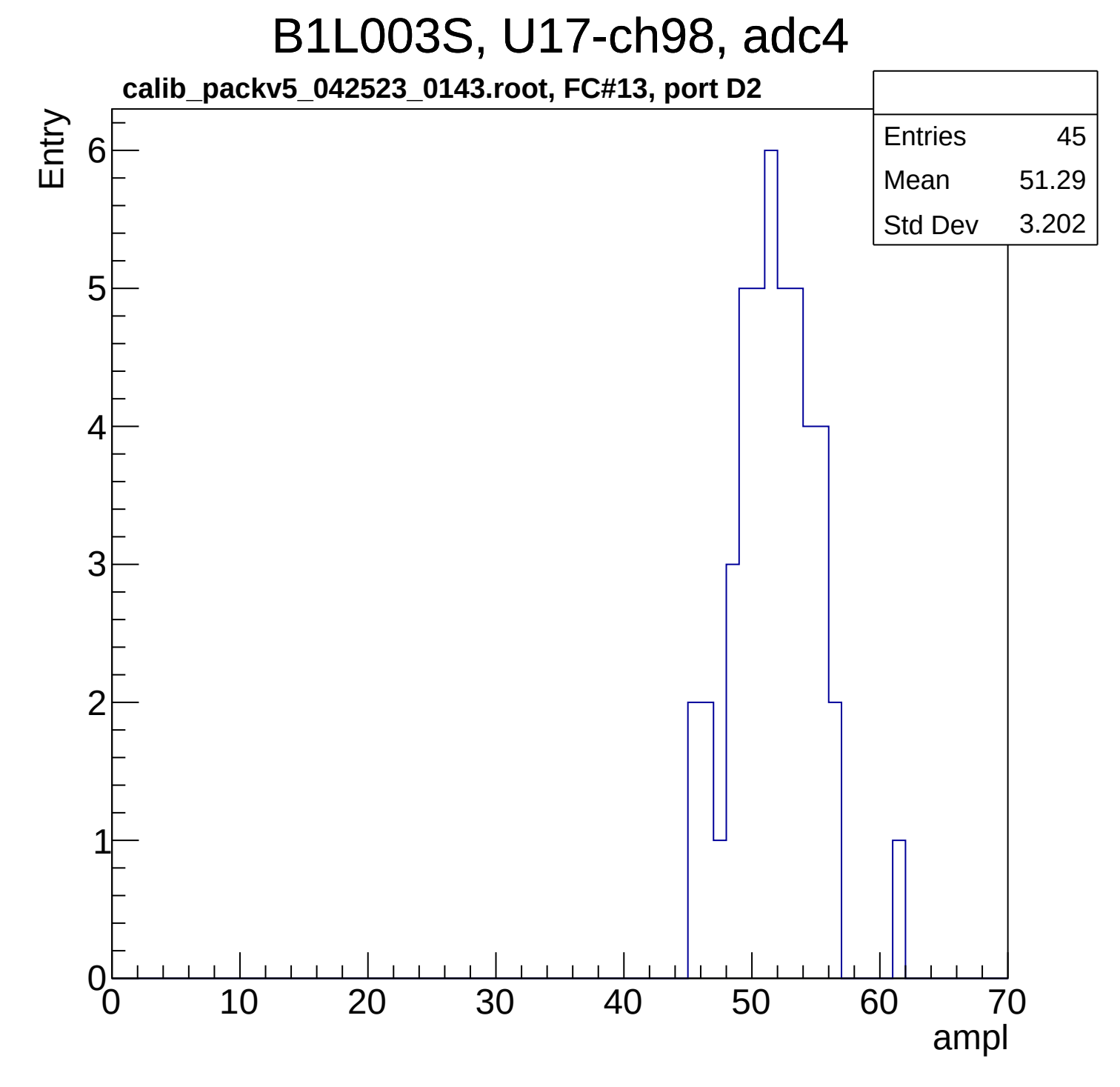
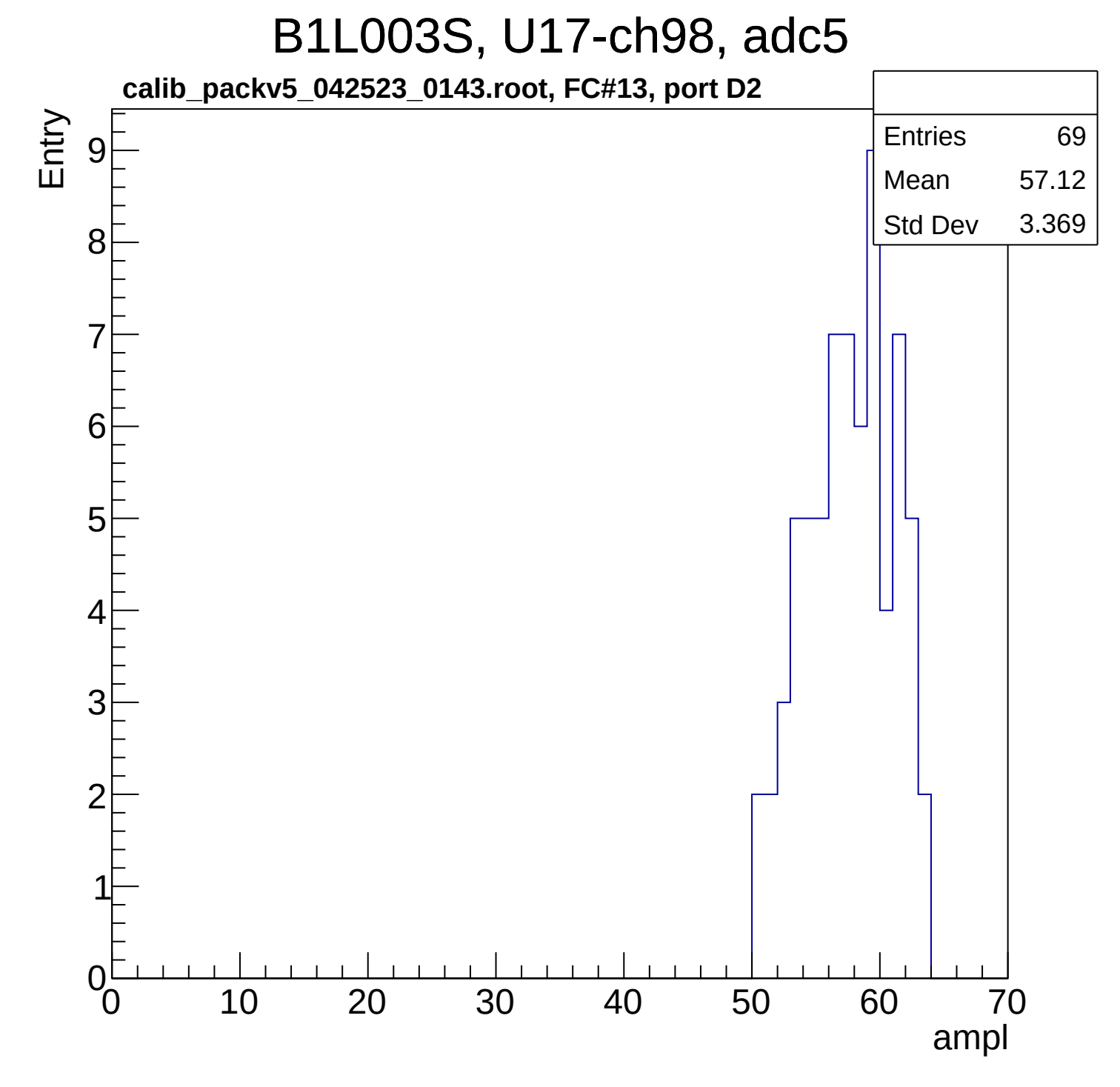

B1L003S, U17-ch98, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 51.29
Std Dev 3.202
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch98, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 57.12
Std Dev 3.369
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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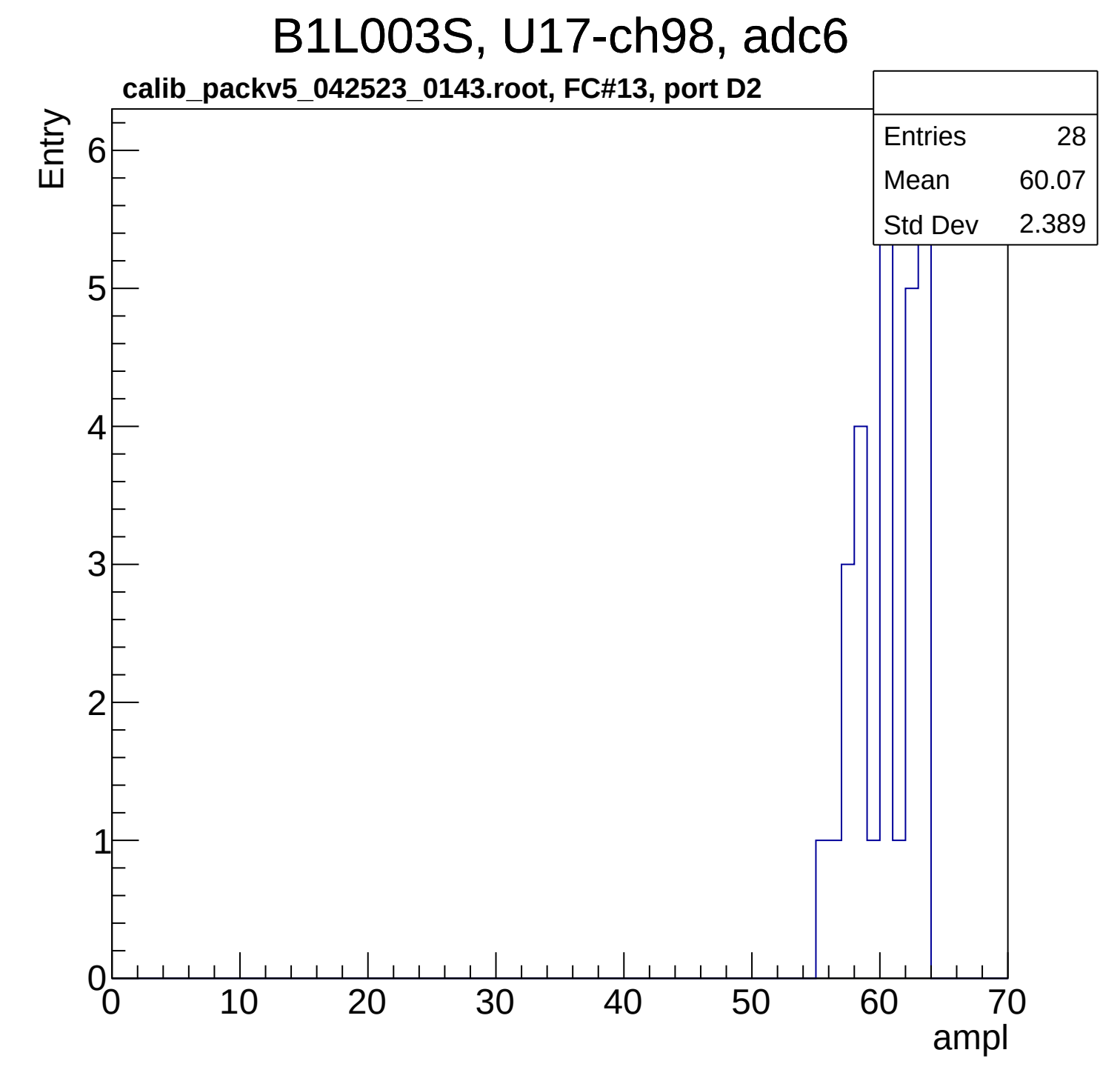
B1L003S, U17-ch98, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 28
Mean 60.07
Std Dev 2.389
Entry
ampl
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1
2
3
4
5
6
0
10
20
30
40
50
60
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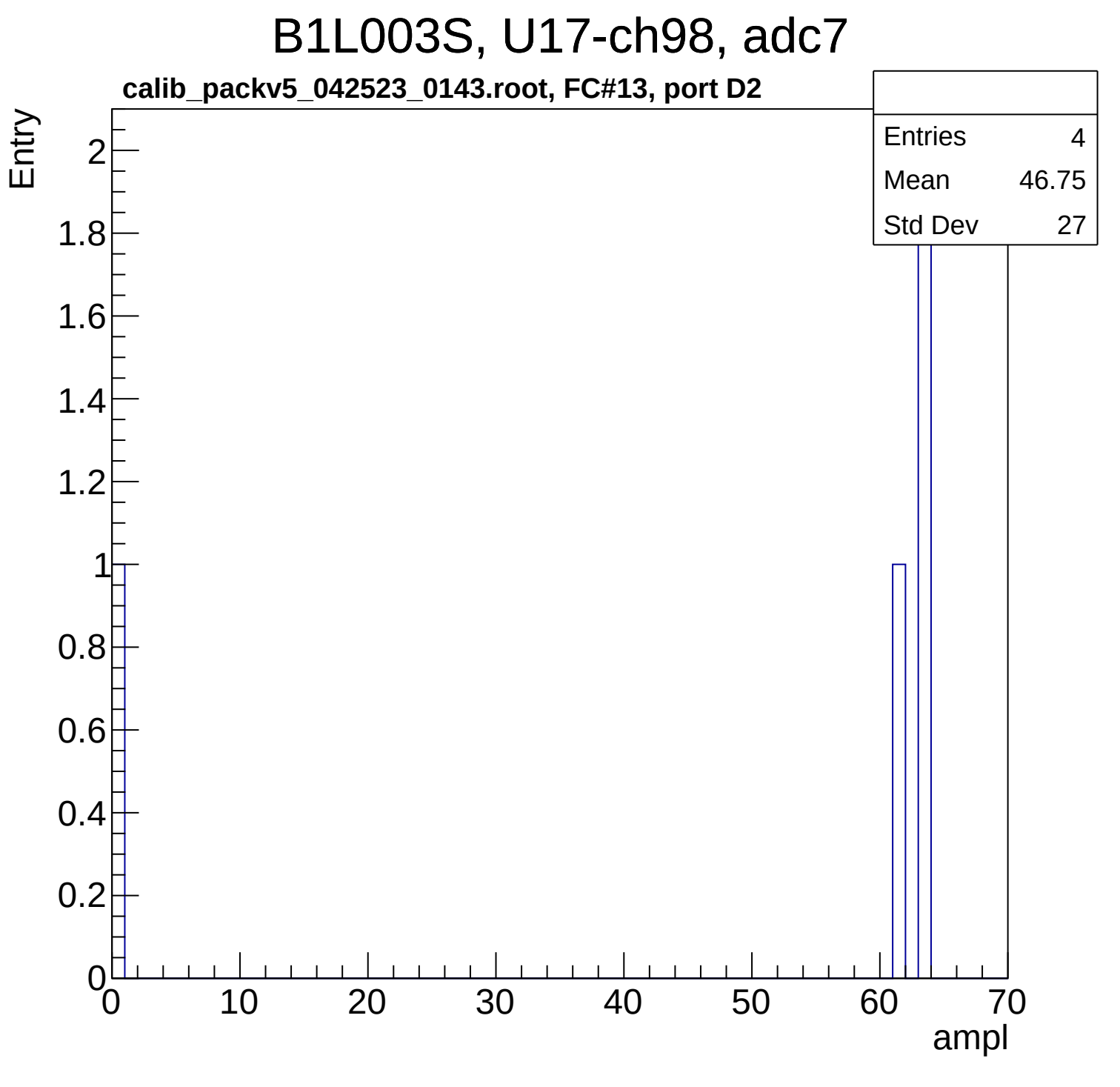
B1L003S, U17-ch98, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
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0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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60
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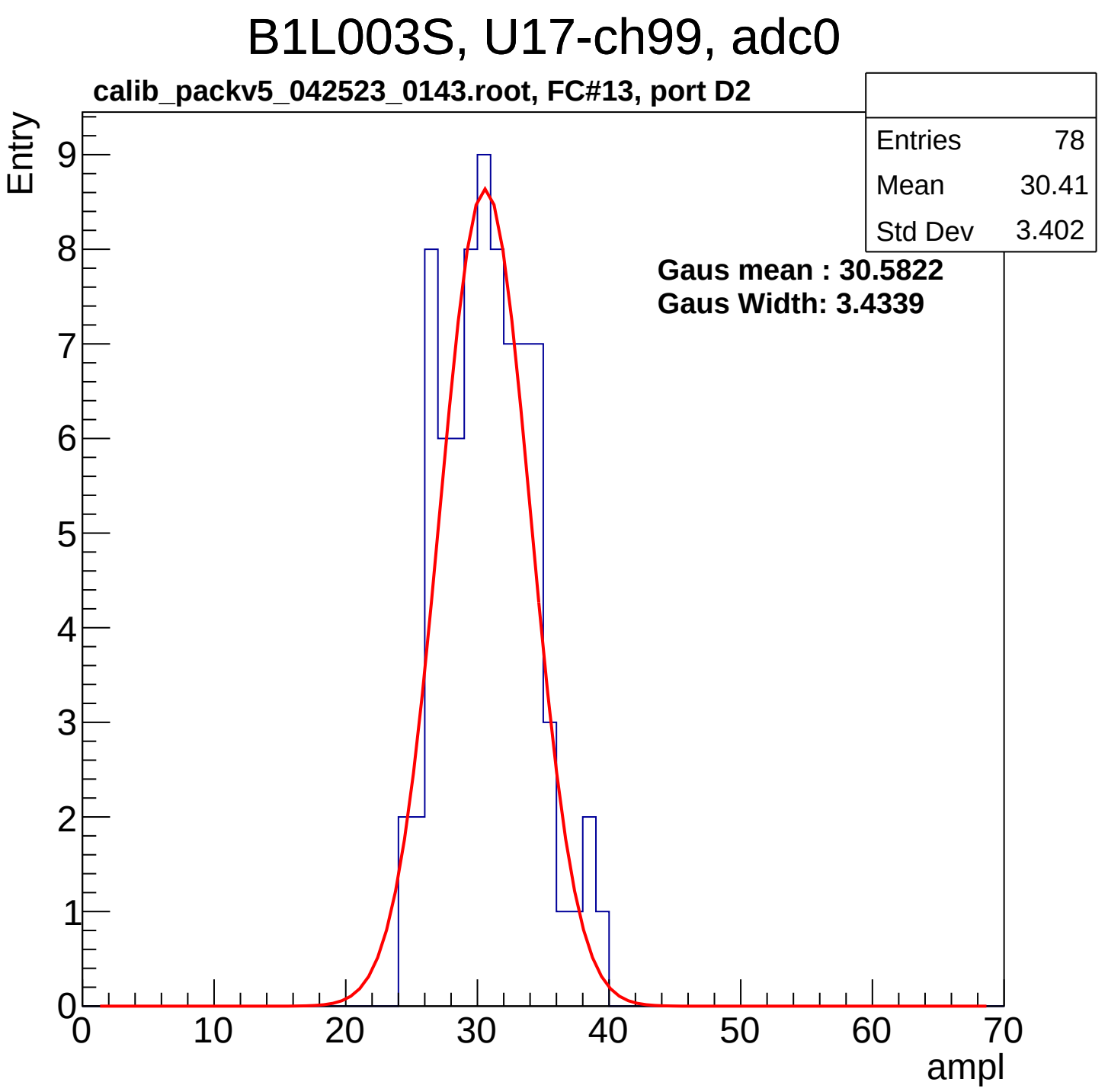
B1L003S, U17-ch99, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 30.41
Std Dev 3.402
Gaus mean : 30.5822
Gaus Width: 3.4339
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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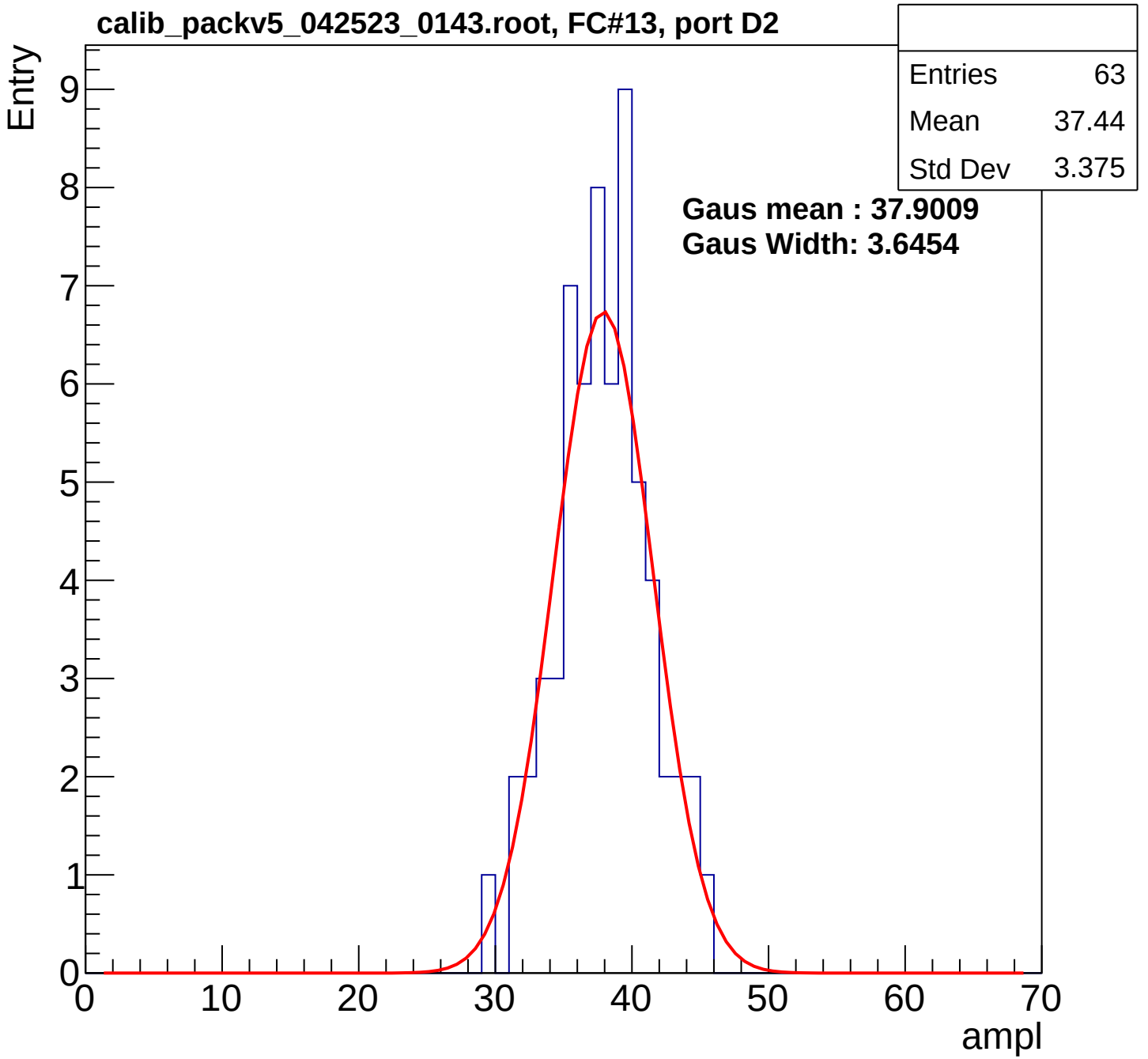
20
30
40
50
60
70


B1L003S, U17-ch99, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 37.44
Std Dev 3.375
Gaus mean : 37.9009
Gaus Width: 3.6454
Entry
ampl
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20
30
40
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1
2
3
4
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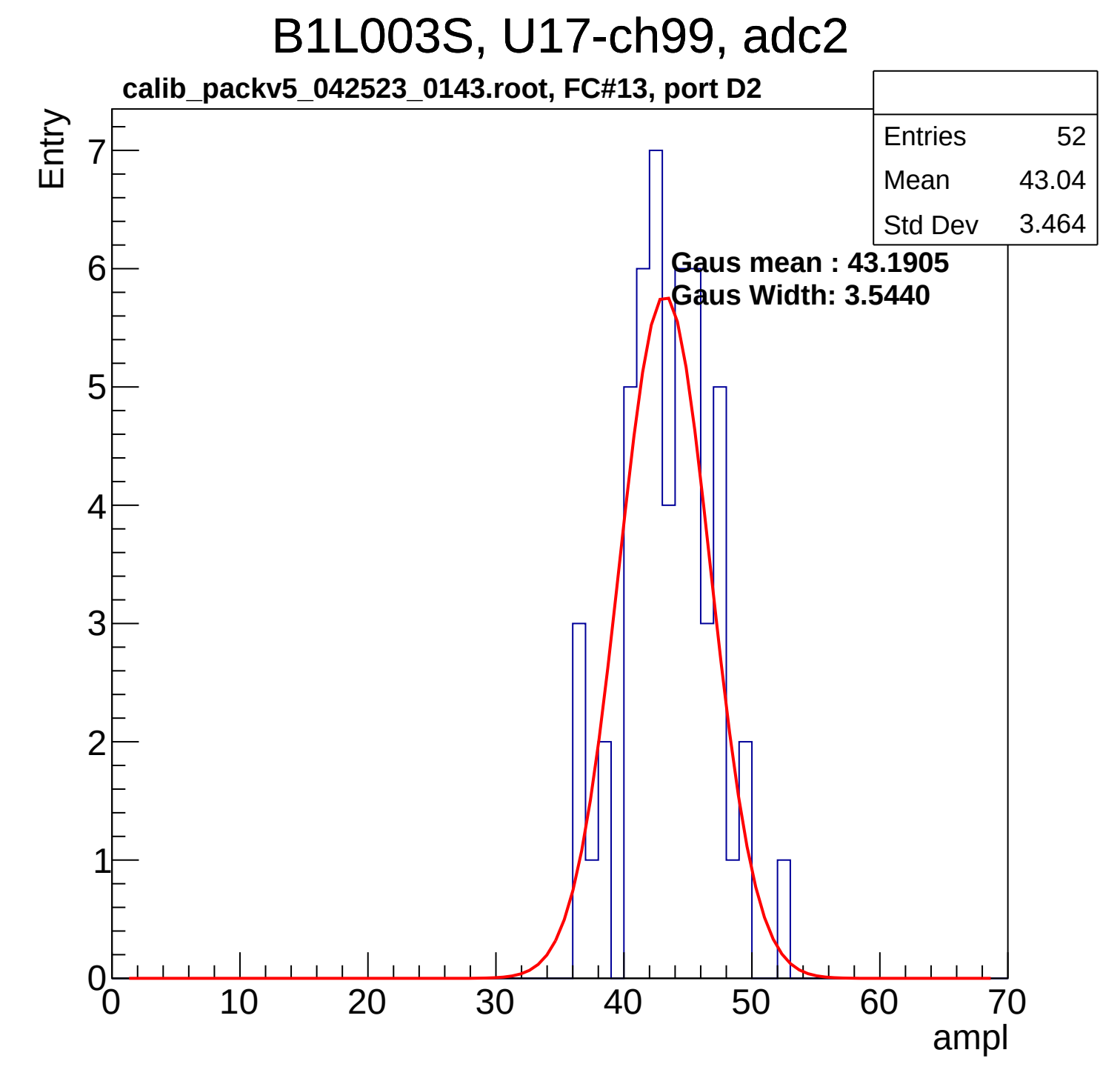
6
7
8
9


B1L003S, U17-ch99, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 43.04
Std Dev 3.464
Gaus mean : 43.1905
Gaus Width: 3.5440
Entry
ampl
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1
2
3
4
5
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0
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60
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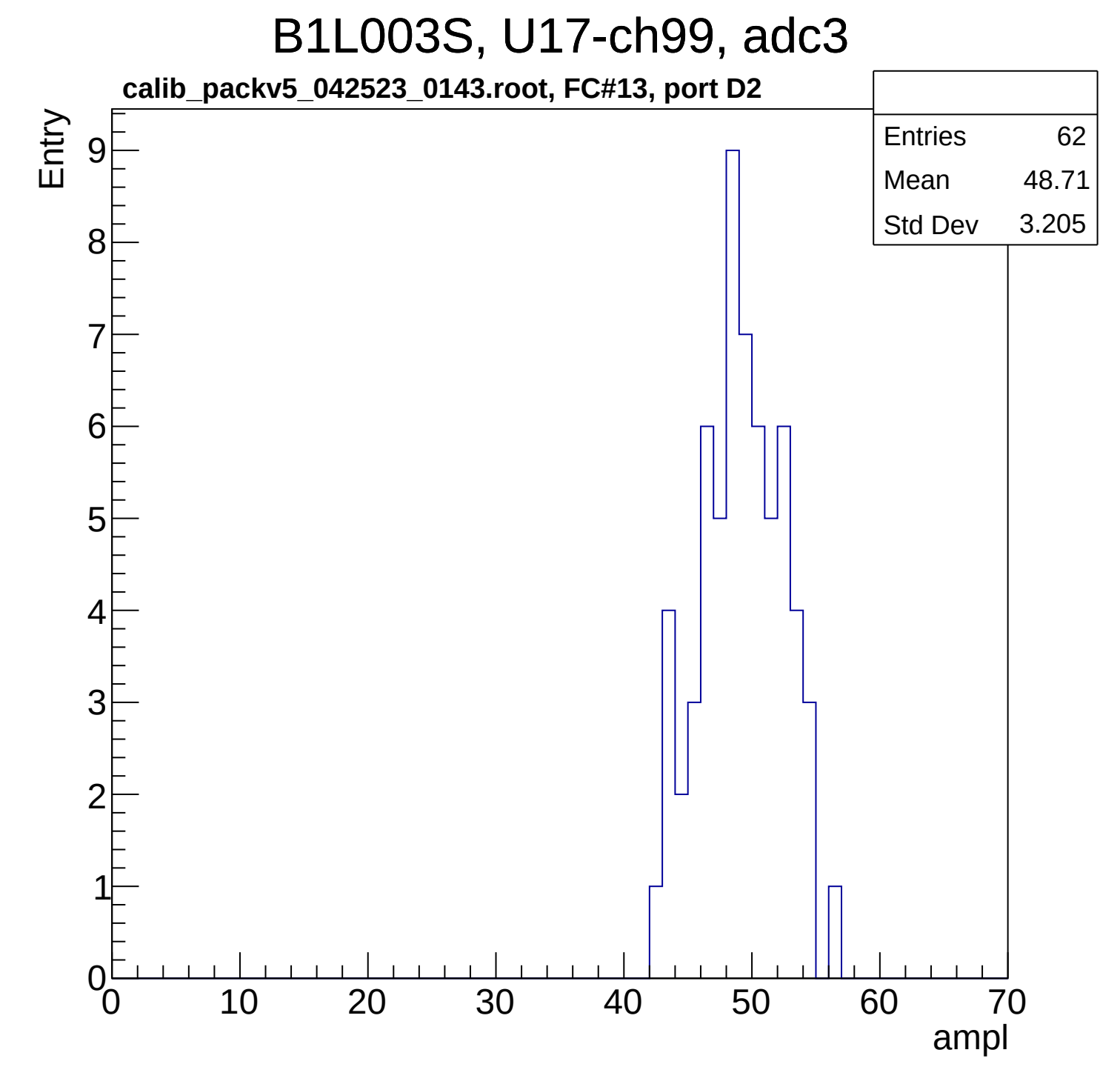
B1L003S, U17-ch99, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.71
Std Dev 3.205
Entry
ampl
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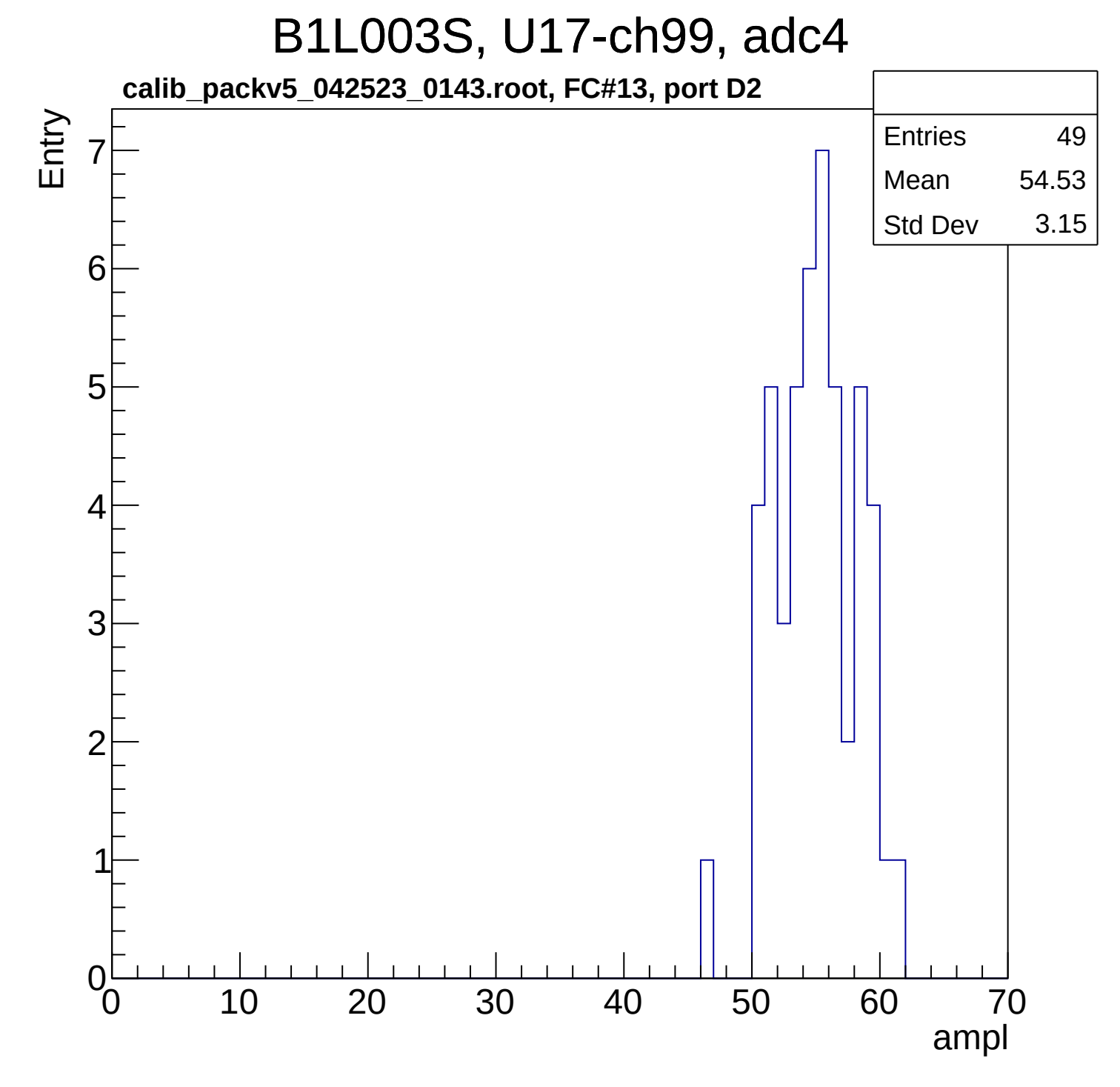
B1L003S, U17-ch99, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.53
Std Dev 3.15
Entry
ampl
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1
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0
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40
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60
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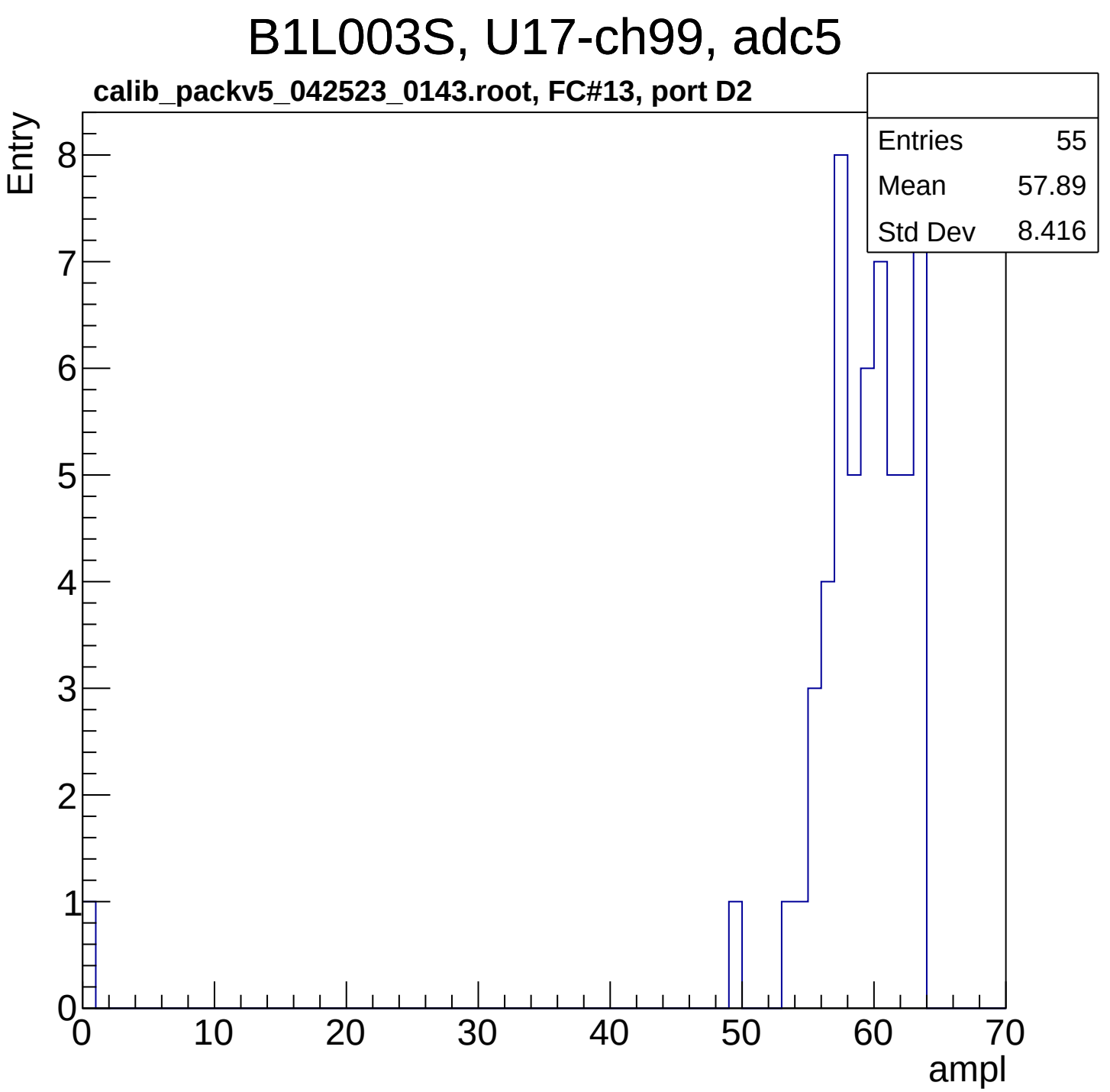
B1L003S, U17-ch99, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.89
Std Dev 8.416
Entry
ampl
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2
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4
5
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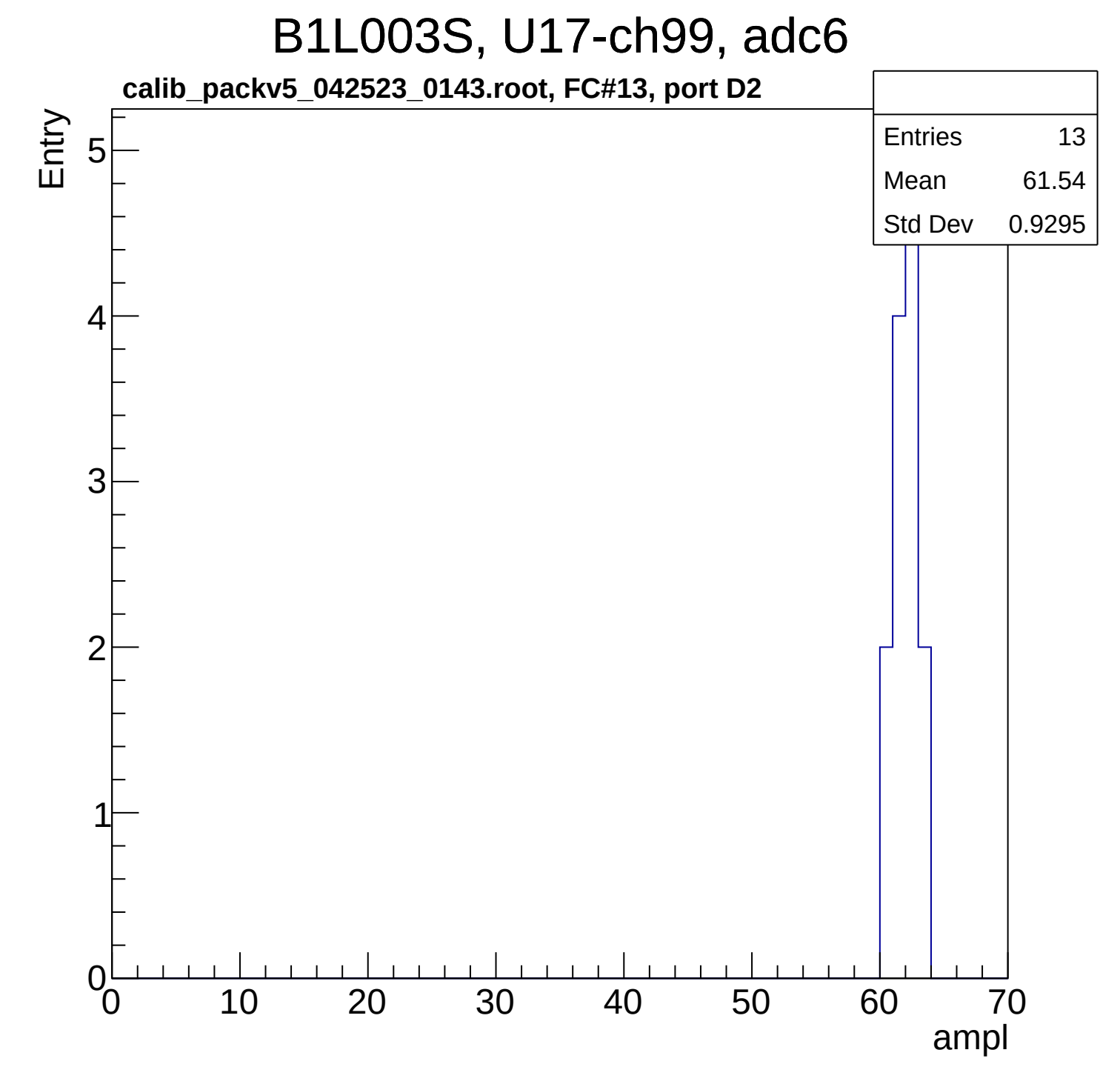
B1L003S, U17-ch99, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.54
Std Dev 0.9295
Entry
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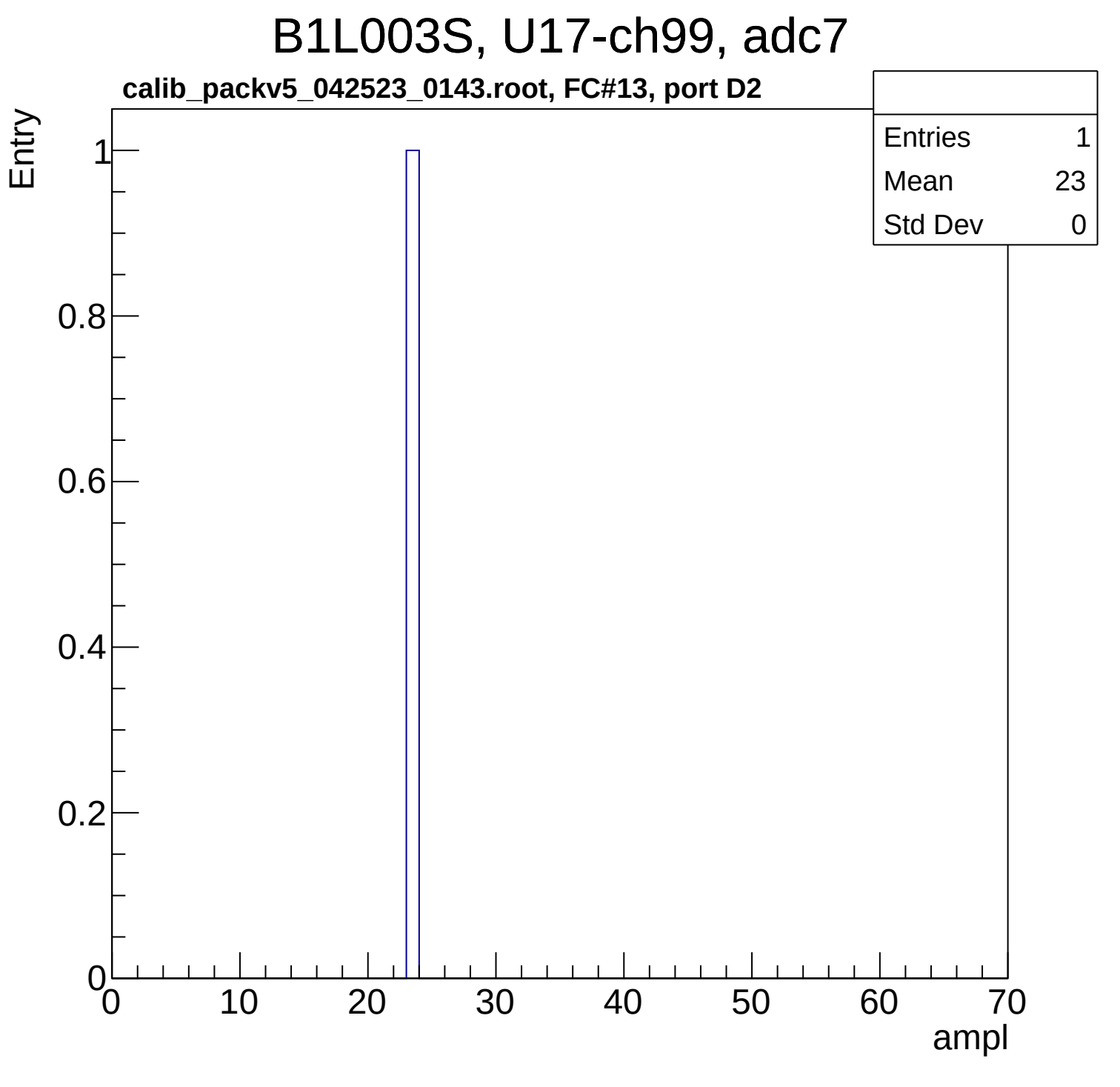
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch99, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
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0.2
0
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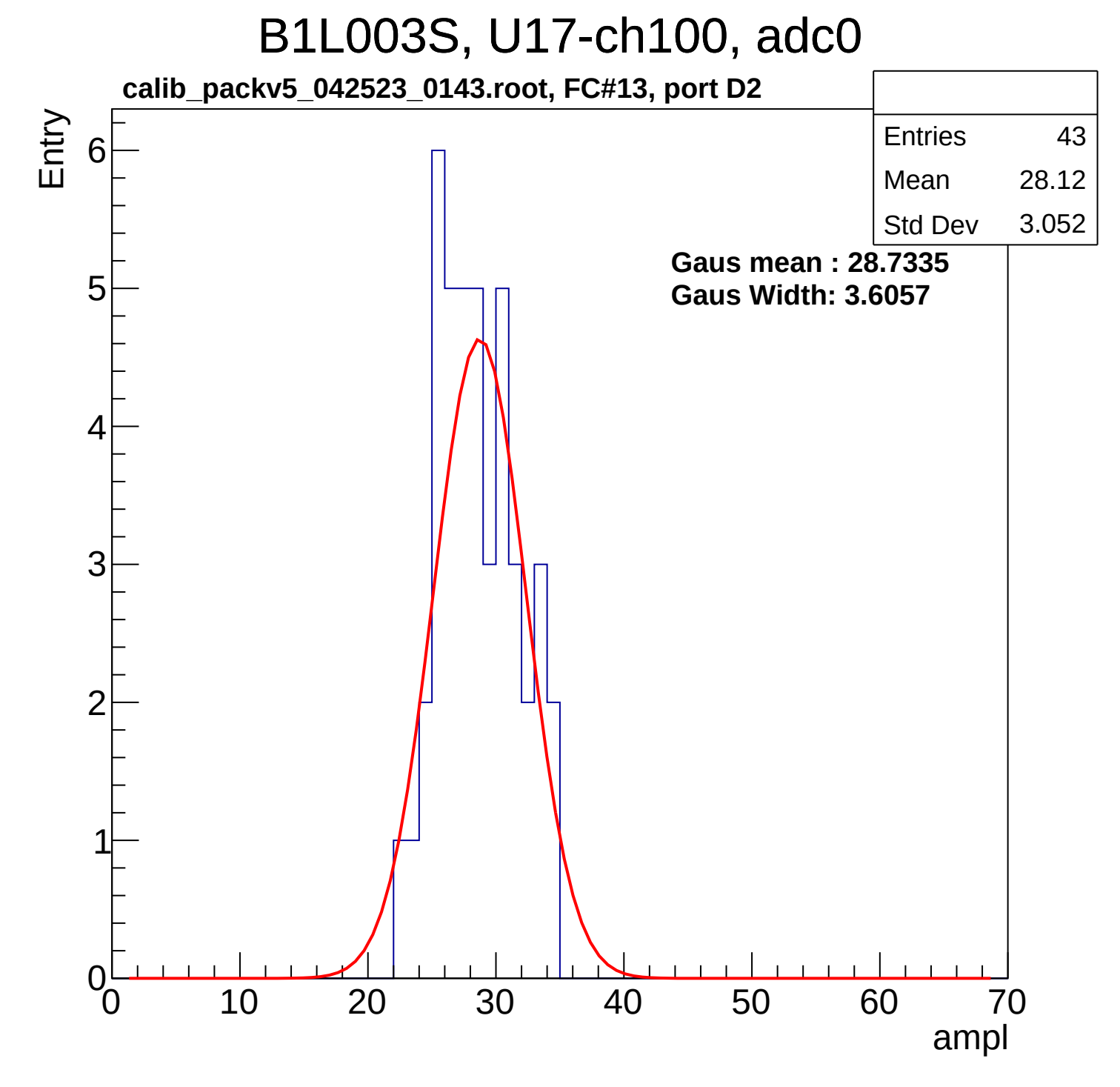
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch100, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 28.12
Std Dev 3.052
Gaus mean : 28.7335
Gaus Width: 3.6057
Entry
ampl
0
1
2
3
4
5
6
0
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20
30
40
50
60
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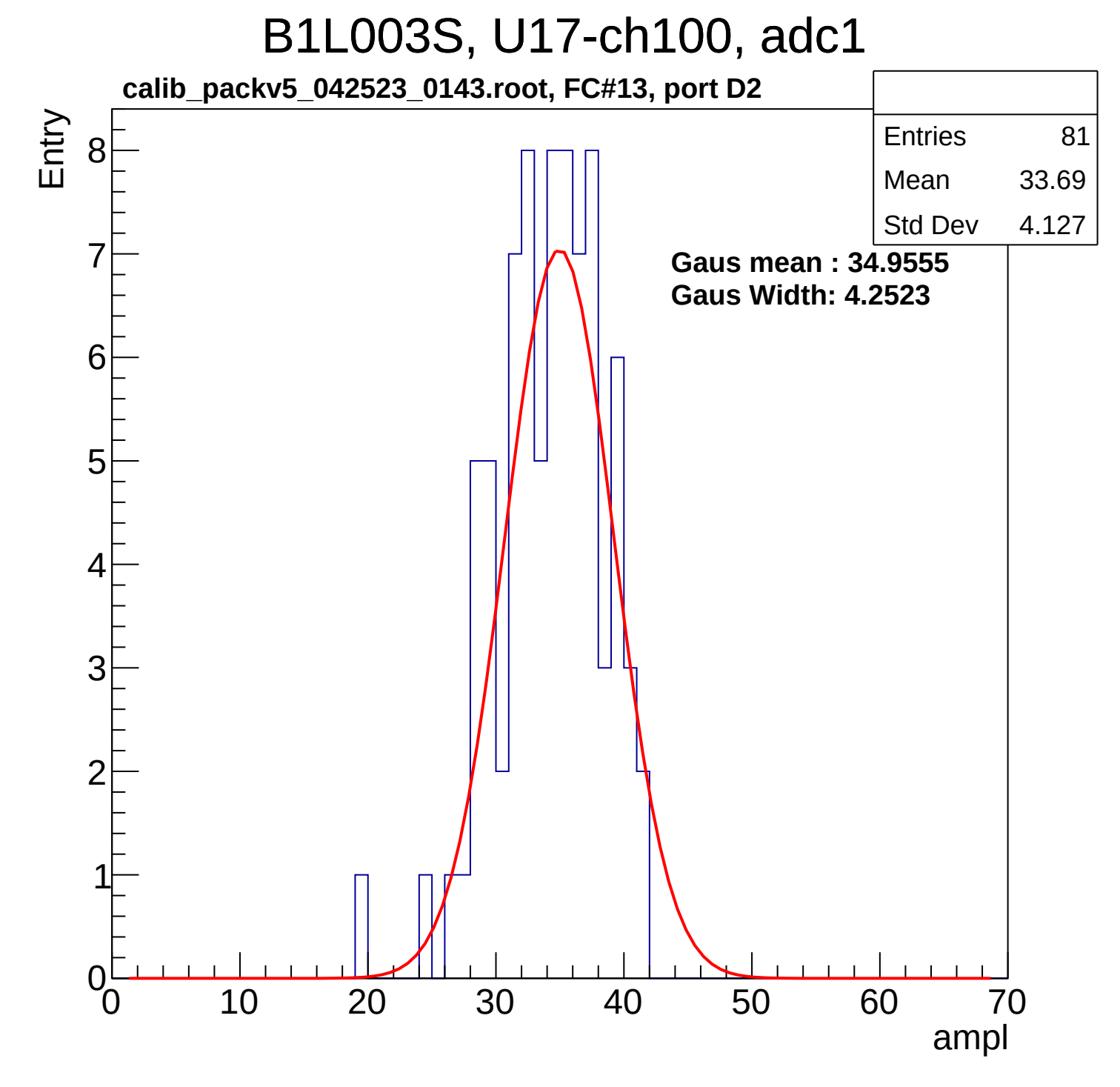
B1L003S, U17-ch100, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 33.69
Std Dev 4.127
Gaus mean : 34.9555
Gaus Width: 4.2523
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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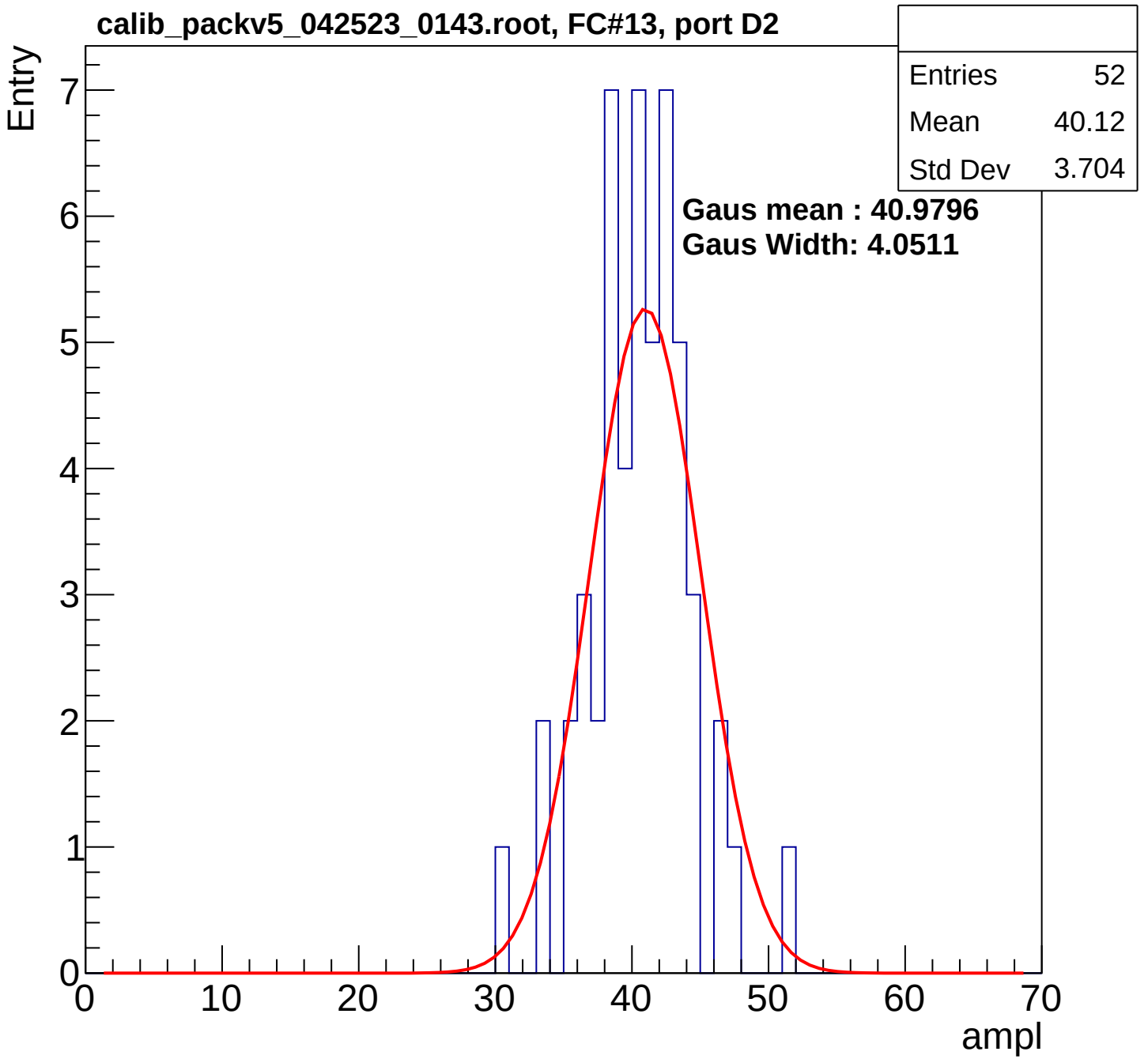
B1L003S, U17-ch100, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 40.12
Std Dev 3.704
Gaus mean : 40.9796
Gaus Width: 4.0511
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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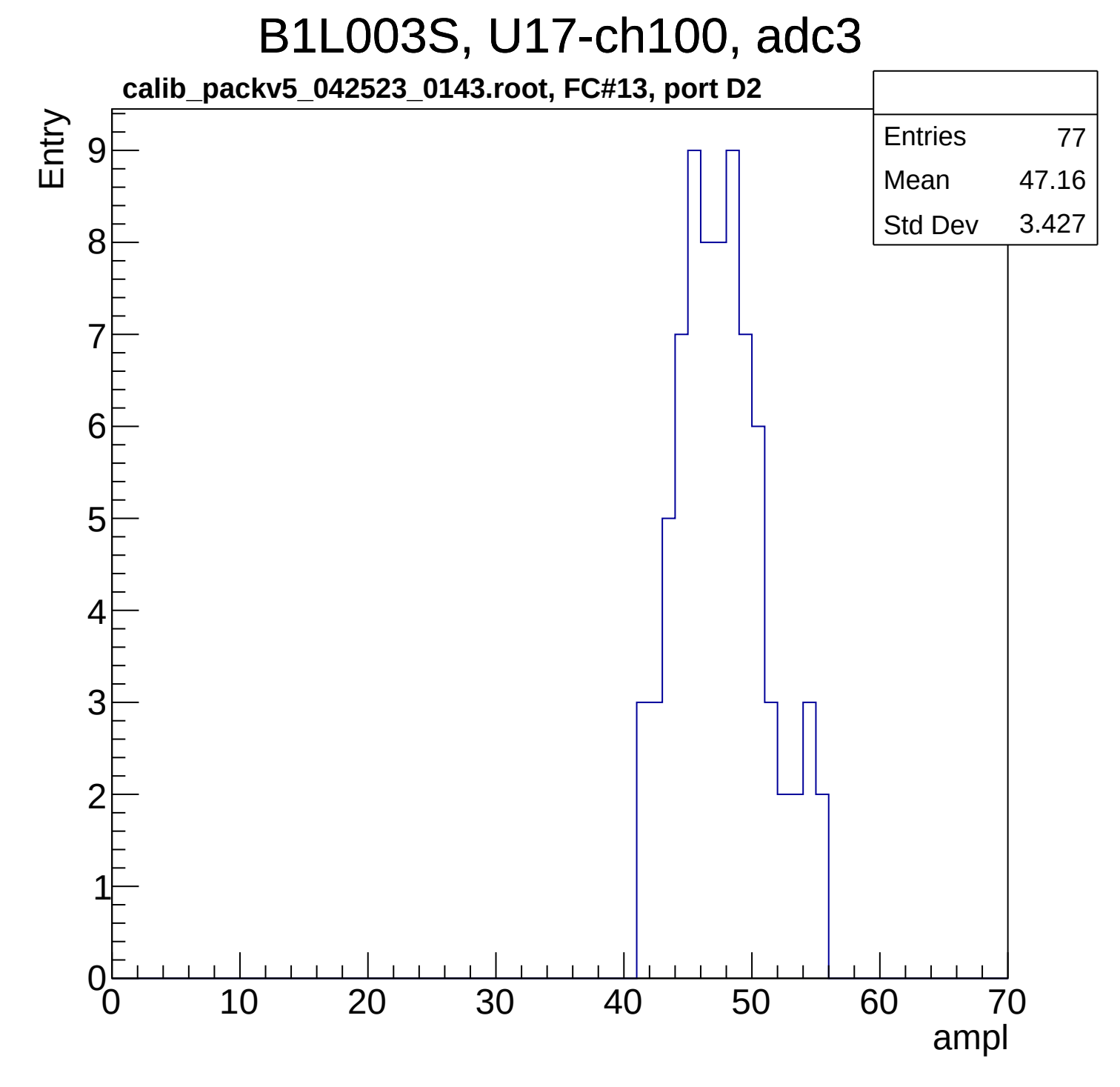
B1L003S, U17-ch100, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 47.16
Std Dev 3.427
Entry
ampl
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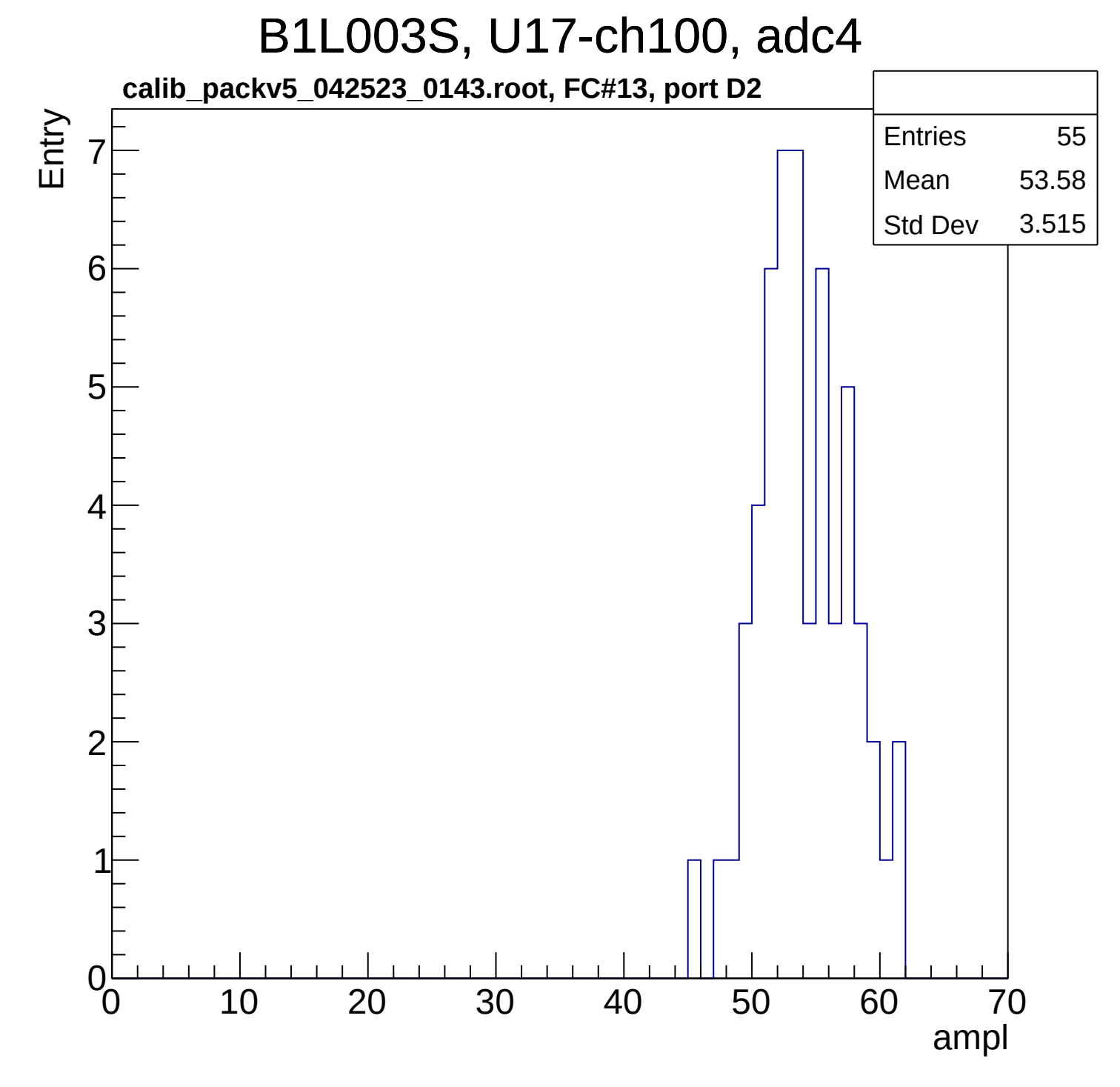
B1L003S, U17-ch100, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.58
Std Dev 3.515
Entry
ampl
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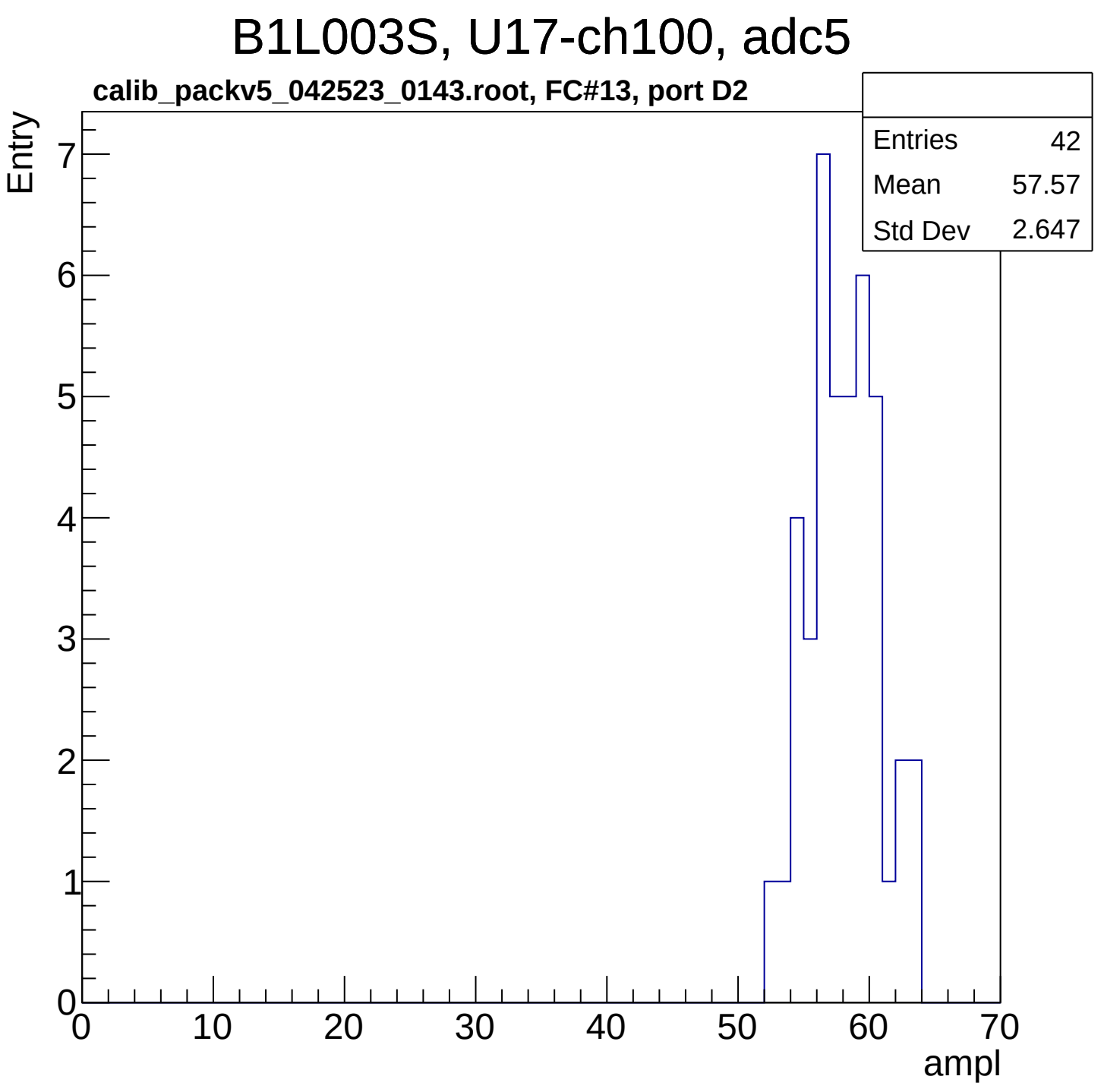
B1L003S, U17-ch100, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 57.57
Std Dev 2.647
Entry
ampl
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4
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20
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40
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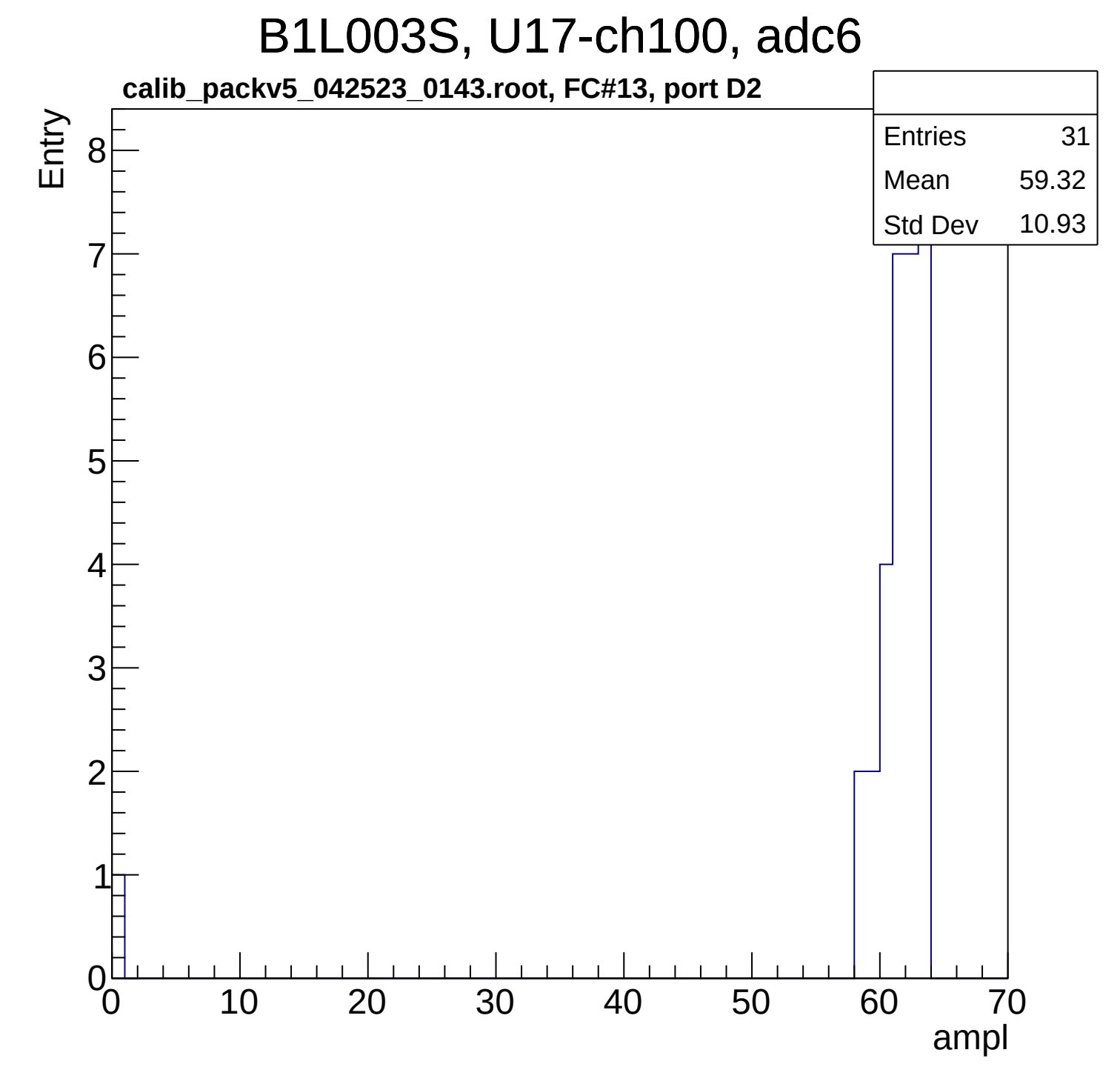
B1L003S, U17-ch100, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 31
Mean 59.32
Std Dev 10.93
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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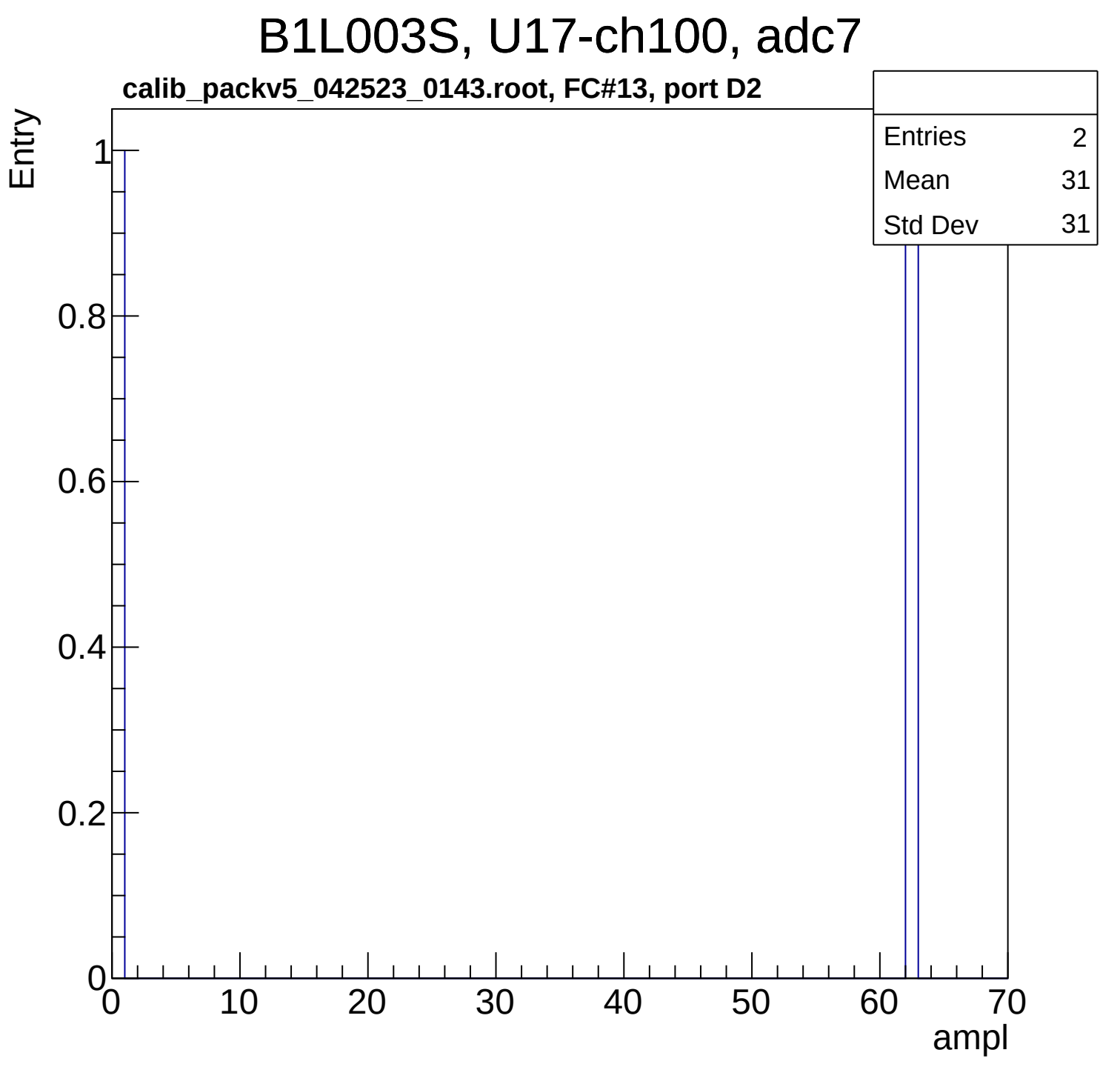
40
50
60
70


B1L003S, U17-ch100, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
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0.6
0.4
0.2
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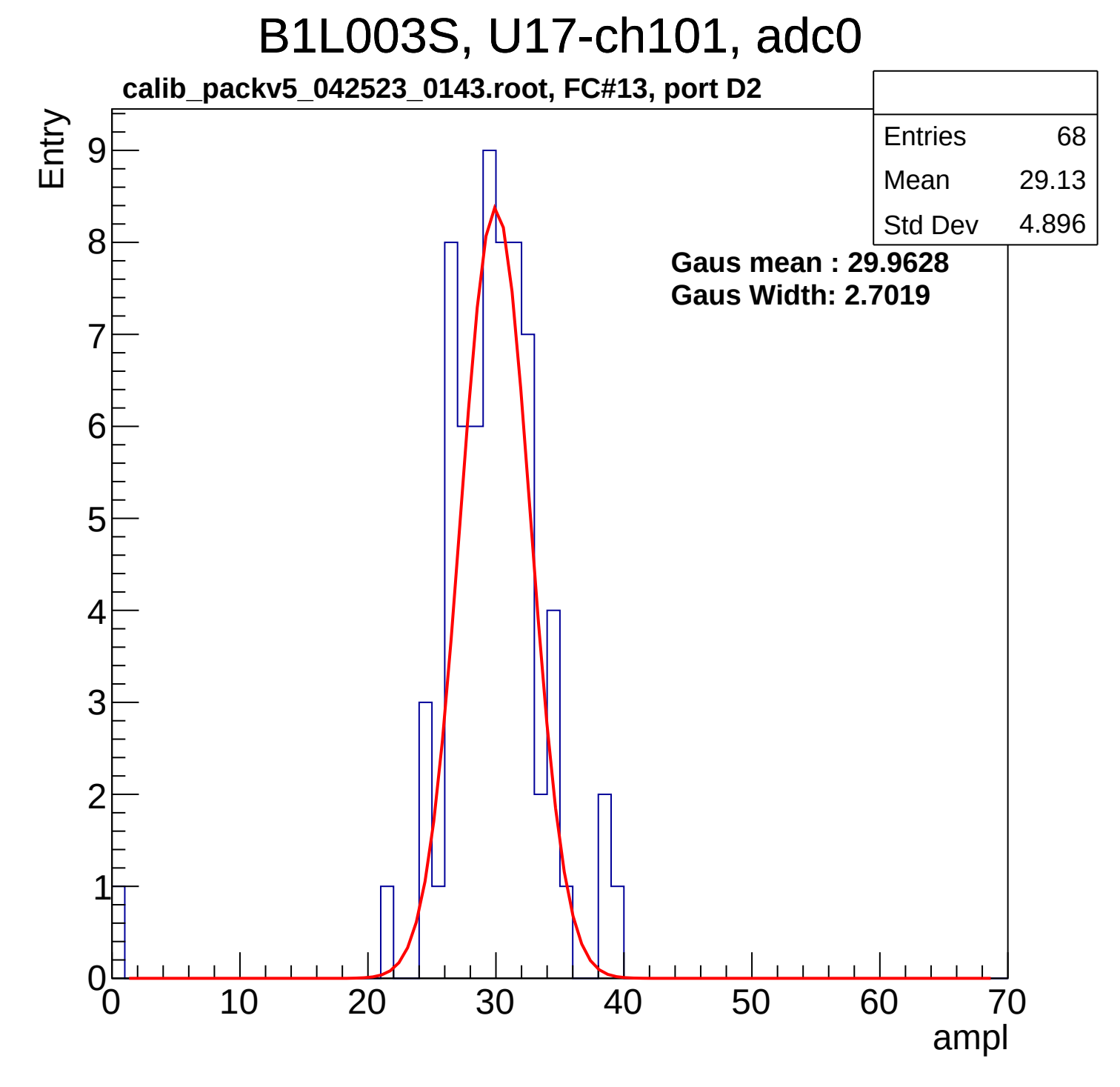
70
ampl


B1L003S, U17-ch101, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.13
Std Dev 4.896
Gaus mean : 29.9628
Gaus Width: 2.7019
Entry
ampl
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2
3
4
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6
7
8
9
0
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40
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60
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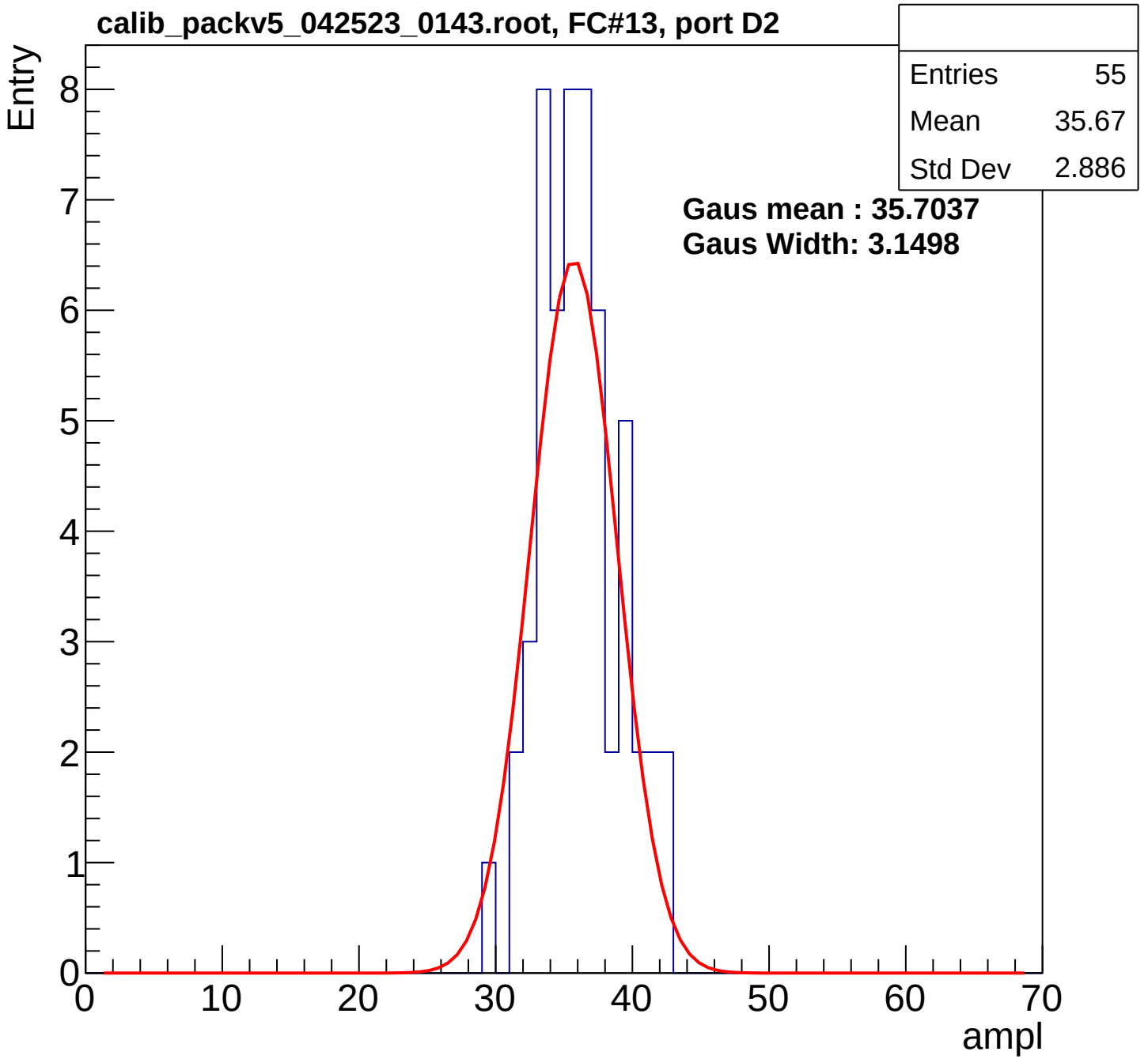
B1L003S, U17-ch101, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 35.67
Std Dev 2.886
Gaus mean : 35.7037
Gaus Width: 3.1498
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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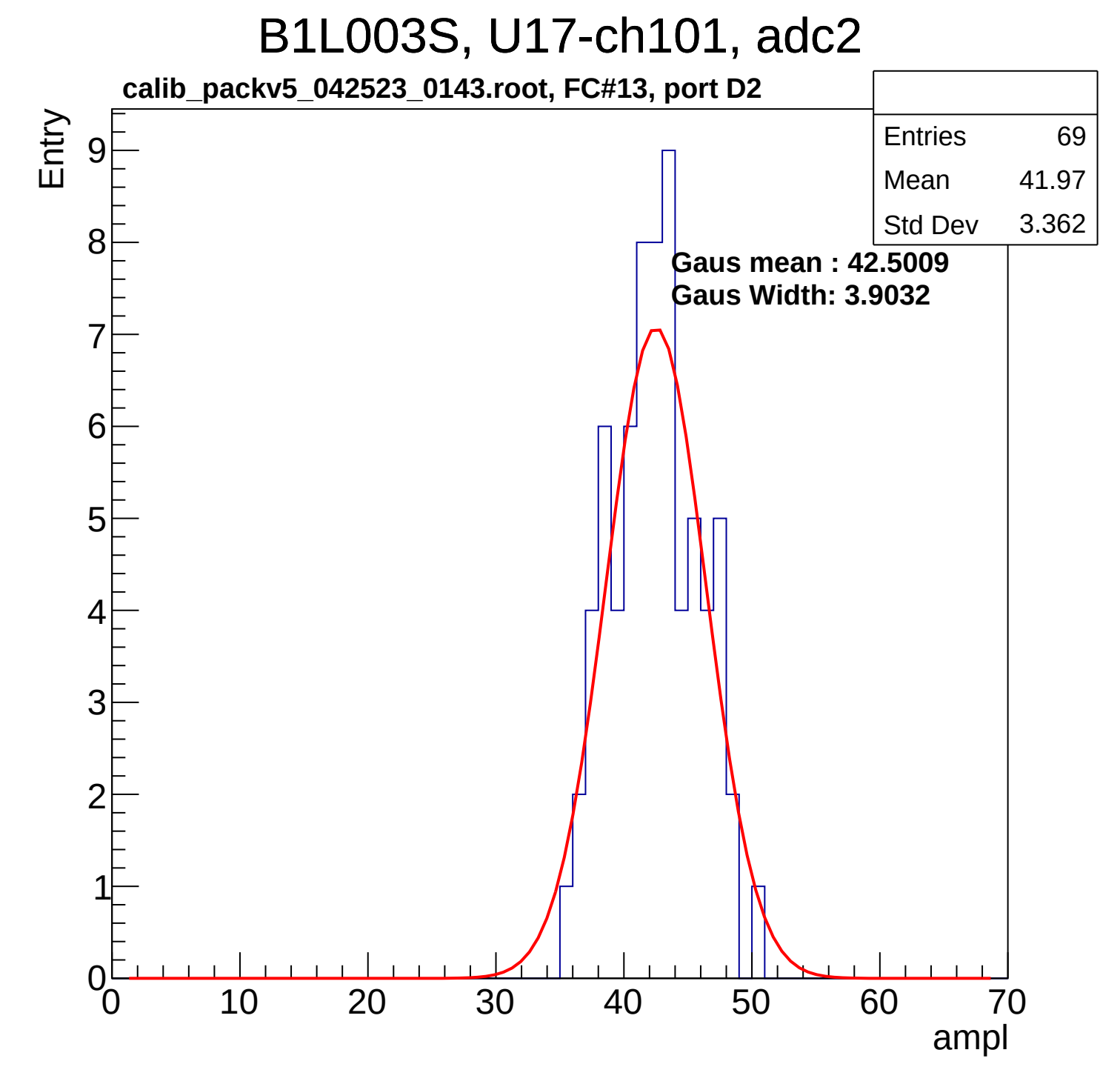
B1L003S, U17-ch101, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 41.97
Std Dev 3.362
Gaus mean : 42.5009
Gaus Width: 3.9032
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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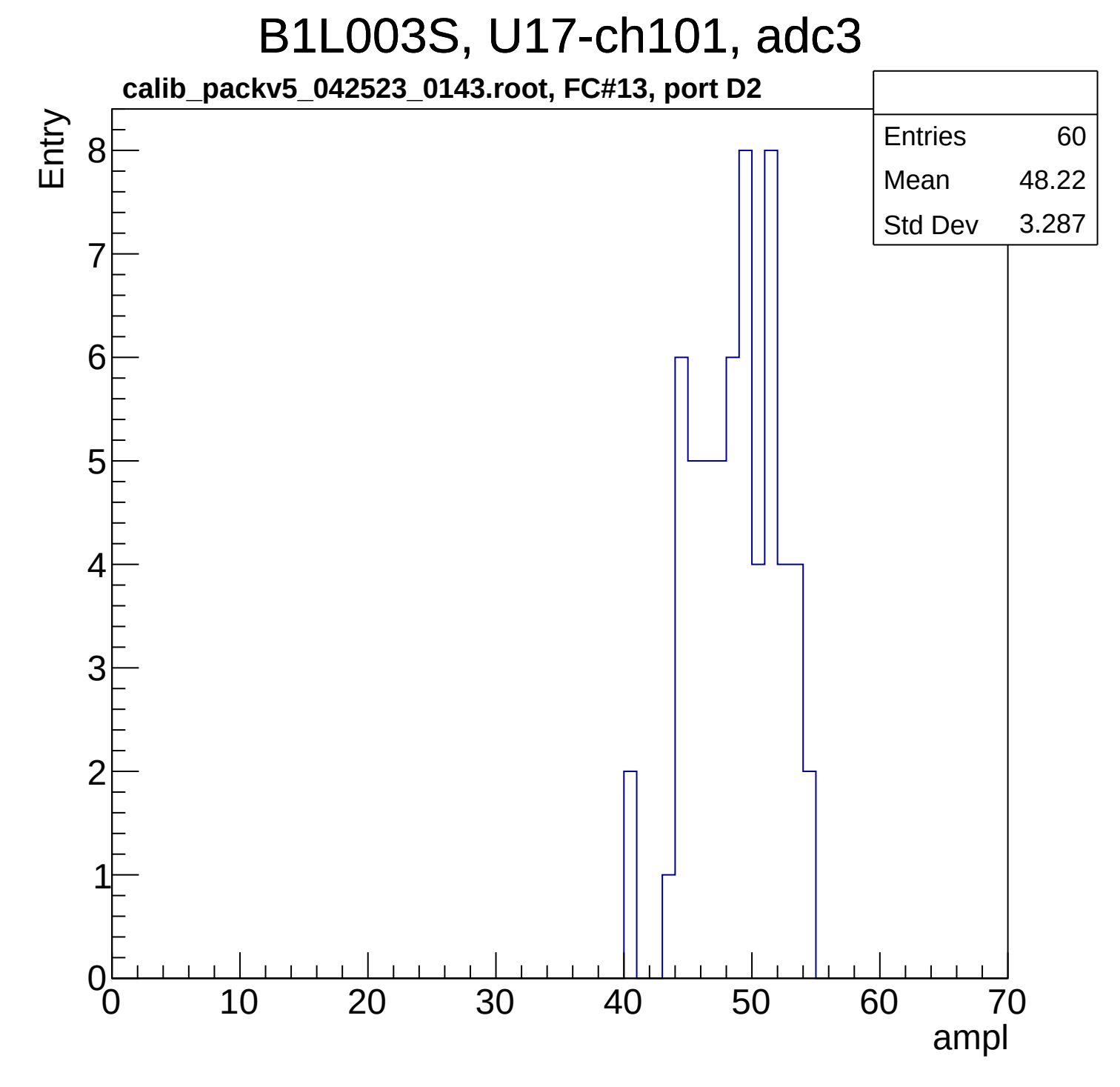
B1L003S, U17-ch101, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 48.22
Std Dev 3.287
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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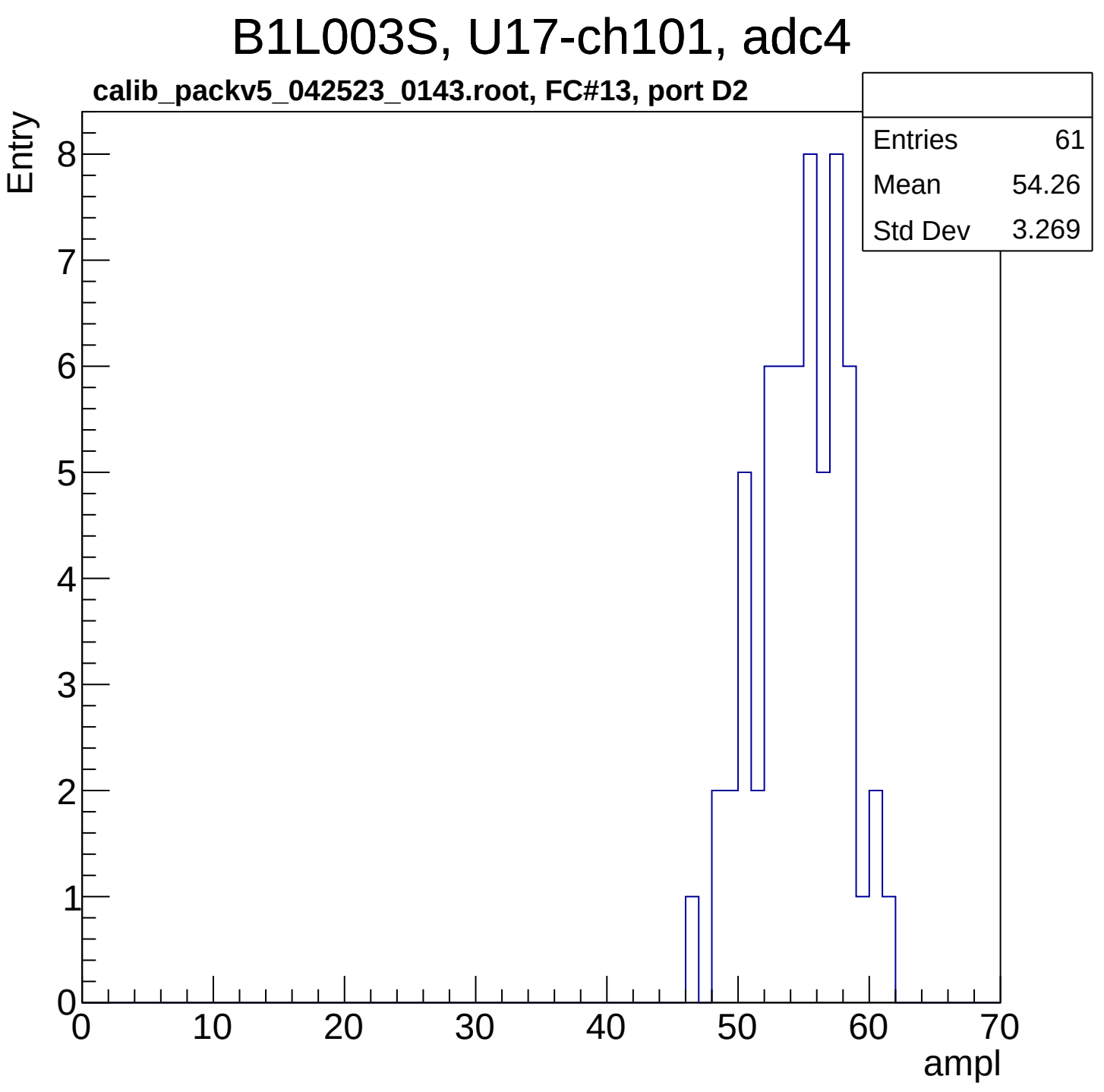
B1L003S, U17-ch101, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.26
Std Dev 3.269
Entry
ampl
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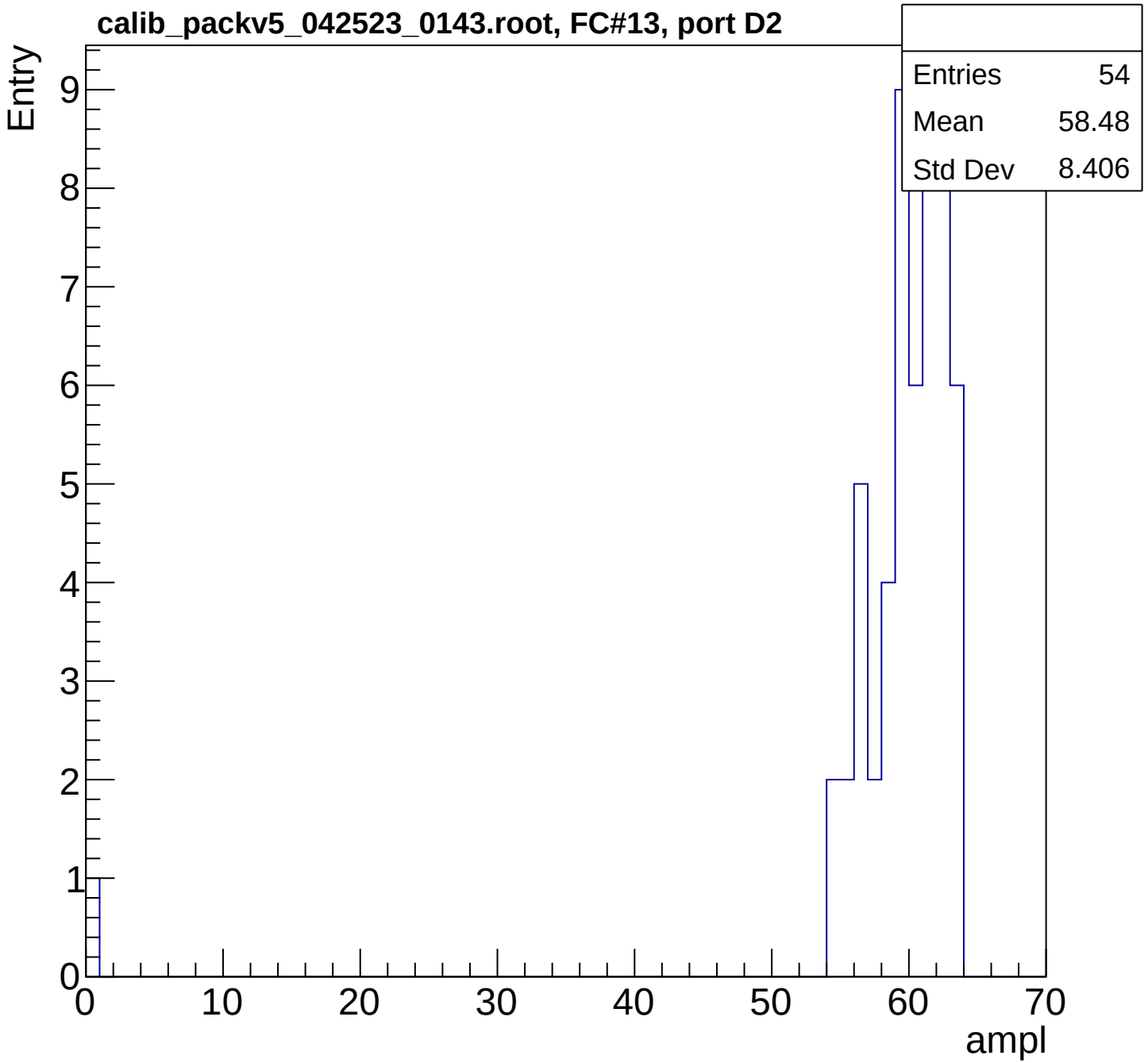
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch101, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.48
Std Dev 8.406
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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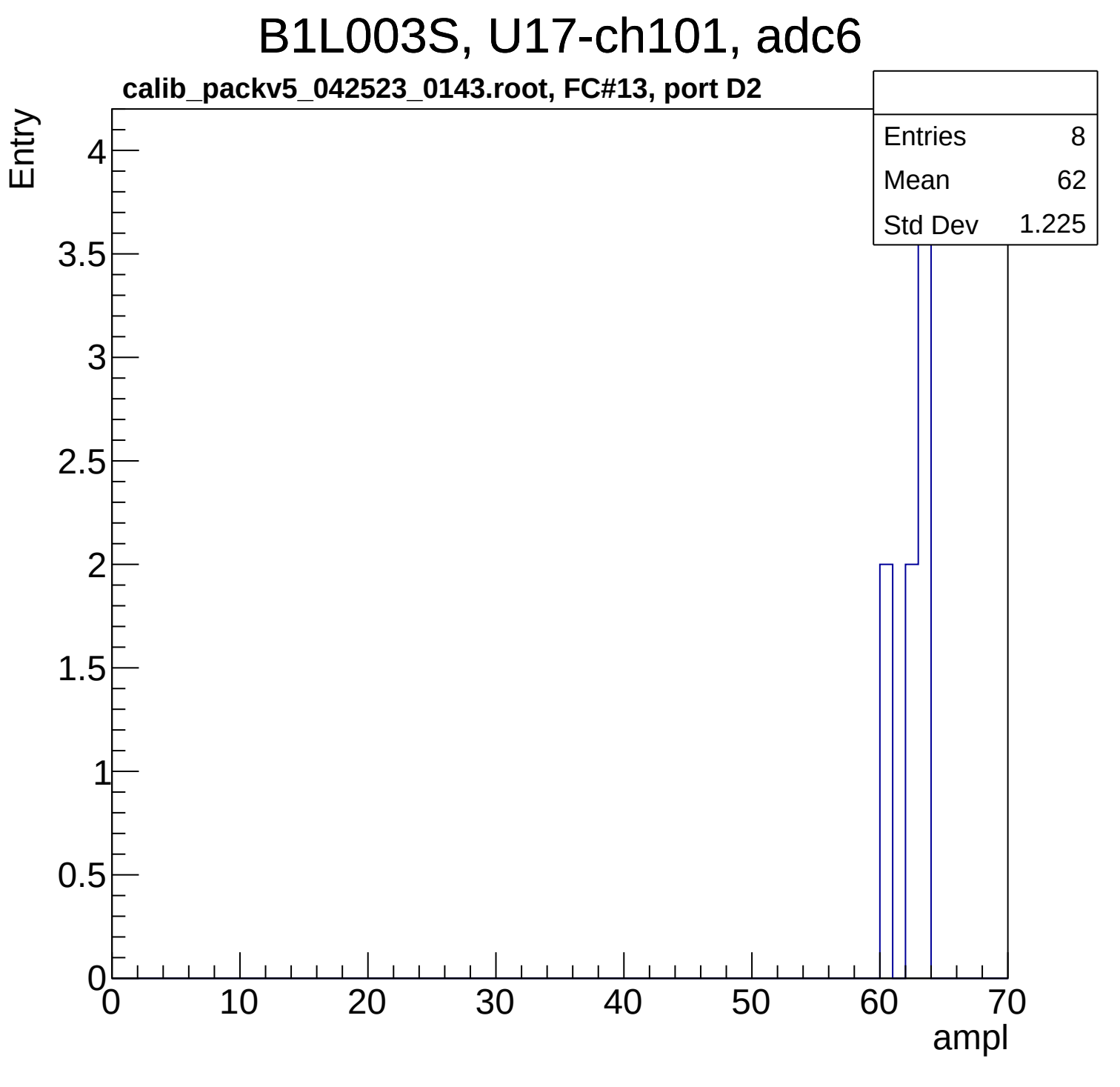
B1L003S, U17-ch101, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62
Std Dev 1.225
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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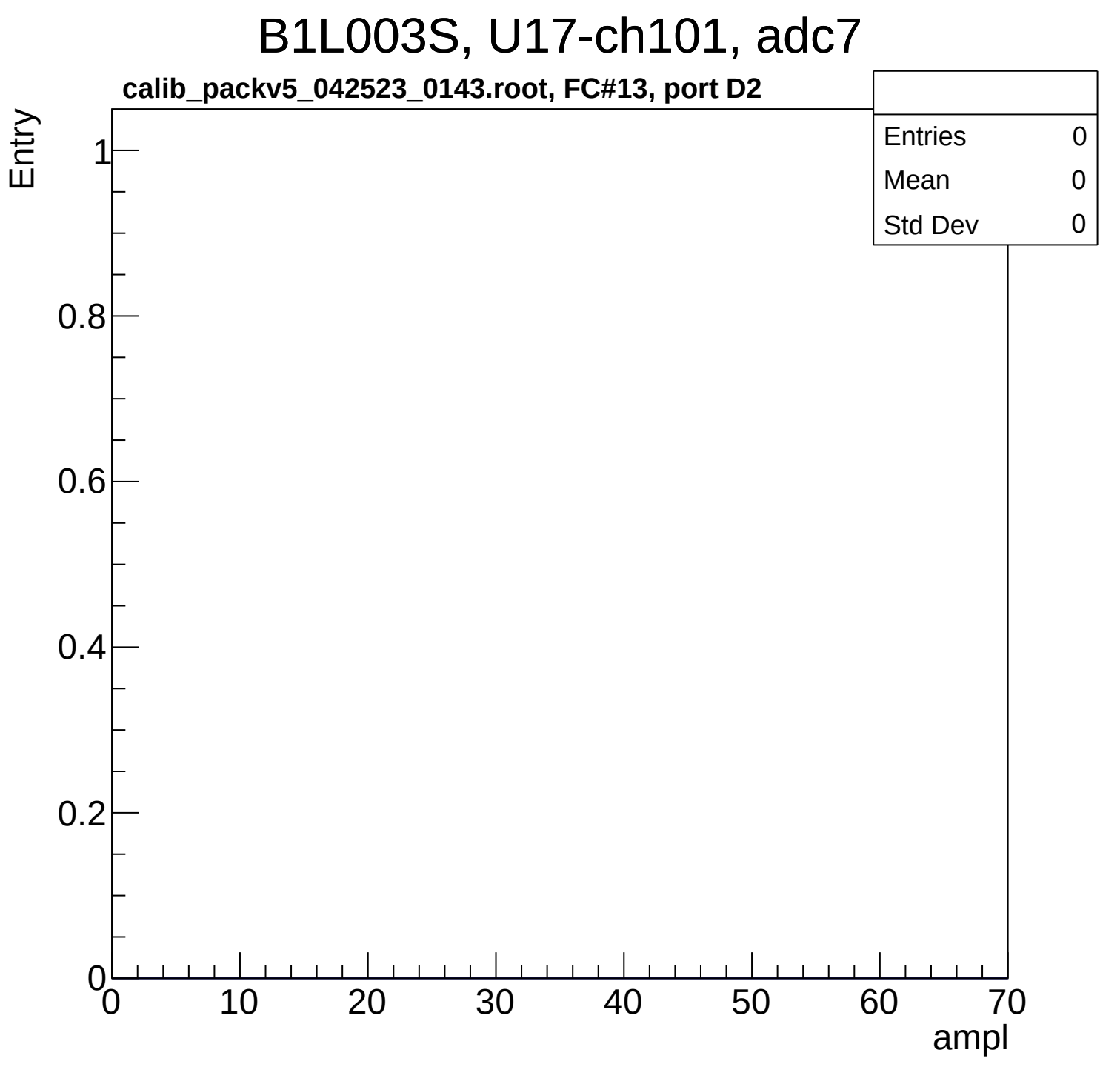
40
50
60
70


B1L003S, U17-ch101, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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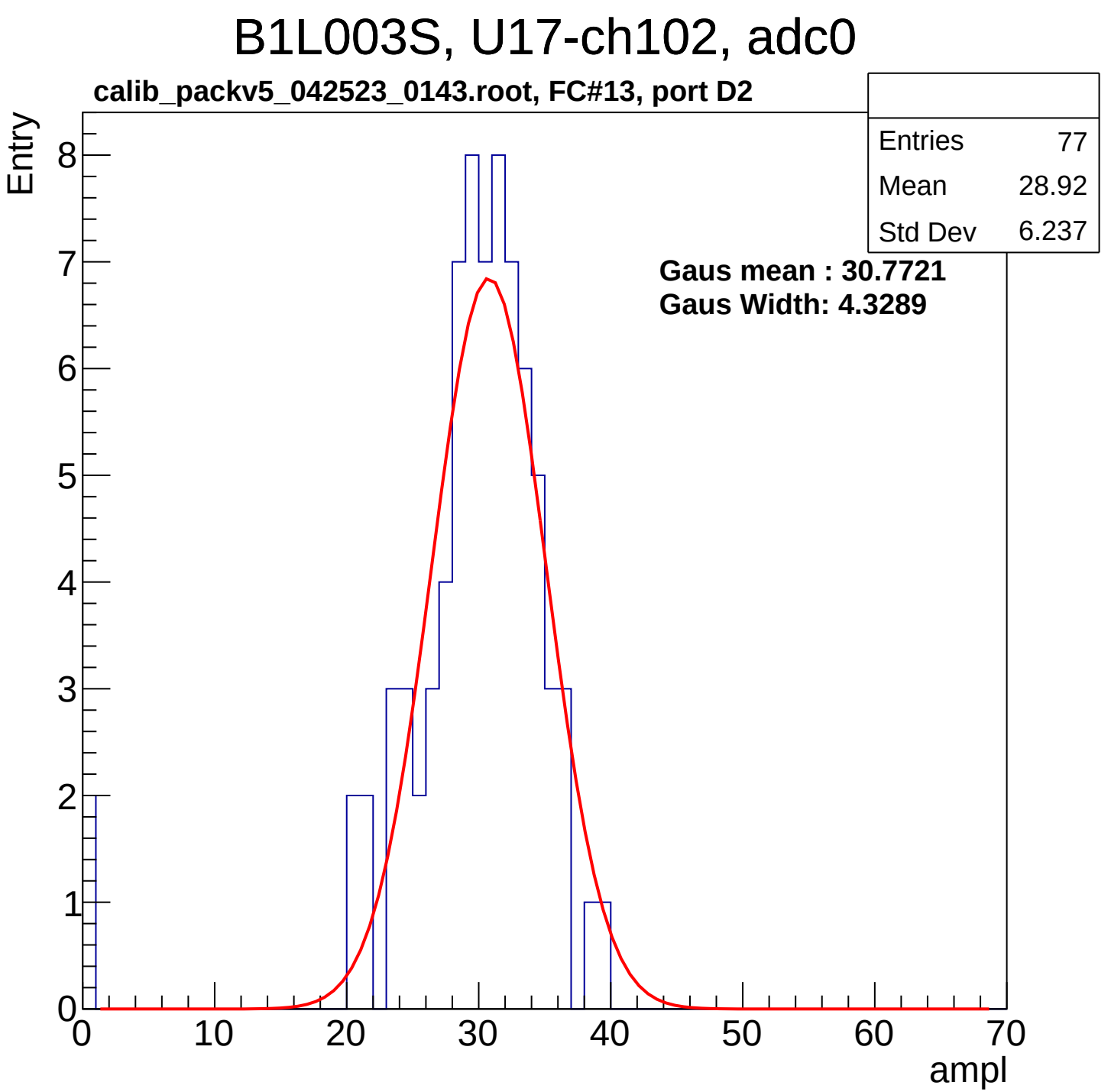
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch102, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.92
Std Dev 6.237
Gaus mean : 30.7721
Gaus Width: 4.3289
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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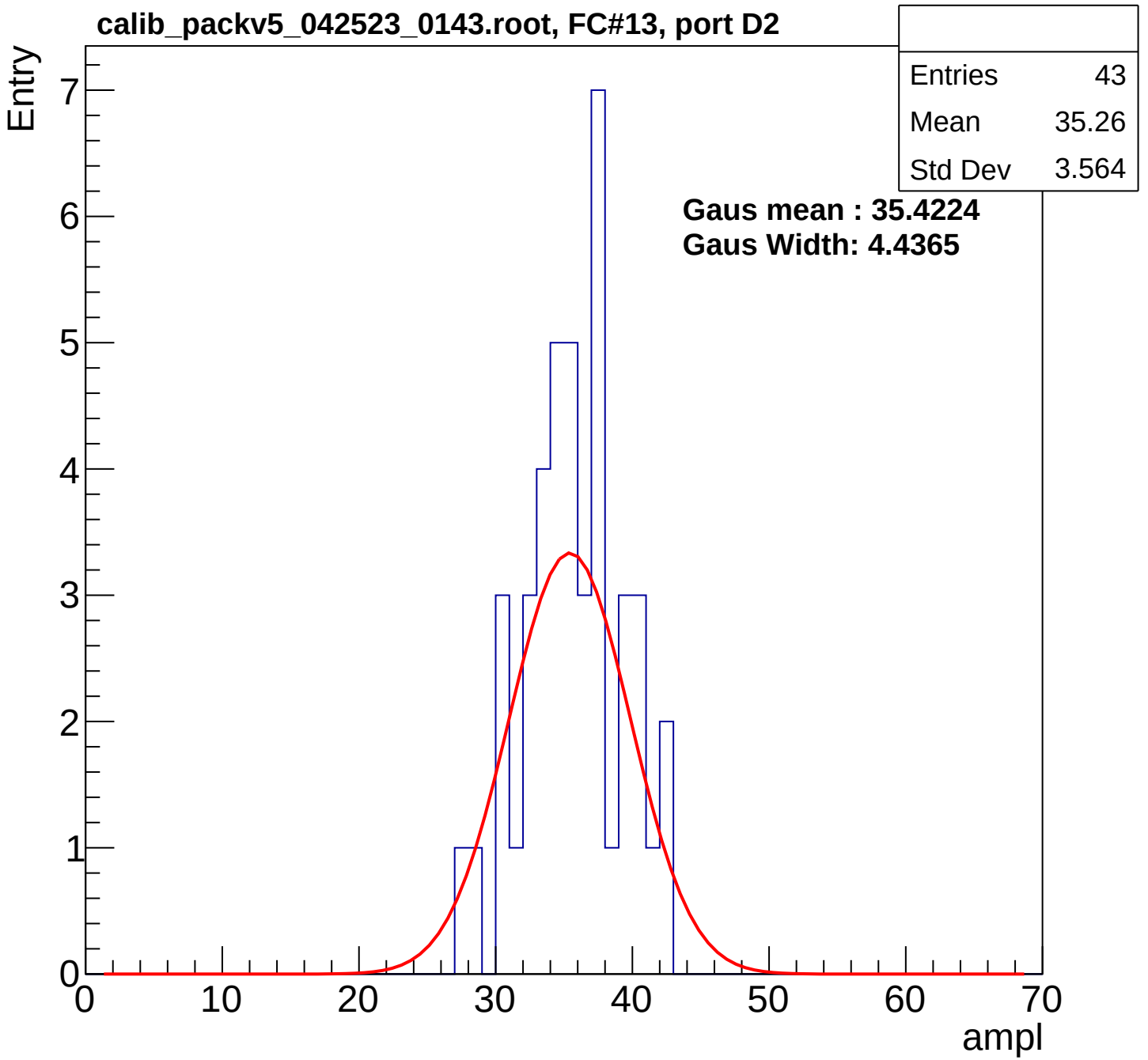
B1L003S, U17-ch102, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 35.26
Std Dev 3.564
Gaus mean : 35.4224
Gaus Width: 4.4365
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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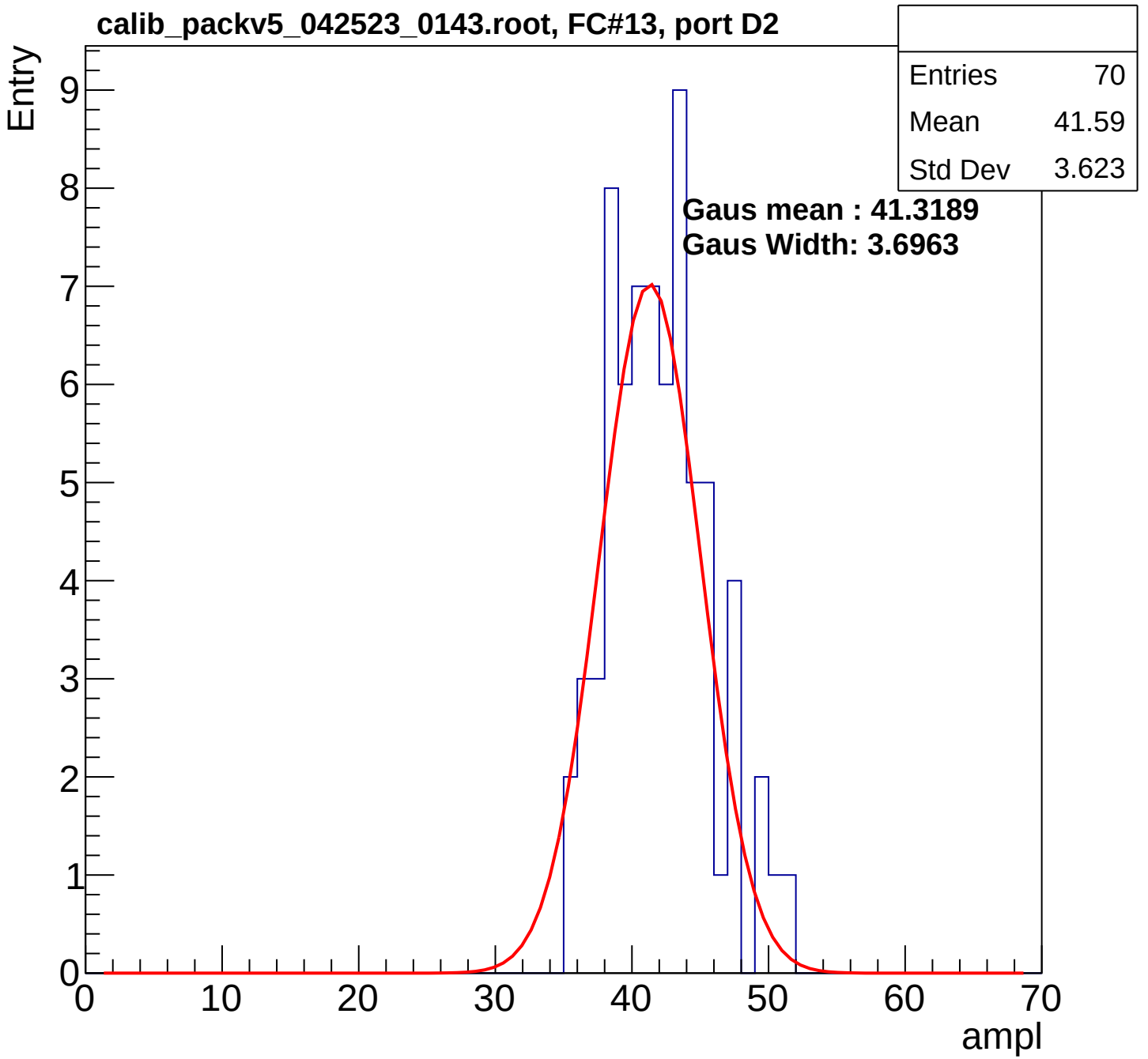
B1L003S, U17-ch102, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.59
Std Dev 3.623
Gaus mean : 41.3189
Gaus Width: 3.6963
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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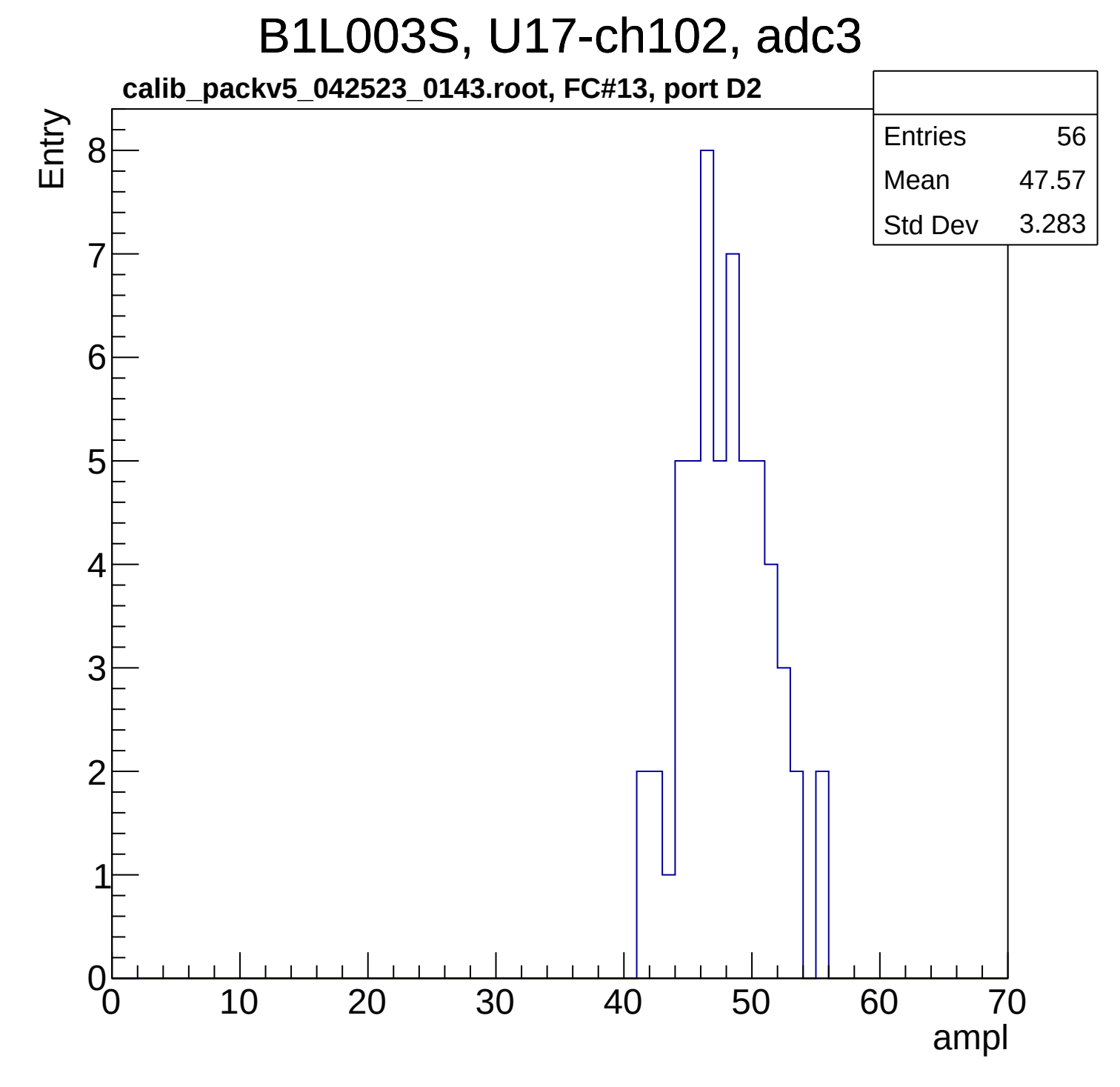
B1L003S, U17-ch102, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 47.57
Std Dev 3.283
Entry
ampl
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4
5
6
7
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0
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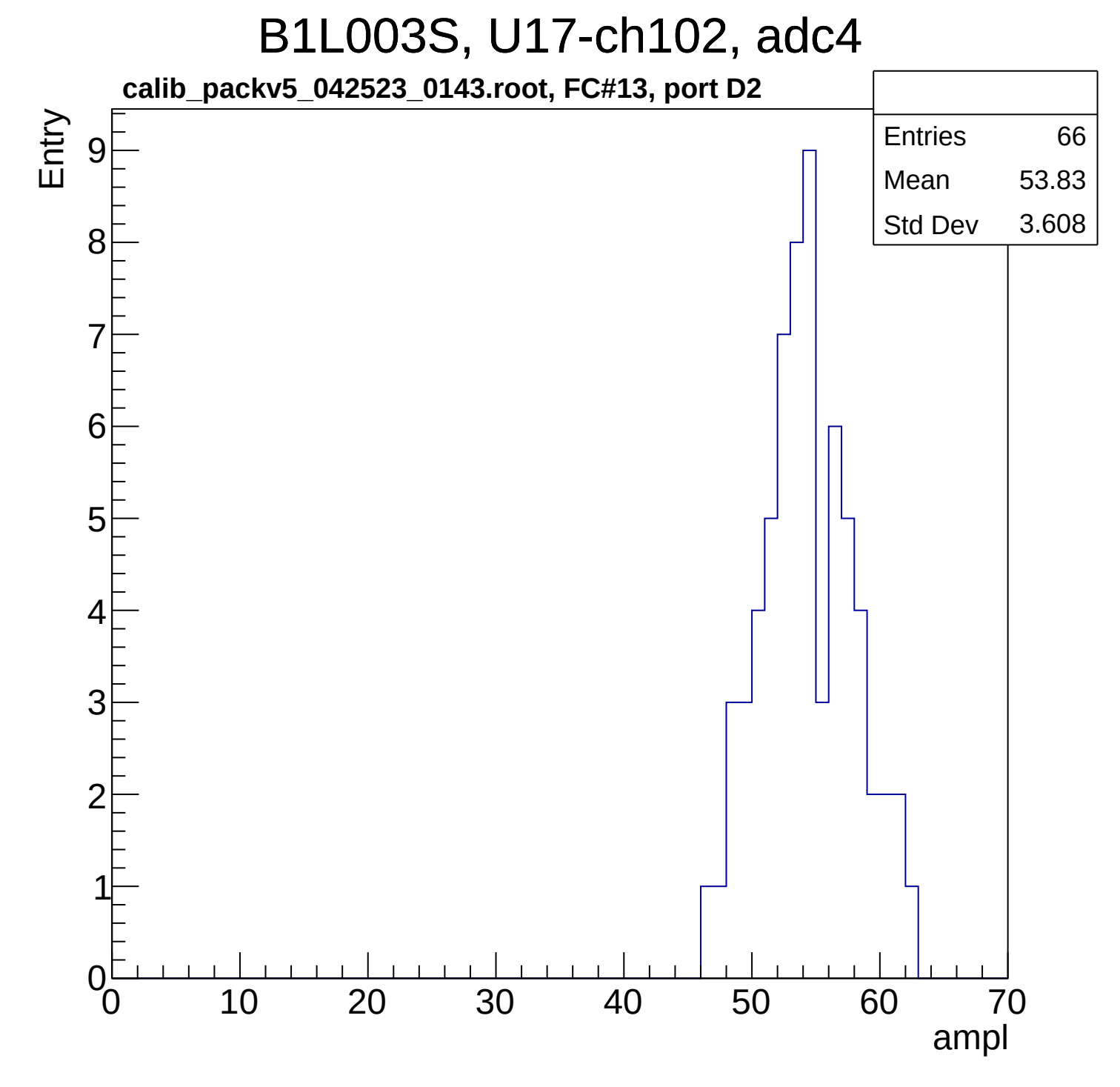
B1L003S, U17-ch102, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 53.83
Std Dev 3.608
Entry
ampl
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3
4
5
6
7
8
9
0
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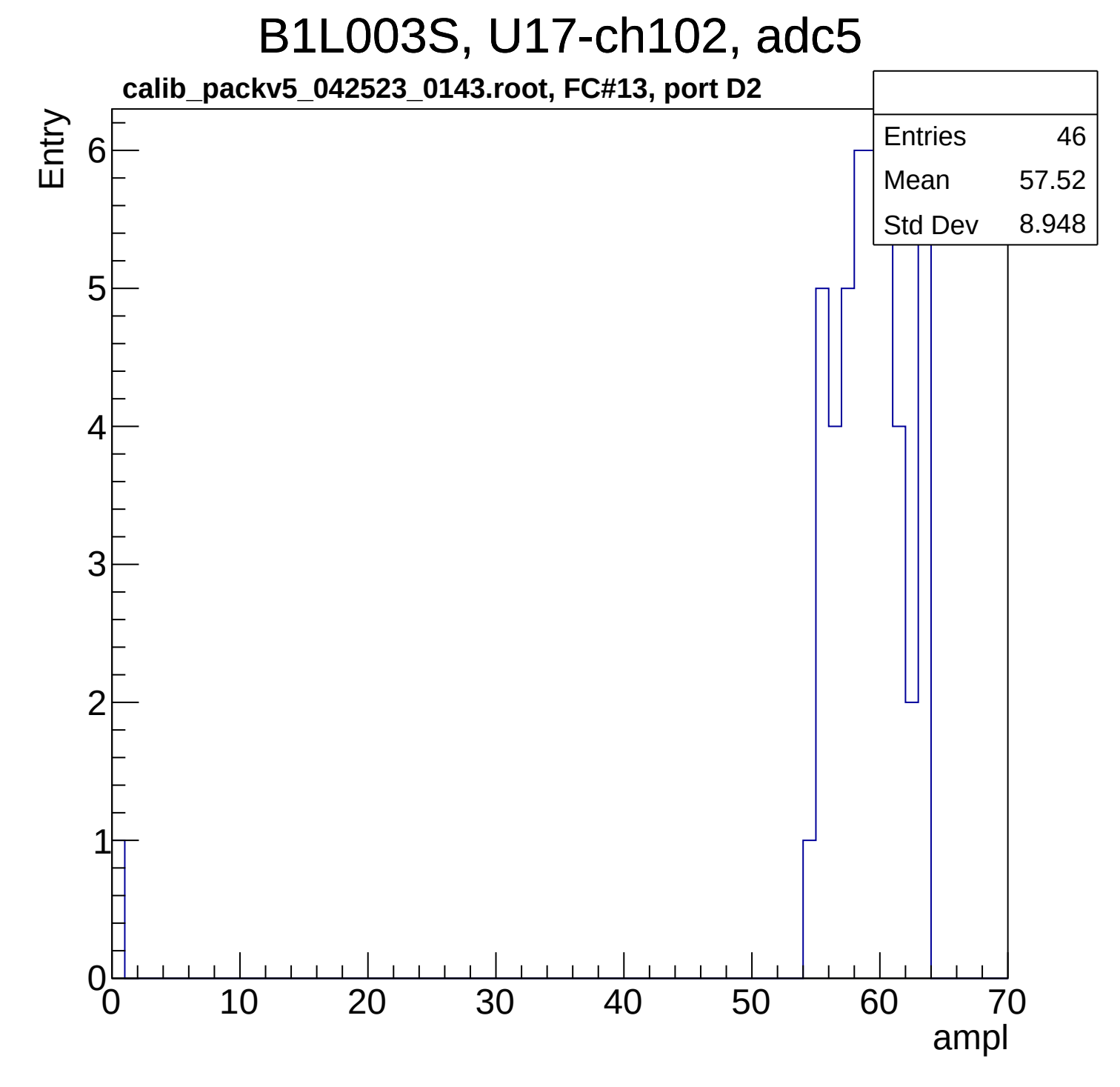
B1L003S, U17-ch102, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 57.52
Std Dev 8.948
Entry
ampl
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1
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3
4
5
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0
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40
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60
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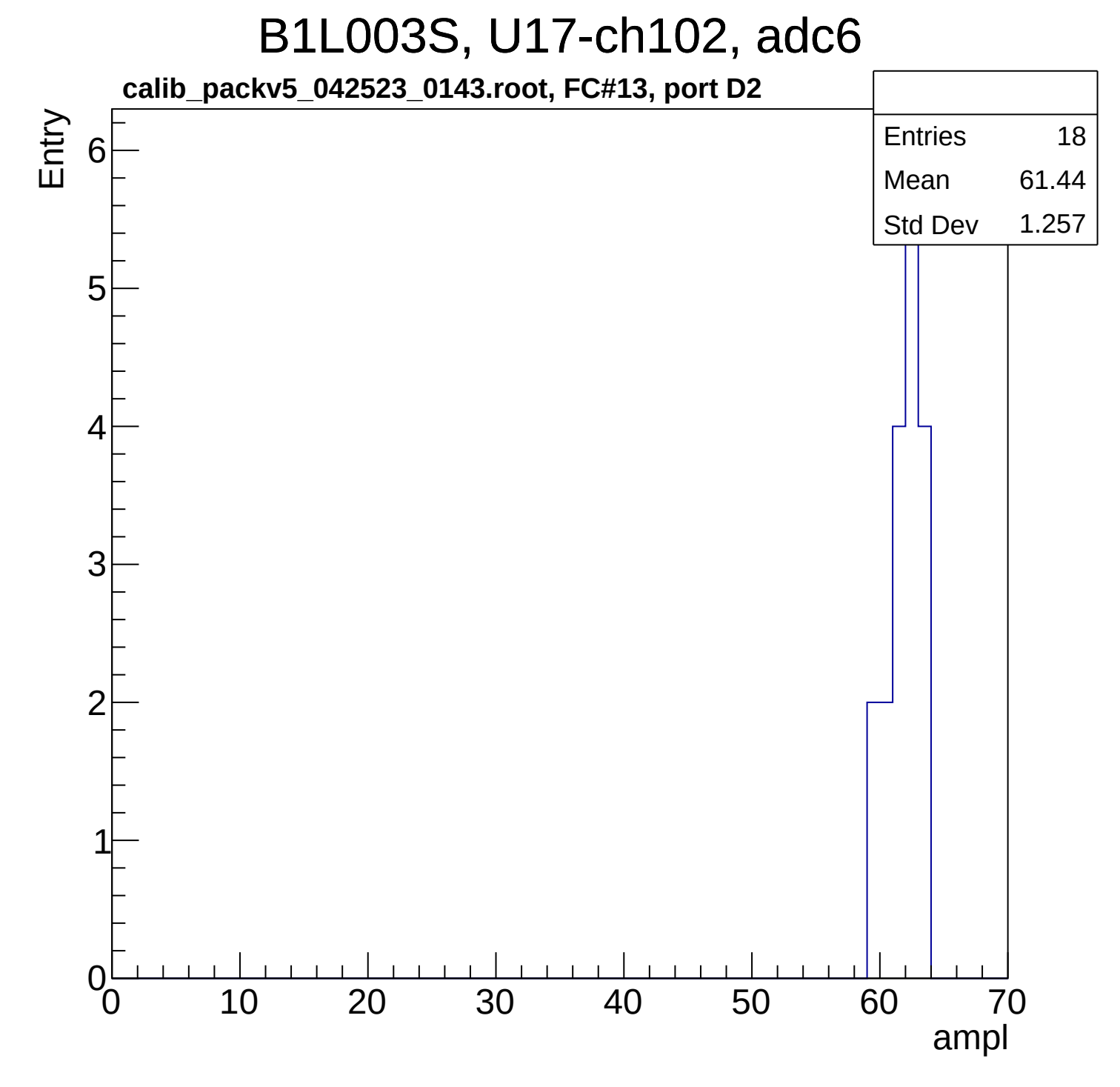
B1L003S, U17-ch102, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 61.44
Std Dev 1.257
Entry
ampl
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2
3
4
5
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0
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20
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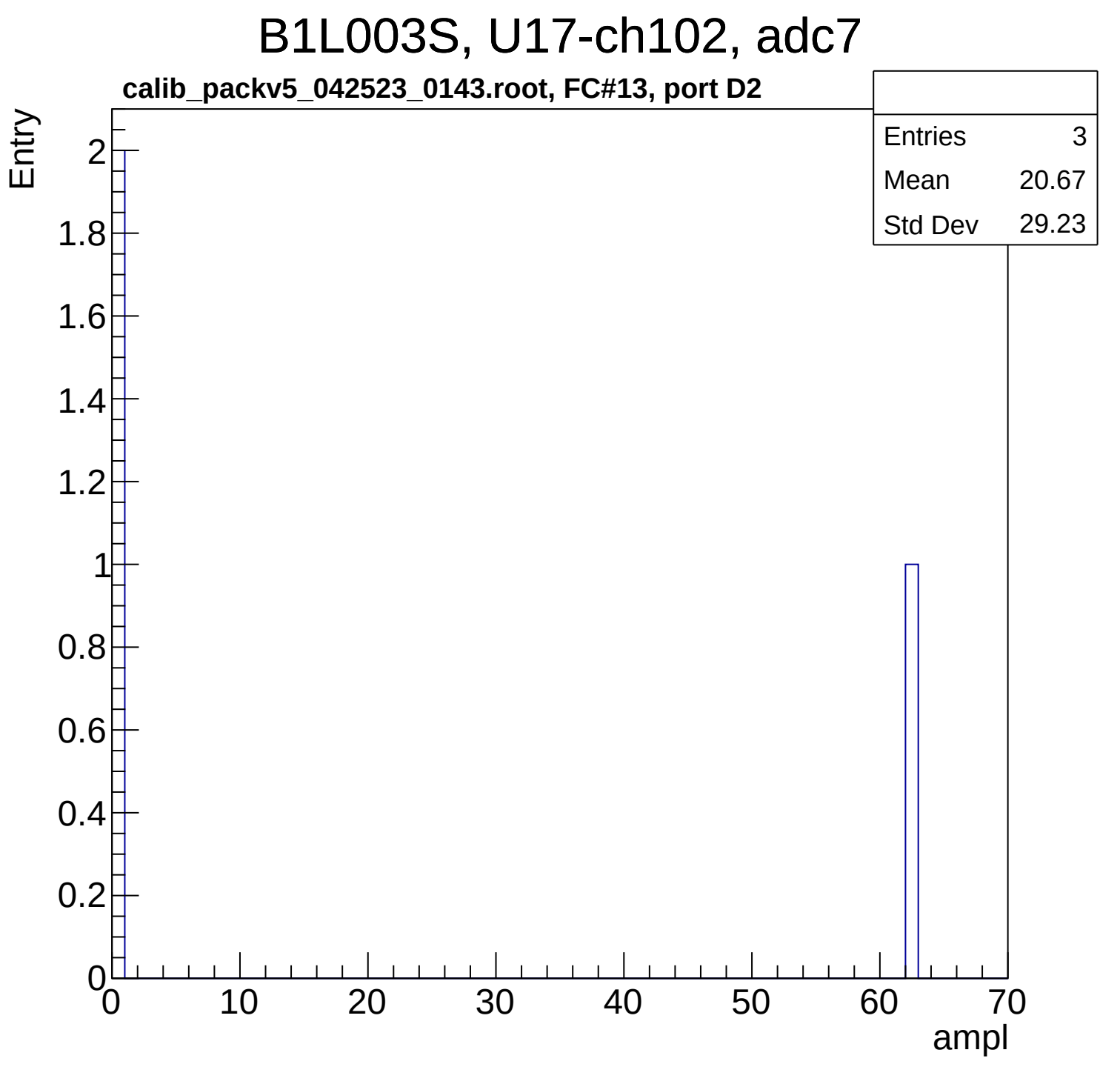
B1L003S, U17-ch102, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
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60
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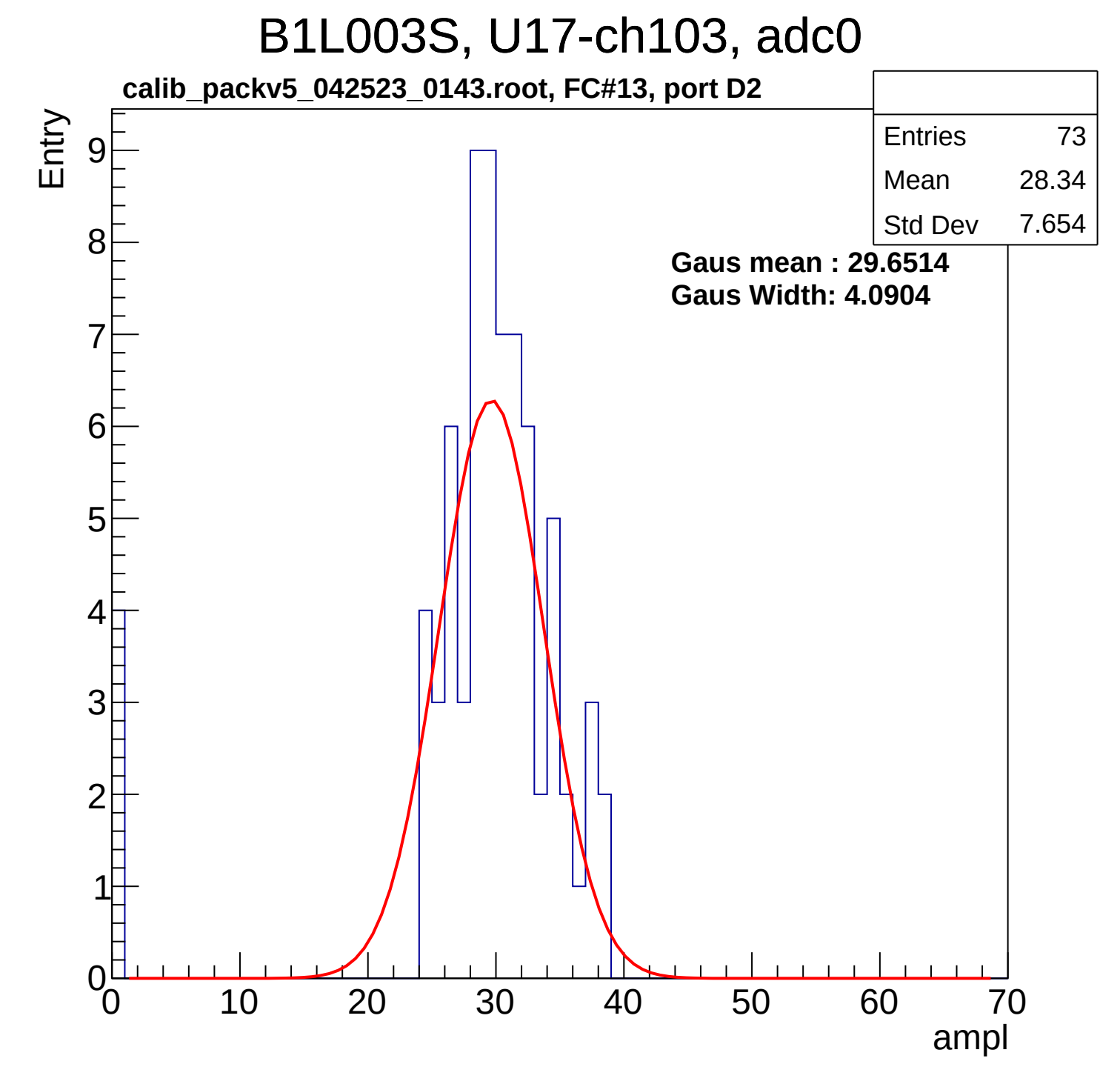
B1L003S, U17-ch103, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 28.34
Std Dev 7.654
Gaus mean : 29.6514
Gaus Width: 4.0904
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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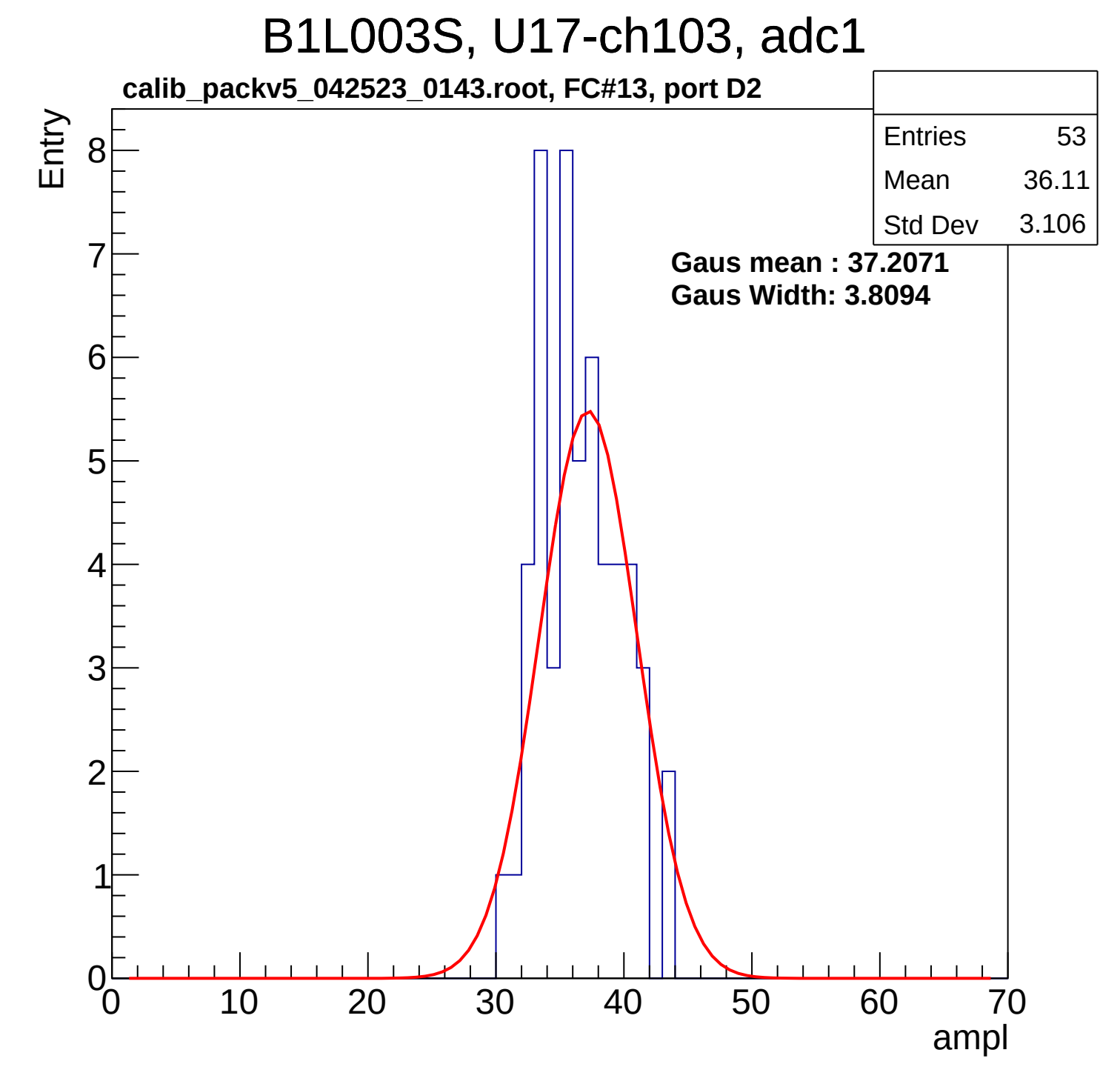
B1L003S, U17-ch103, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 36.11
Std Dev 3.106
Gaus mean : 37.2071
Gaus Width: 3.8094
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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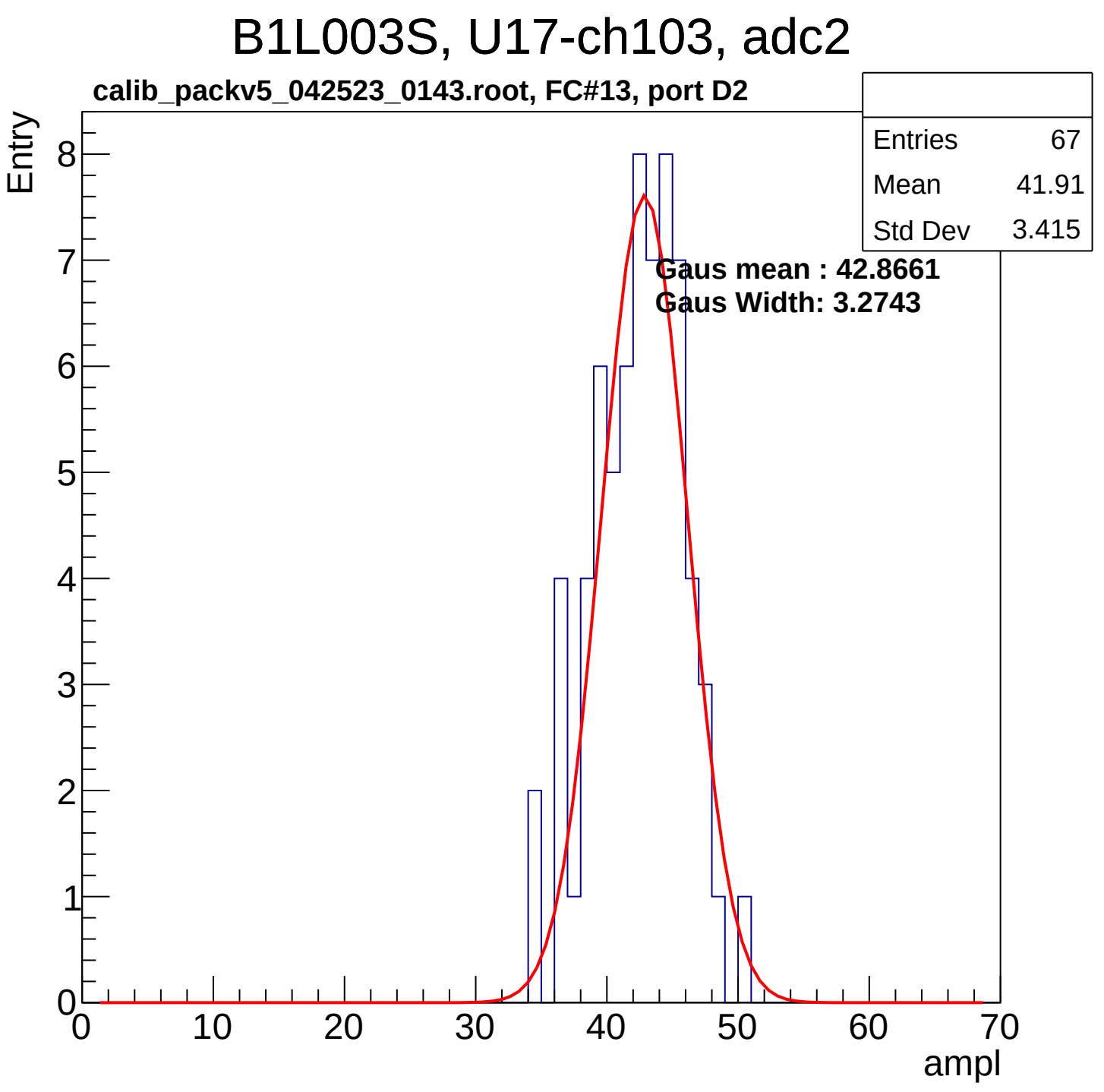
B1L003S, U17-ch103, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 41.91
Std Dev 3.415
Gaus mean : 42.8661
Gaus Width: 3.2743
Entry
ampl
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5
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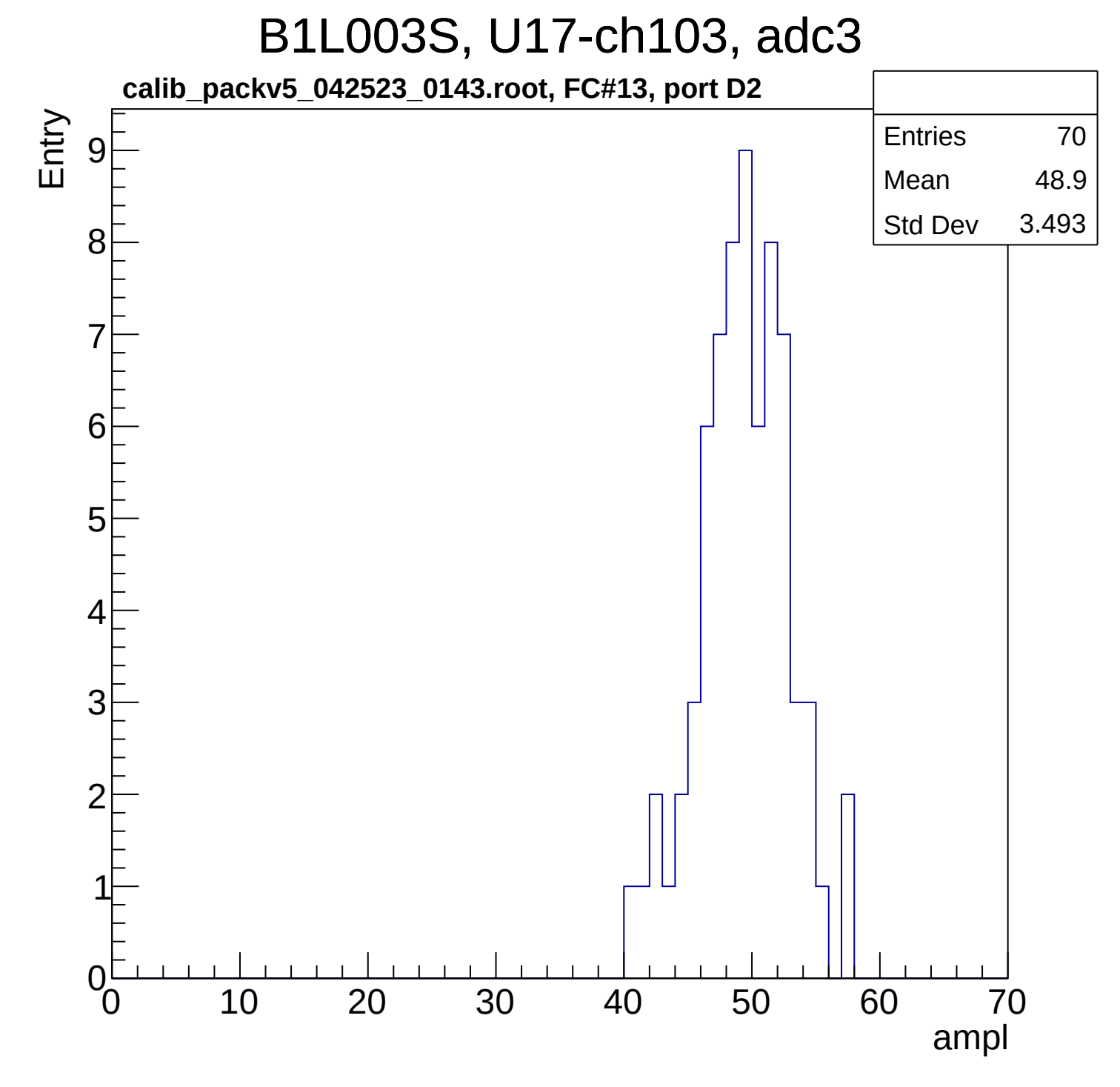
B1L003S, U17-ch103, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 48.9
Std Dev 3.493
Entry
ampl
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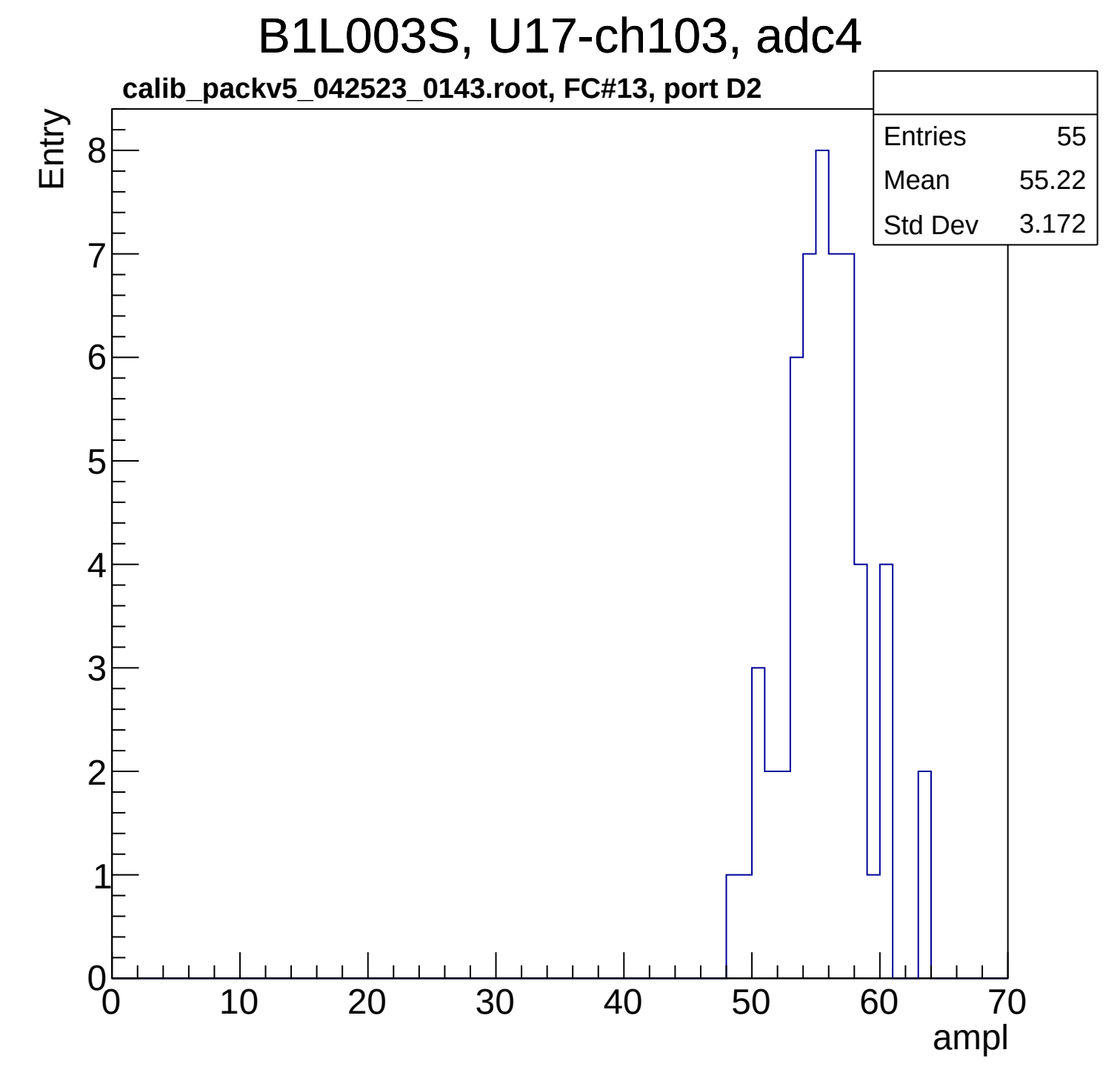
B1L003S, U17-ch103, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55.22
Std Dev 3.172
Entry
ampl
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1
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3
4
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7
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0
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60
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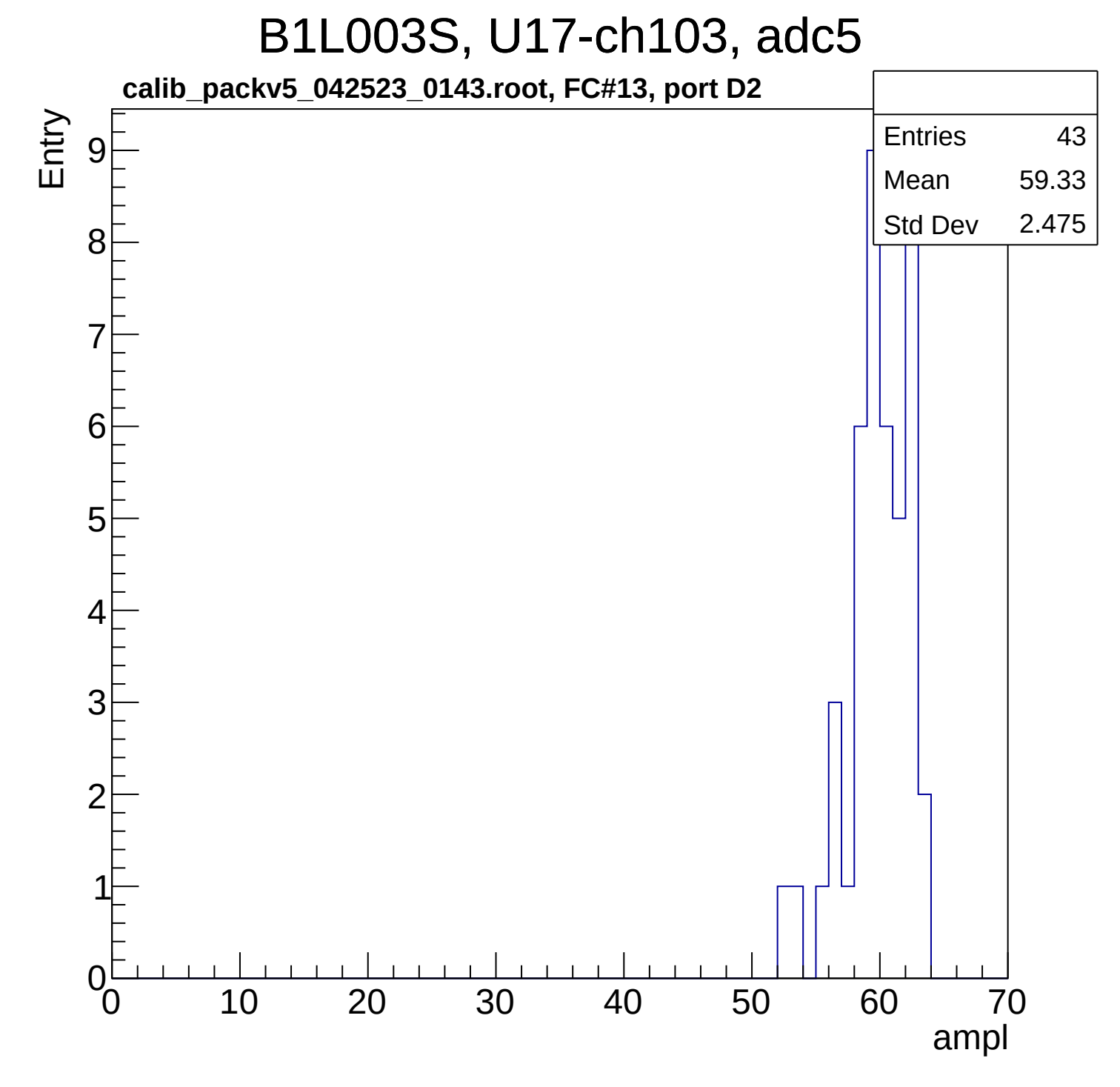
B1L003S, U17-ch103, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.33
Std Dev 2.475
Entry
ampl
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30
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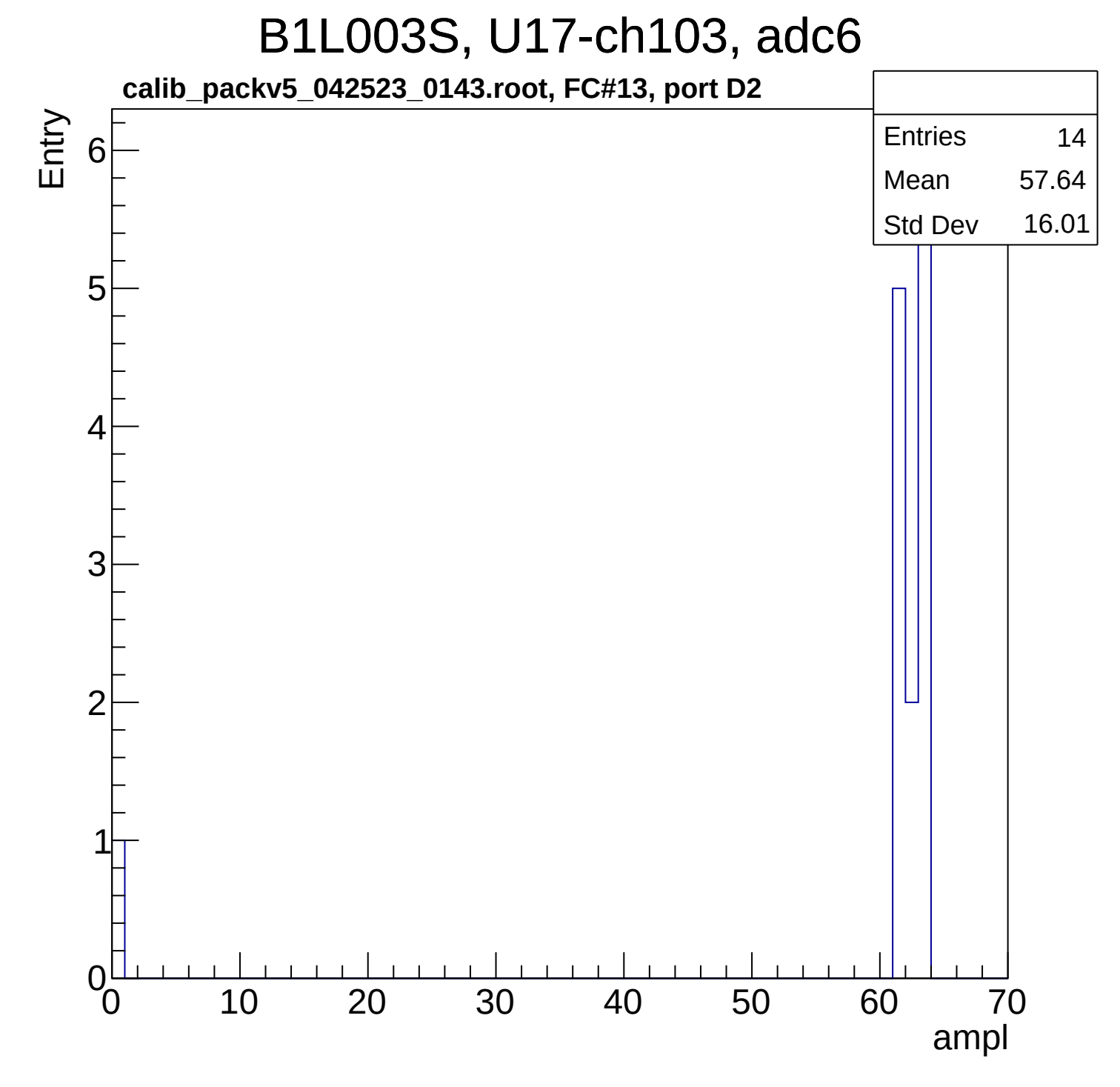
6
7
8
9


B1L003S, U17-ch103, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.64
Std Dev 16.01
Entry
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4
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30
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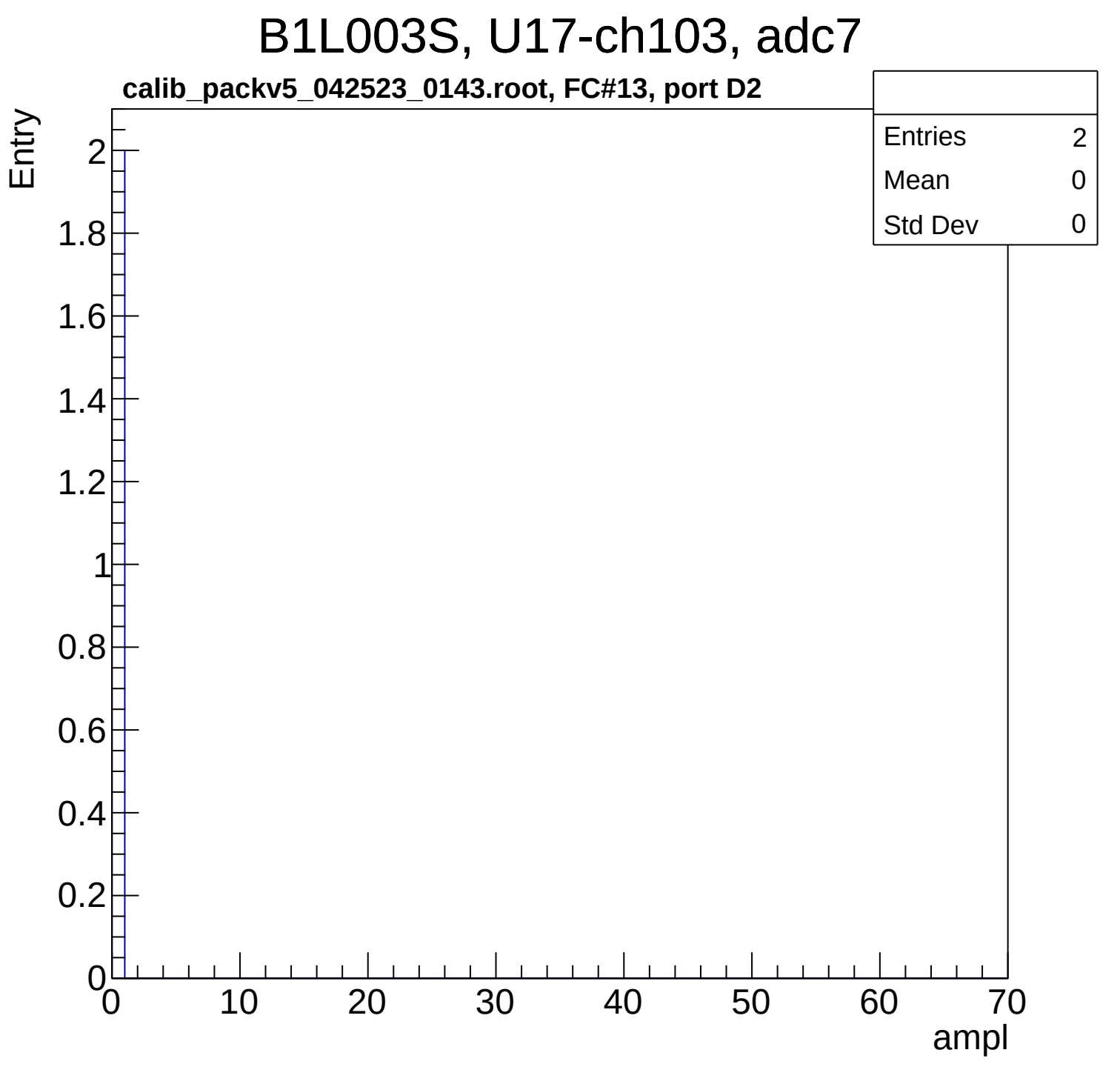
70
ampl


B1L003S, U17-ch103, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.4
1.6
1.8
2
0
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60
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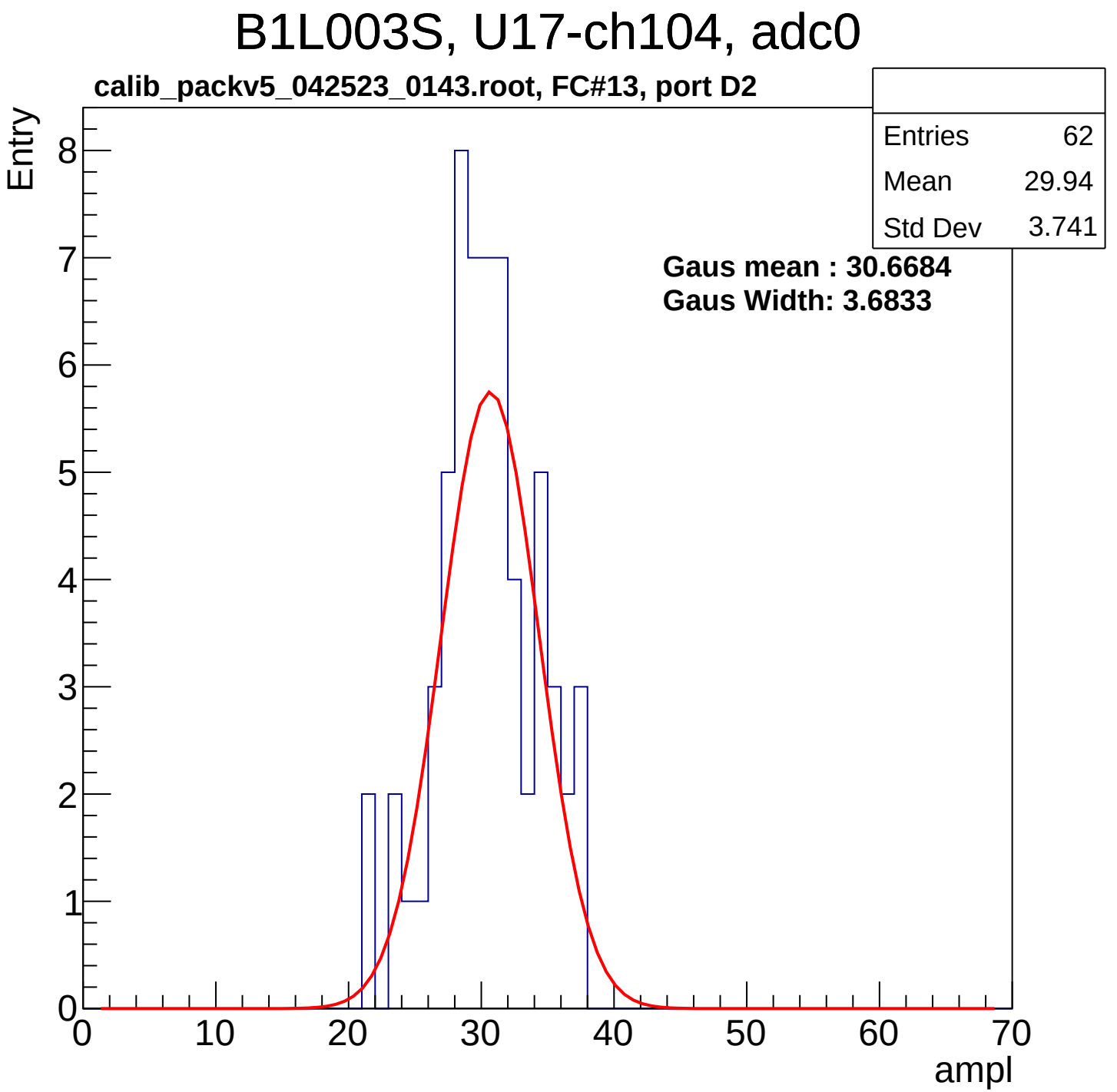
B1L003S, U17-ch104, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 29.94
Std Dev 3.741
Gaus mean : 30.6684
Gaus Width: 3.6833
Entry
ampl
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3
4
5
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7
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0
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40
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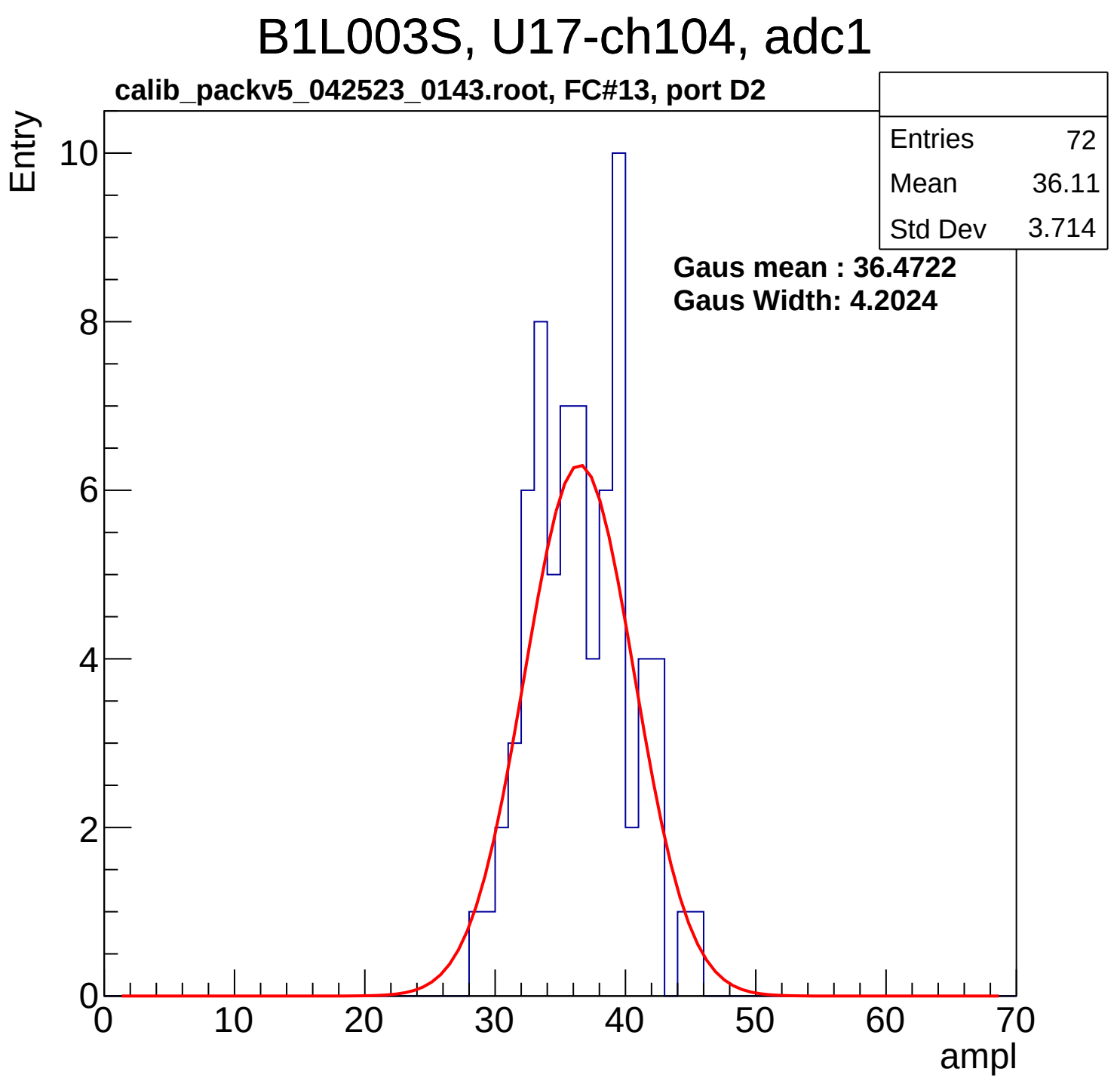
B1L003S, U17-ch104, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.11
Std Dev 3.714
Gaus mean : 36.4722
Gaus Width: 4.2024
Entry
ampl
0
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4
6
8
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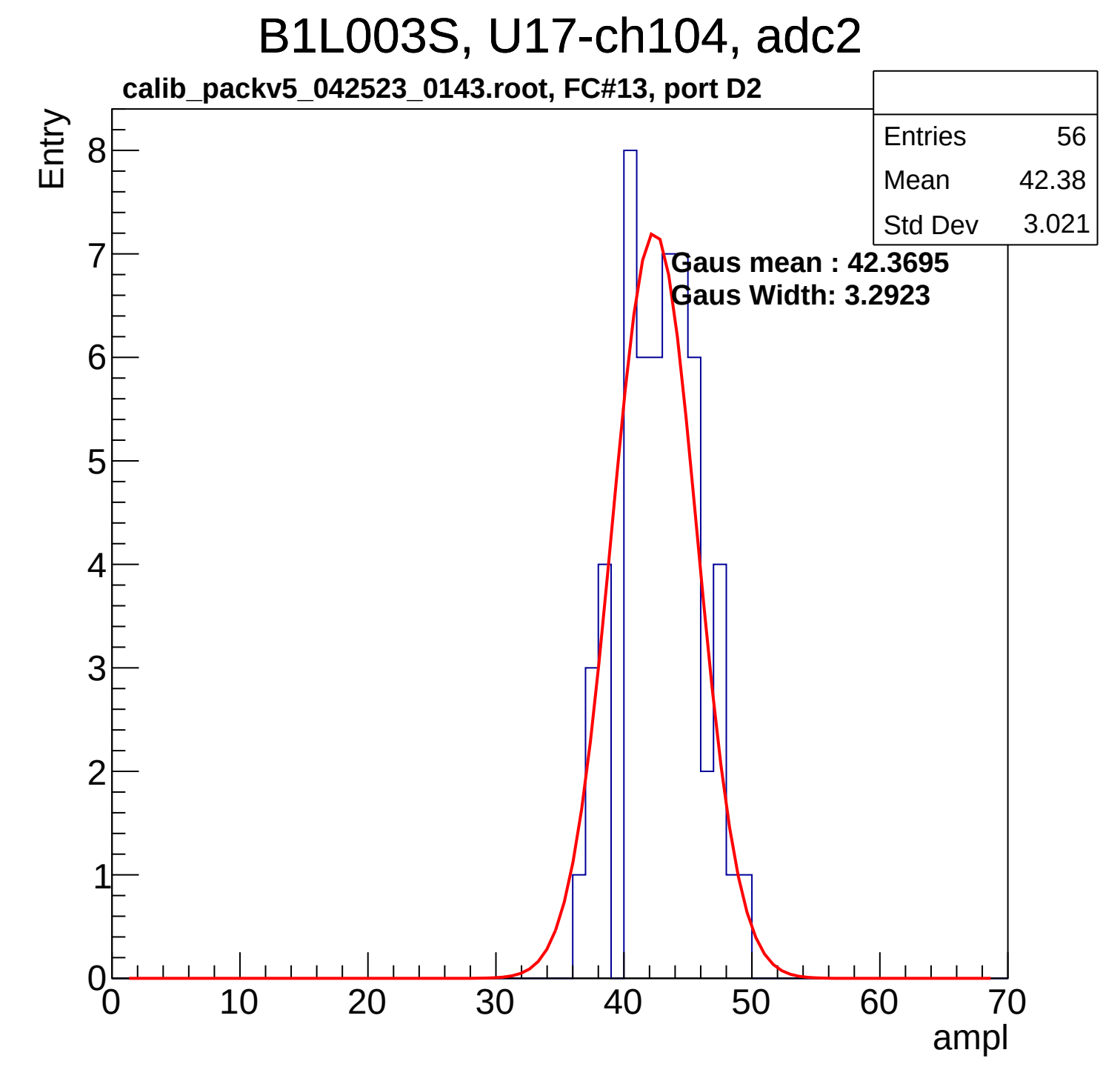
B1L003S, U17-ch104, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.38
Std Dev 3.021
Gaus mean : 42.3695
Gaus Width: 3.2923
Entry
ampl
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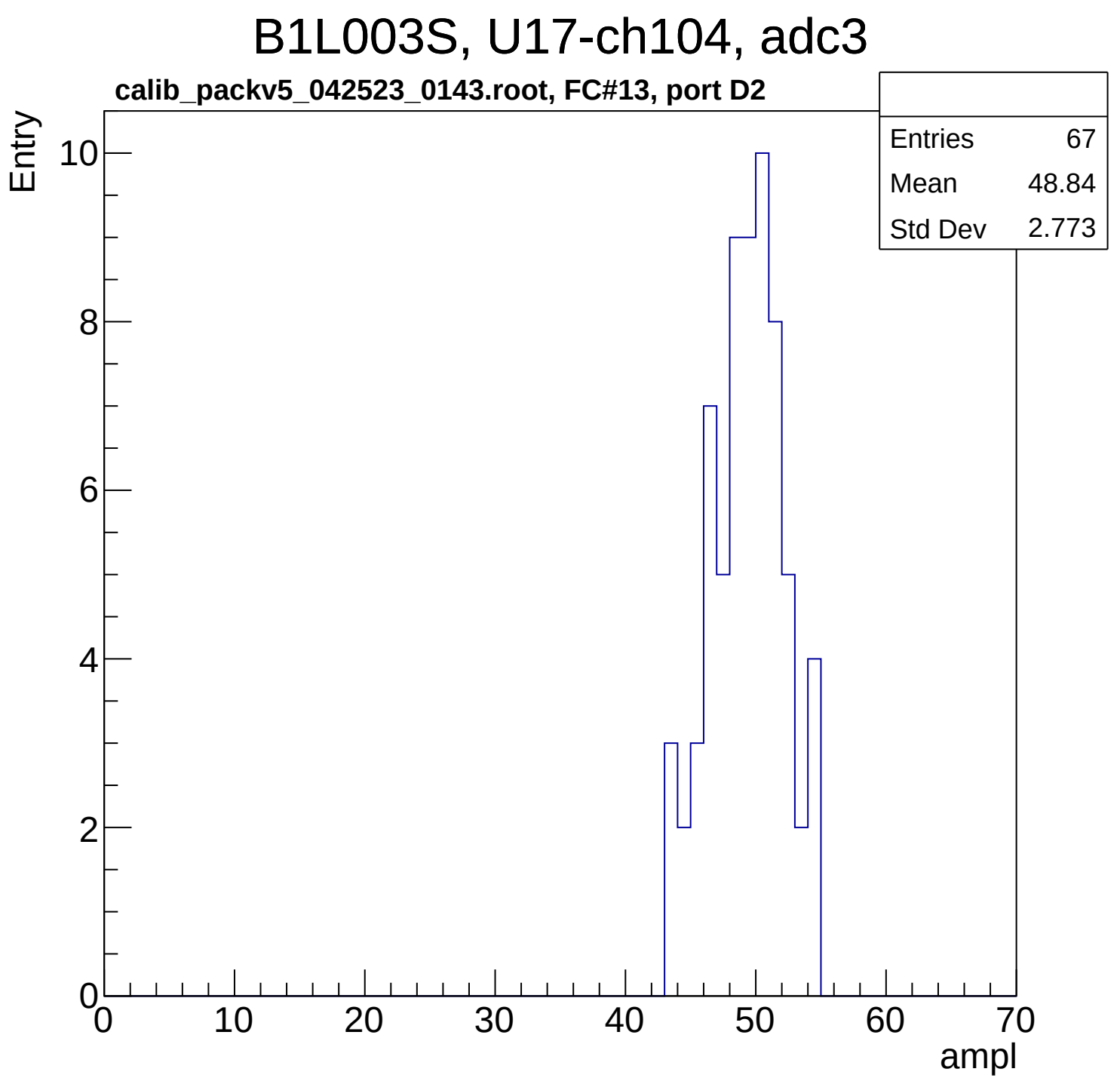
B1L003S, U17-ch104, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.84
Std Dev 2.773
Entry
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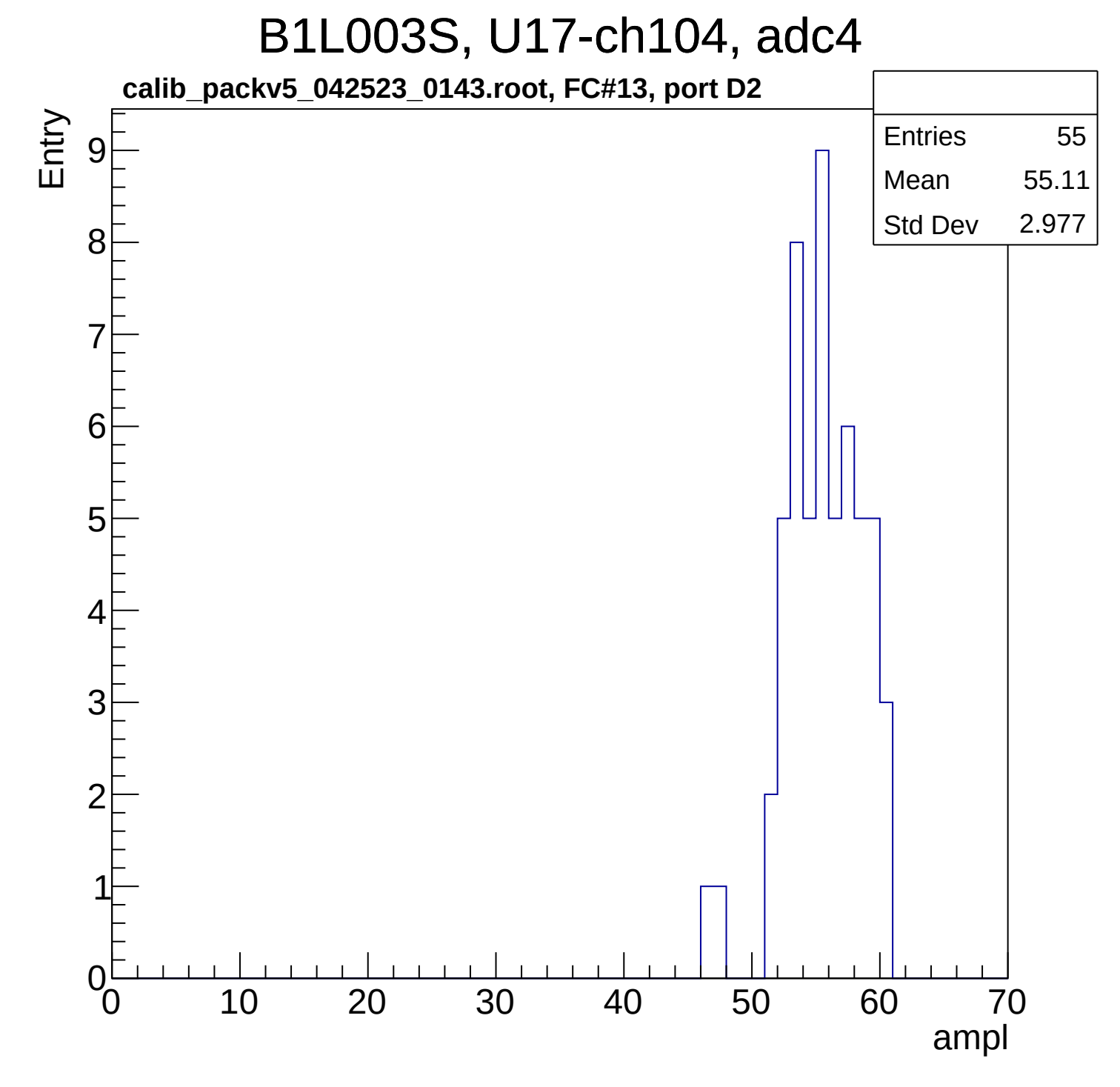
ampl


B1L003S, U17-ch104, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55.11
Std Dev 2.977
Entry
ampl
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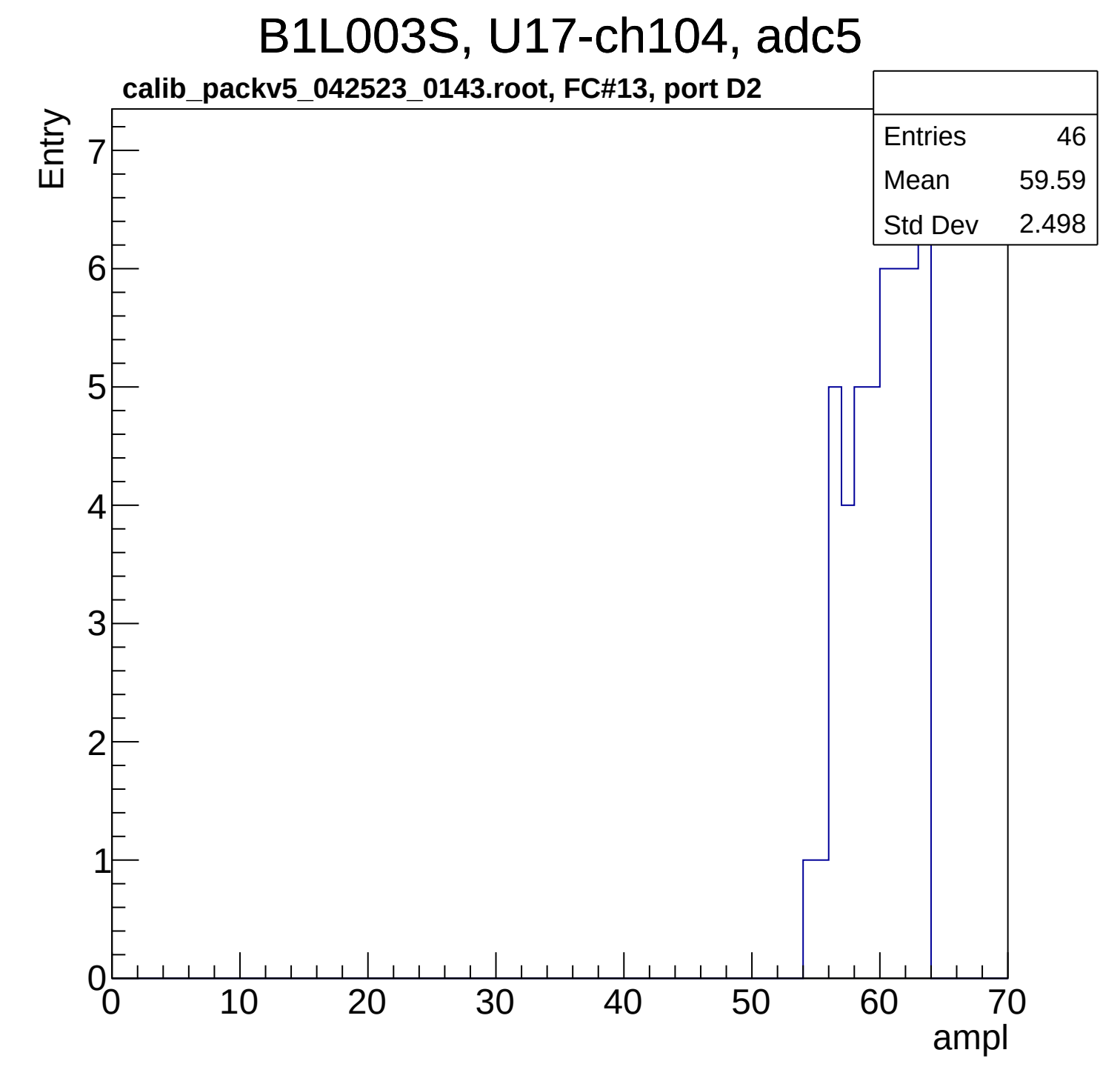
B1L003S, U17-ch104, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.59
Std Dev 2.498
Entry
ampl
0
1
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4
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0
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60
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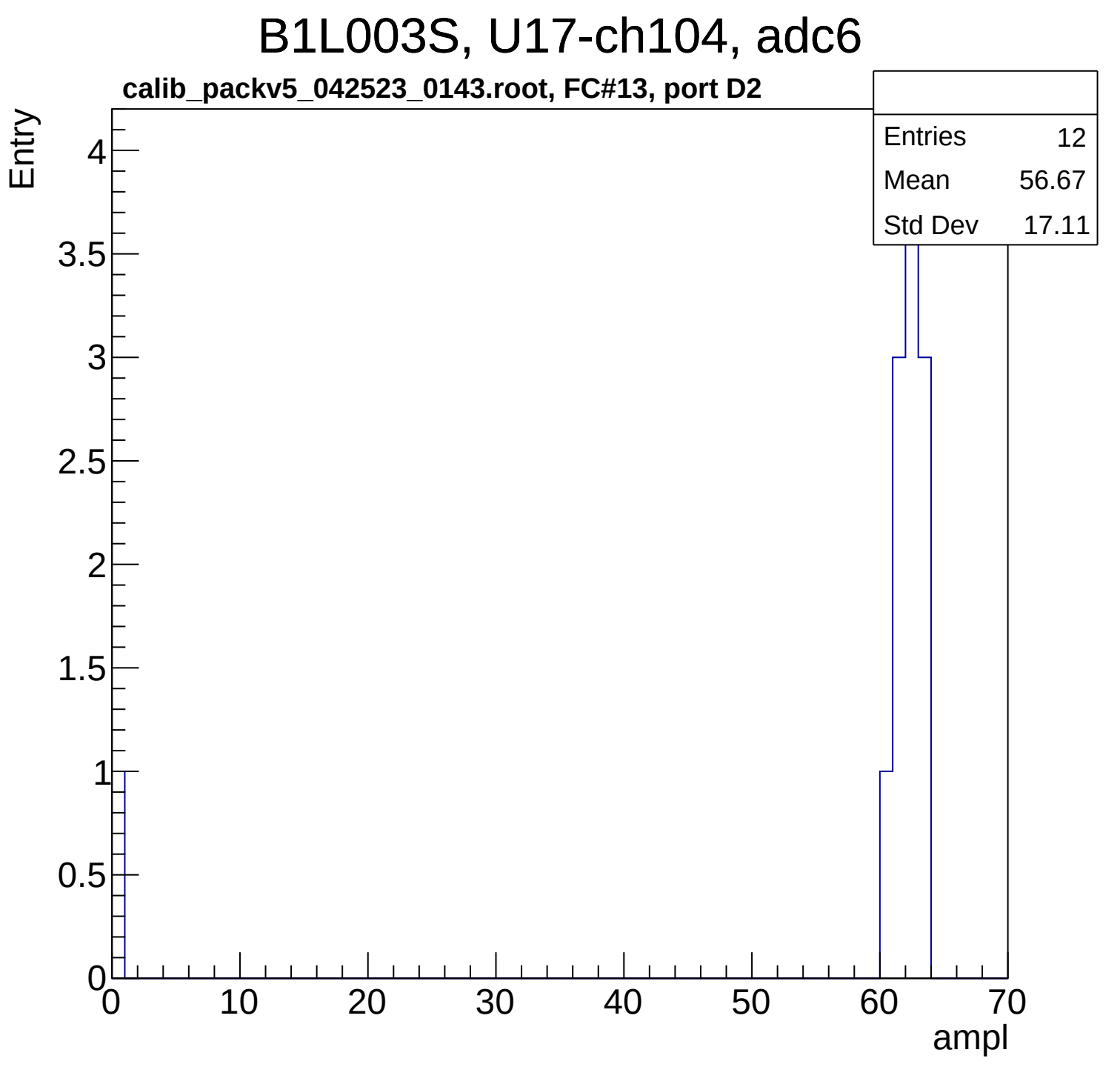
B1L003S, U17-ch104, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.67
Std Dev 17.11
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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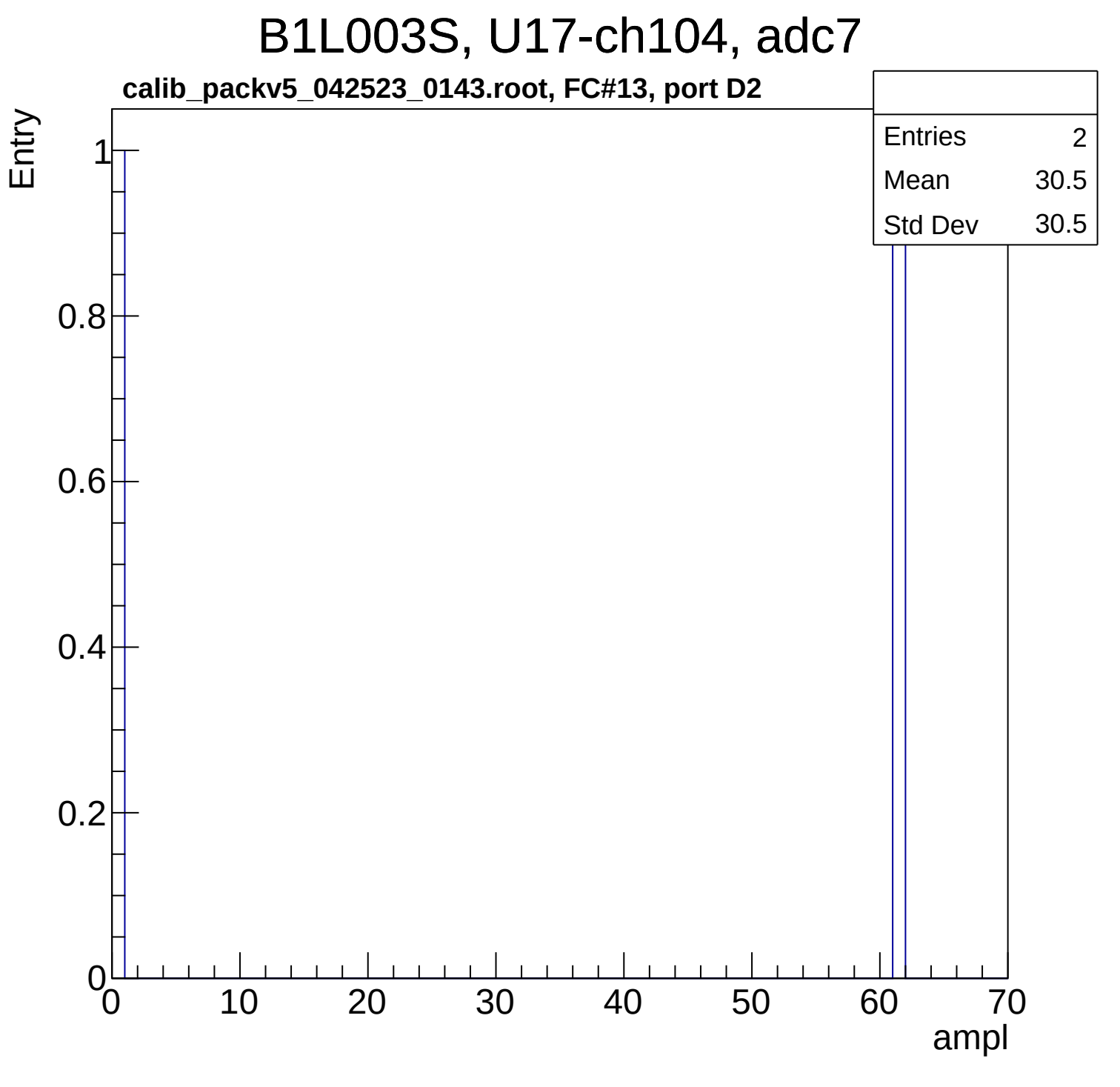
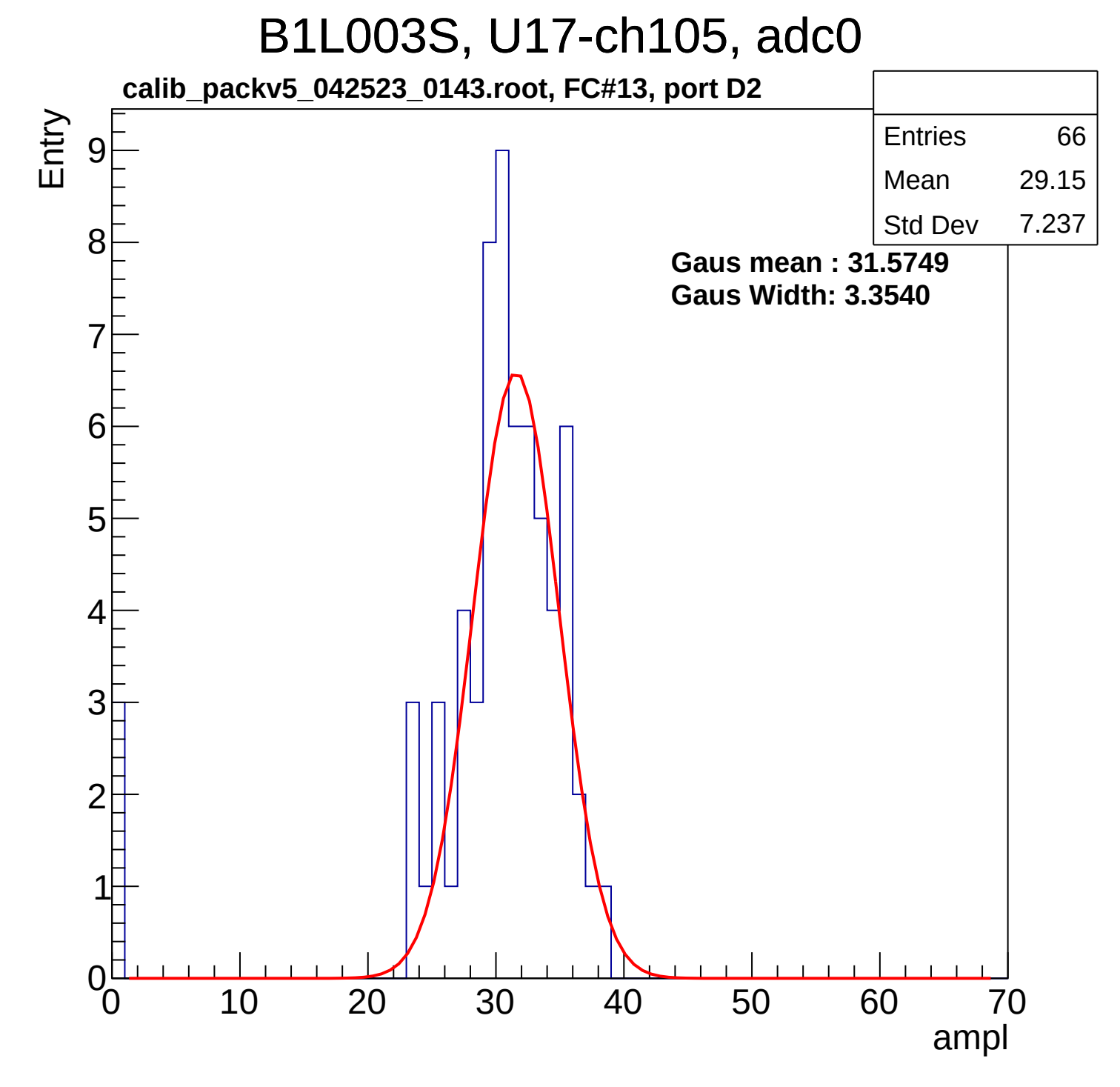

B1L003S, U17-ch104, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch105, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 29.15
Std Dev 7.237
Gaus mean : 31.5749
Gaus Width: 3.3540
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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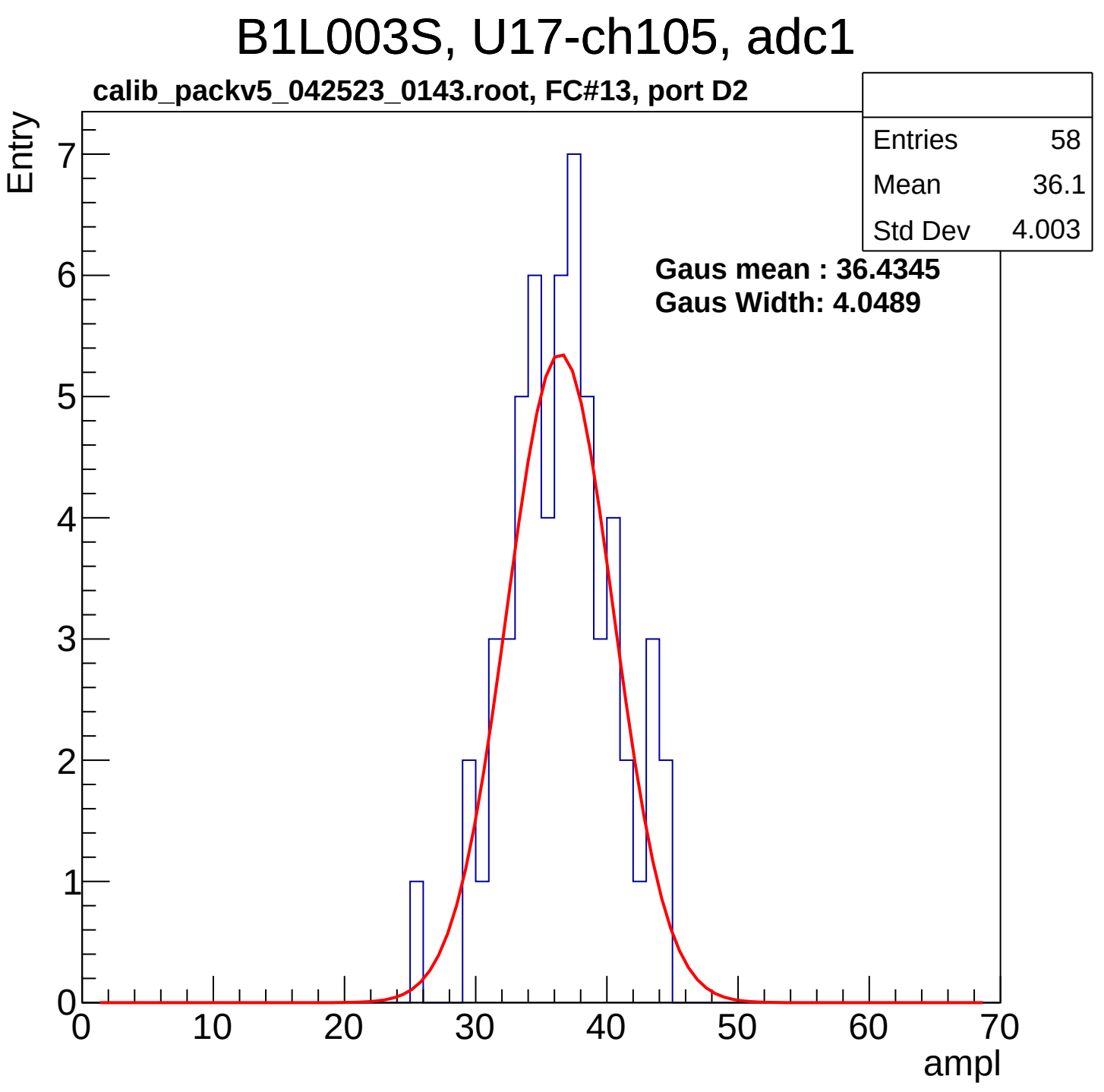
B1L003S, U17-ch105, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 36.1
Std Dev 4.003
Gaus mean : 36.4345
Gaus Width: 4.0489
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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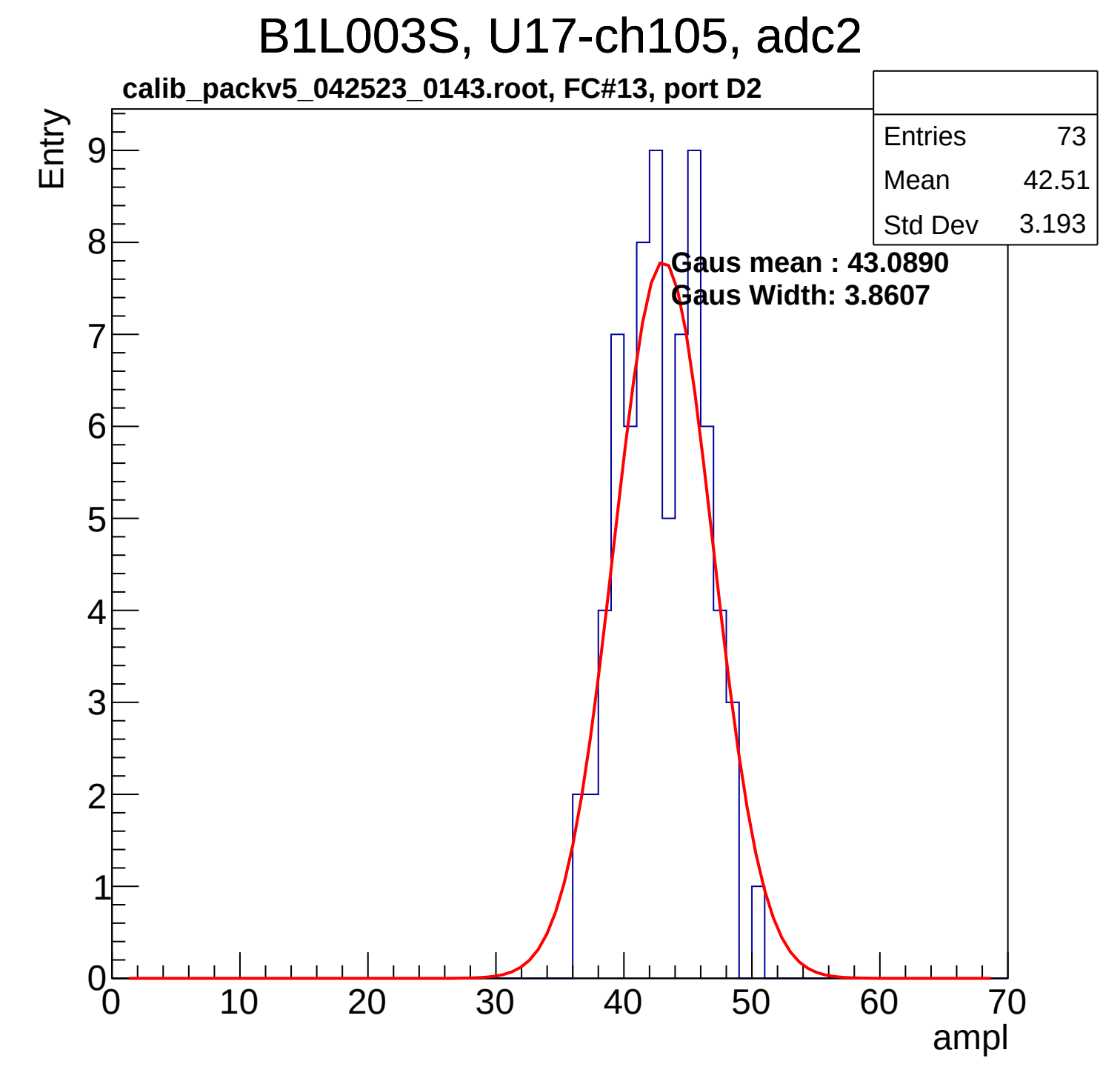
B1L003S, U17-ch105, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 42.51
Std Dev 3.193
Gaus mean : 43.0890
Gaus Width: 3.8607
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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30
40
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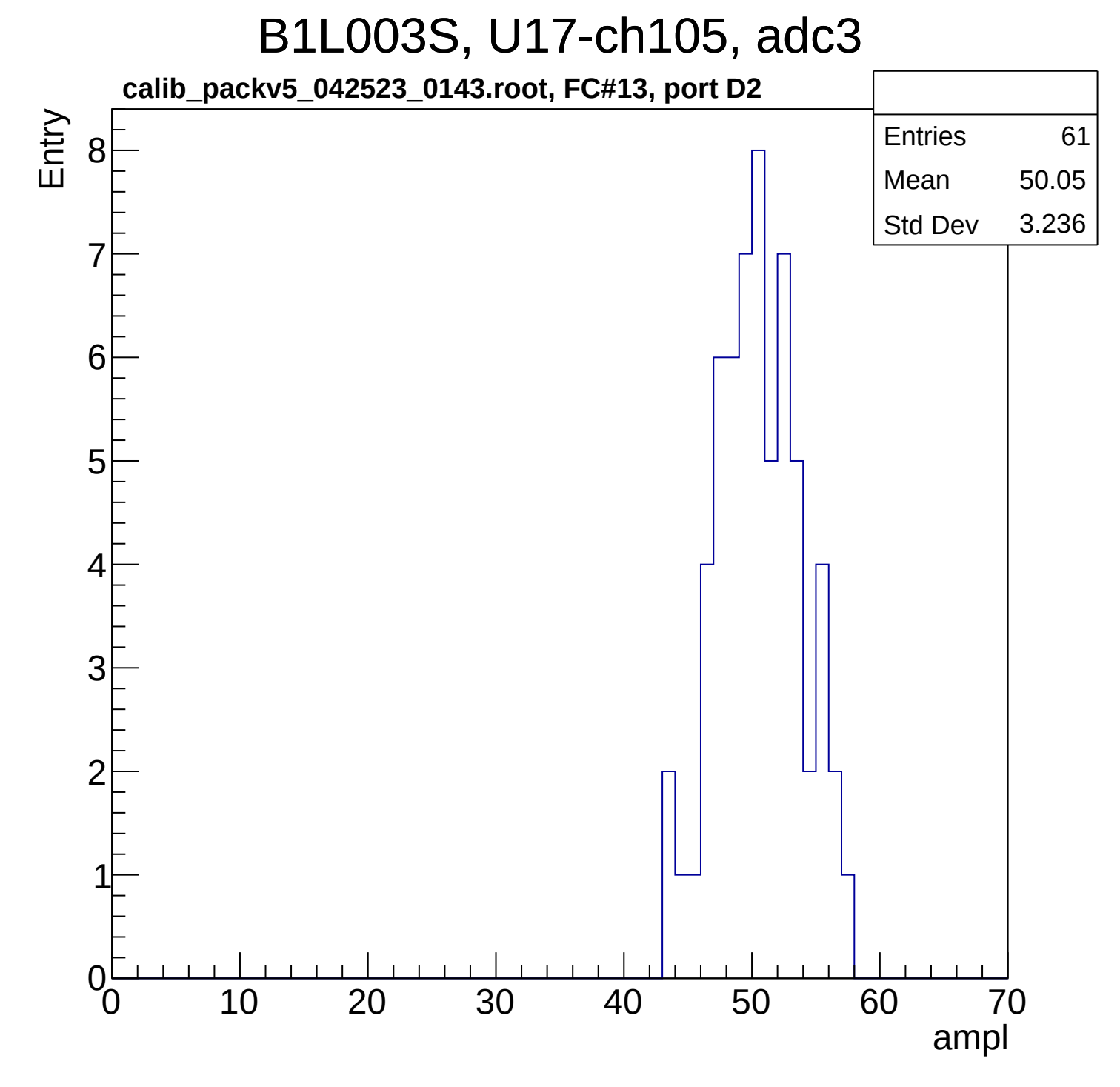
B1L003S, U17-ch105, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.05
Std Dev 3.236
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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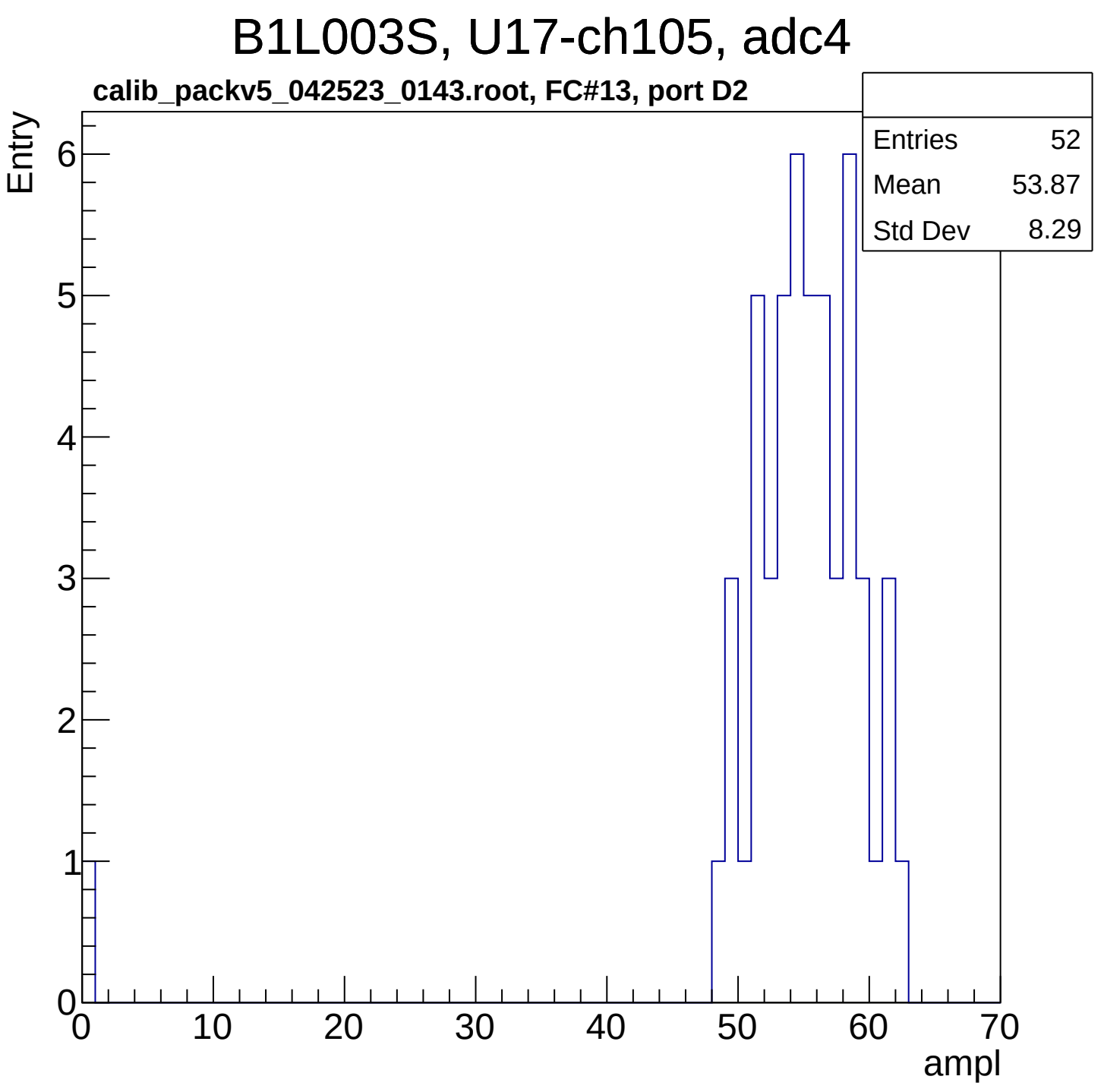
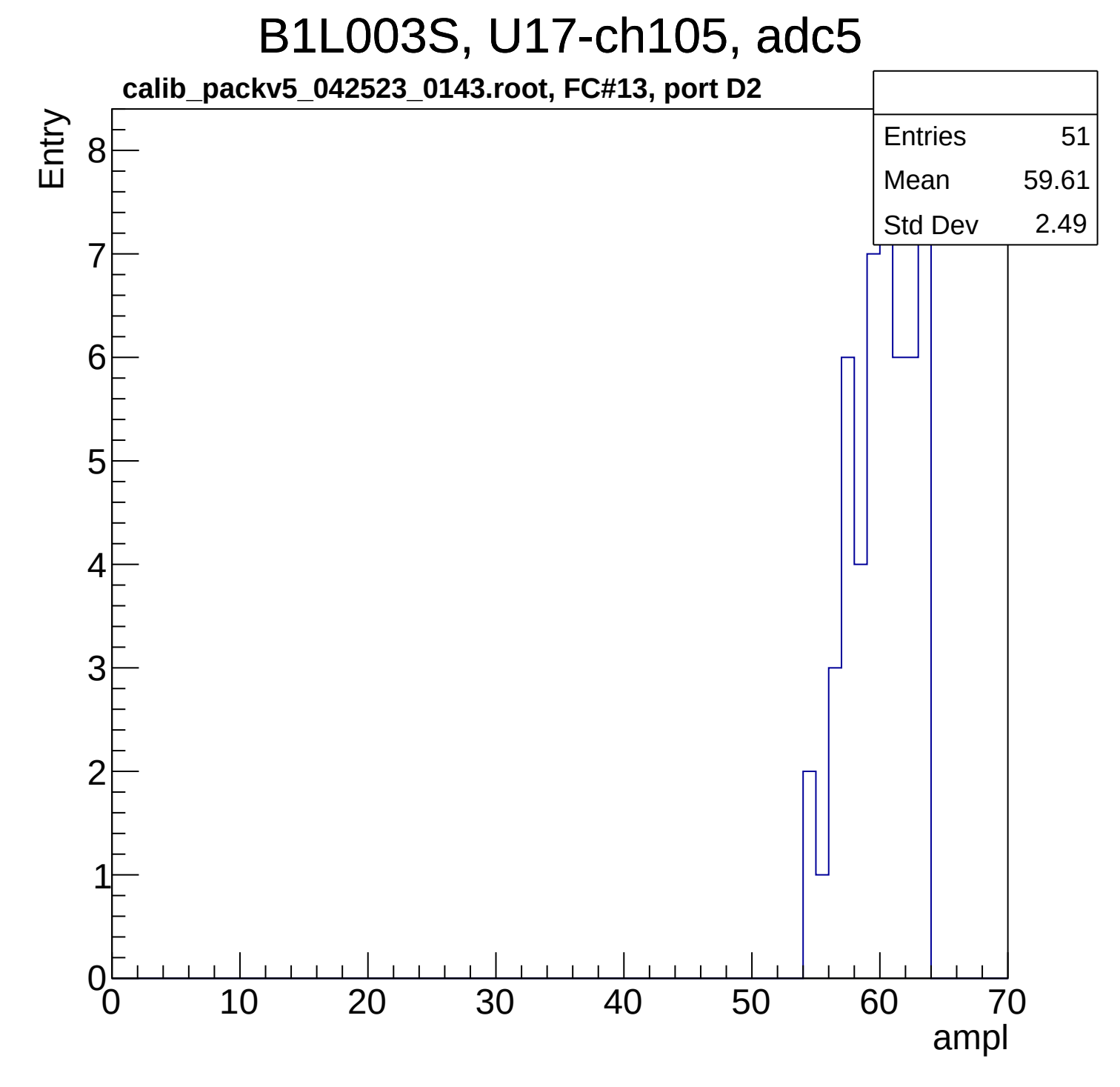
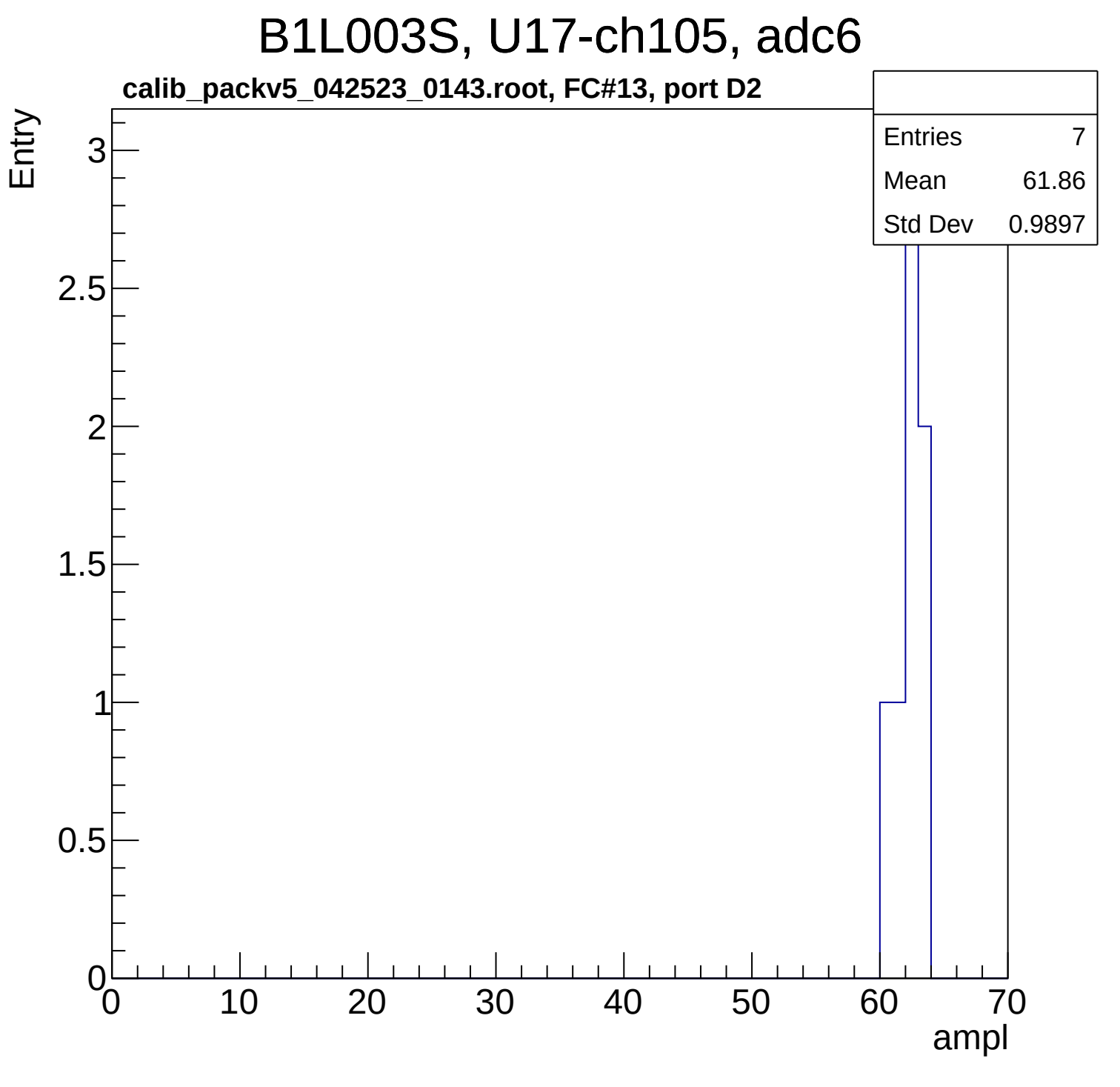
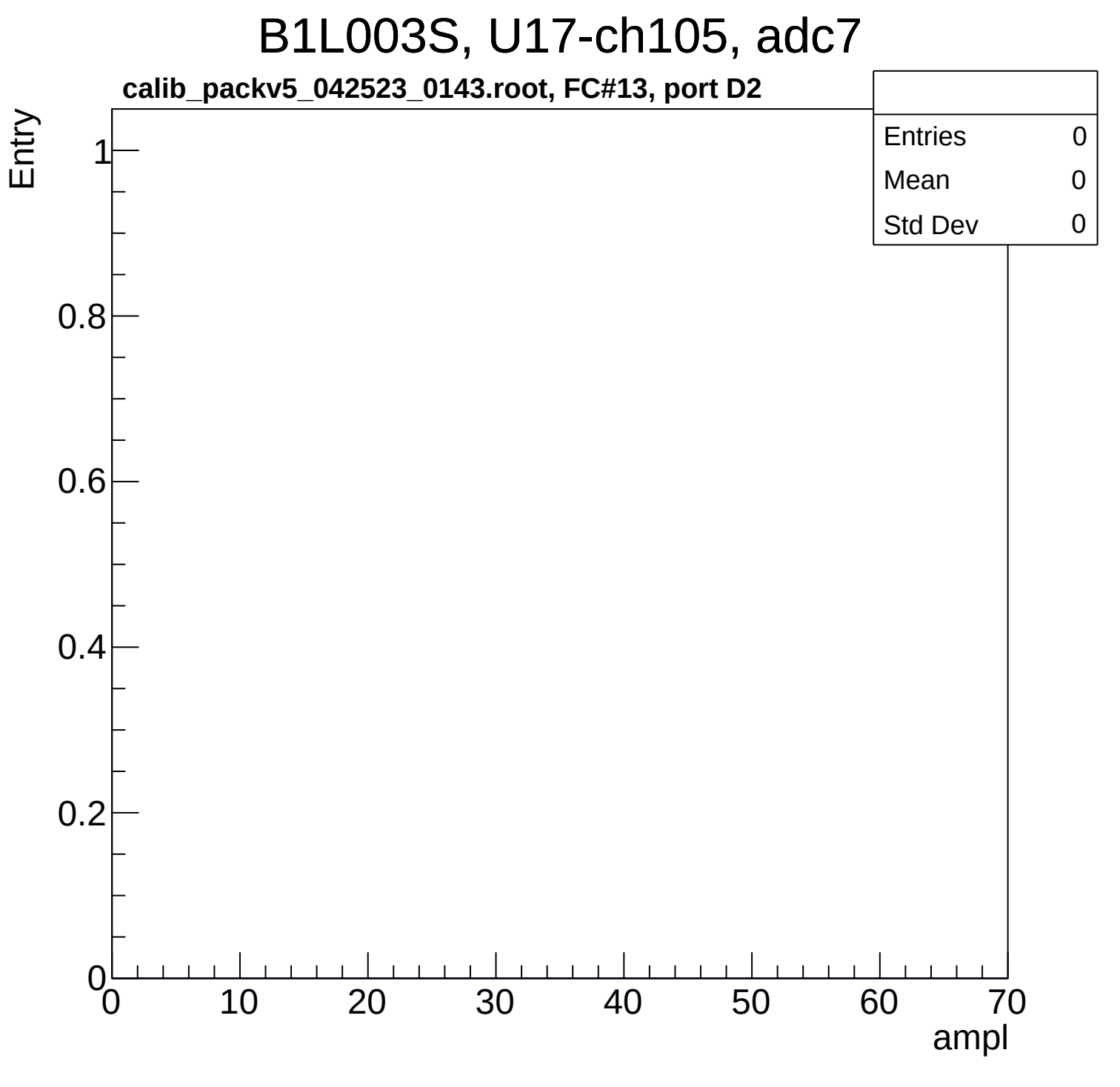

B1L003S, U17-ch105, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 53.87
Std Dev 8.29
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch105, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.61
Std Dev 2.49
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch105, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch105, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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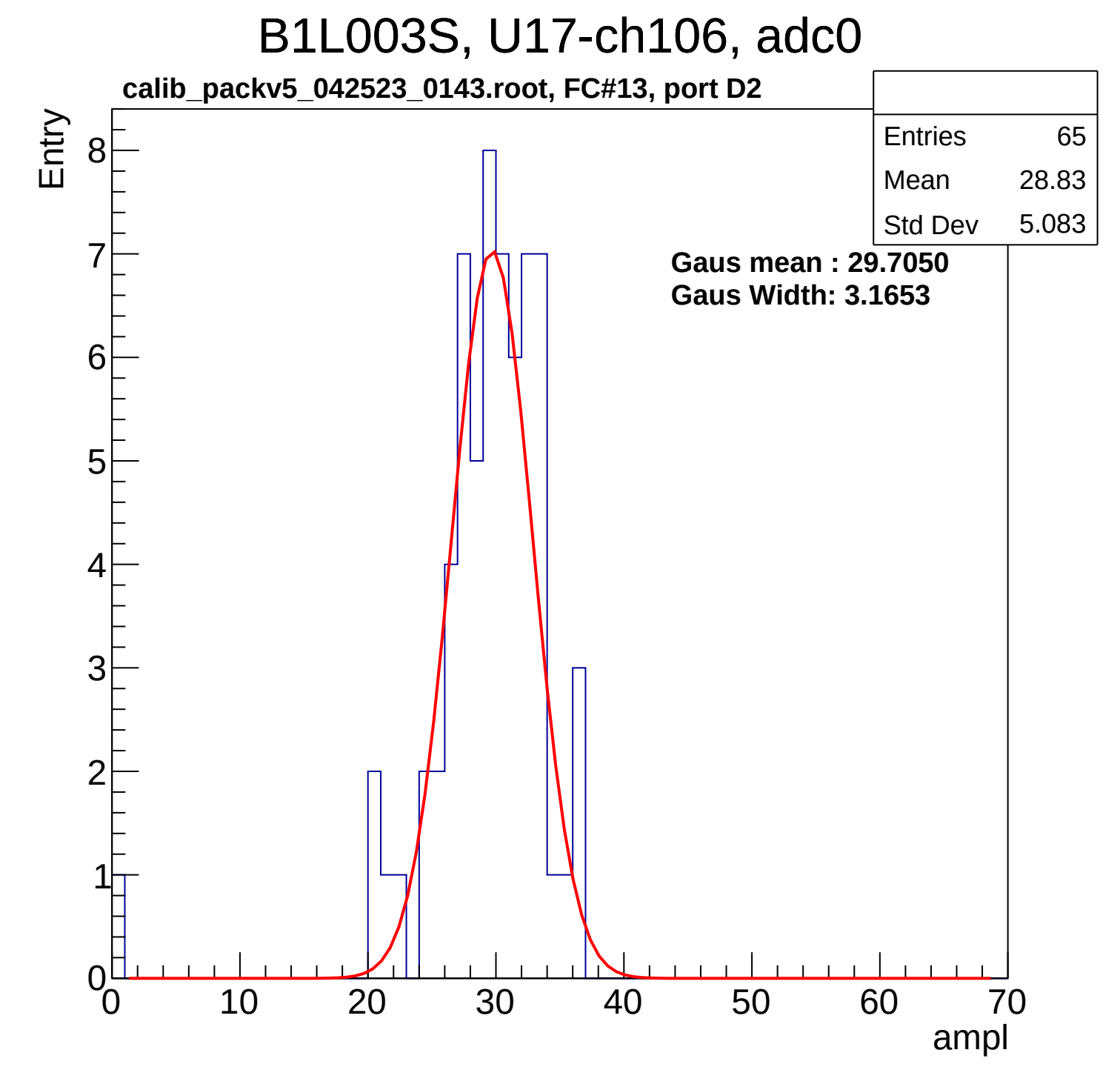
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch106, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.83
Std Dev 5.083
Gaus mean : 29.7050
Gaus Width: 3.1653
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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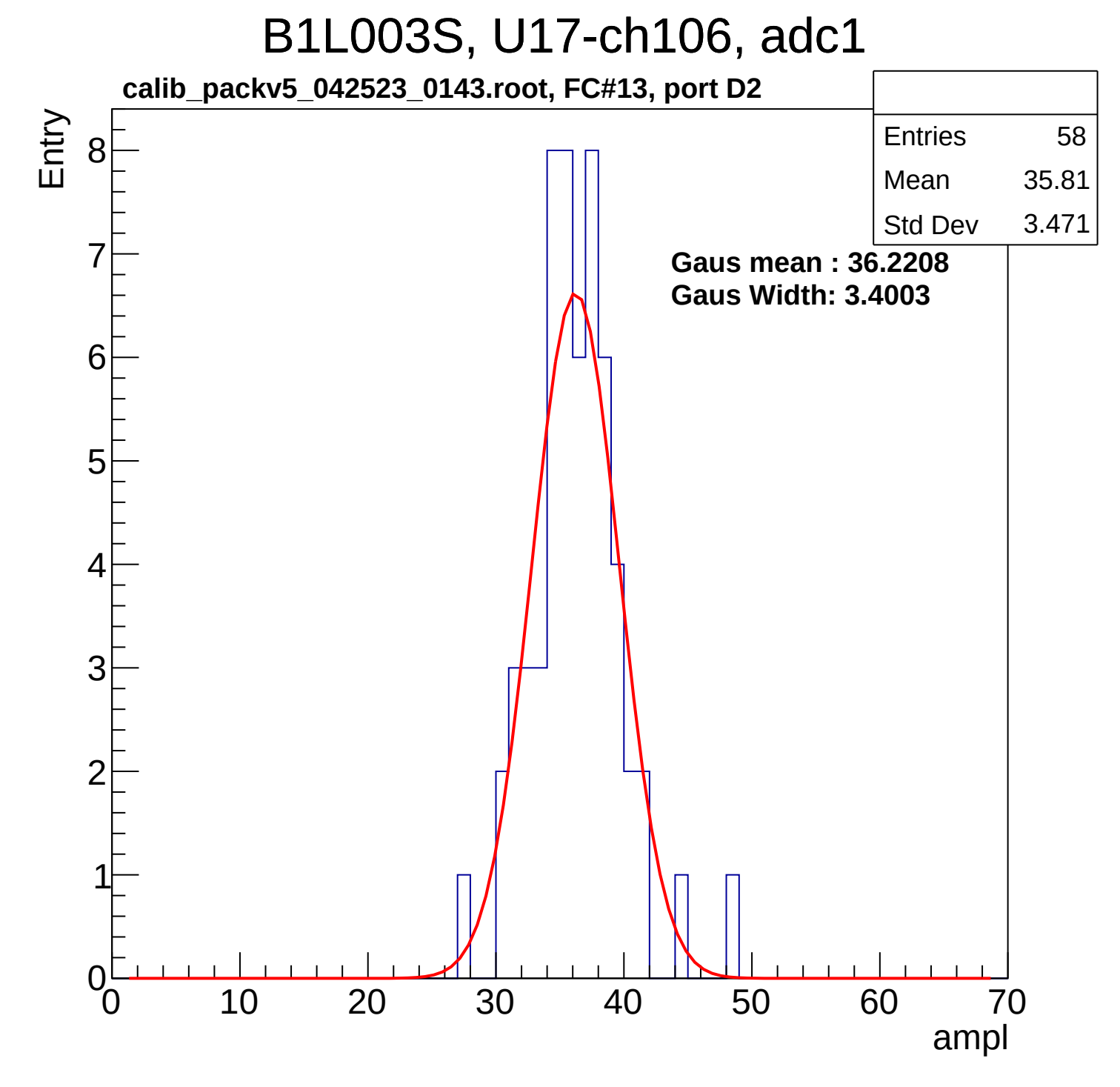
B1L003S, U17-ch106, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 35.81
Std Dev 3.471
Gaus mean : 36.2208
Gaus Width: 3.4003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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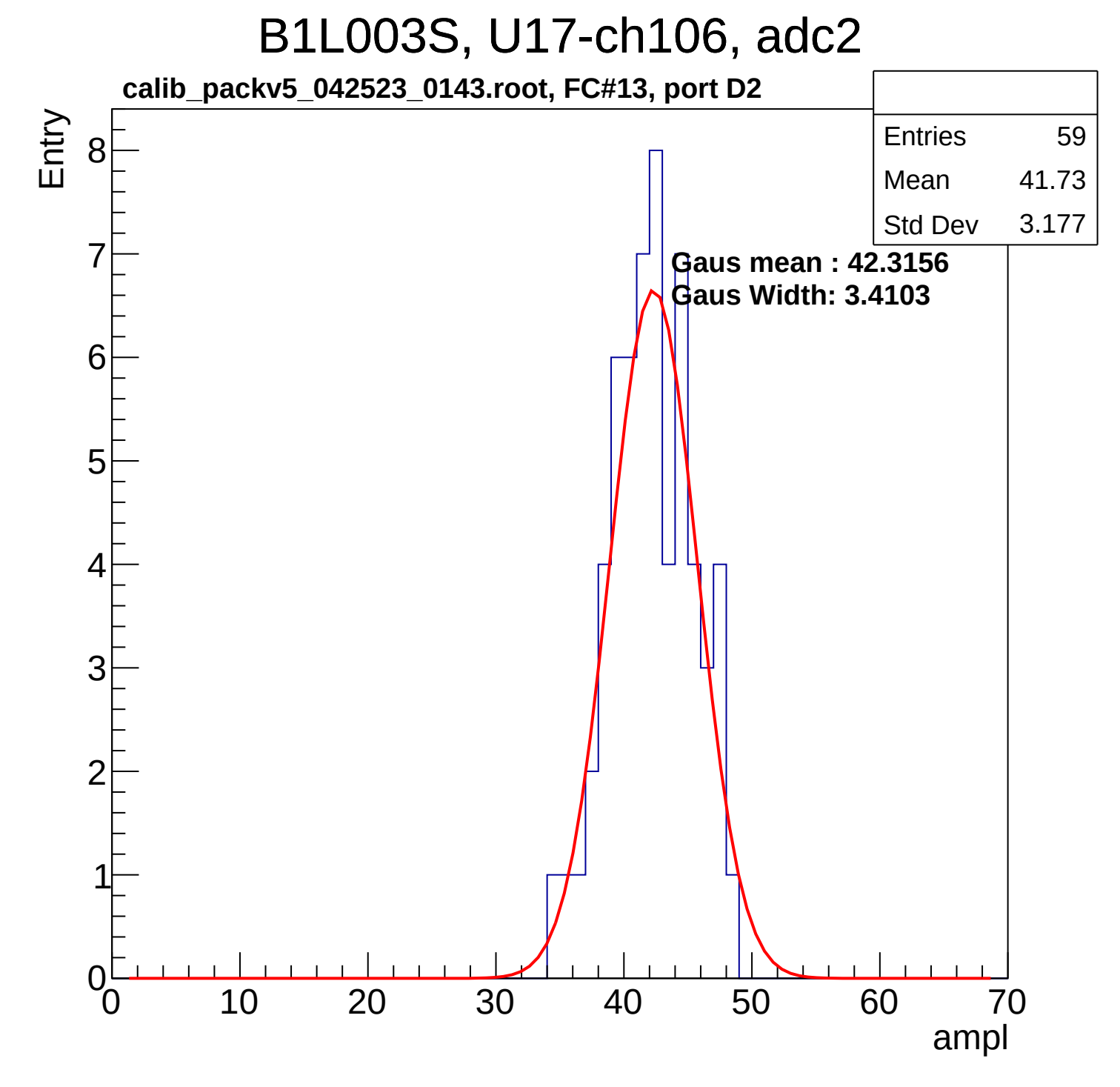
B1L003S, U17-ch106, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 41.73
Std Dev 3.177
Gaus mean : 42.3156
Gaus Width: 3.4103
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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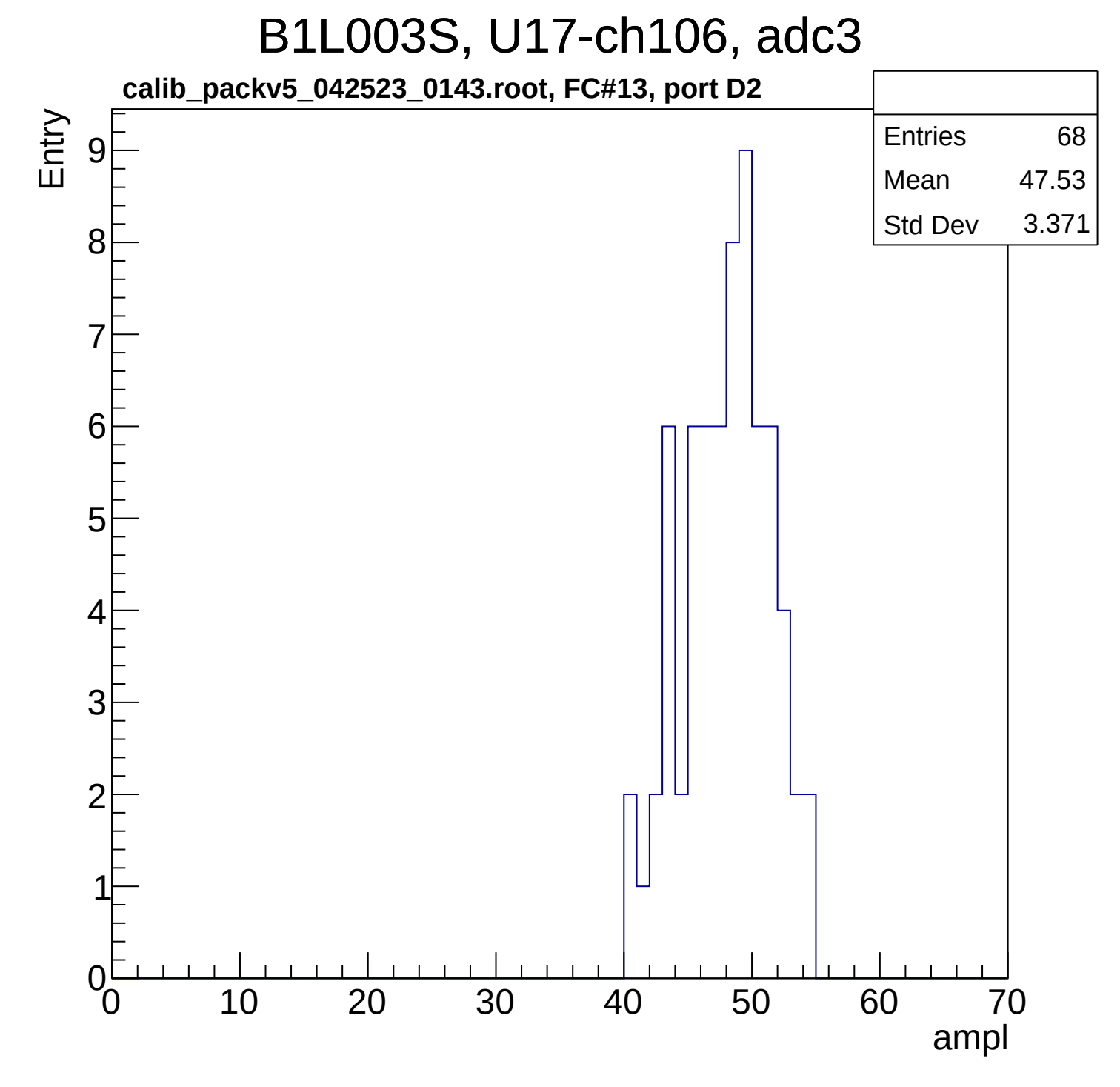
B1L003S, U17-ch106, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 47.53
Std Dev 3.371
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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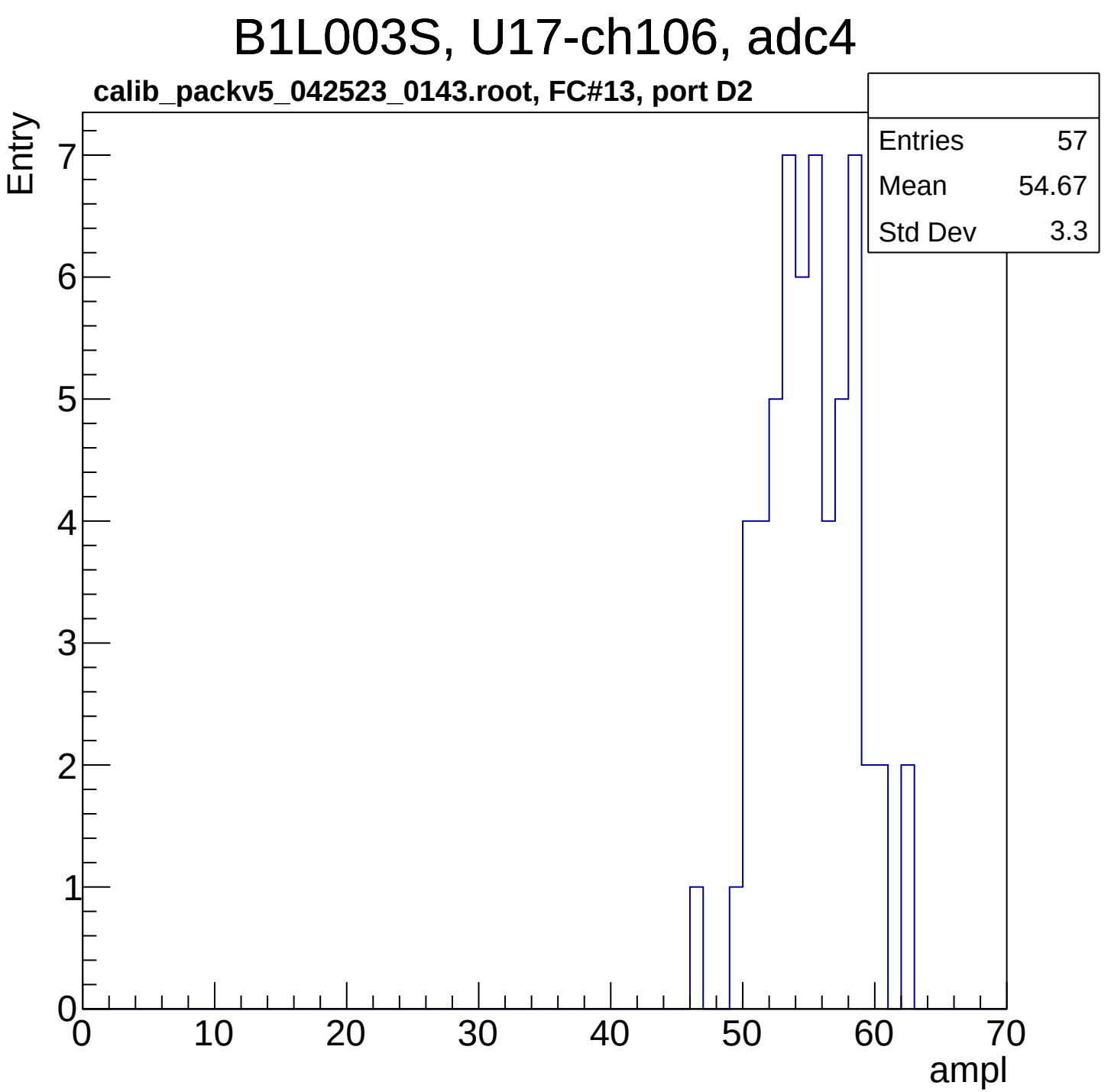
B1L003S, U17-ch106, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.67
Std Dev 3.3
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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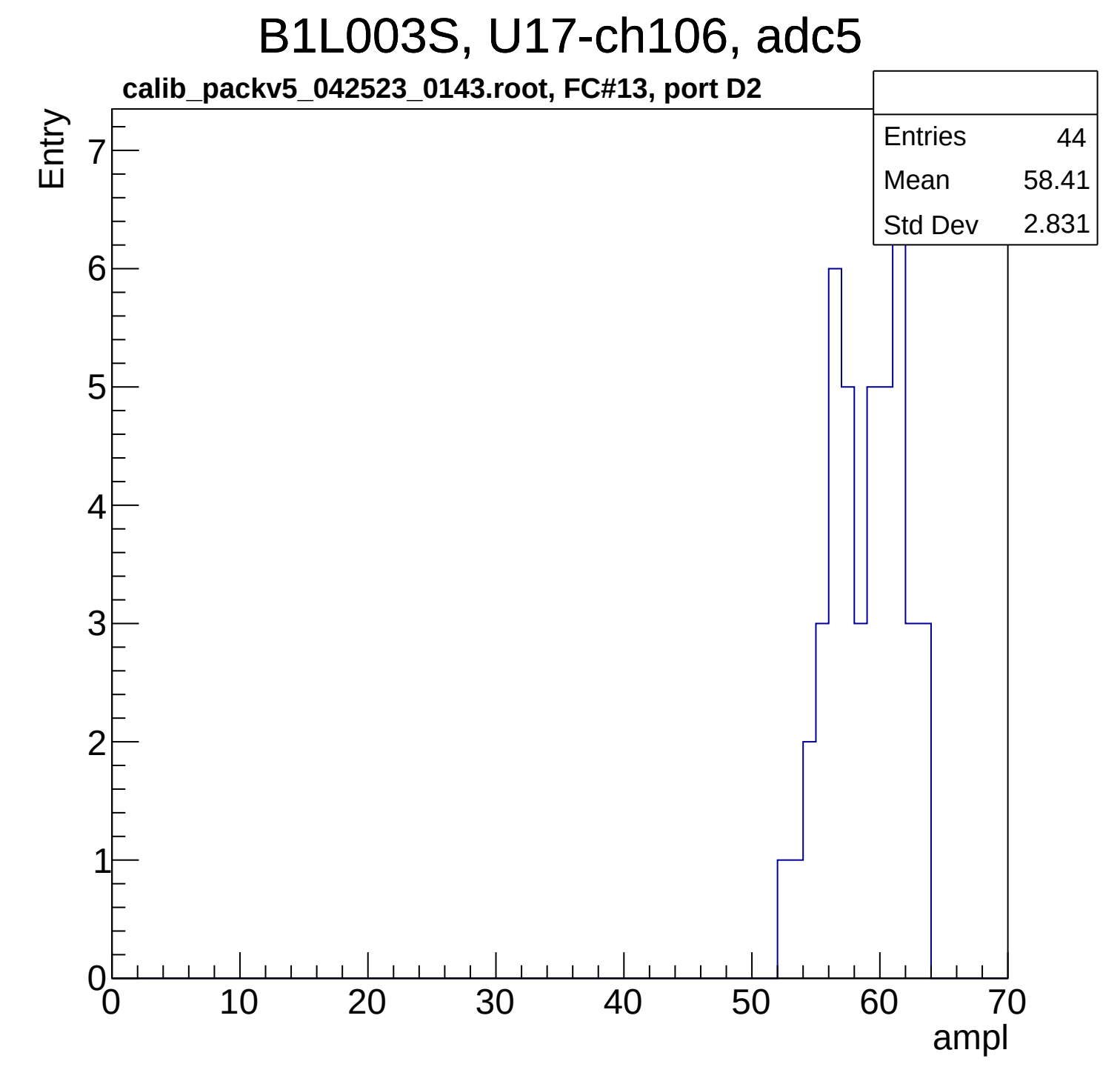
B1L003S, U17-ch106, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.41
Std Dev 2.831
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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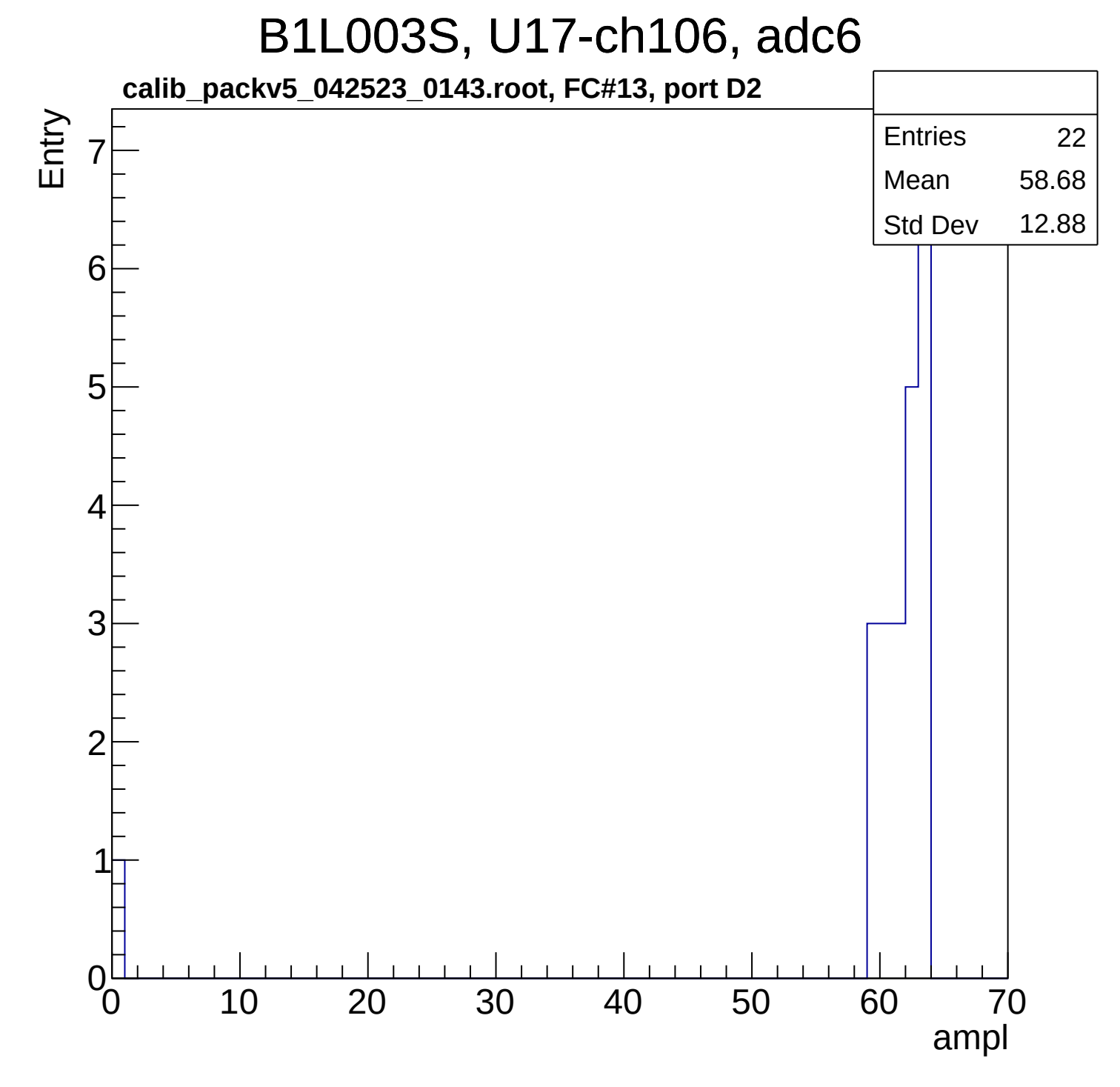
B1L003S, U17-ch106, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 58.68
Std Dev 12.88
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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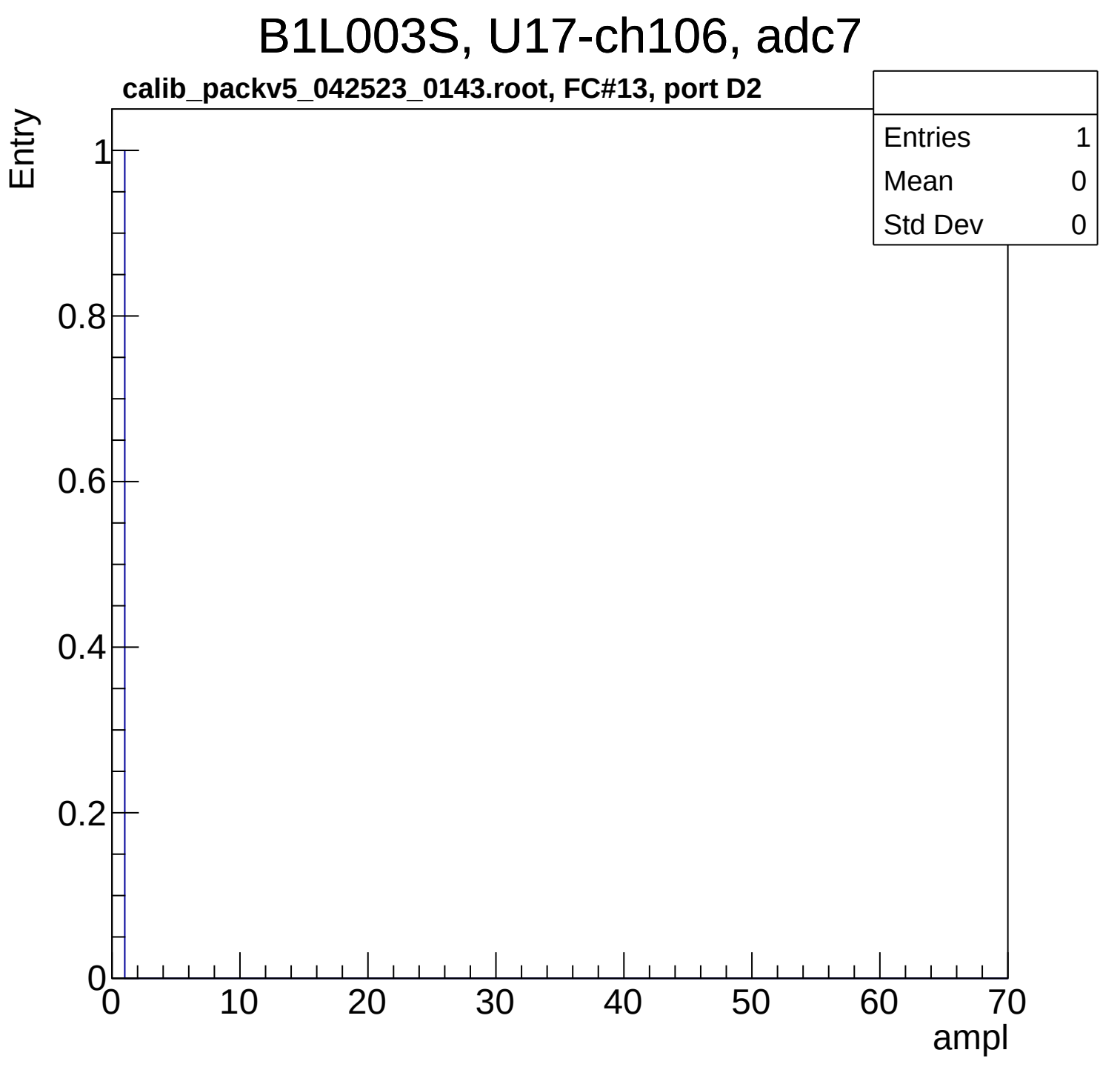
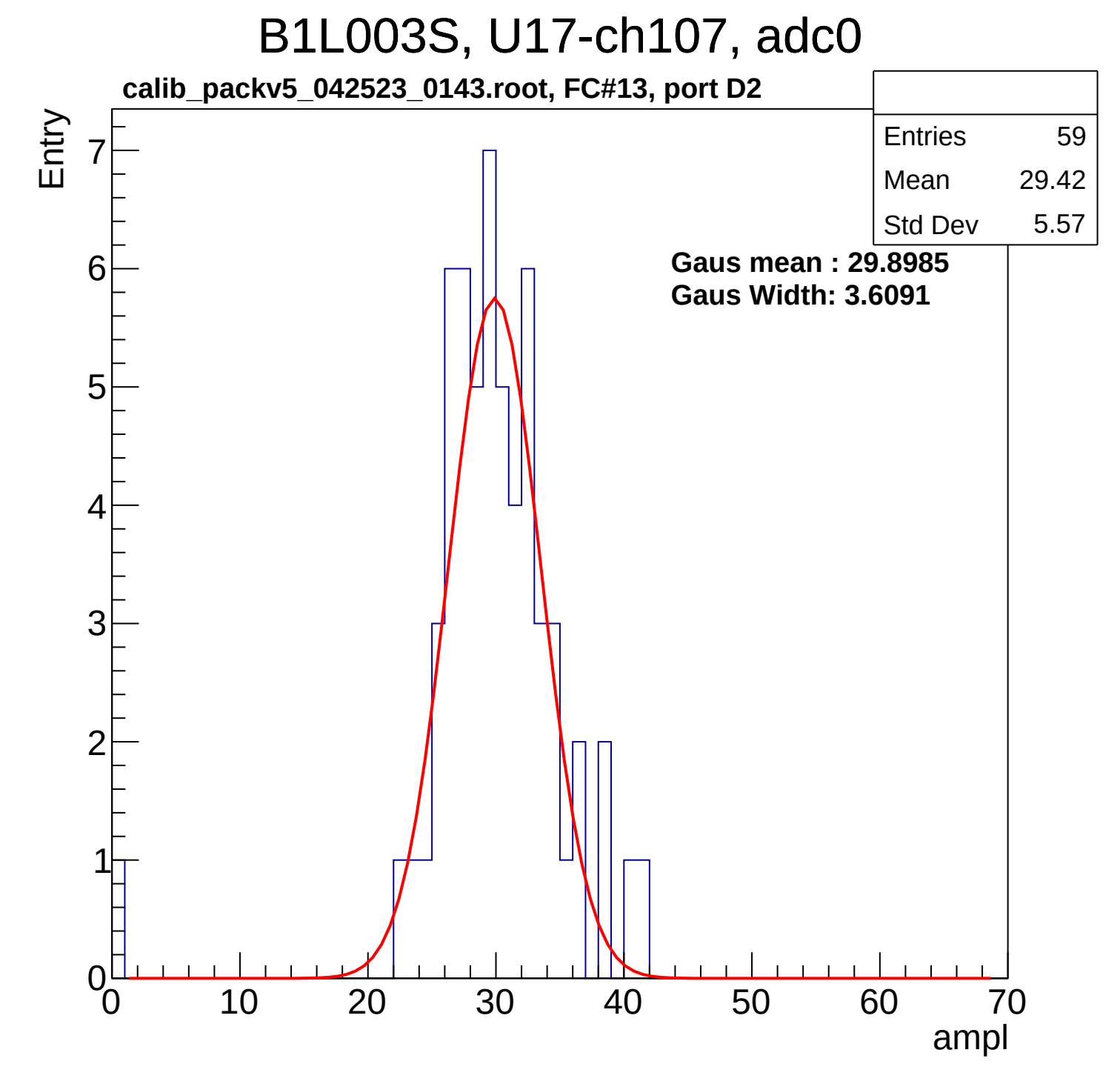

B1L003S, U17-ch106, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch107, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 29.42
Std Dev 5.57
Gaus mean : 29.8985
Gaus Width: 3.6091
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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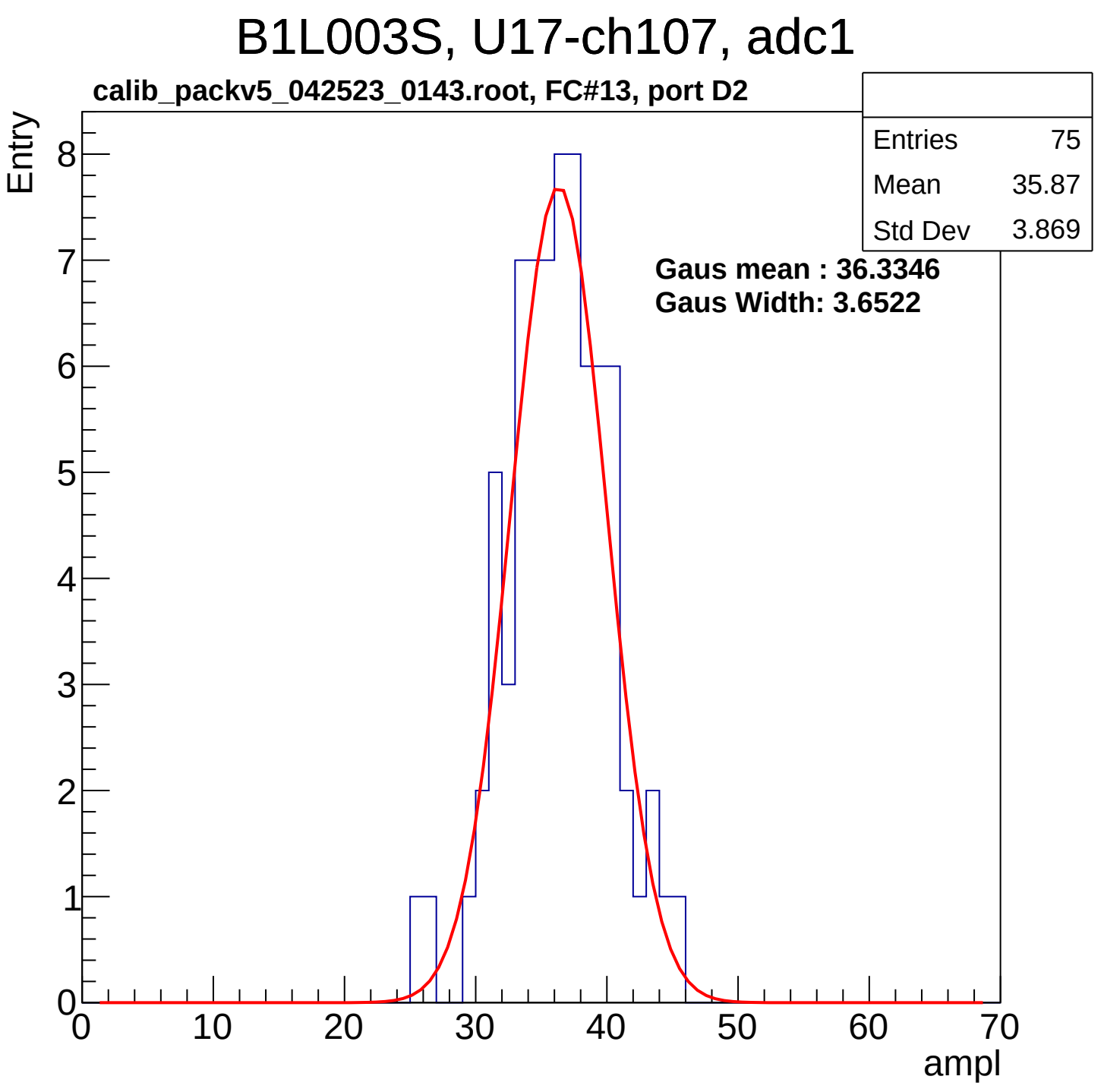
B1L003S, U17-ch107, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 35.87
Std Dev 3.869
Gaus mean : 36.3346
Gaus Width: 3.6522
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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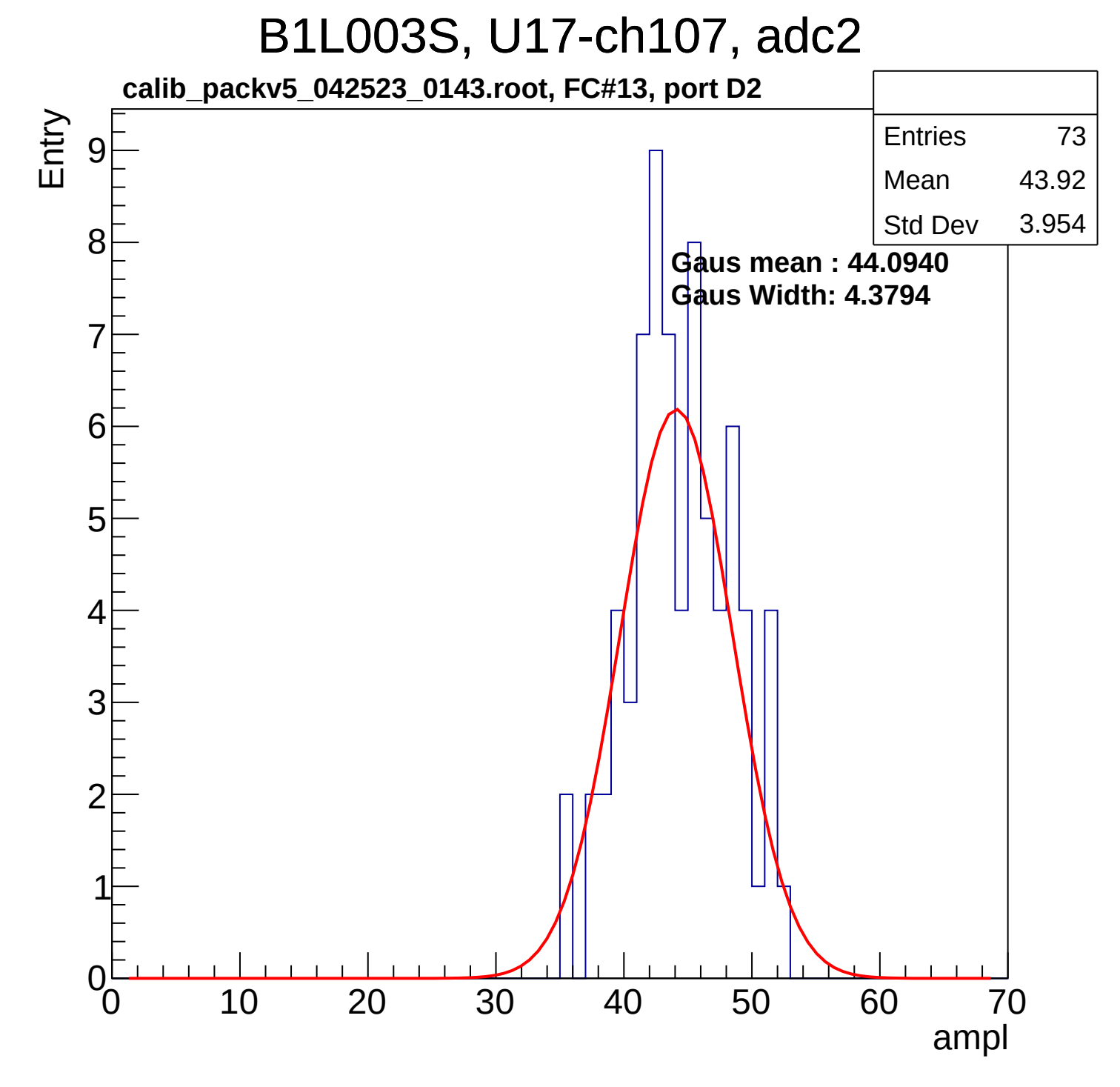
60
70


B1L003S, U17-ch107, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 43.92
Std Dev 3.954
Gaus mean : 44.0940
Gaus Width: 4.3794
Entry
ampl
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2
3
4
5
6
7
8
9
0
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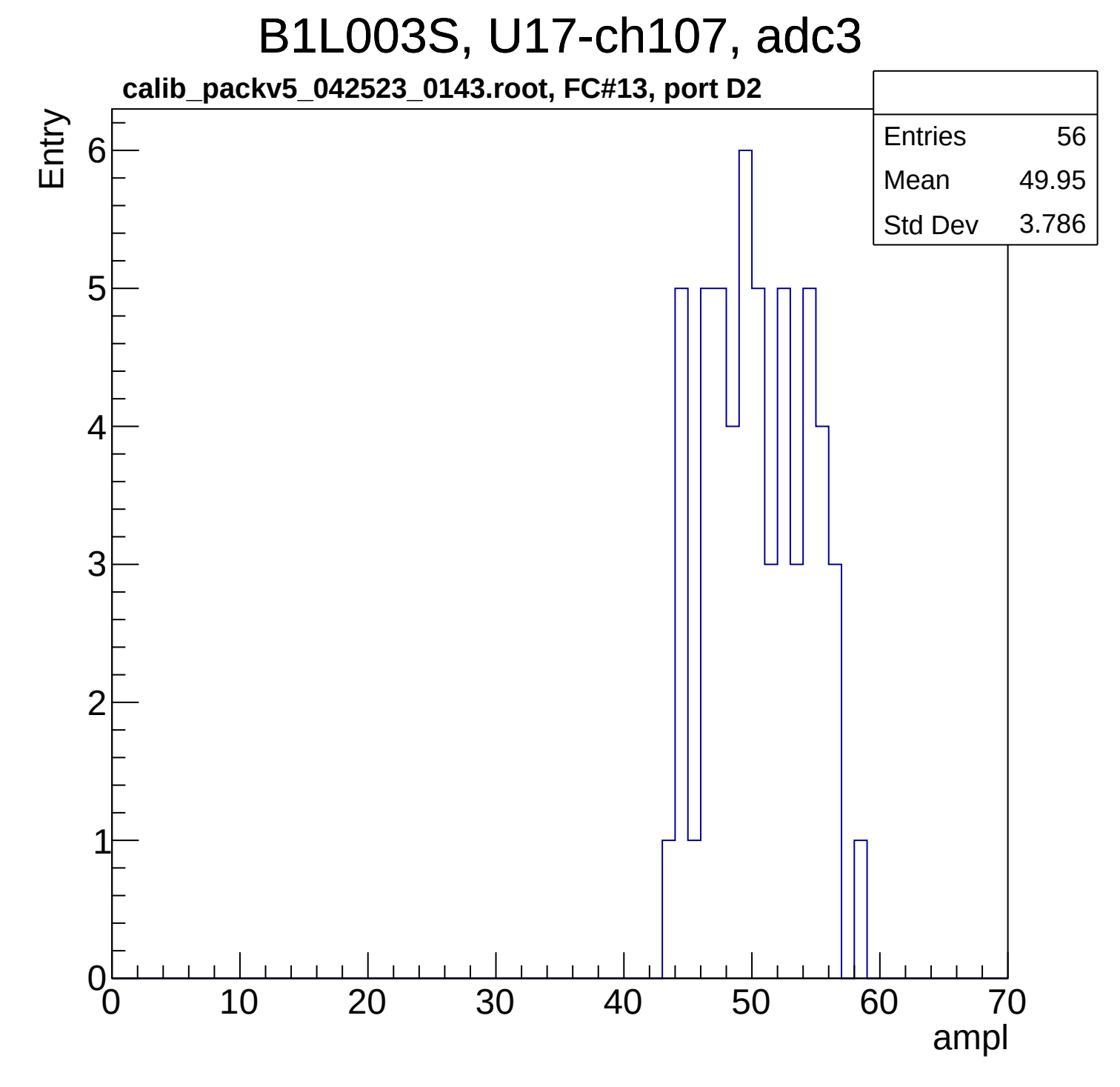
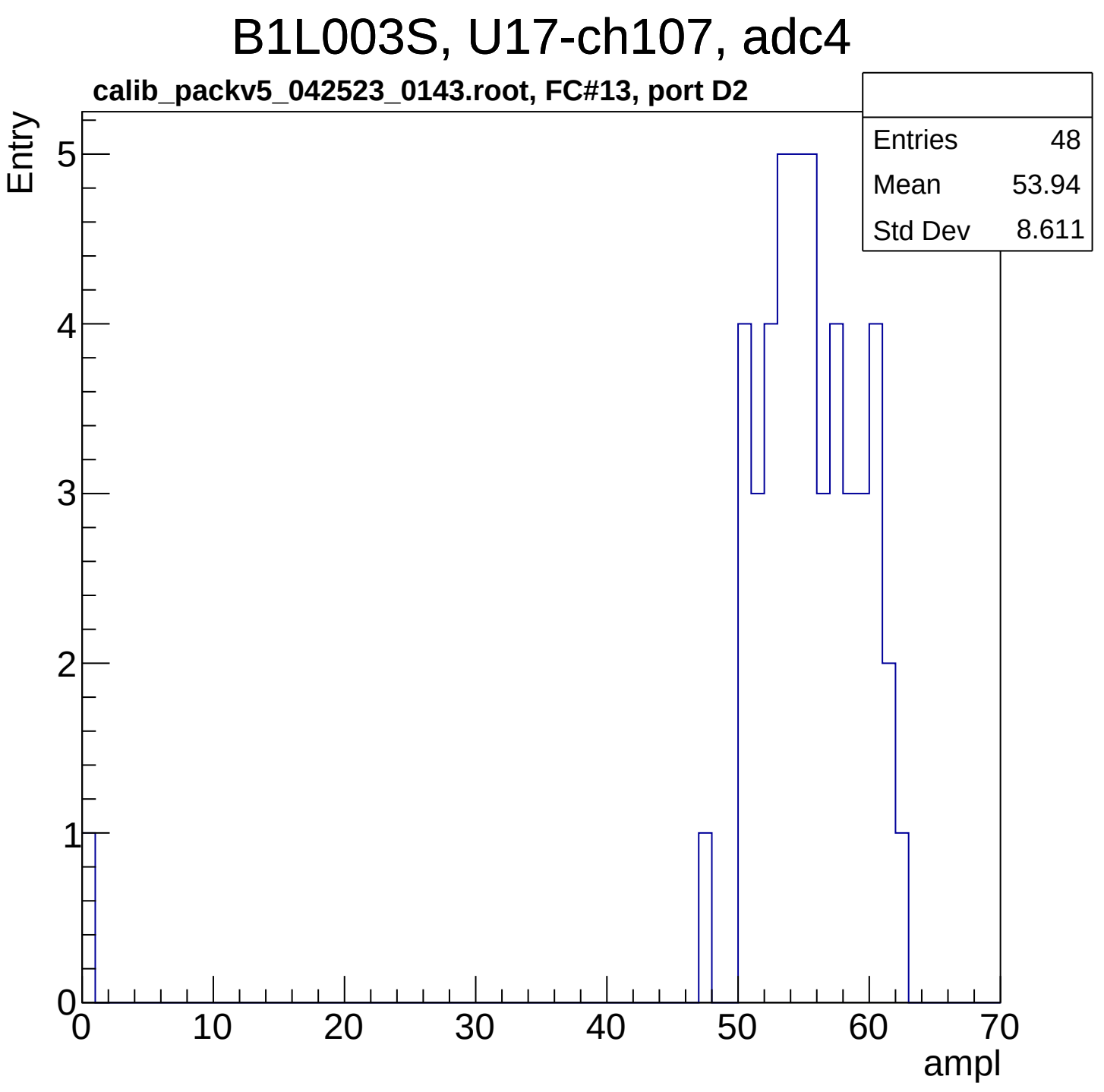
20
30
40
50
60
70


B1L003S, U17-ch107, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 49.95
Std Dev 3.786
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch107, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 53.94
Std Dev 8.611
Entry
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1
2
3
4
5
0
10
20
30
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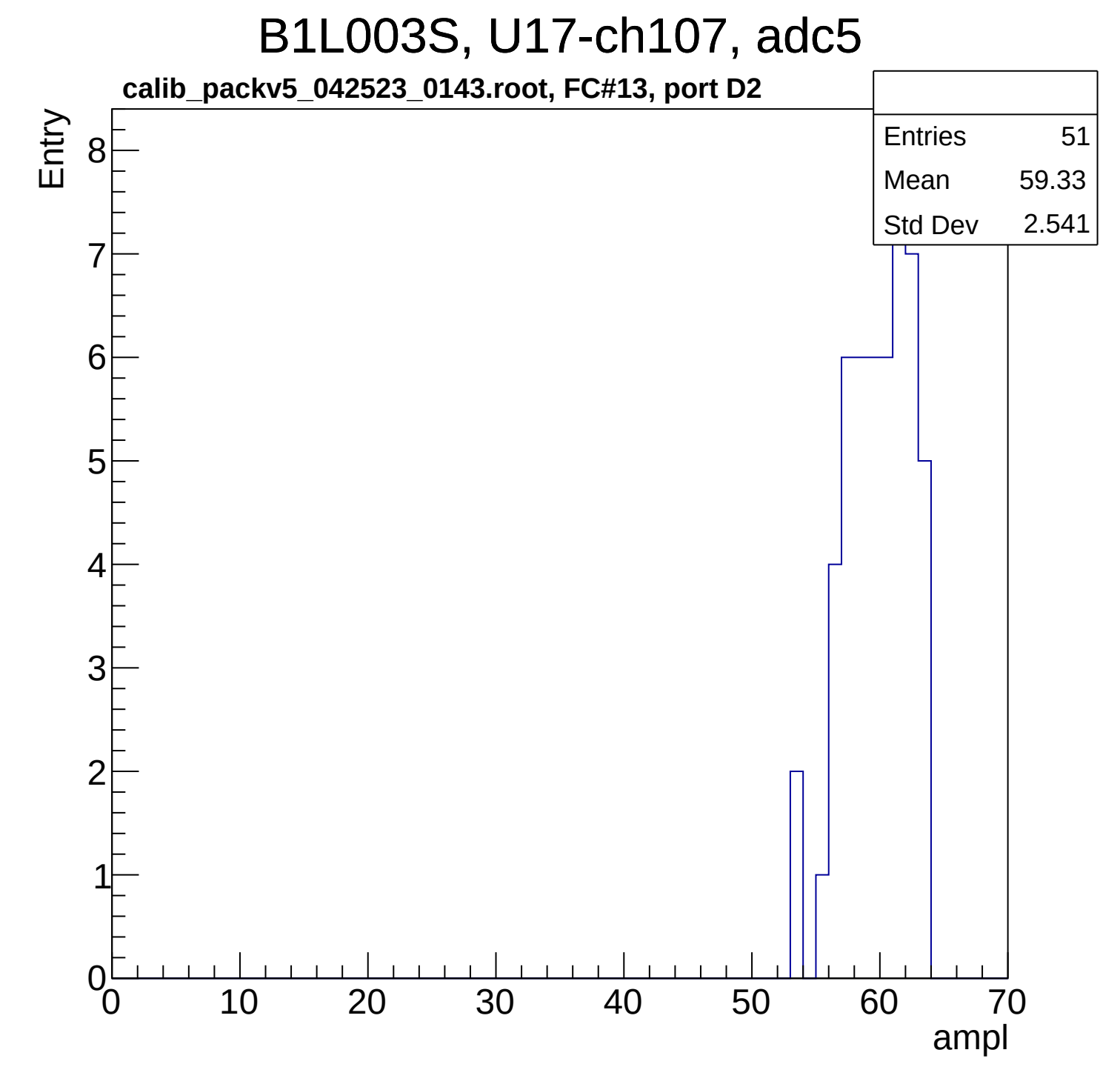
50
60
70
ampl


B1L003S, U17-ch107, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.33
Std Dev 2.541
Entry
ampl
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1
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3
4
5
6
7
8
0
10
20
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40
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60
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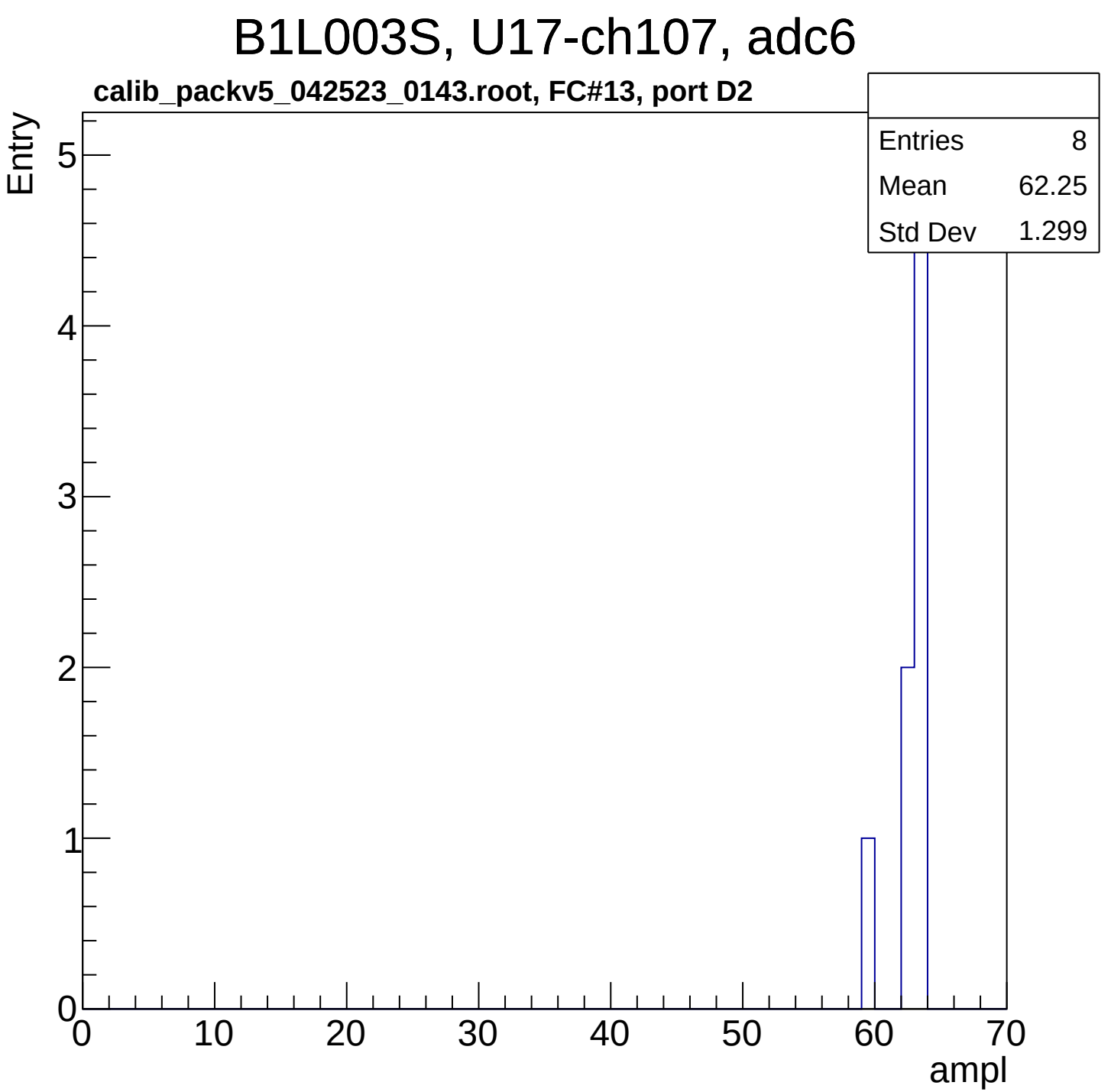
B1L003S, U17-ch107, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.25
Std Dev 1.299
Entry
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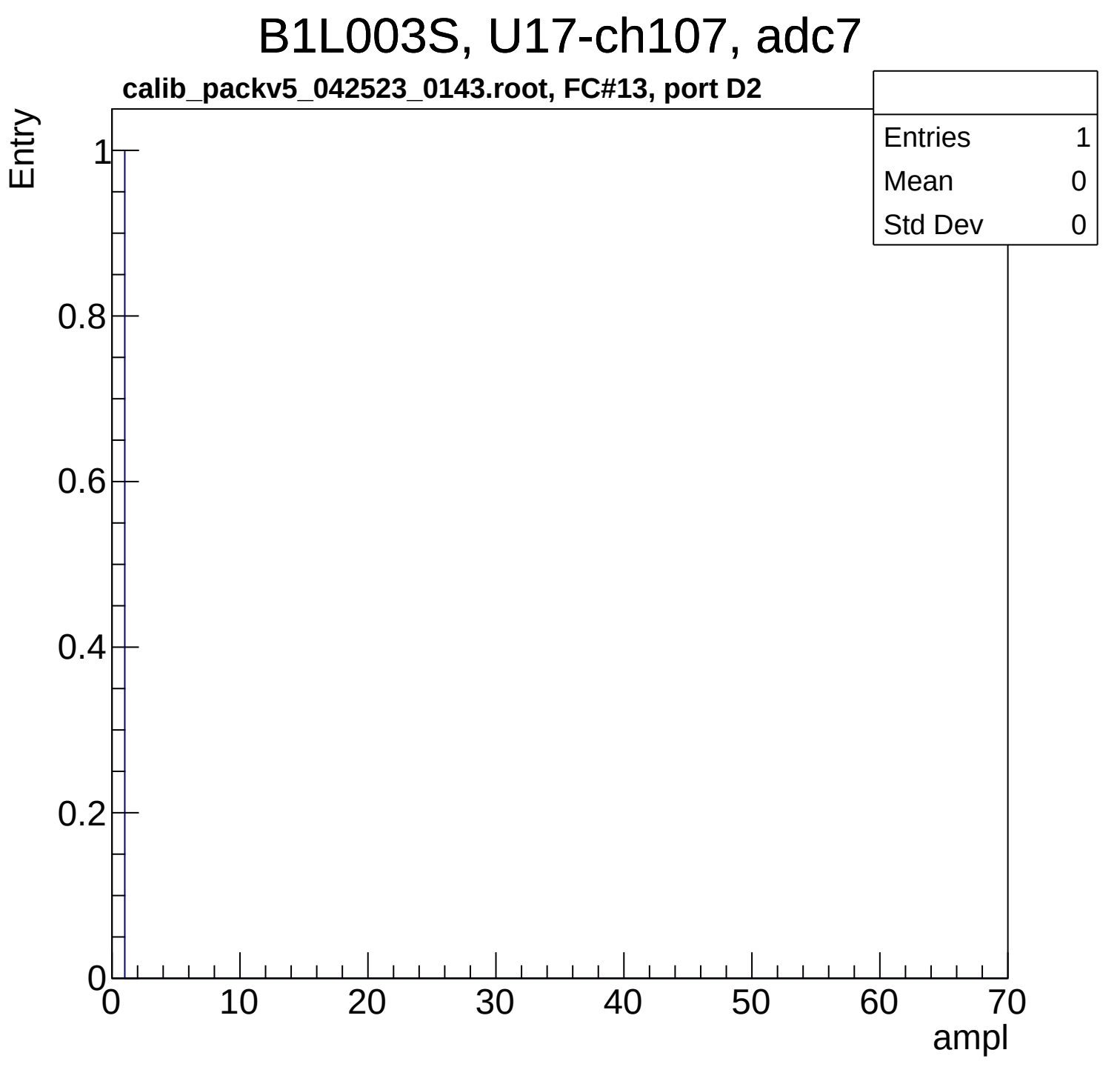
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch107, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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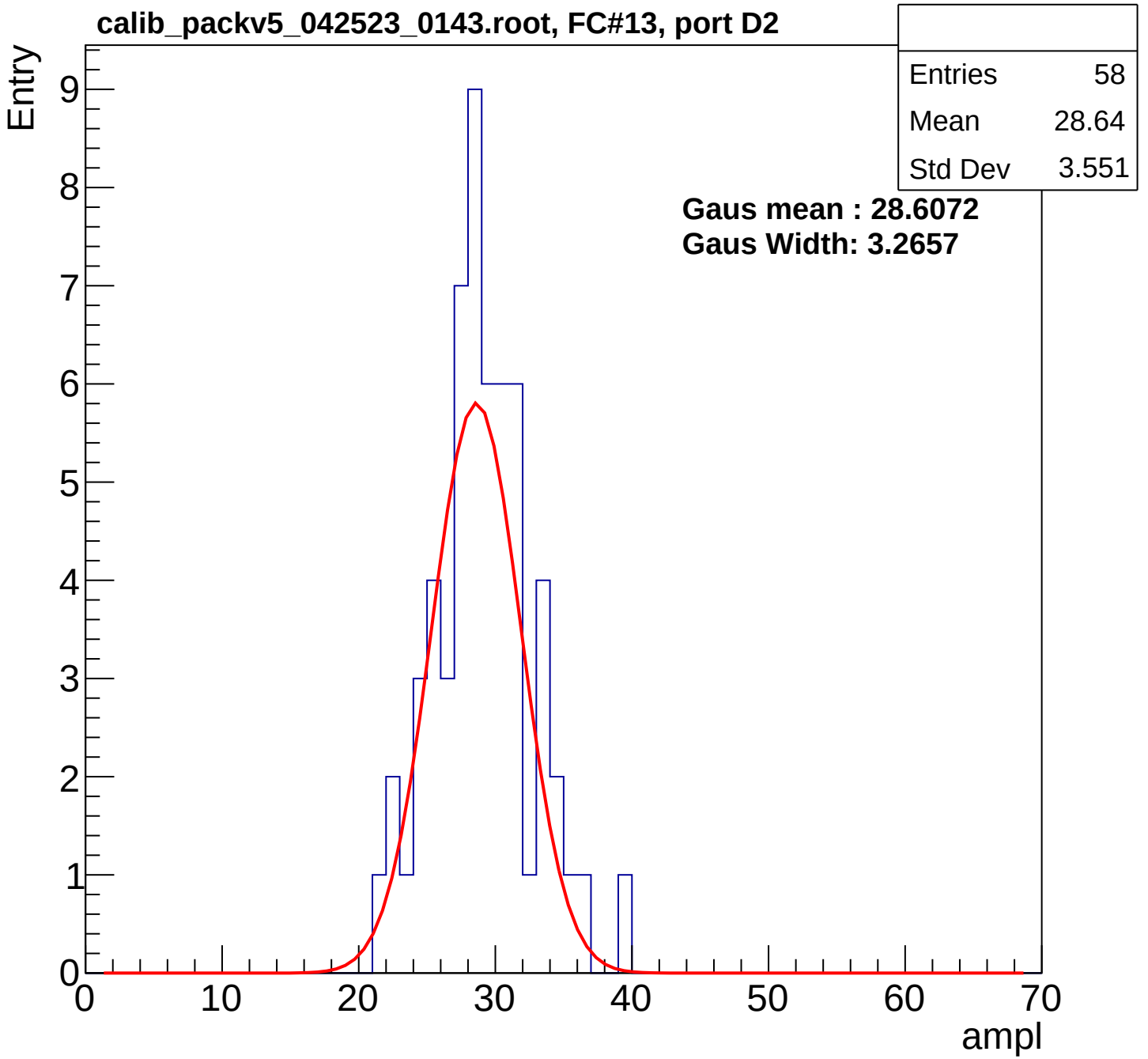
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch108, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.64
Std Dev 3.551
Gaus mean : 28.6072
Gaus Width: 3.2657
Entry
ampl
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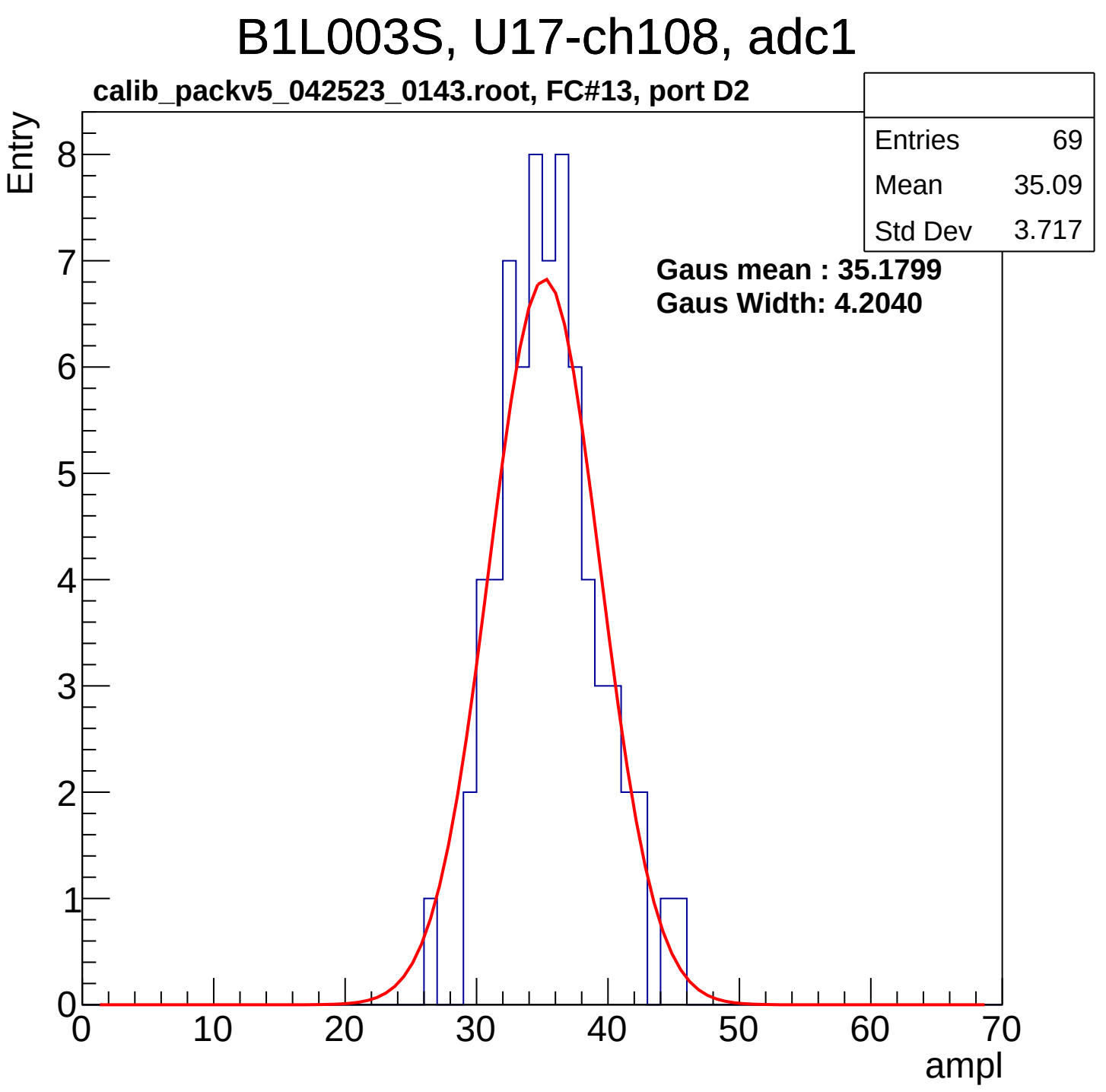
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch108, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.09
Std Dev 3.717
Gaus mean : 35.1799
Gaus Width: 4.2040
Entry
ampl
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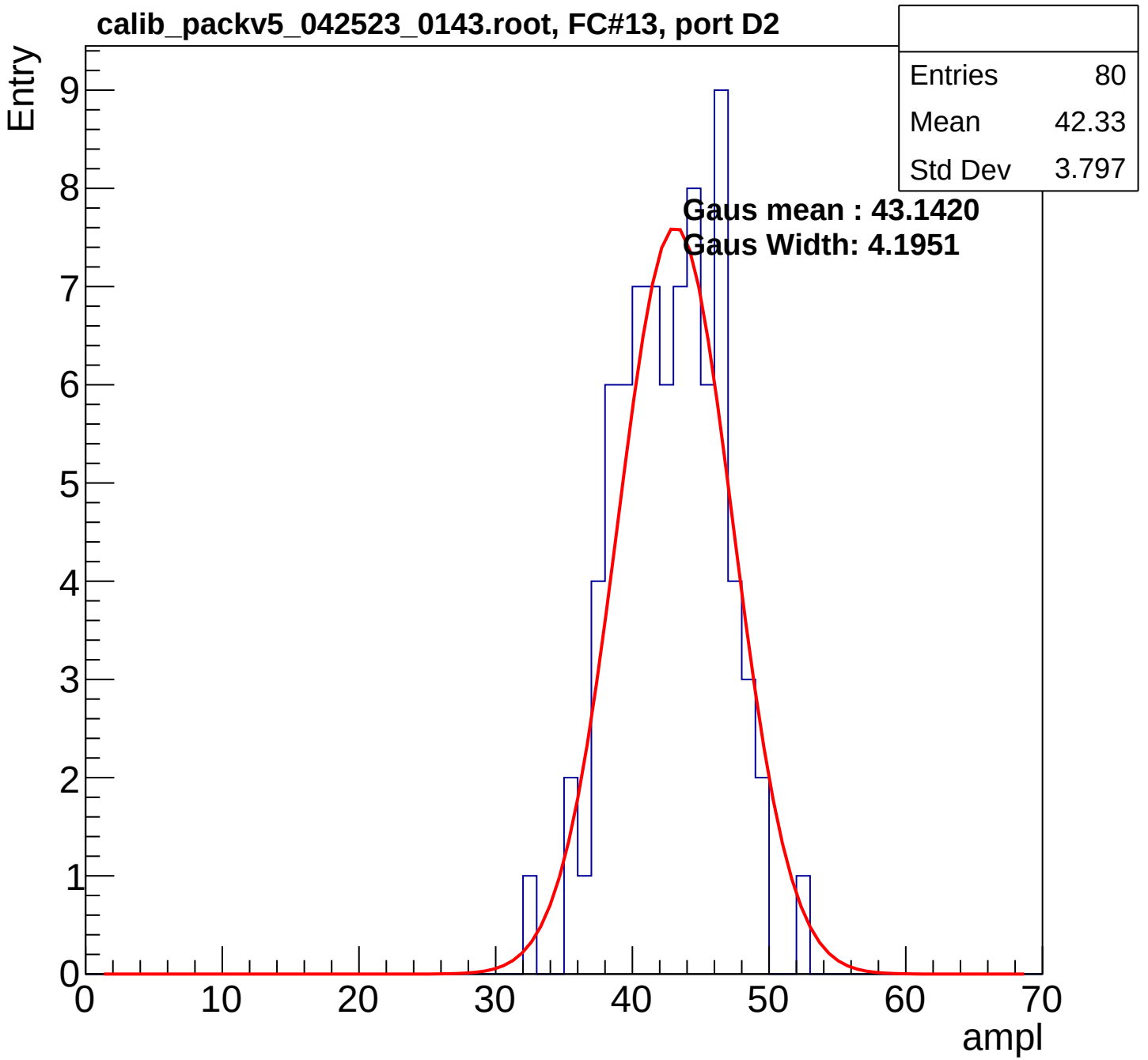
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch108, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 42.33
Std Dev 3.797
Gaus mean : 43.1420
Gaus Width: 4.1951
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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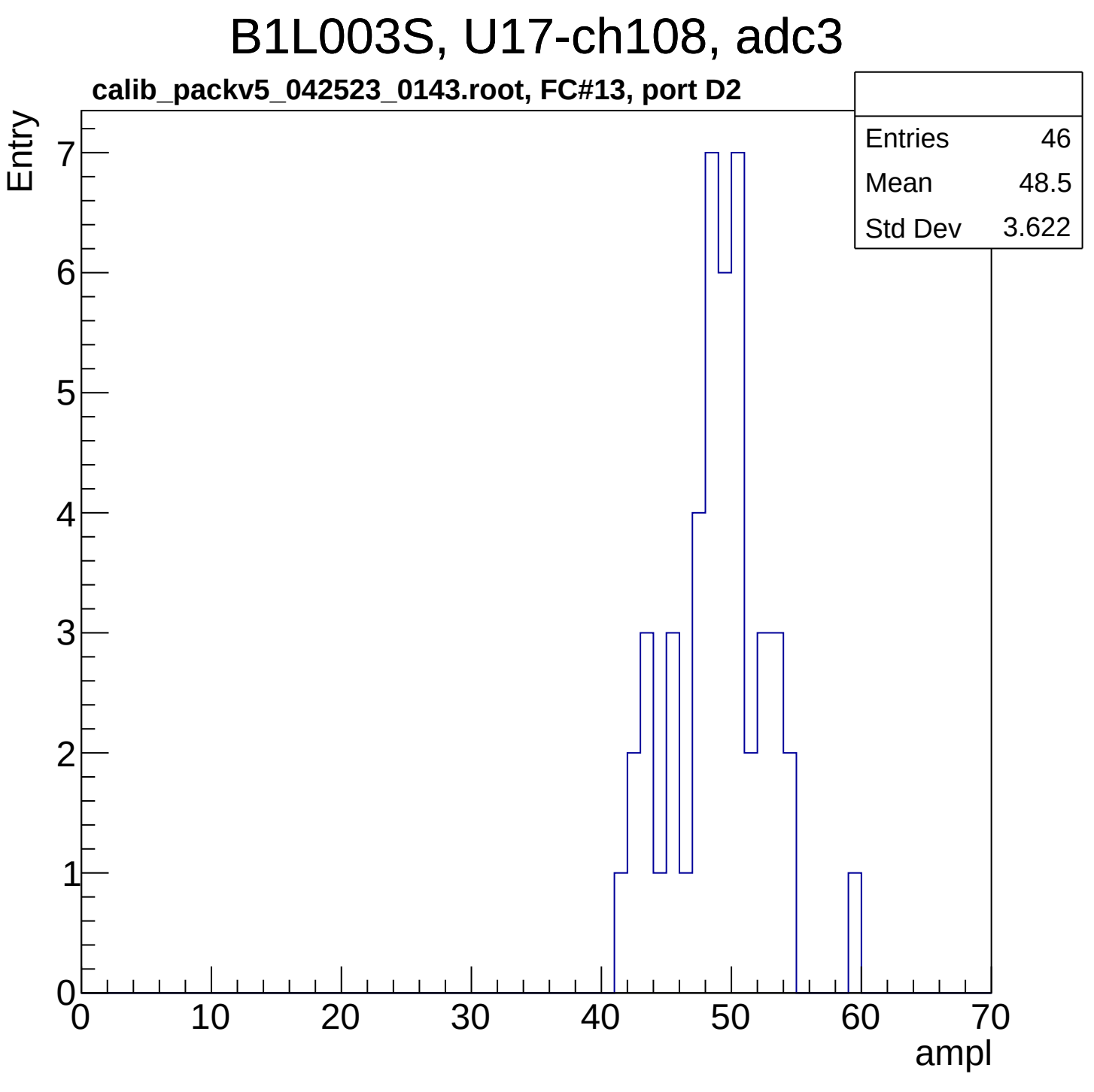
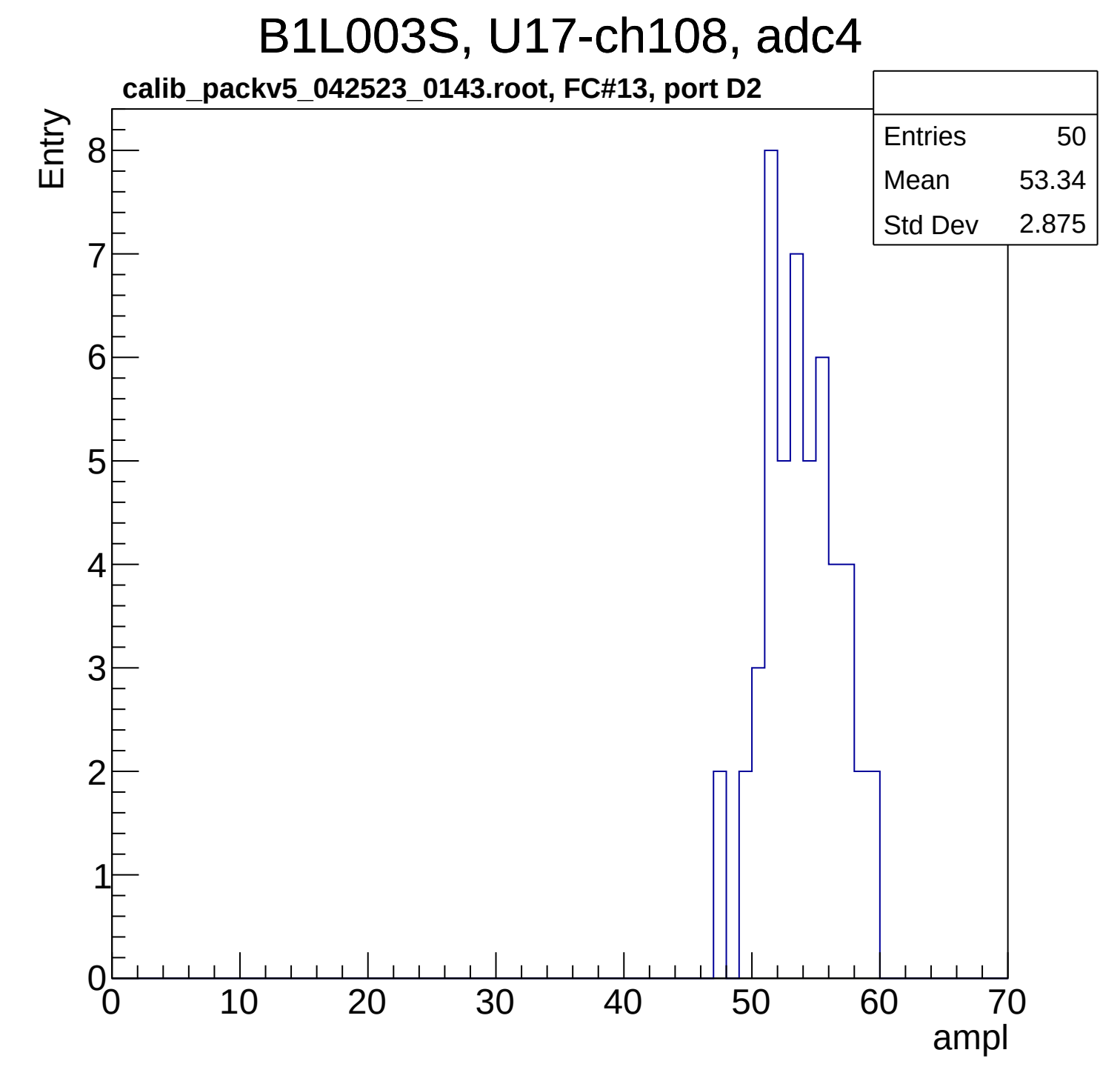

B1L003S, U17-ch108, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 48.5
Std Dev 3.622
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch108, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 53.34
Std Dev 2.875
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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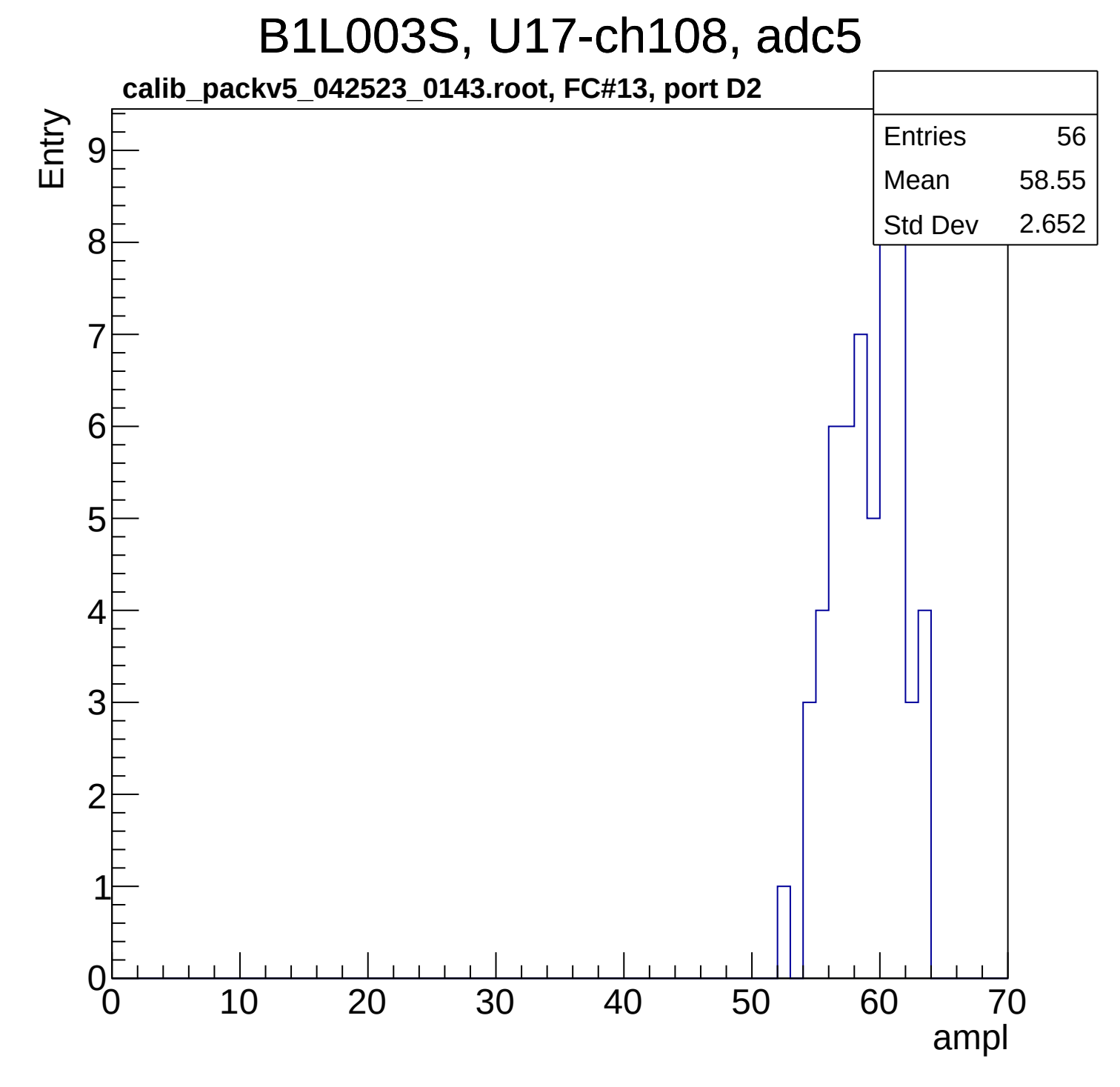
60
70


B1L003S, U17-ch108, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.55
Std Dev 2.652
Entry
ampl
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3
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5
6
7
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9
0
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60
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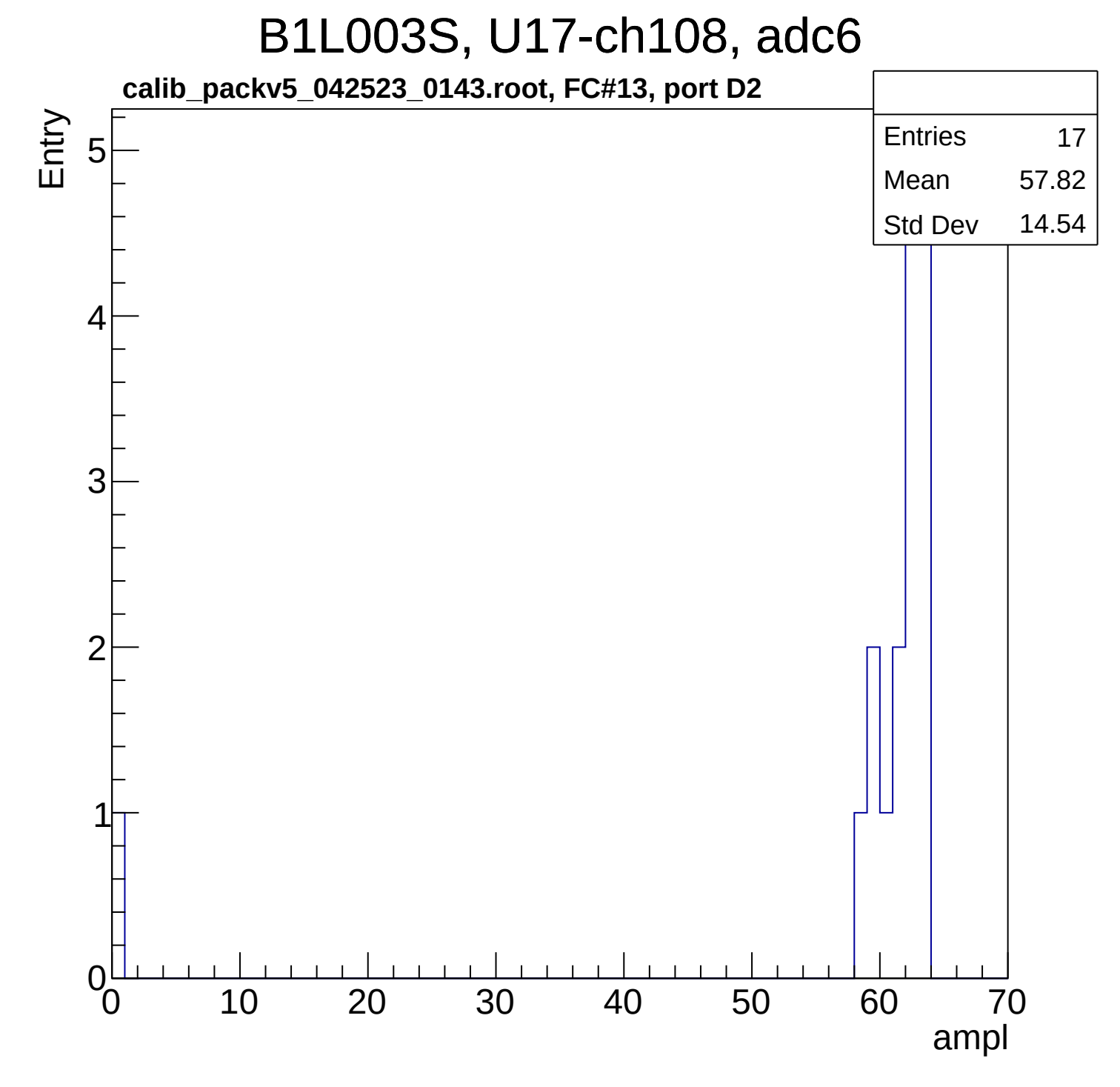
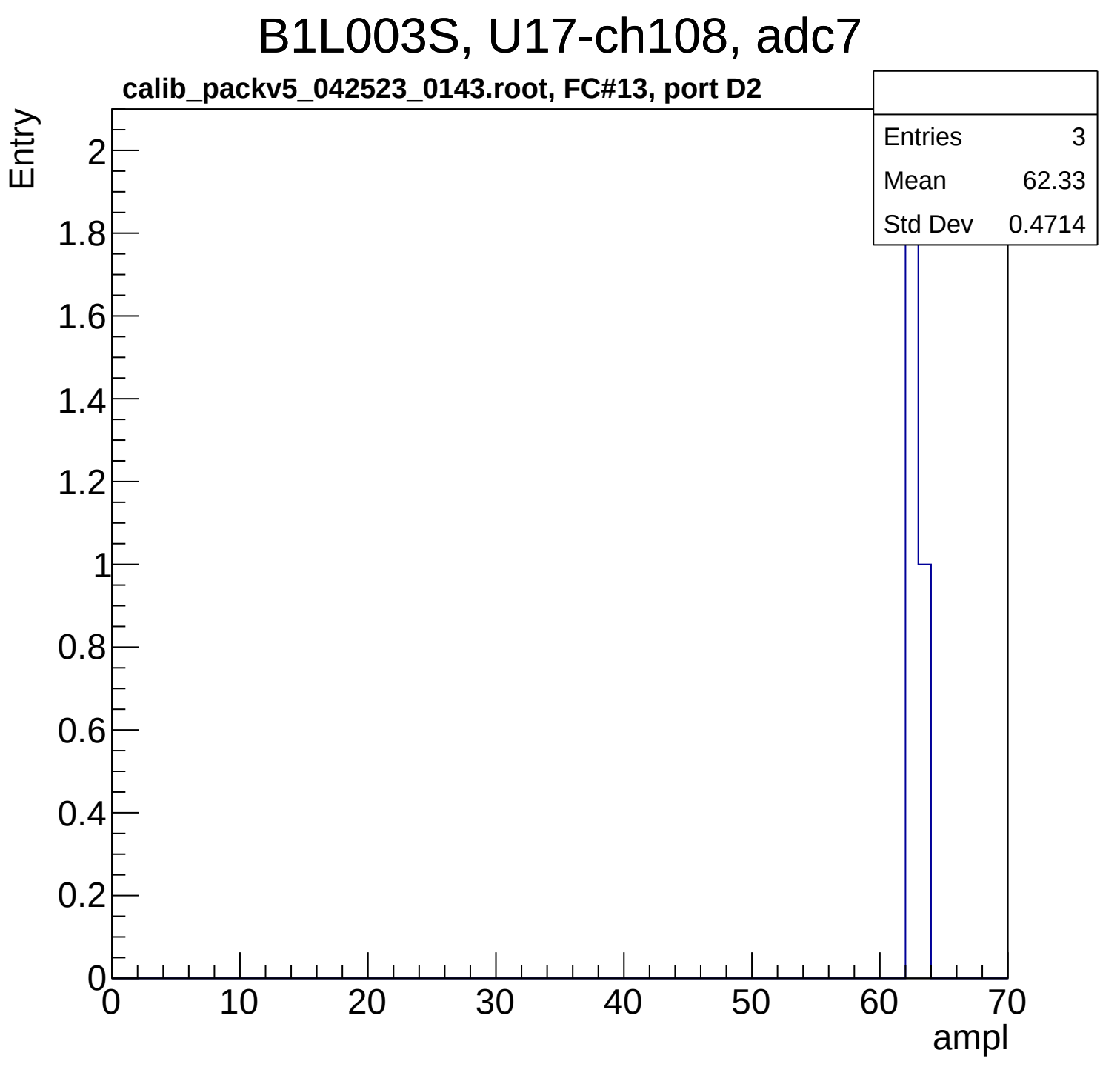

B1L003S, U17-ch108, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.82
Std Dev 14.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch108, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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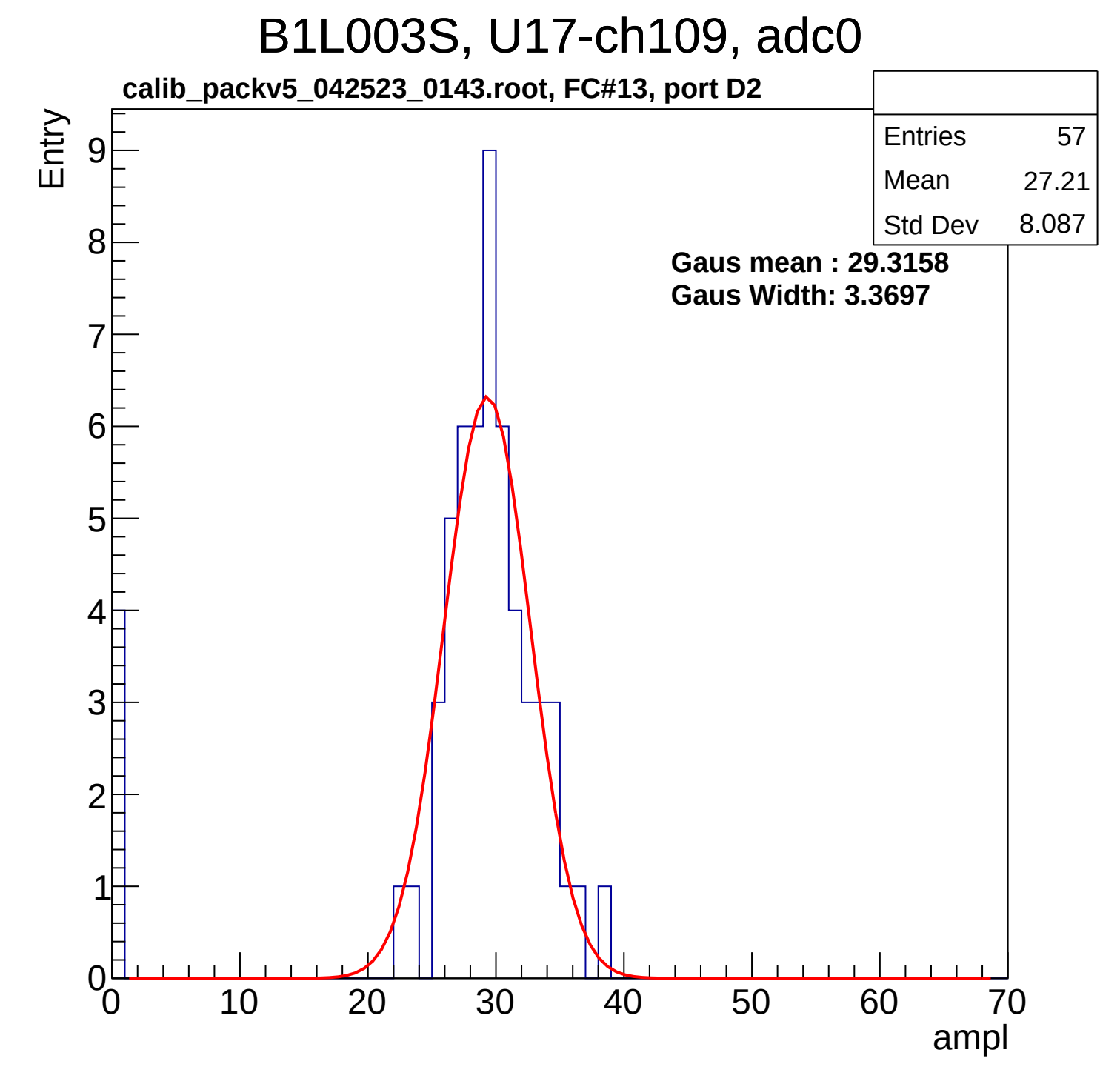
B1L003S, U17-ch109, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 27.21
Std Dev 8.087
Gaus mean : 29.3158
Gaus Width: 3.3697
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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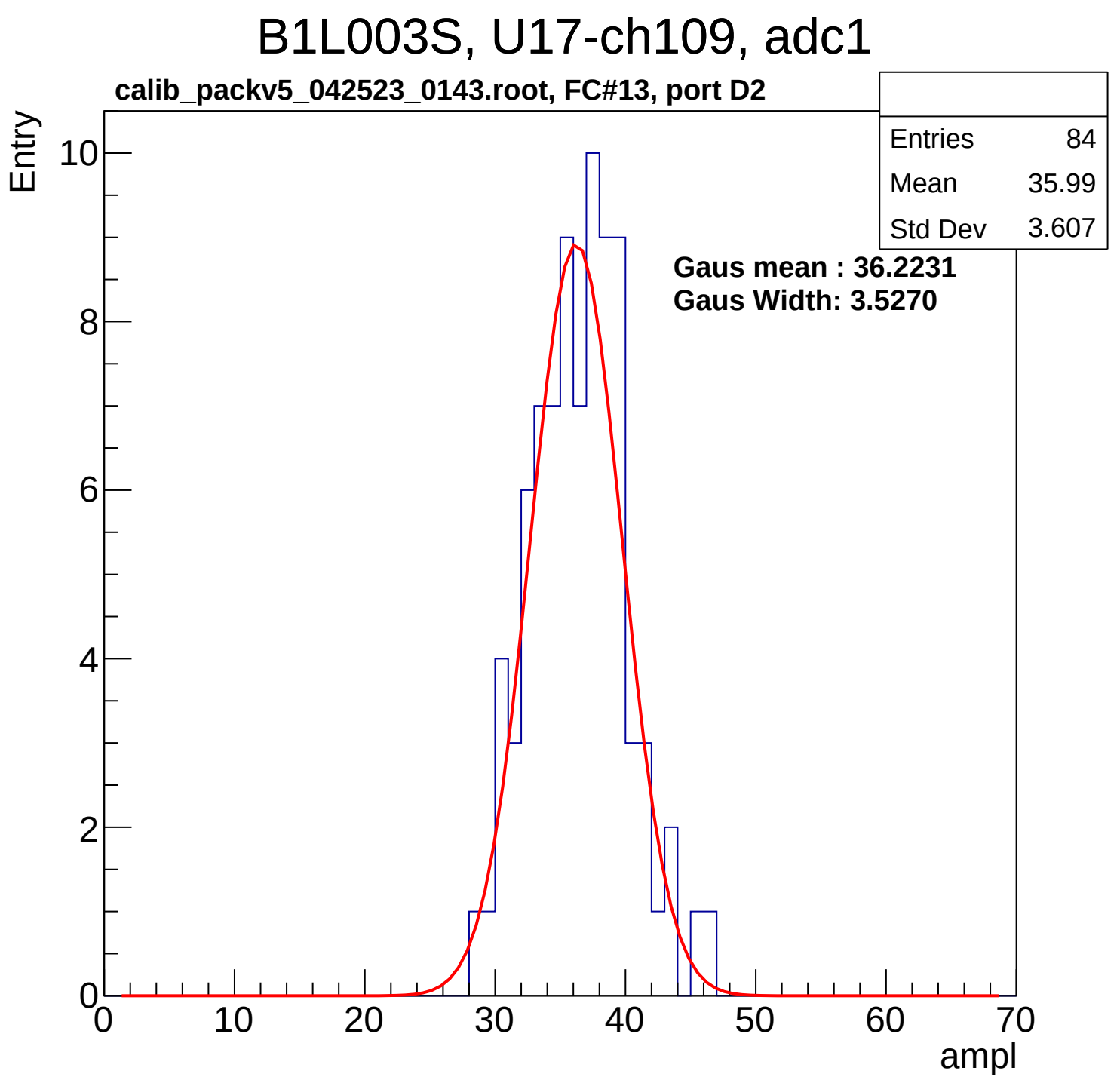
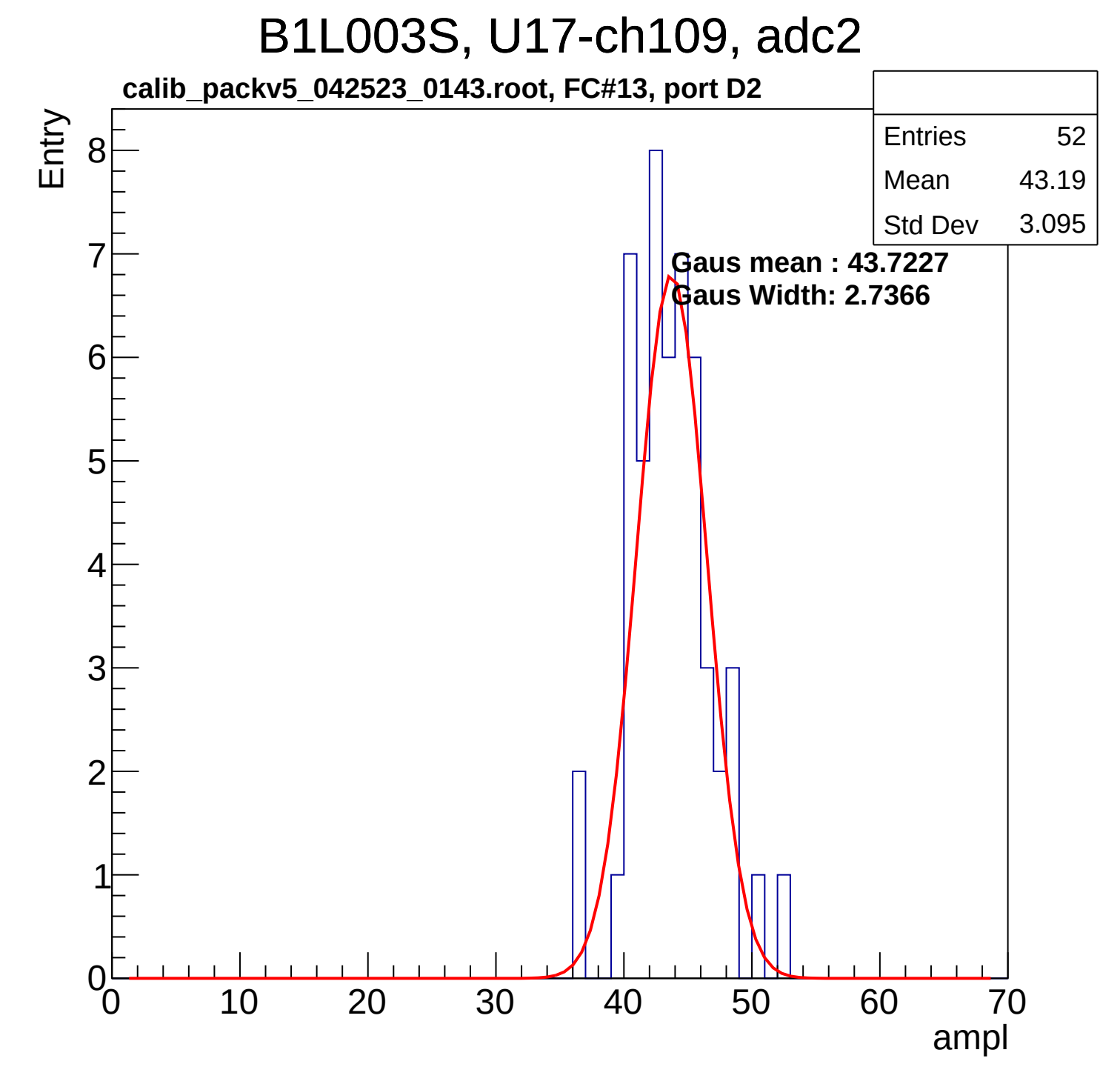

B1L003S, U17-ch109, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 35.99
Std Dev 3.607
Gaus mean : 36.2231
Gaus Width: 3.5270
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch109, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 43.19
Std Dev 3.095
Gaus mean : 43.7227
Gaus Width: 2.7366
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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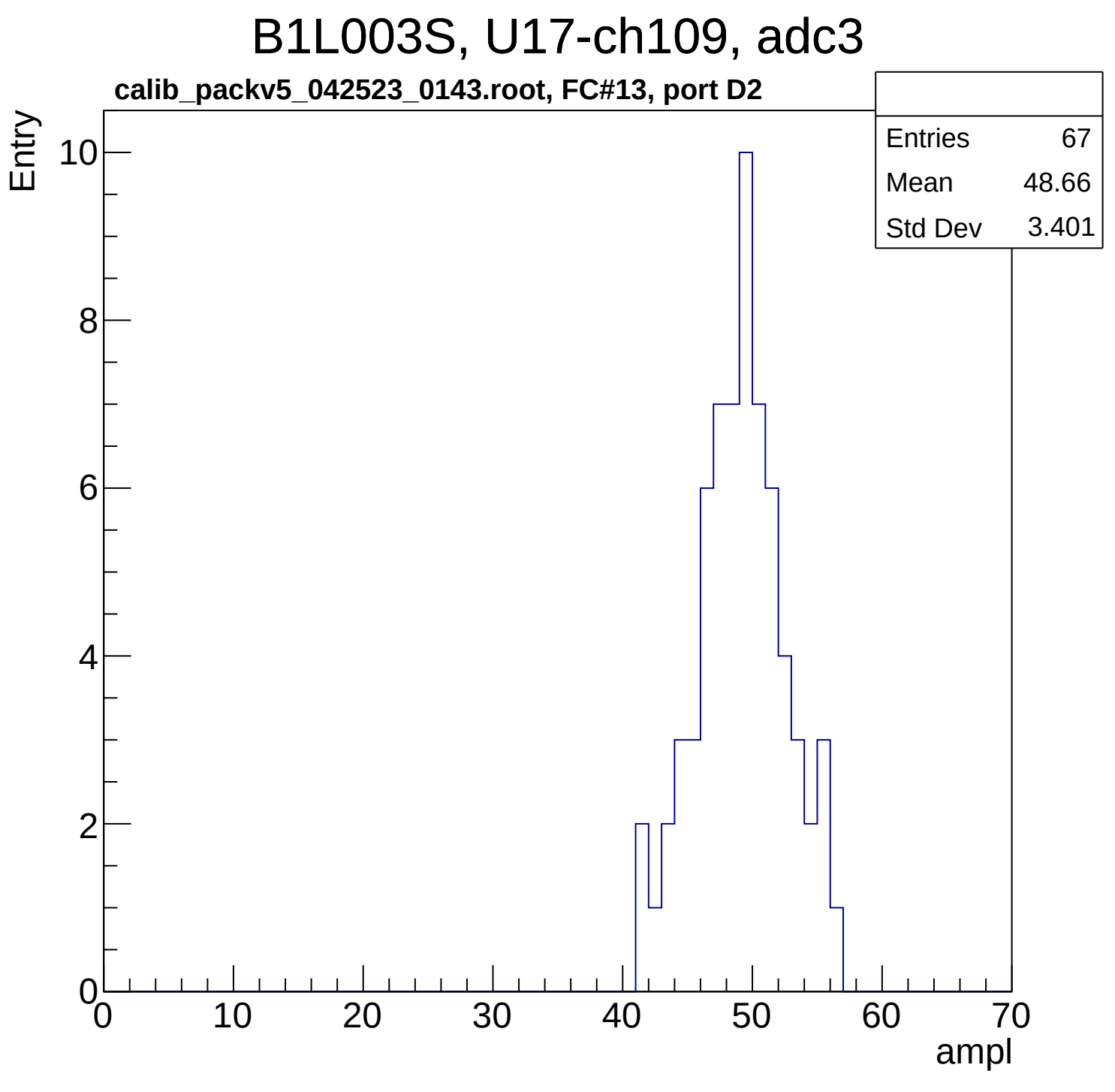
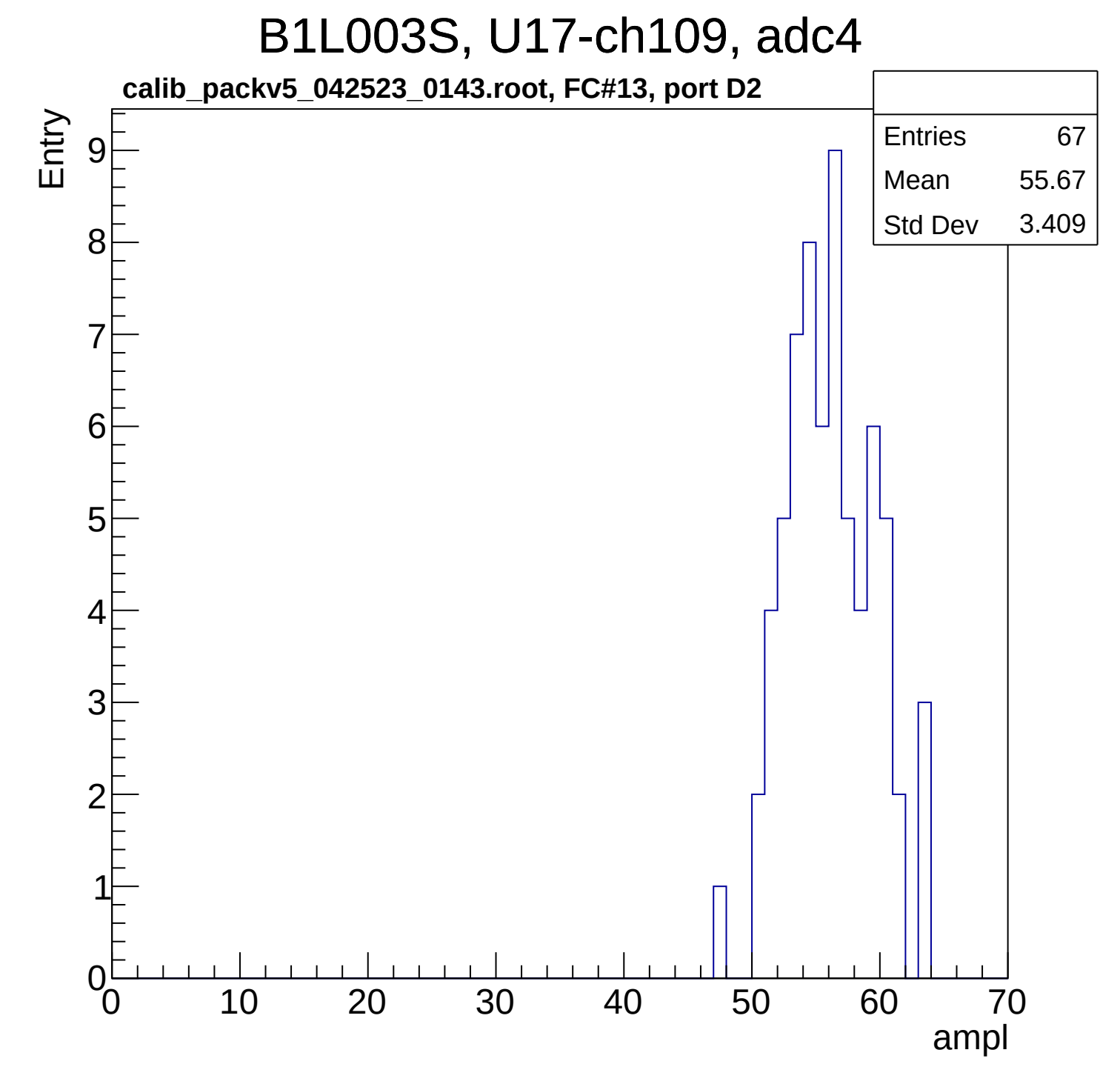
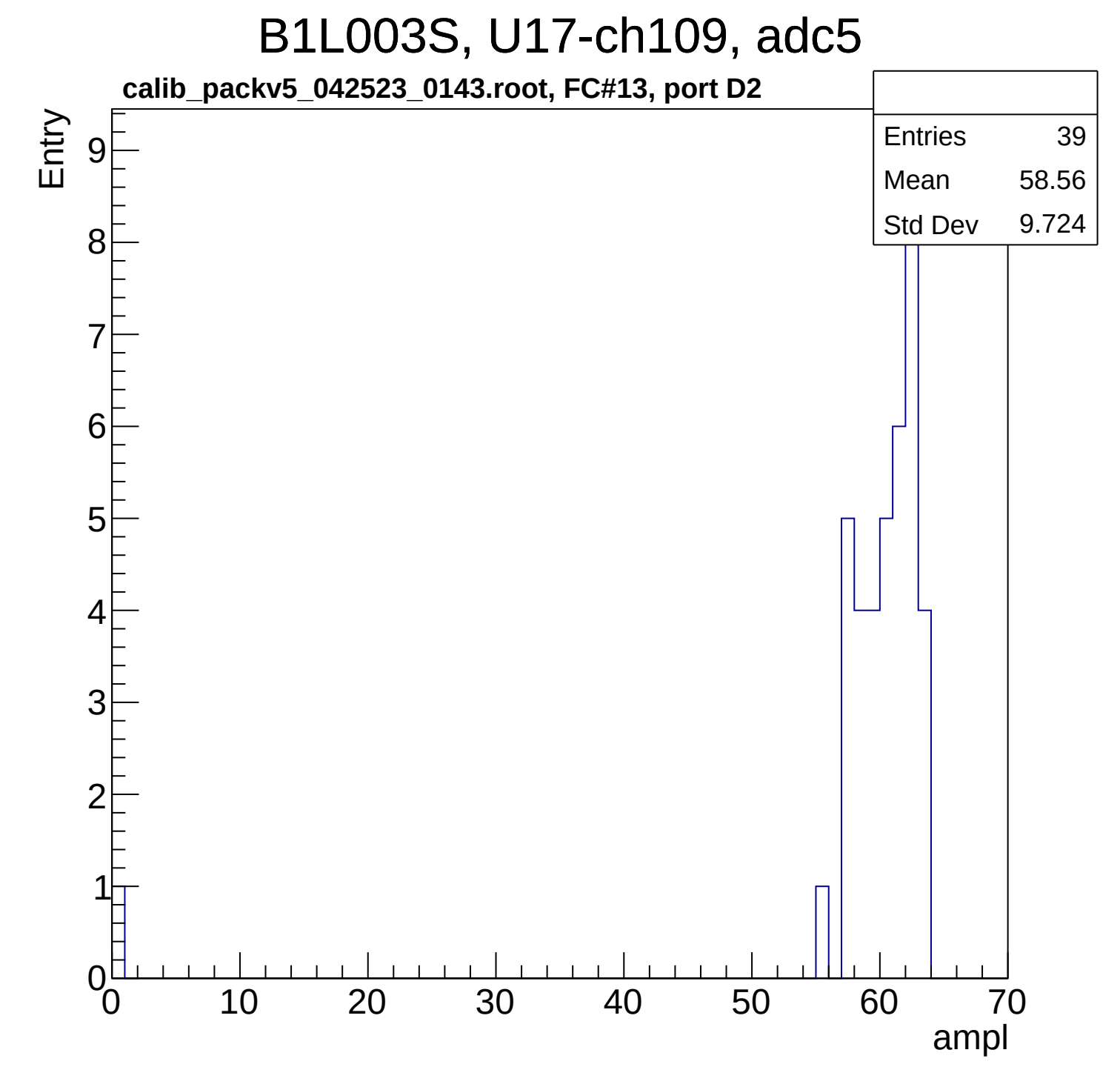

B1L003S, U17-ch109, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.66
Std Dev 3.401
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch109, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 55.67
Std Dev 3.409
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch109, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 58.56
Std Dev 9.724
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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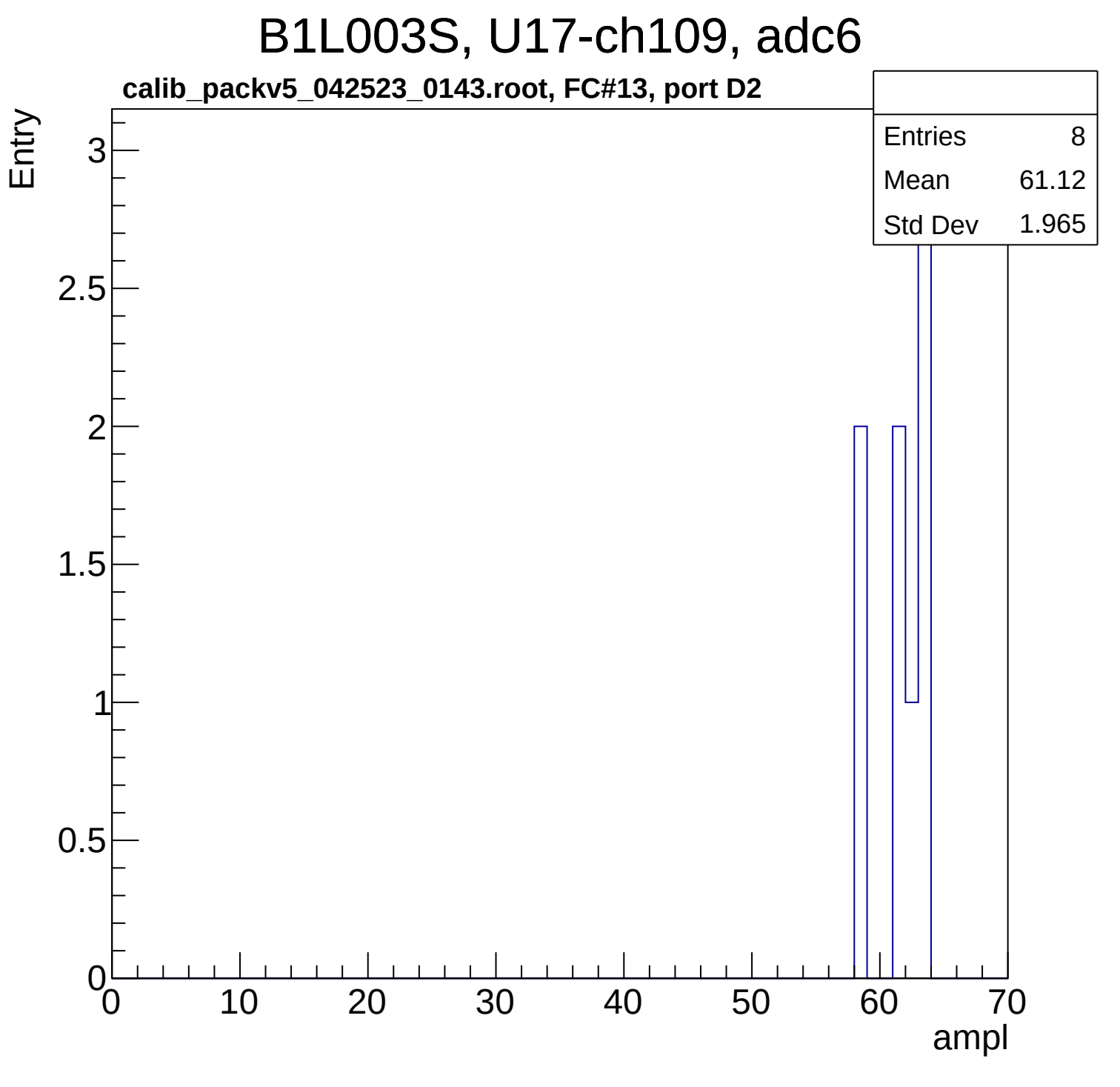
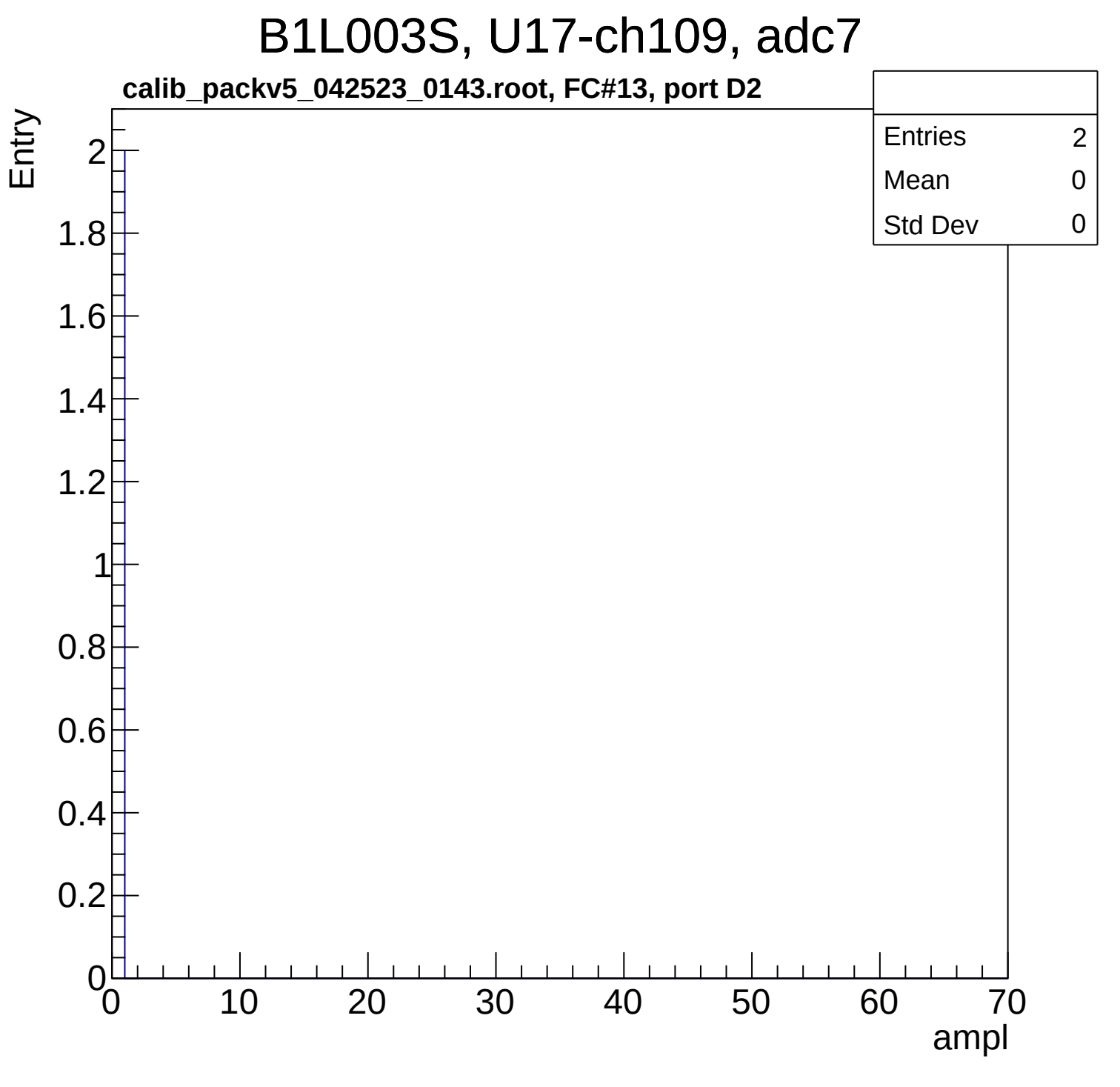

B1L003S, U17-ch109, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.12
Std Dev 1.965
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch109, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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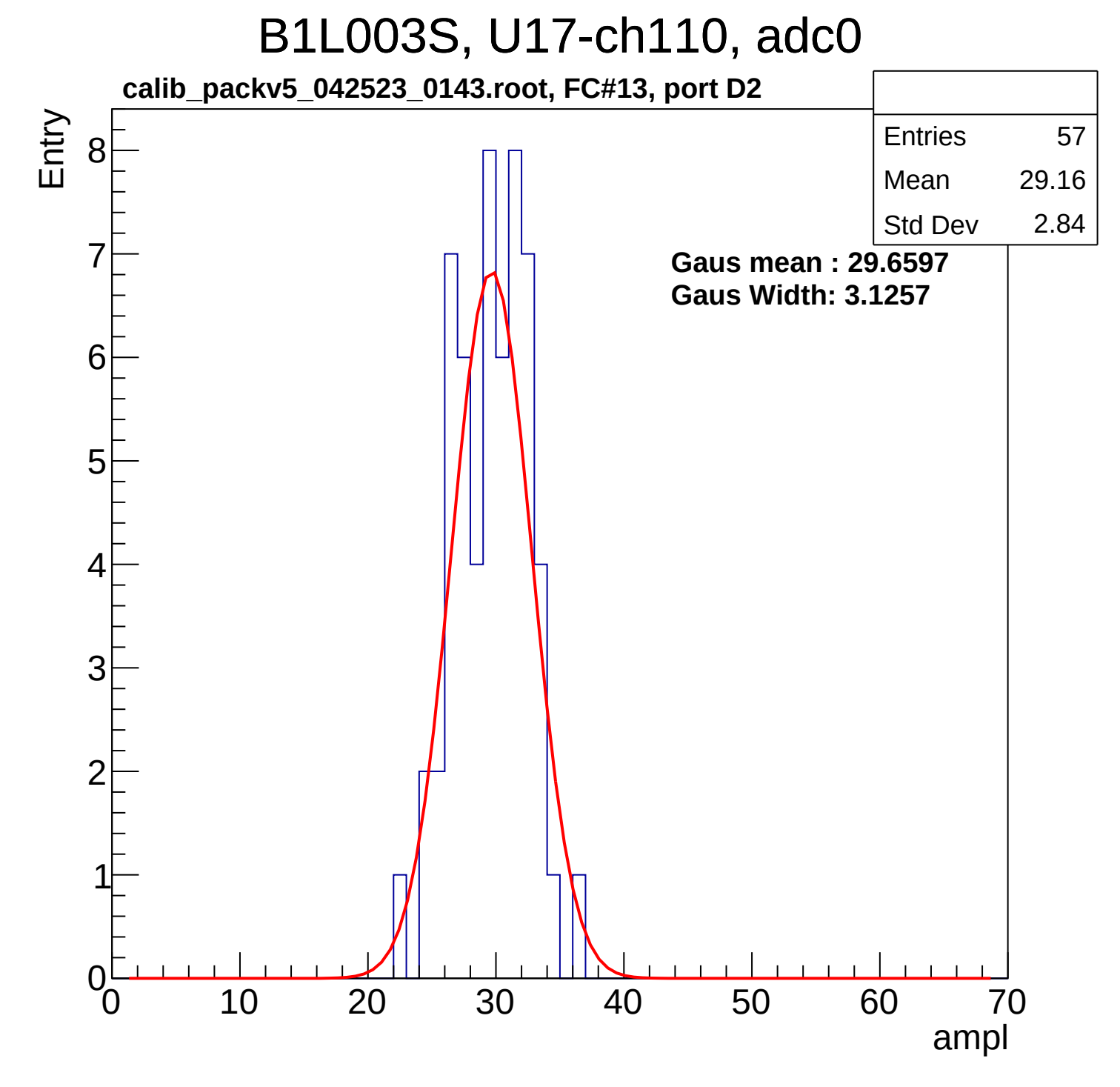
B1L003S, U17-ch110, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 29.16
Std Dev 2.84
Gaus mean : 29.6597
Gaus Width: 3.1257
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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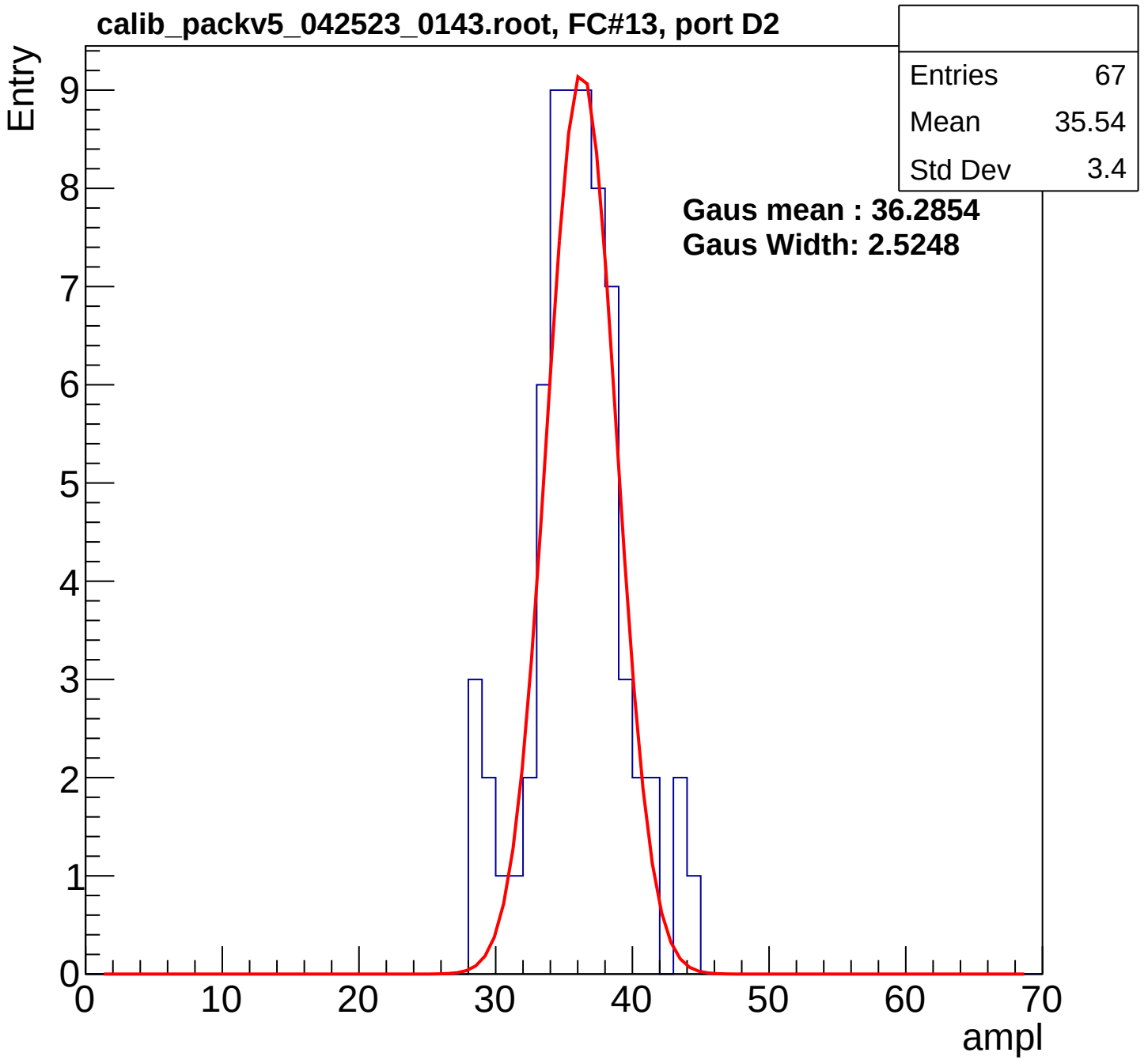
B1L003S, U17-ch110, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 35.54
Std Dev 3.4
Gaus mean : 36.2854
Gaus Width: 2.5248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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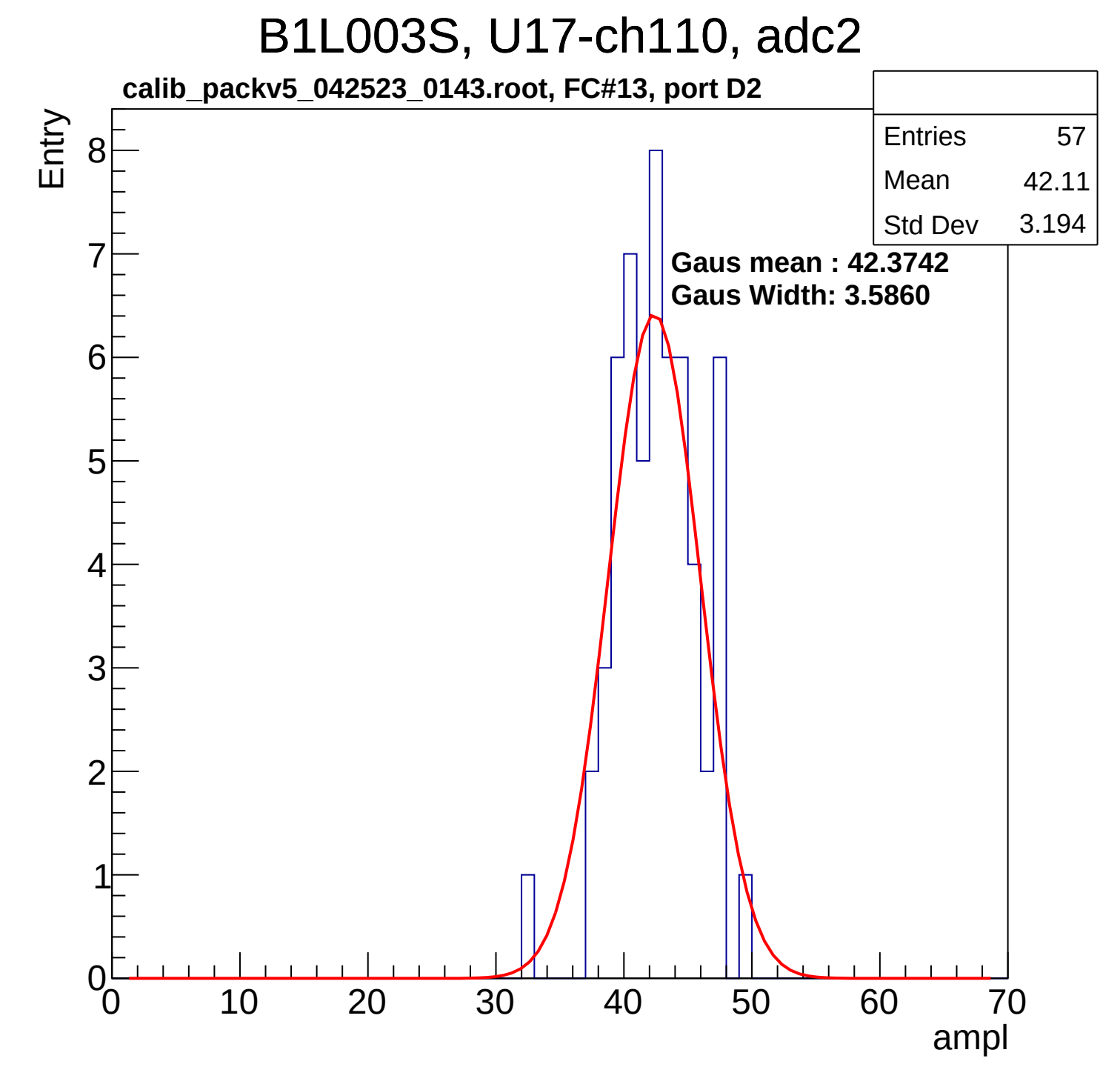
B1L003S, U17-ch110, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 42.11
Std Dev 3.194
Gaus mean : 42.3742
Gaus Width: 3.5860
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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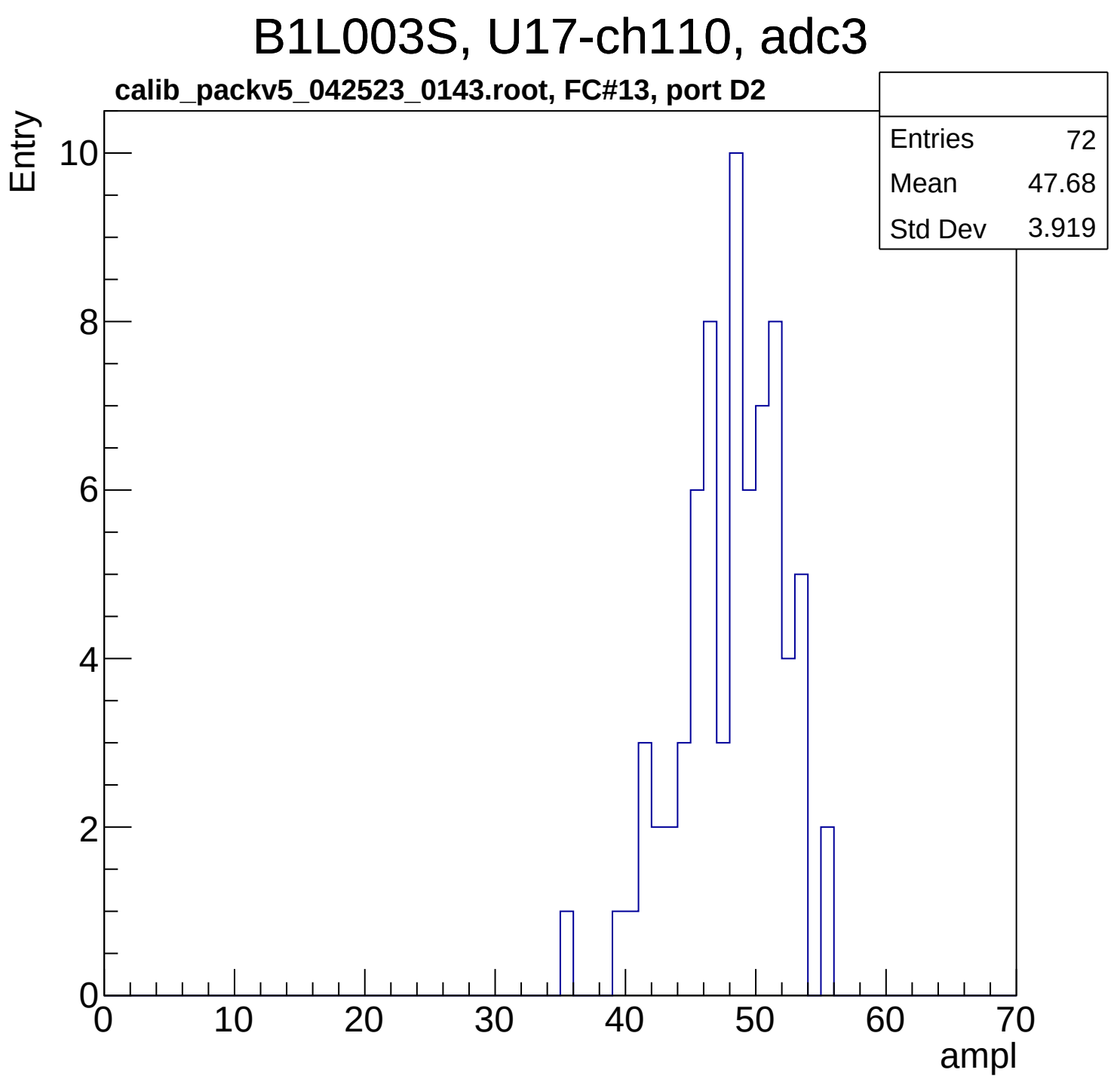
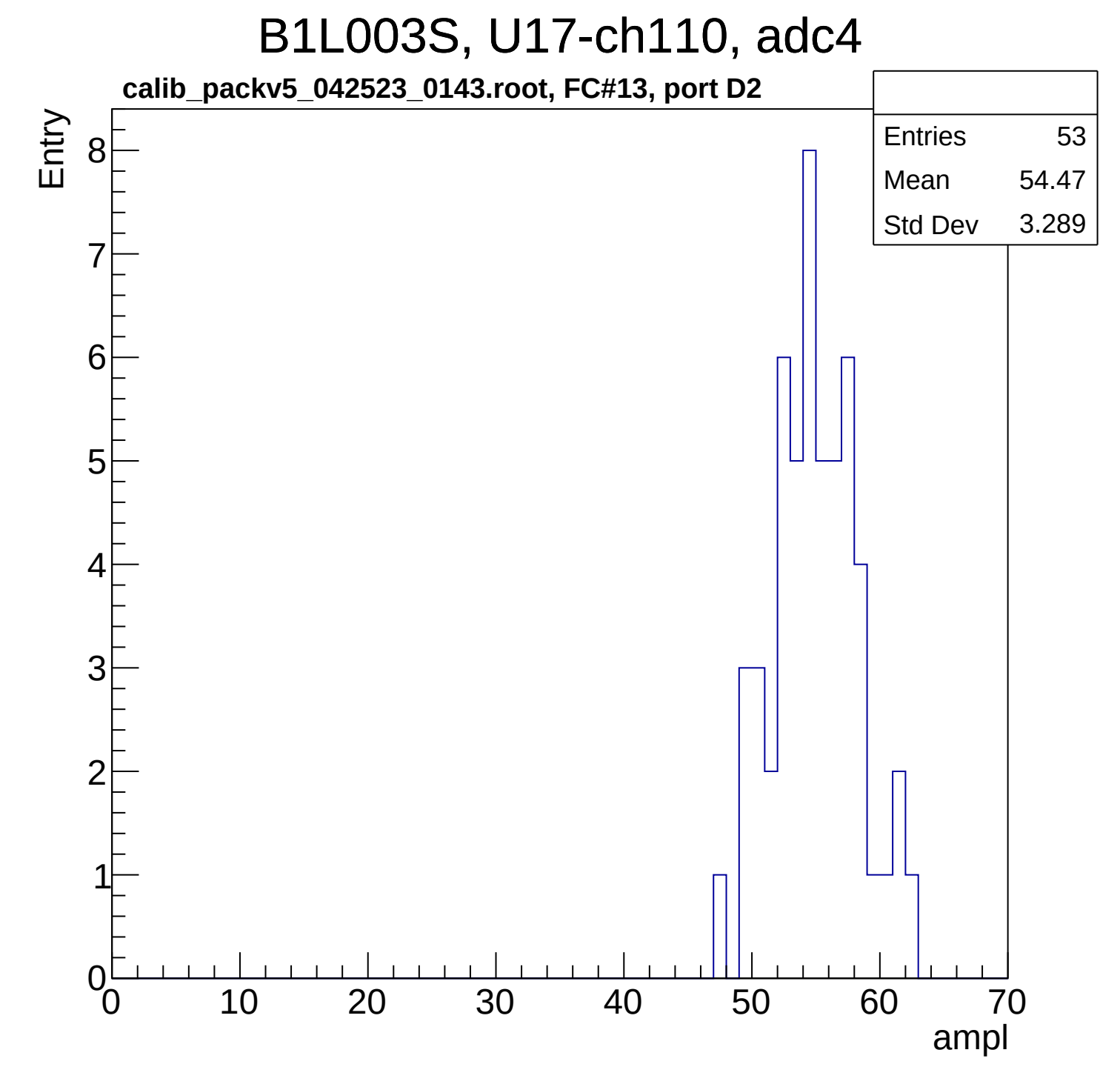

B1L003S, U17-ch110, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 47.68
Std Dev 3.919
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch110, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.47
Std Dev 3.289
Entry
ampl
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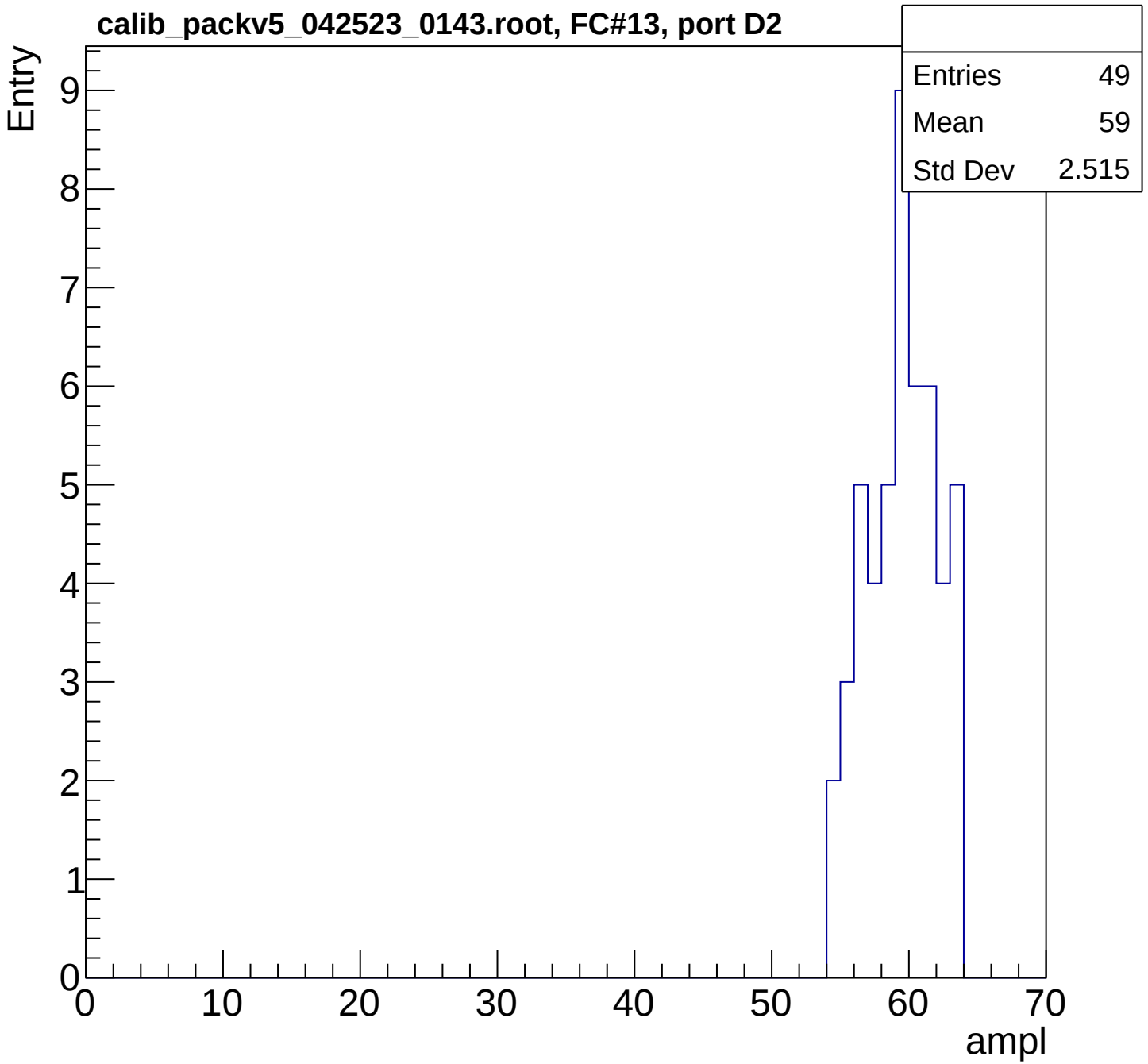
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch110, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 59
Std Dev 2.515
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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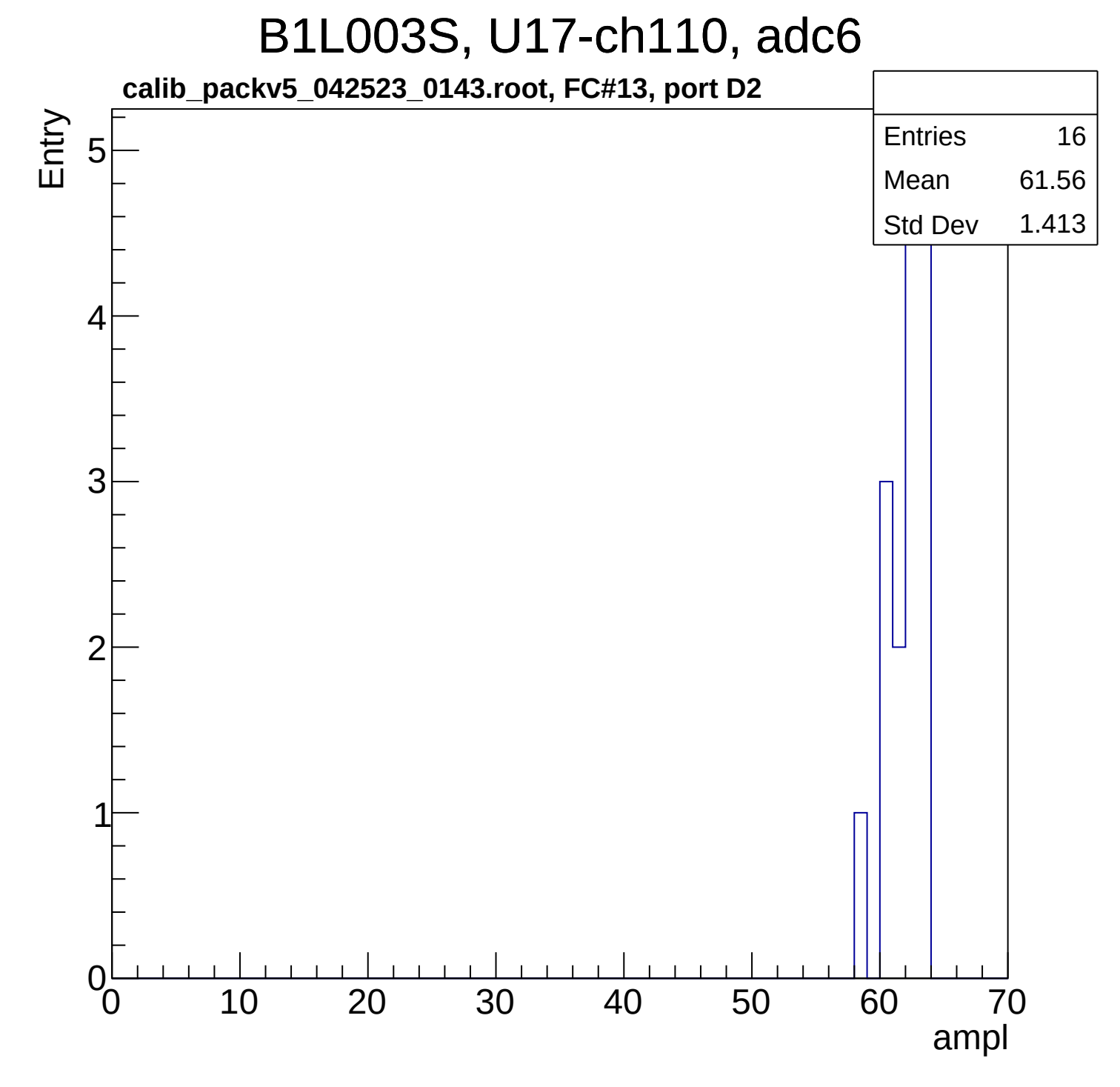
B1L003S, U17-ch110, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 61.56
Std Dev 1.413
Entry
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2
3
4
5
0
10
20
30
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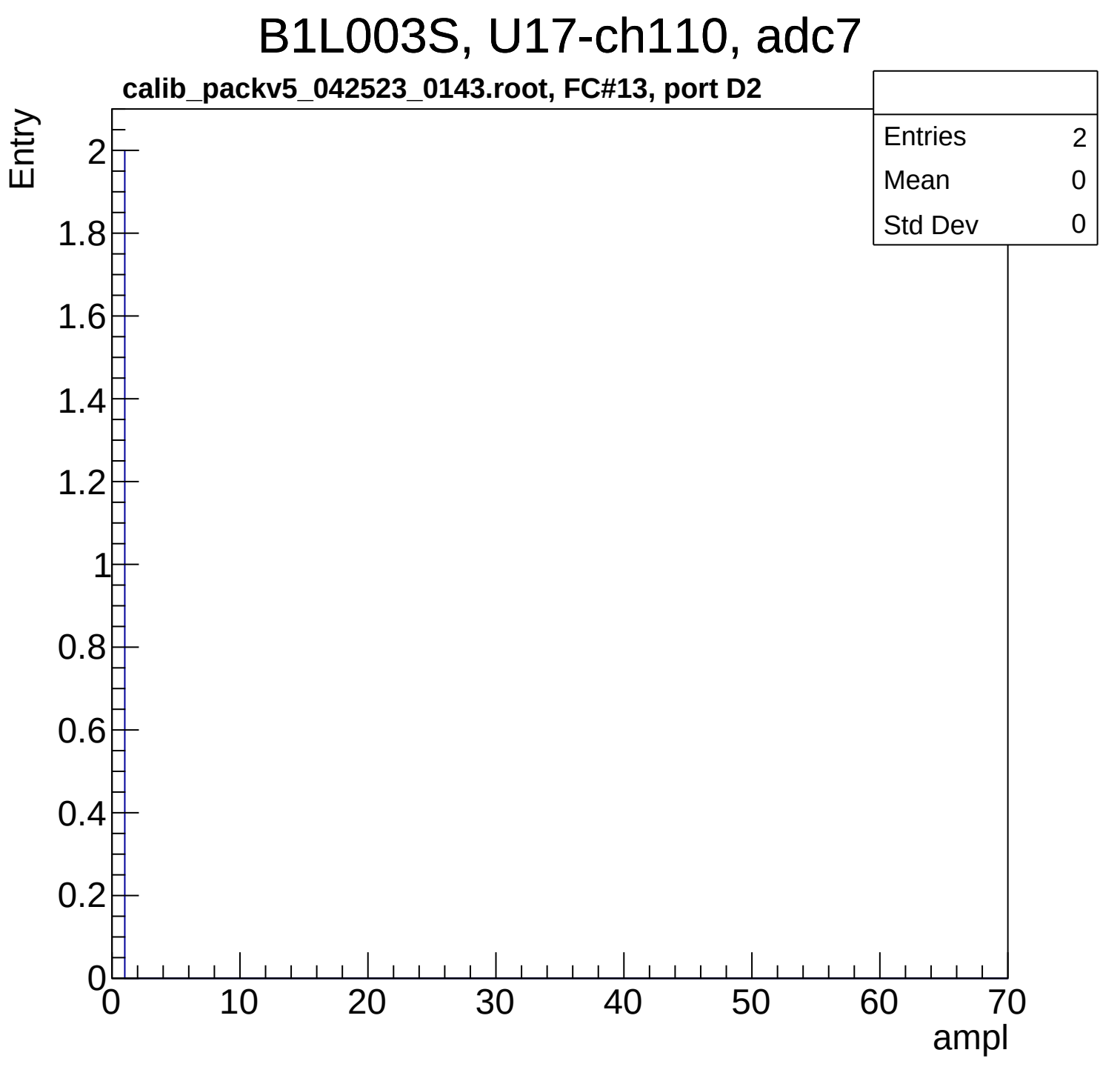
50
60
70
ampl


B1L003S, U17-ch110, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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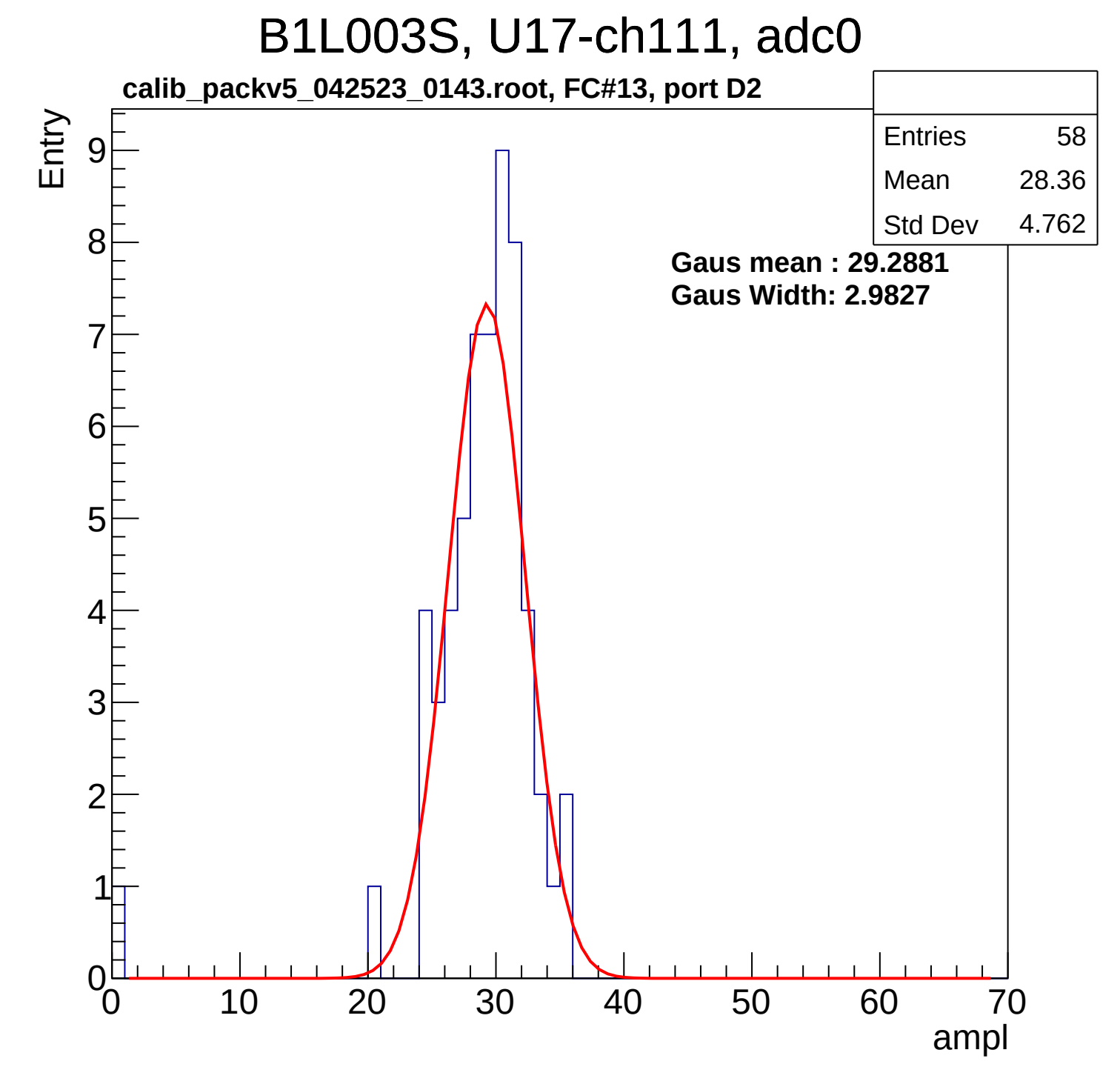
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch111, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.36
Std Dev 4.762
Gaus mean : 29.2881
Gaus Width: 2.9827
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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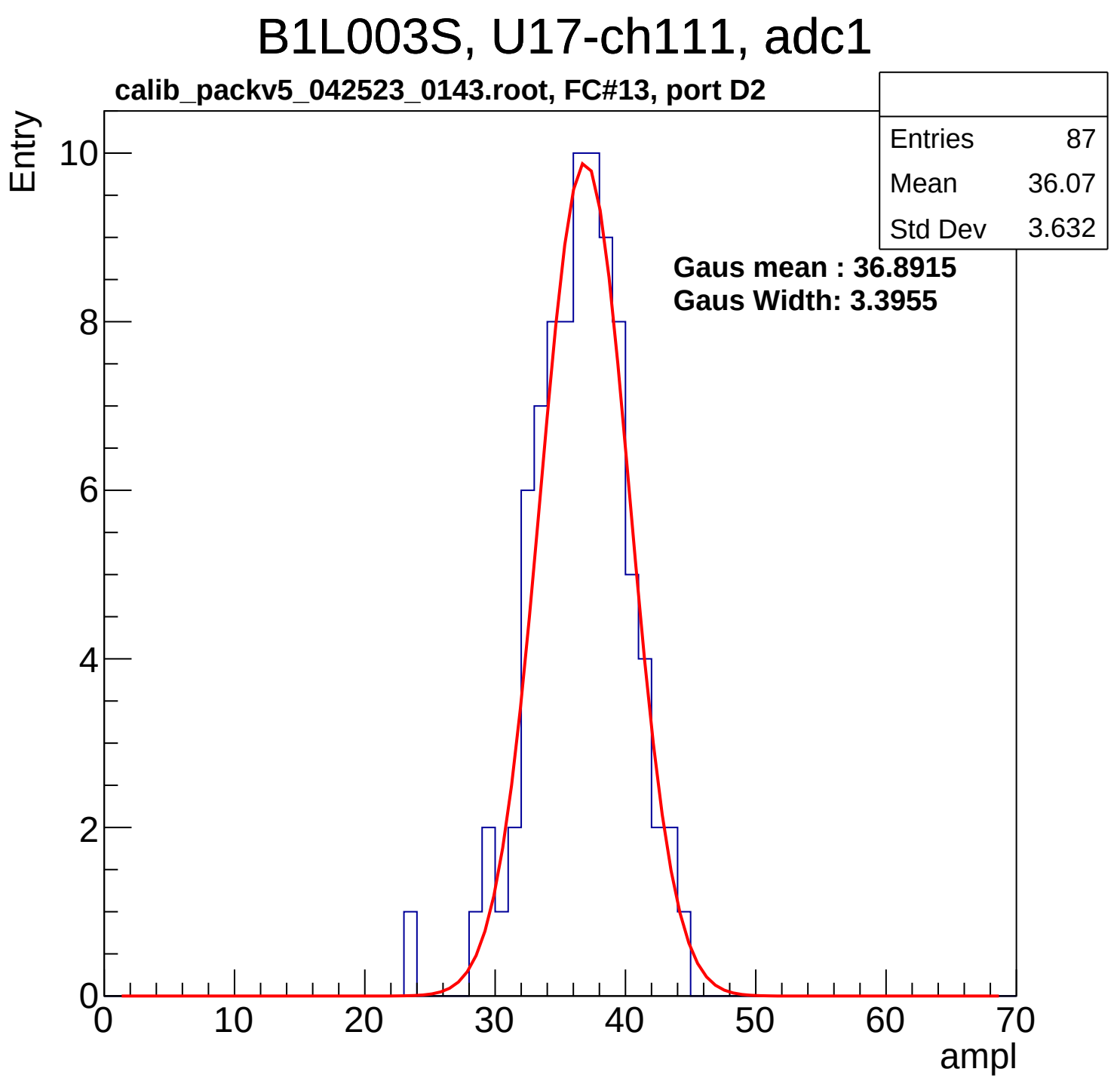
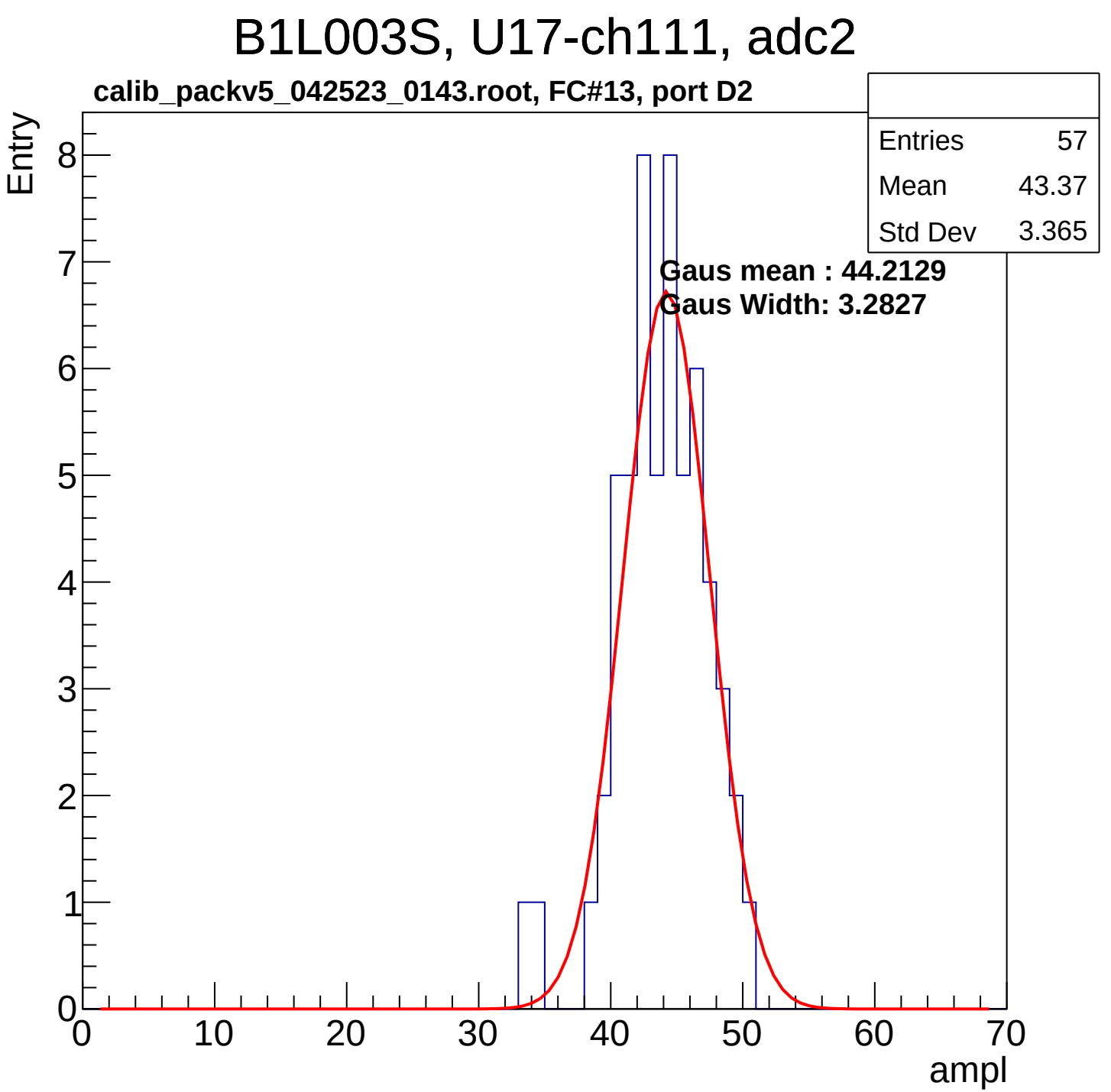

B1L003S, U17-ch111, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 87
Mean 36.07
Std Dev 3.632
Gaus mean : 36.8915
Gaus Width: 3.3955
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch111, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 43.37
Std Dev 3.365
Gaus mean : 44.2129
Gaus Width: 3.2827
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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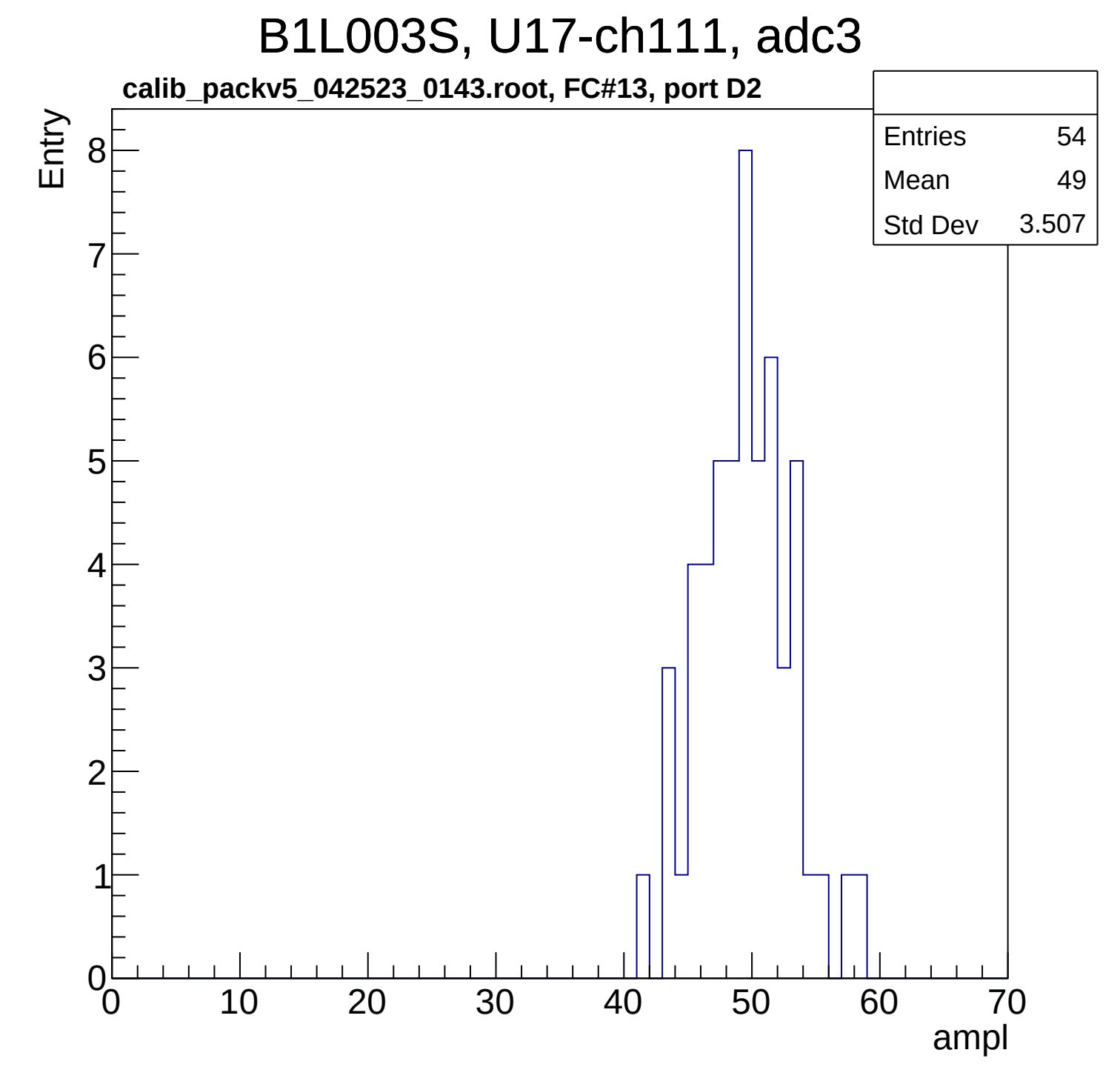
40
50
60
70


B1L003S, U17-ch111, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49
Std Dev 3.507
Entry
ampl
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4
5
6
7
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0
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60
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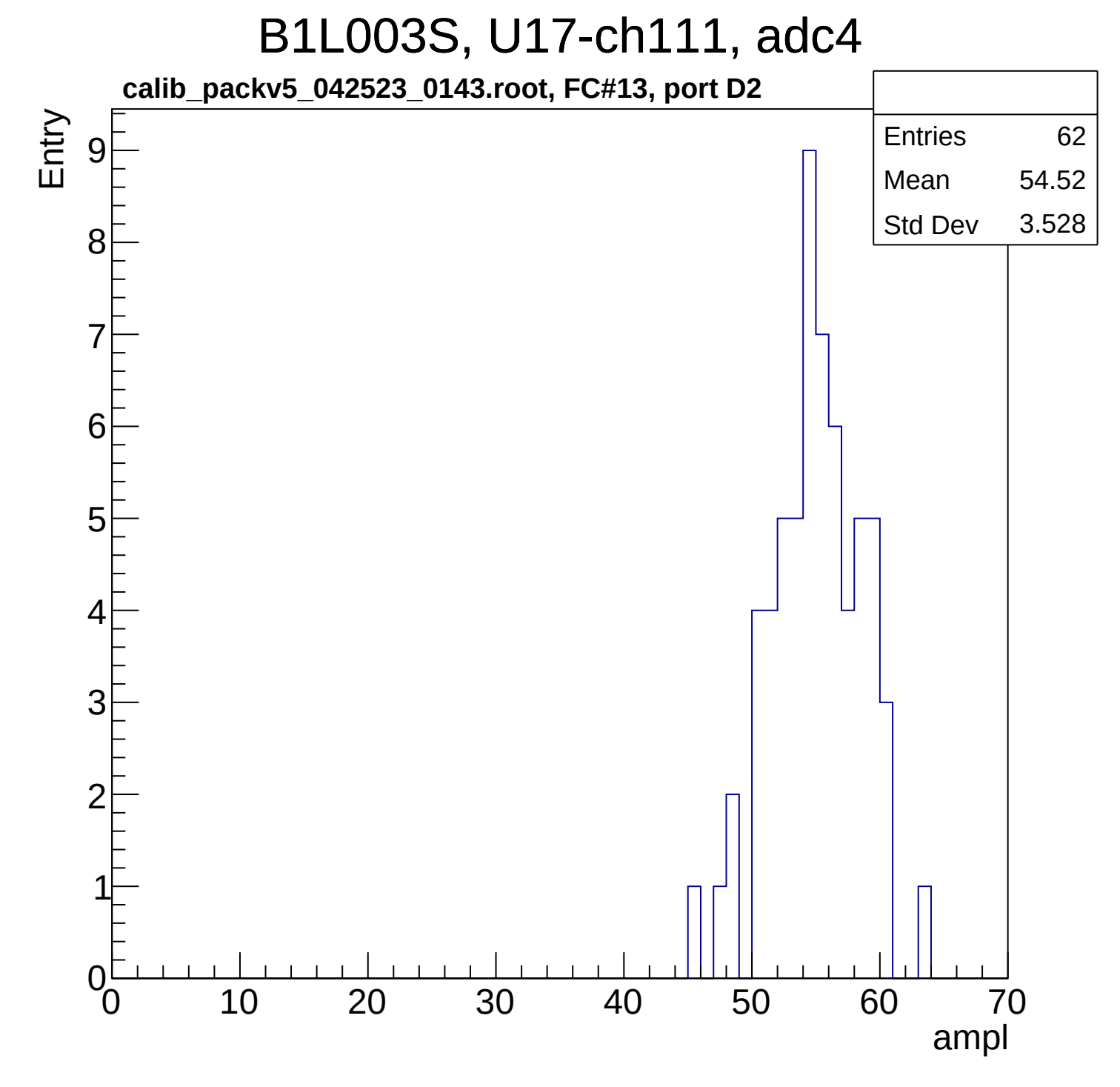
B1L003S, U17-ch111, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 54.52
Std Dev 3.528
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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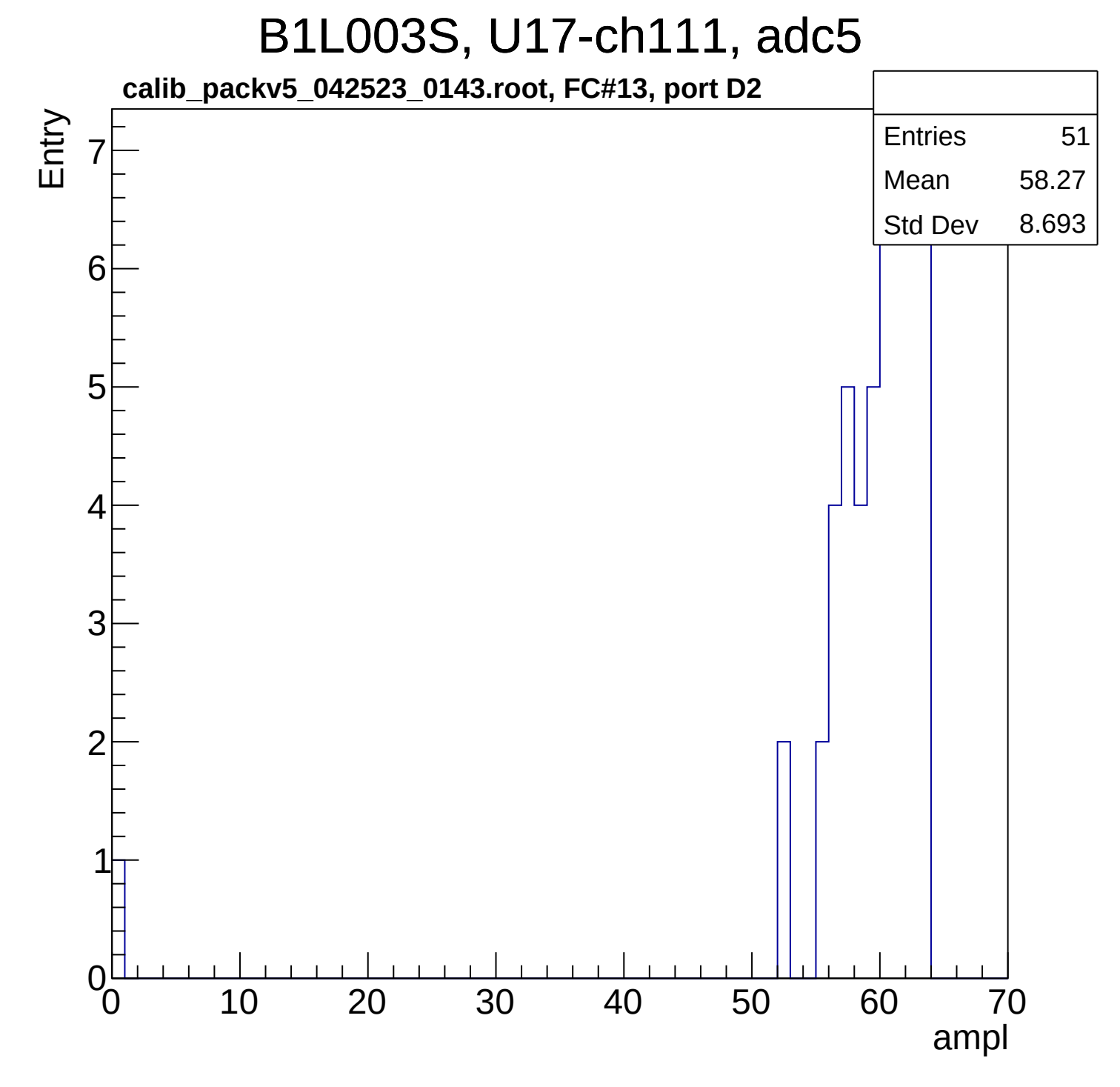
B1L003S, U17-ch111, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.27
Std Dev 8.693
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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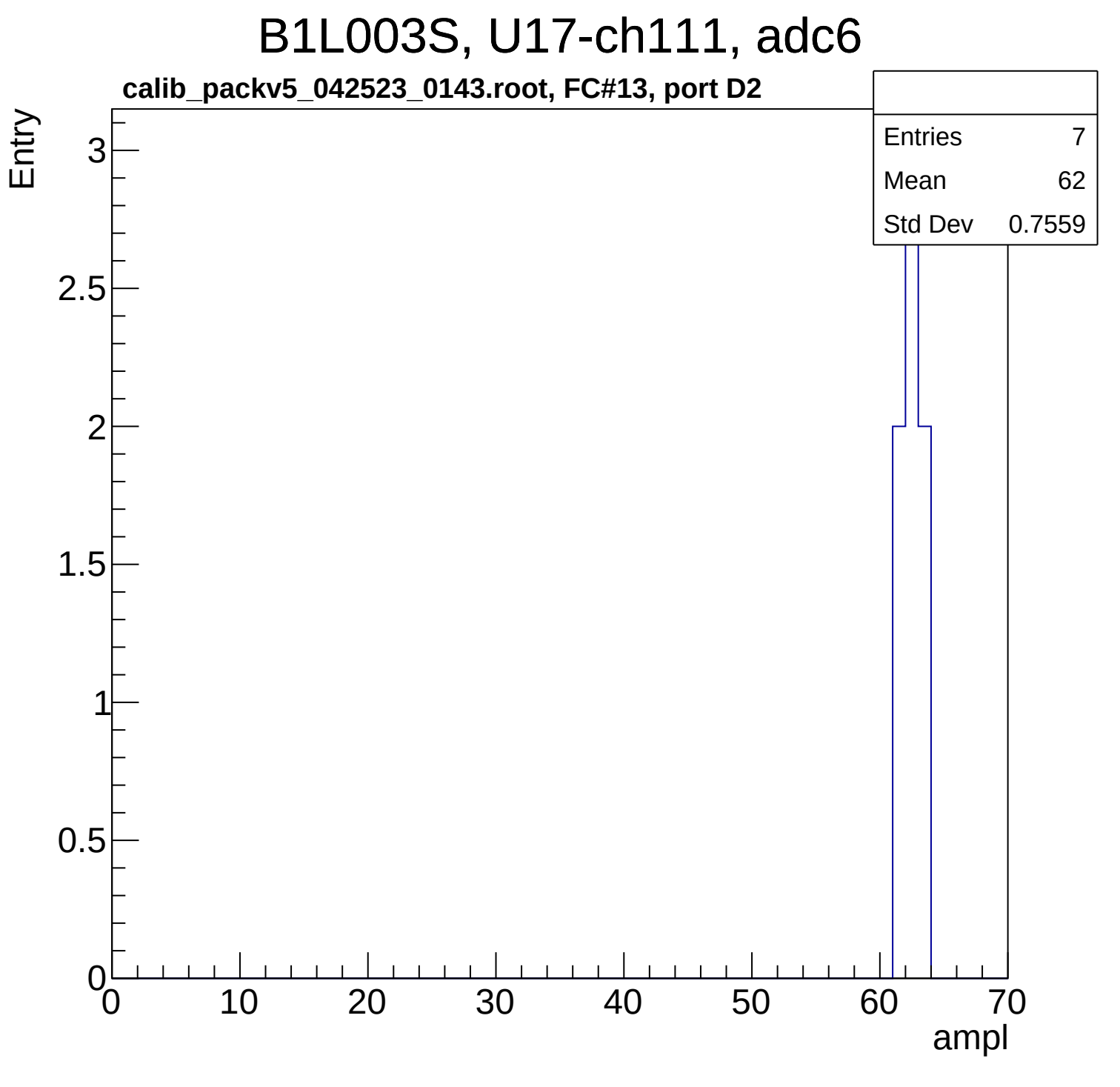
B1L003S, U17-ch111, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62
Std Dev 0.7559
Entry
ampl
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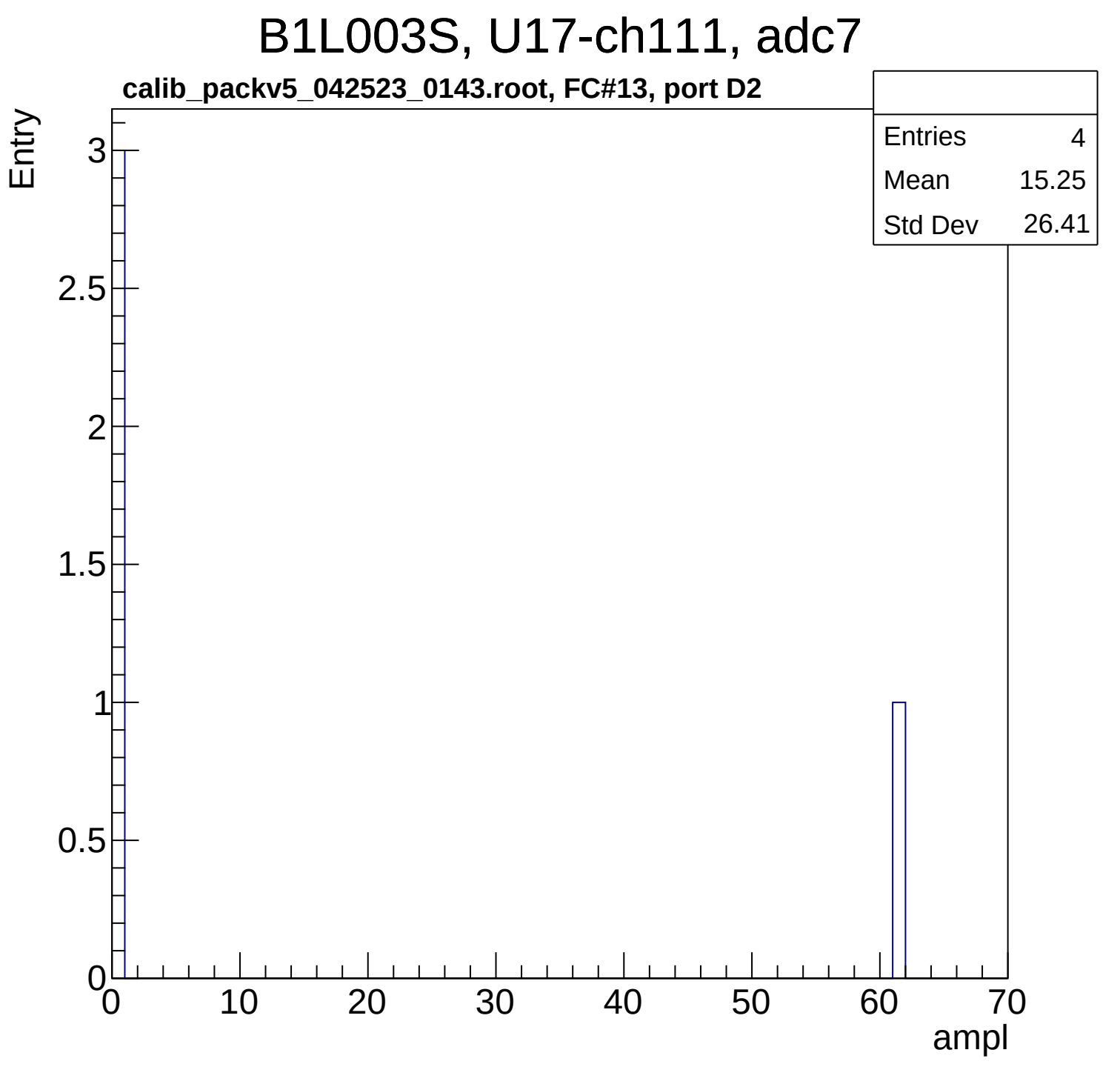
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch111, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 15.25
Std Dev 26.41
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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20
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40
50
60
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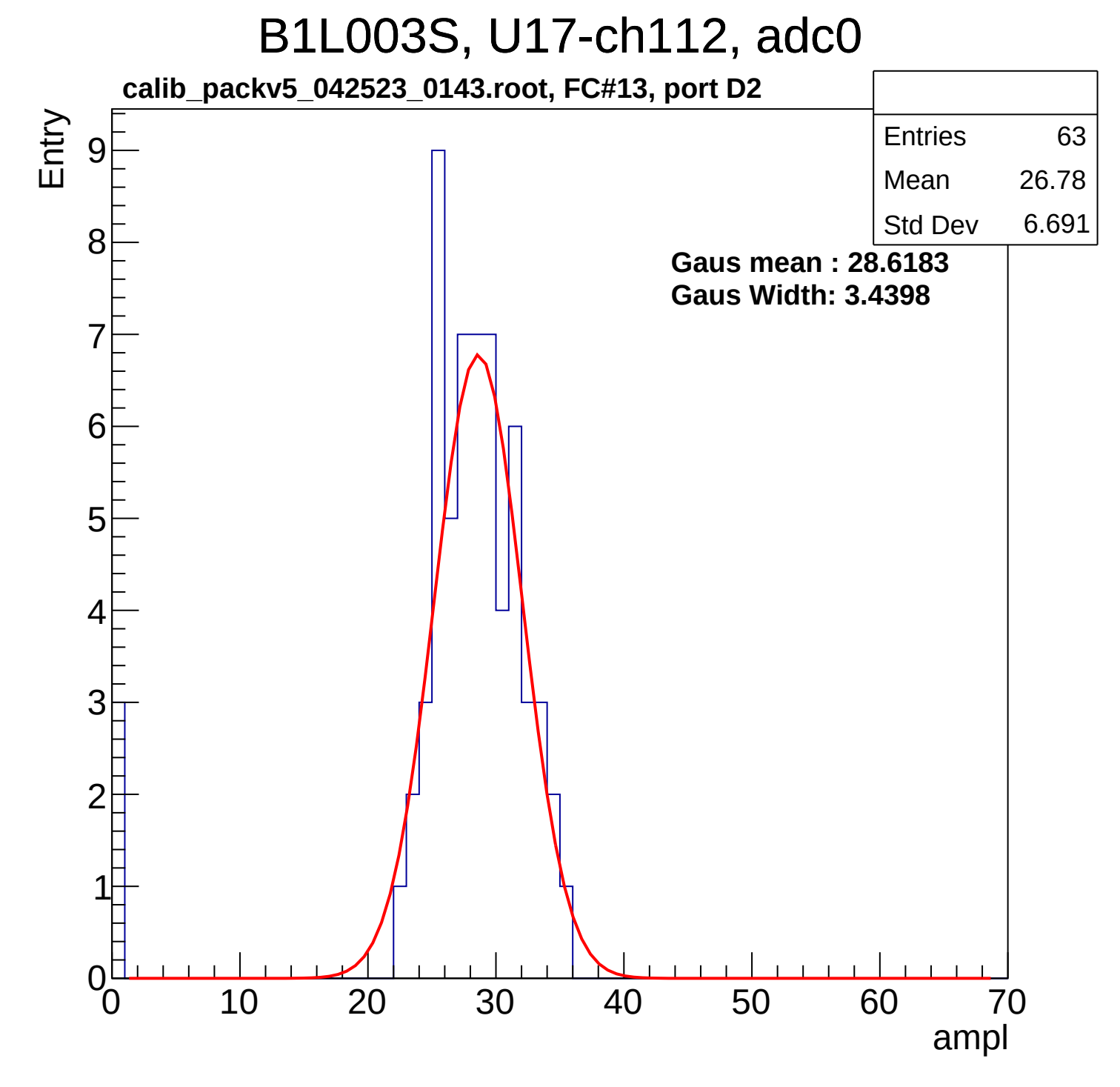
B1L003S, U17-ch112, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 26.78
Std Dev 6.691
Gaus mean : 28.6183
Gaus Width: 3.4398
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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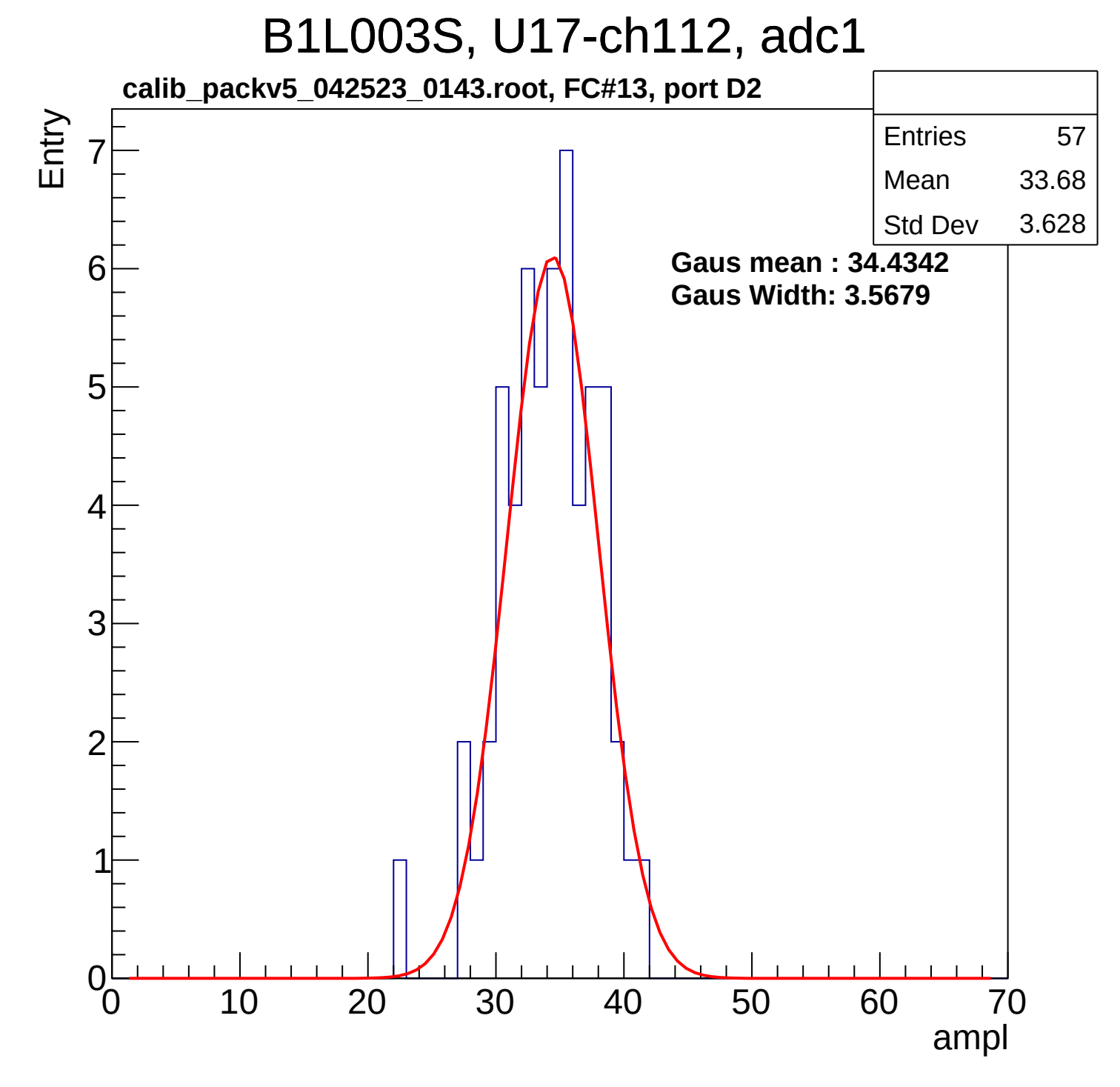
B1L003S, U17-ch112, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 33.68
Std Dev 3.628
Gaus mean : 34.4342
Gaus Width: 3.5679
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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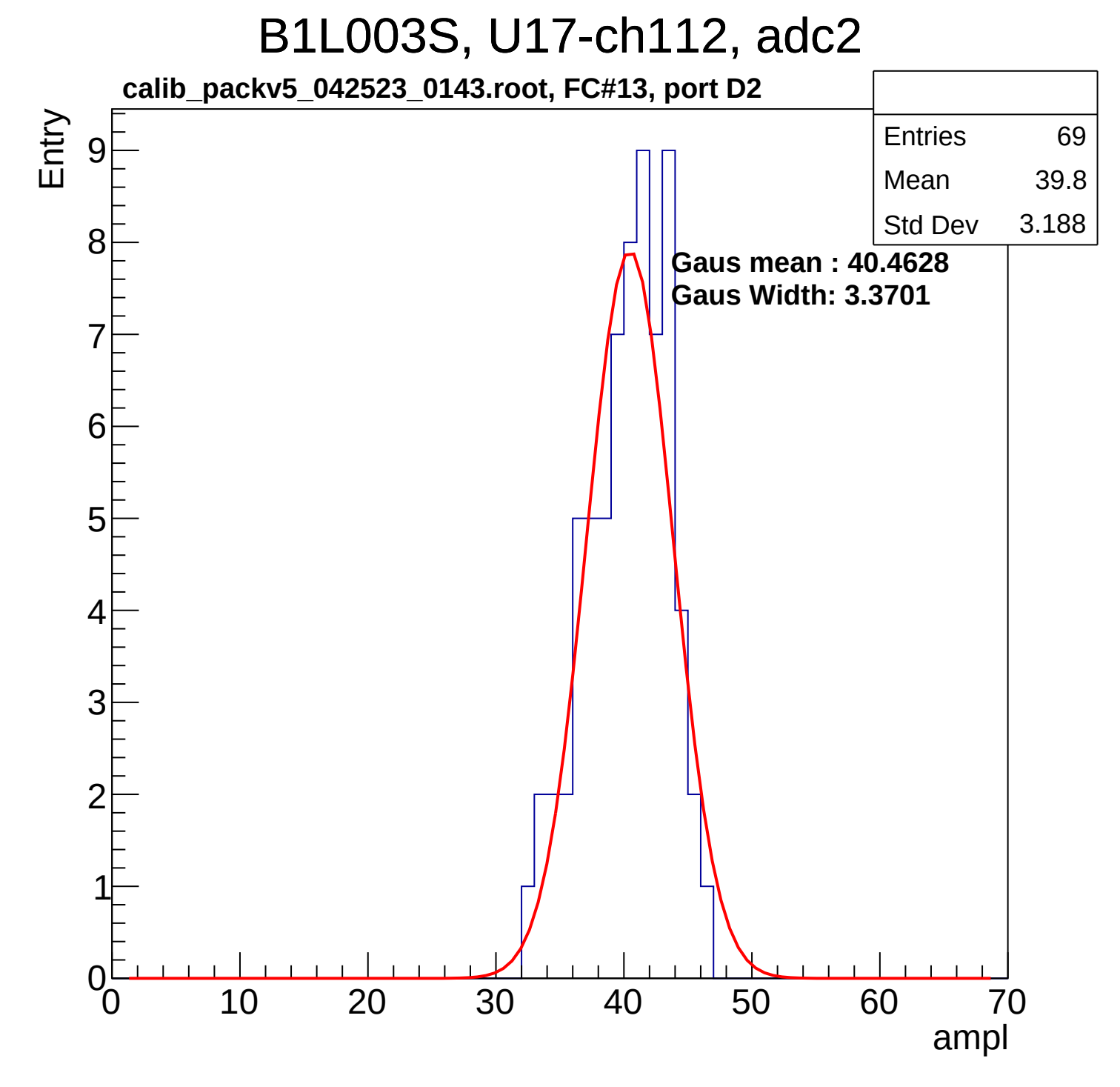
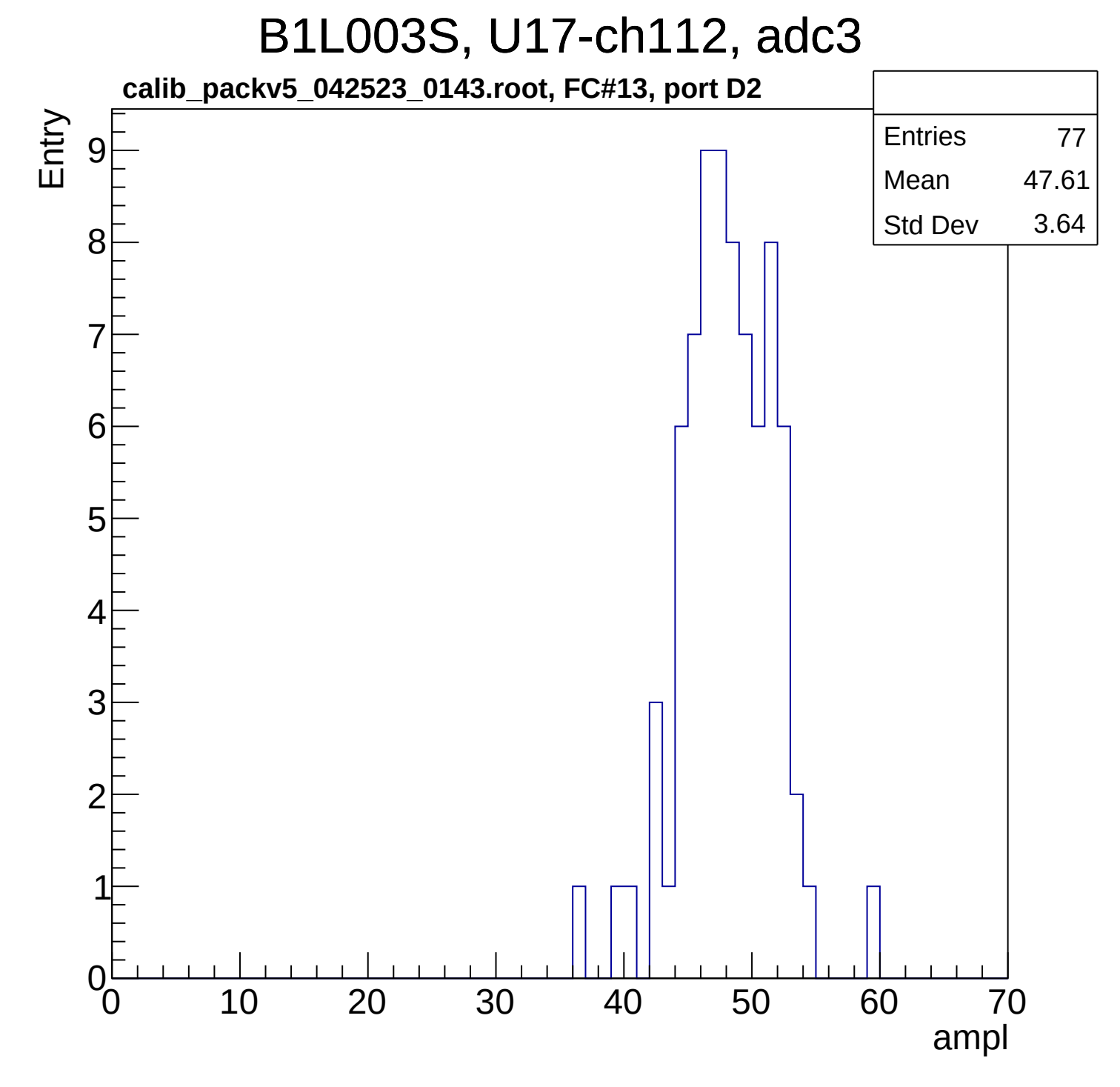

B1L003S, U17-ch112, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 39.8
Std Dev 3.188
Gaus mean : 40.4628
Gaus Width: 3.3701
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch112, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 47.61
Std Dev 3.64
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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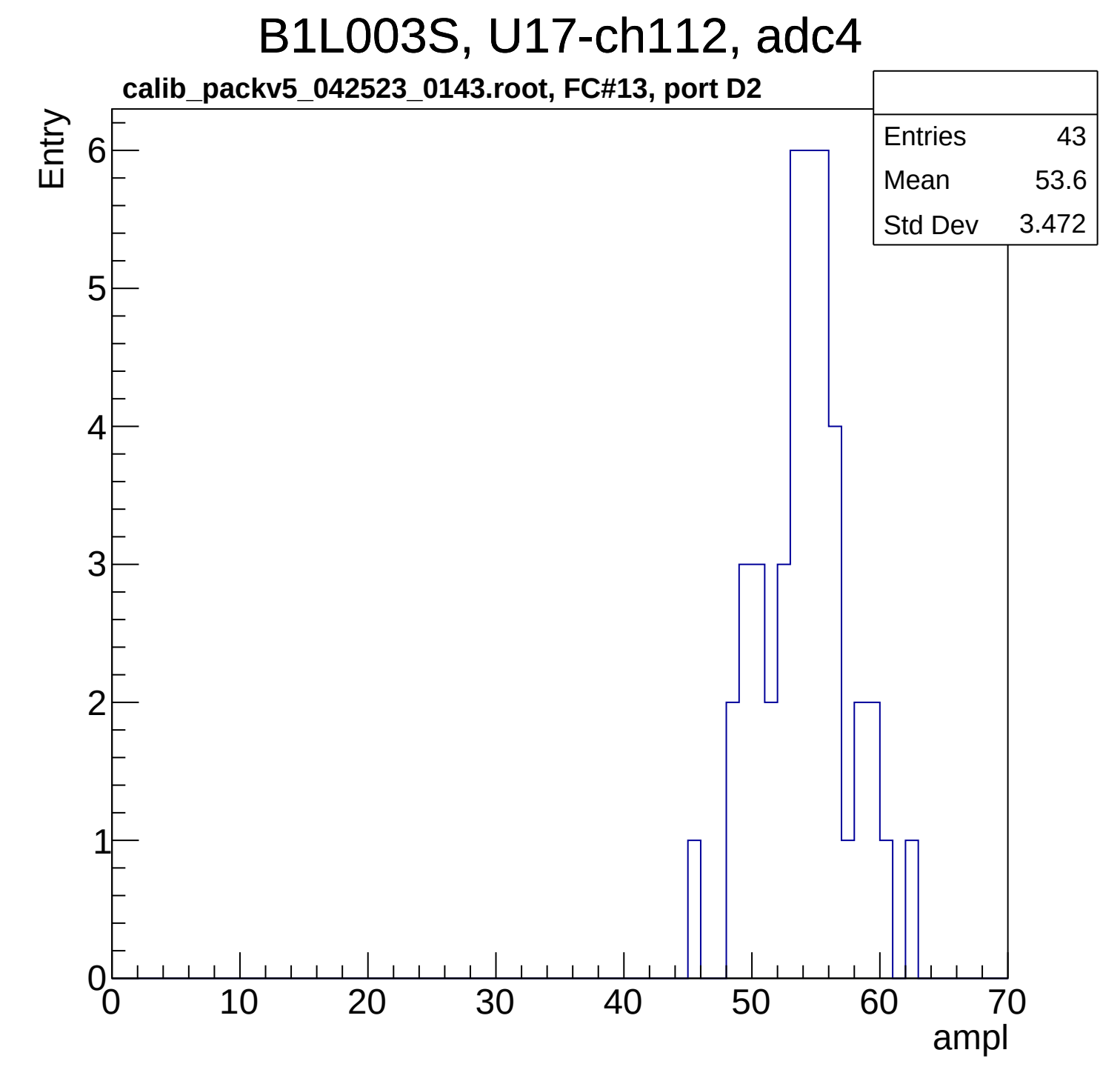
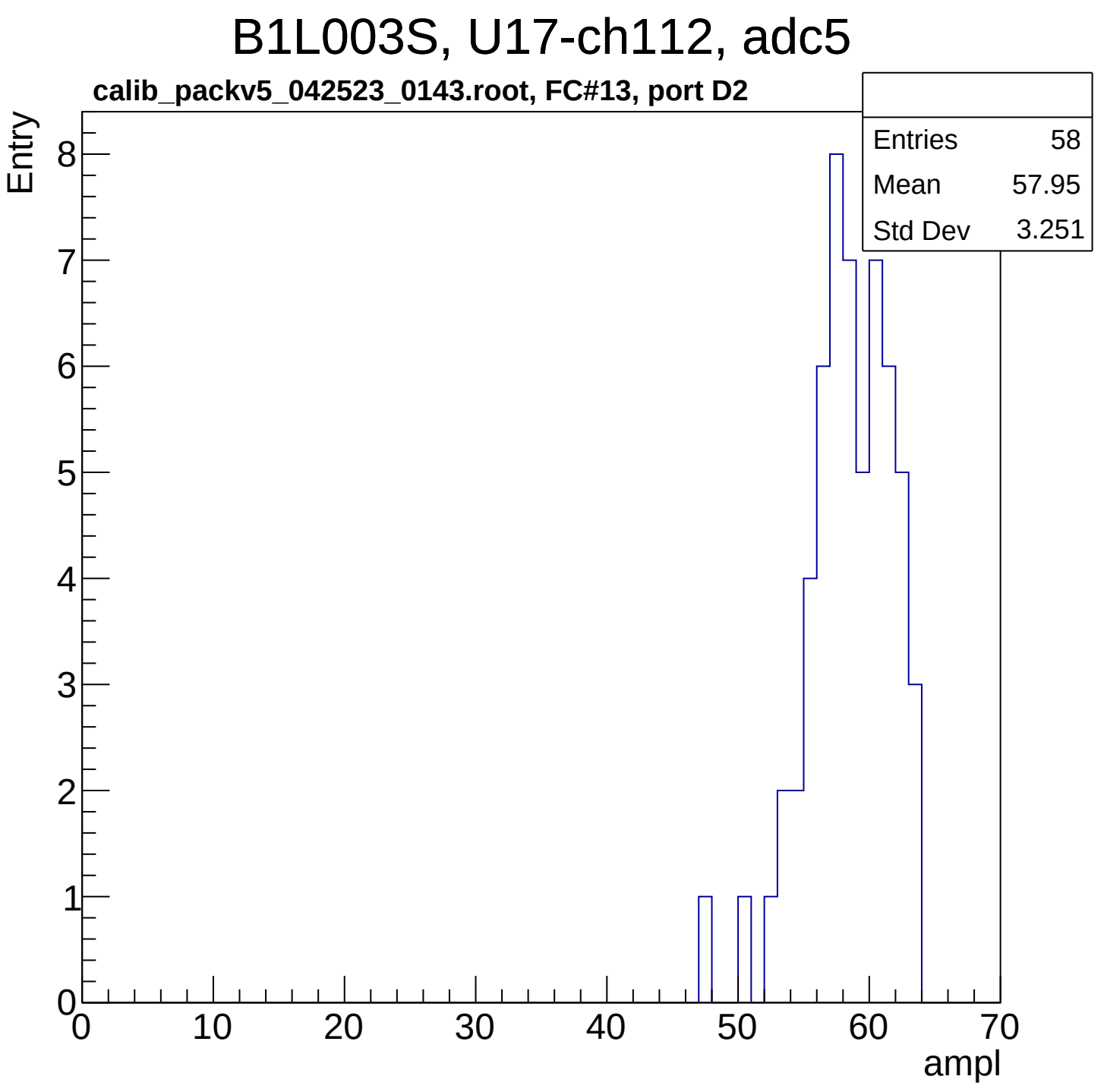

B1L003S, U17-ch112, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 53.6
Std Dev 3.472
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U17-ch112, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 57.95
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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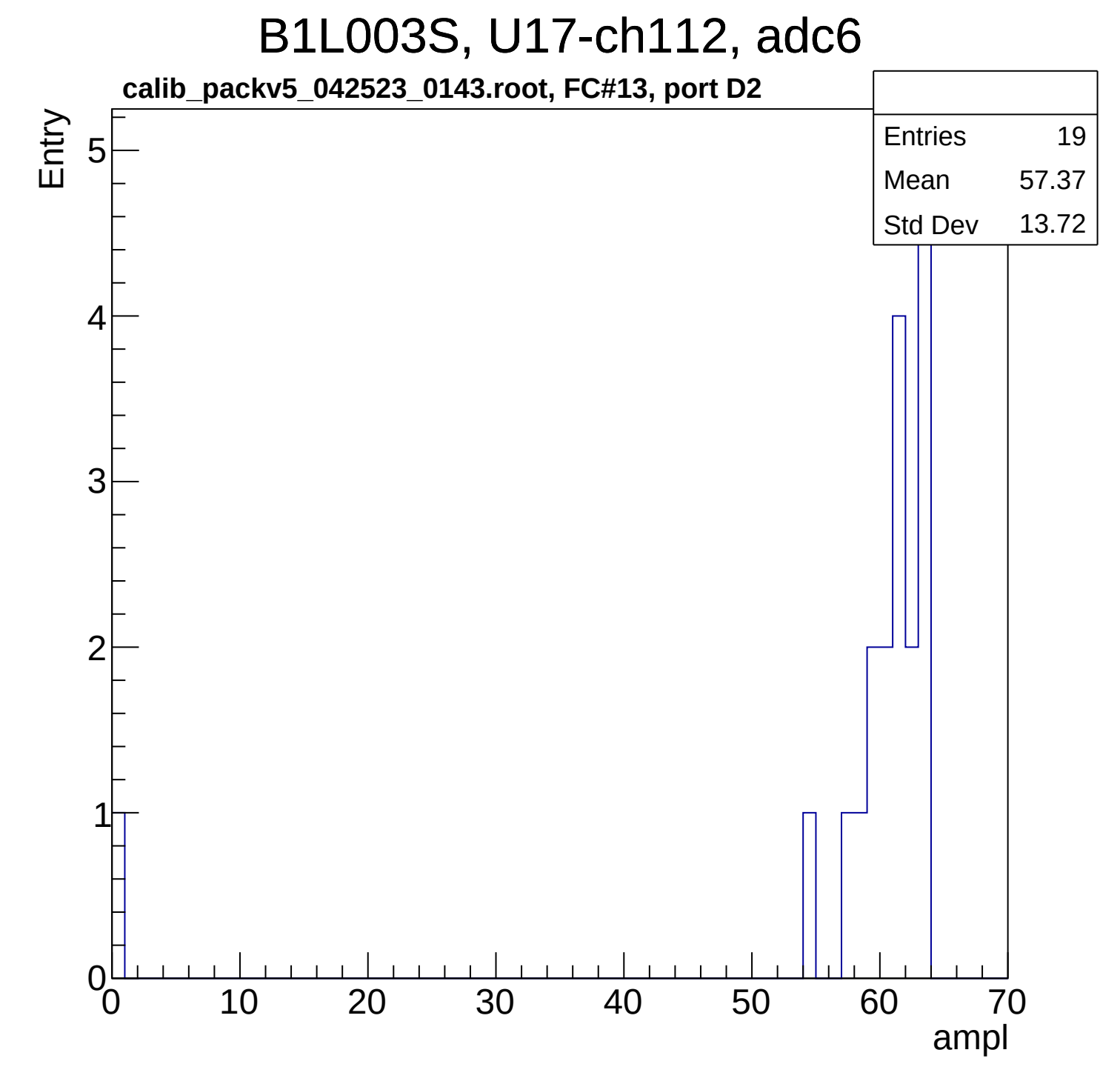
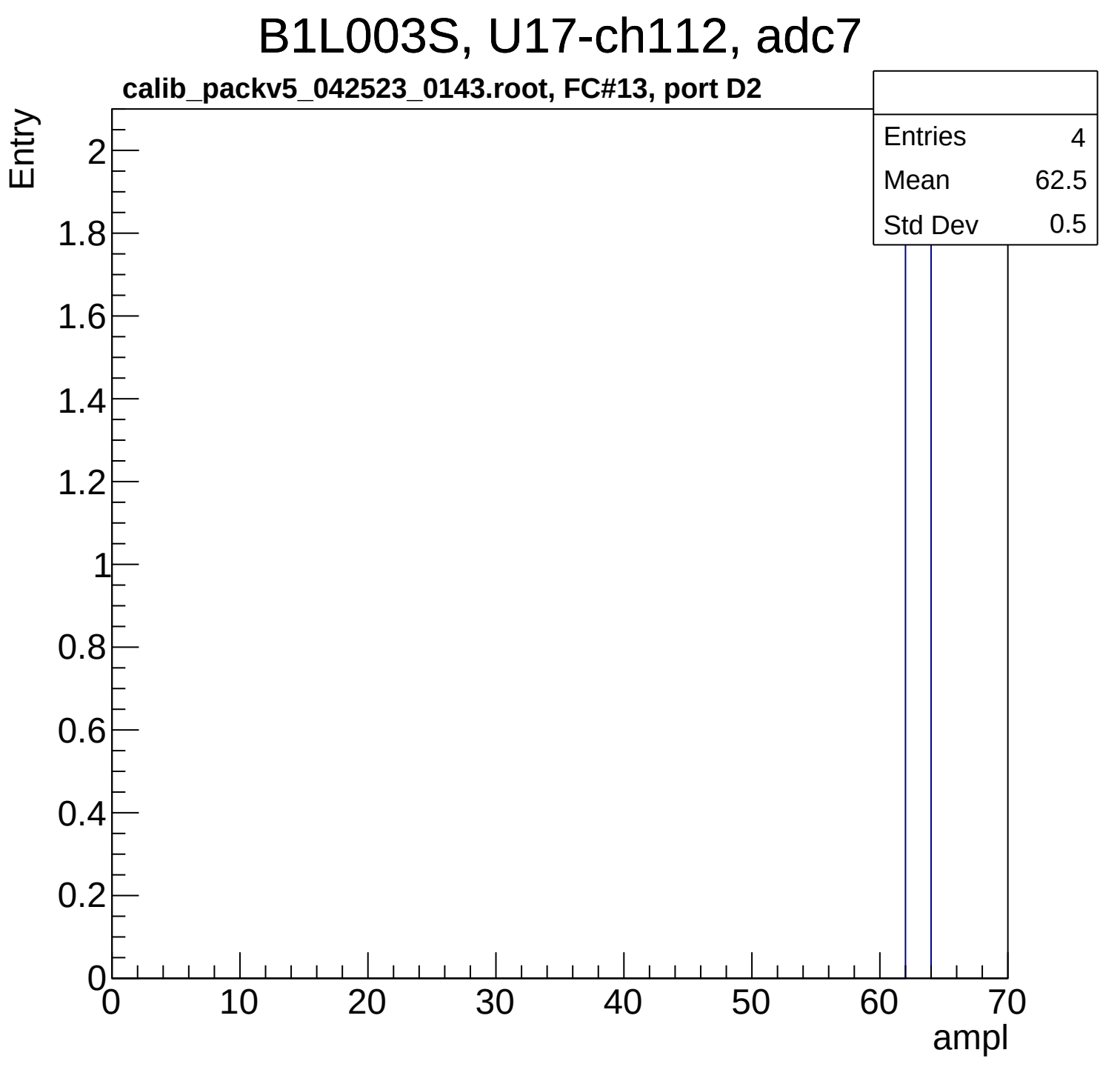

B1L003S, U17-ch112, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 57.37
Std Dev 13.72
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U17-ch112, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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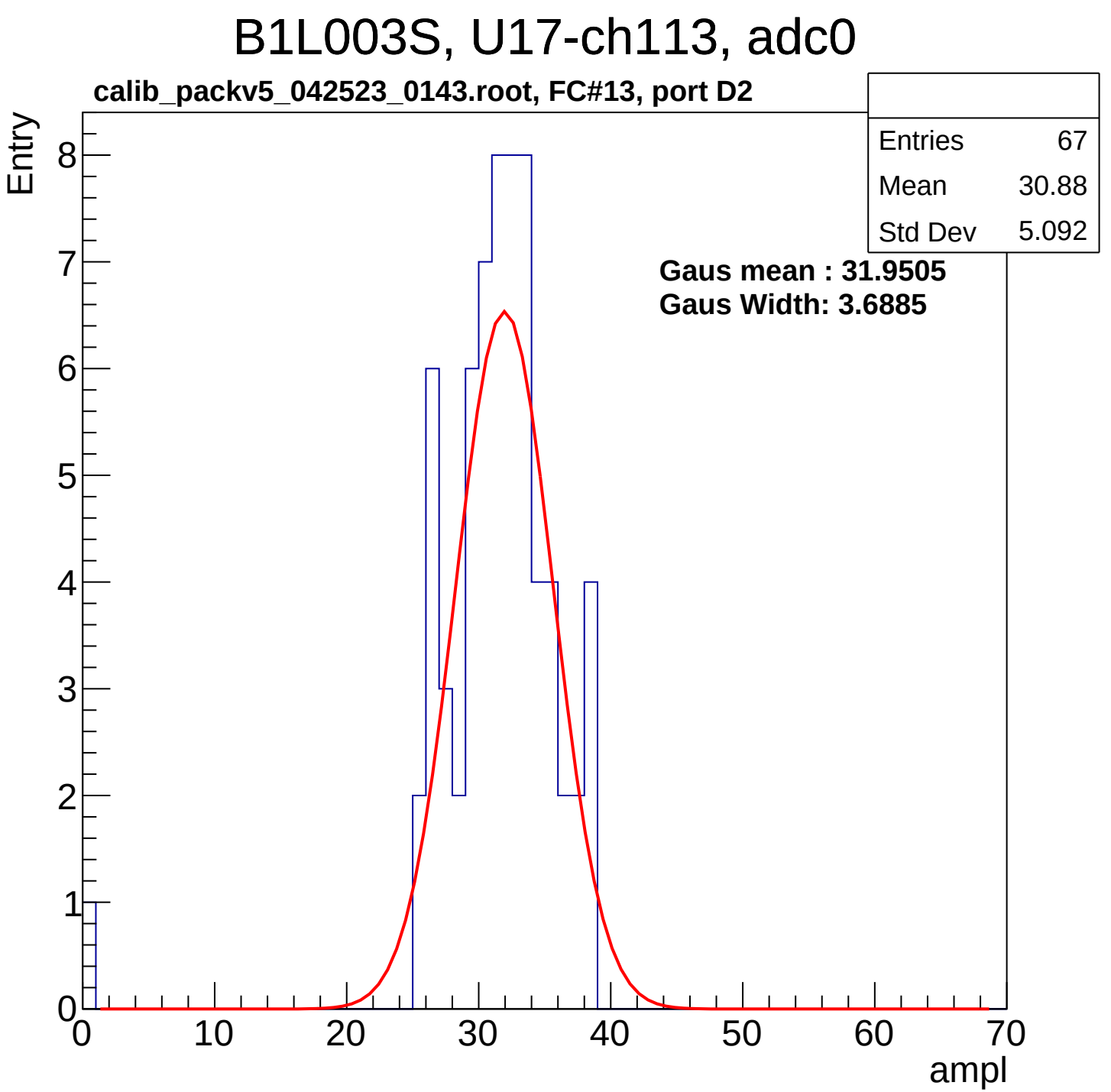
B1L003S, U17-ch113, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 30.88
Std Dev 5.092
Gaus mean : 31.9505
Gaus Width: 3.6885
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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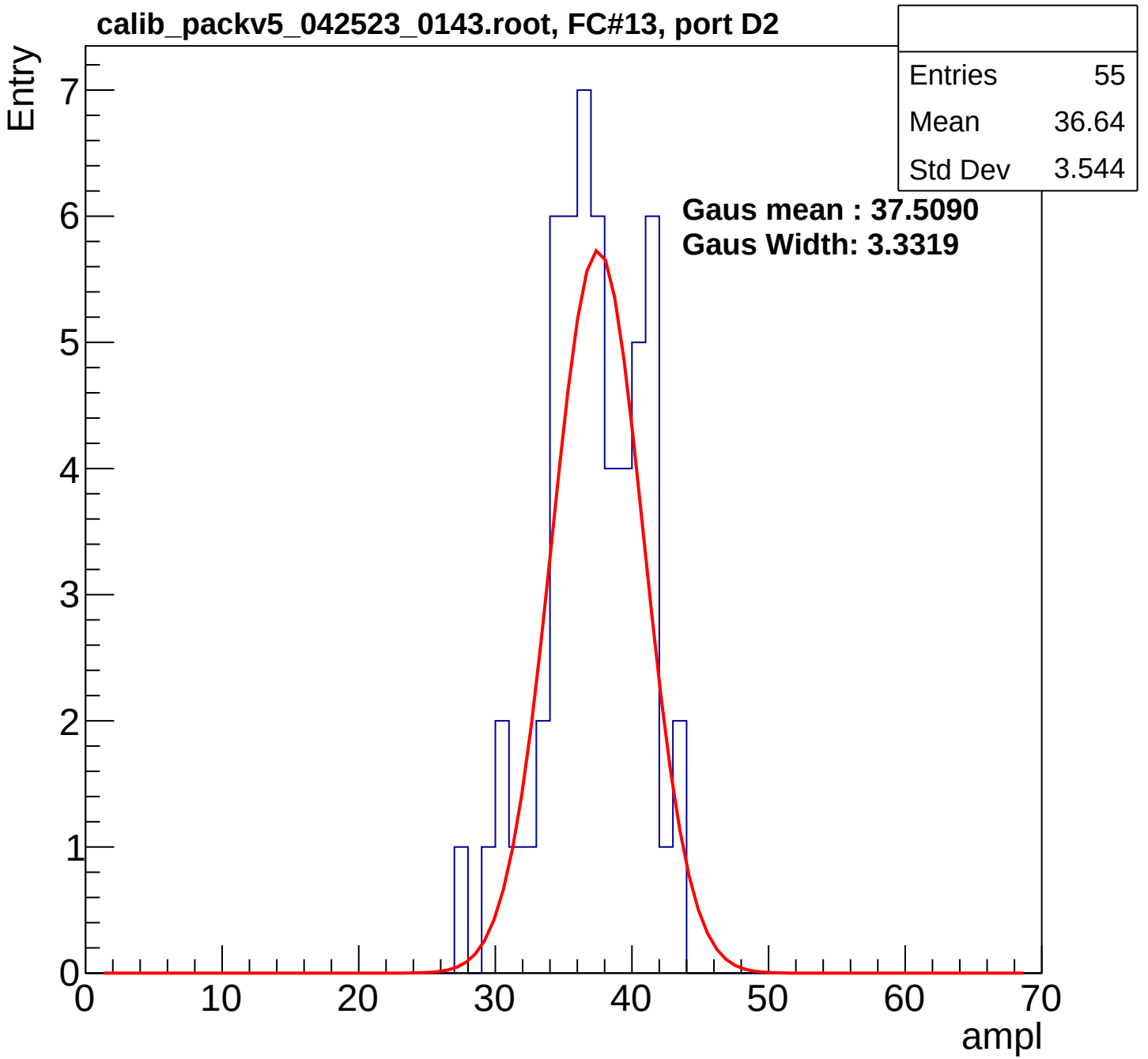
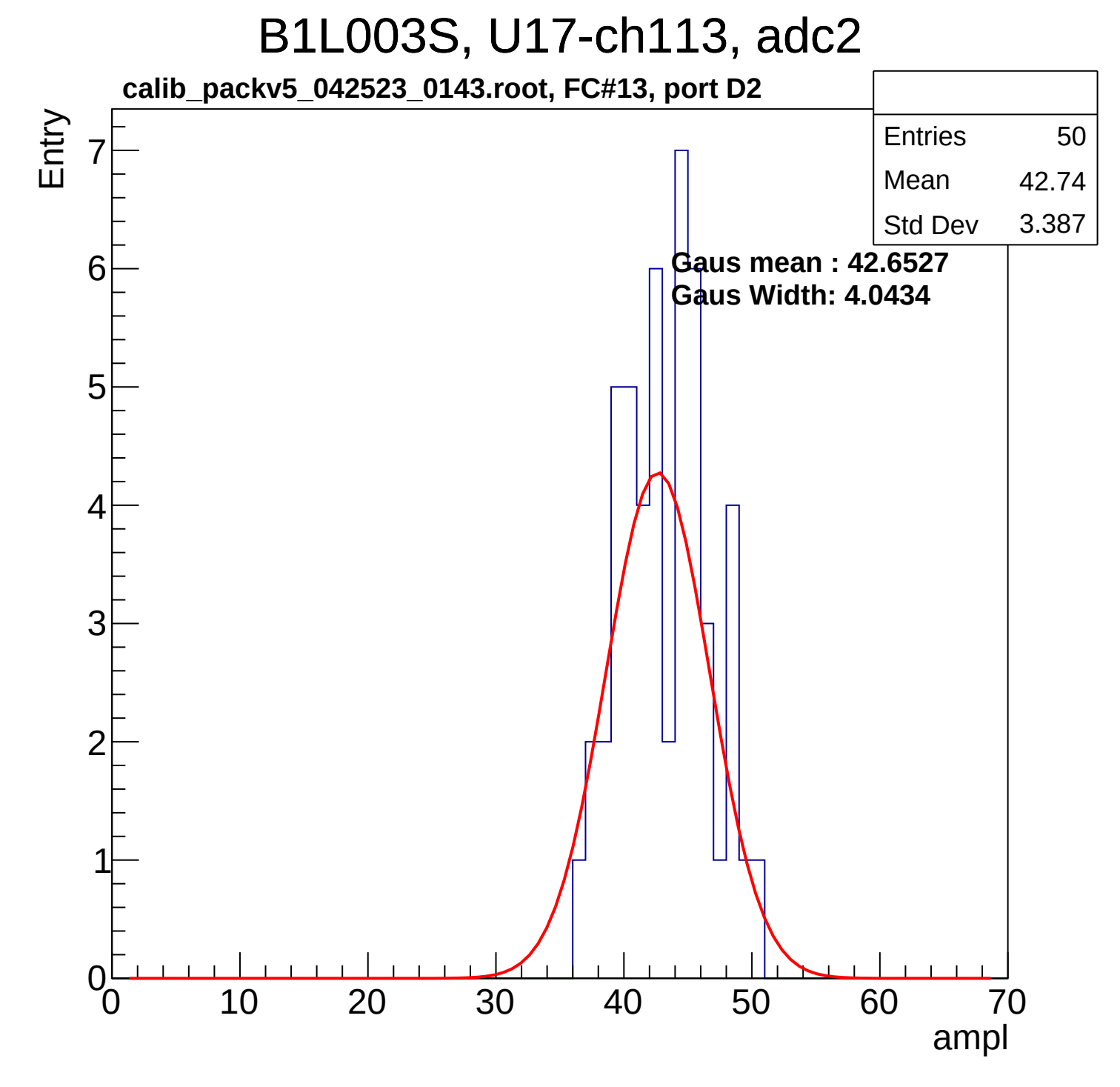
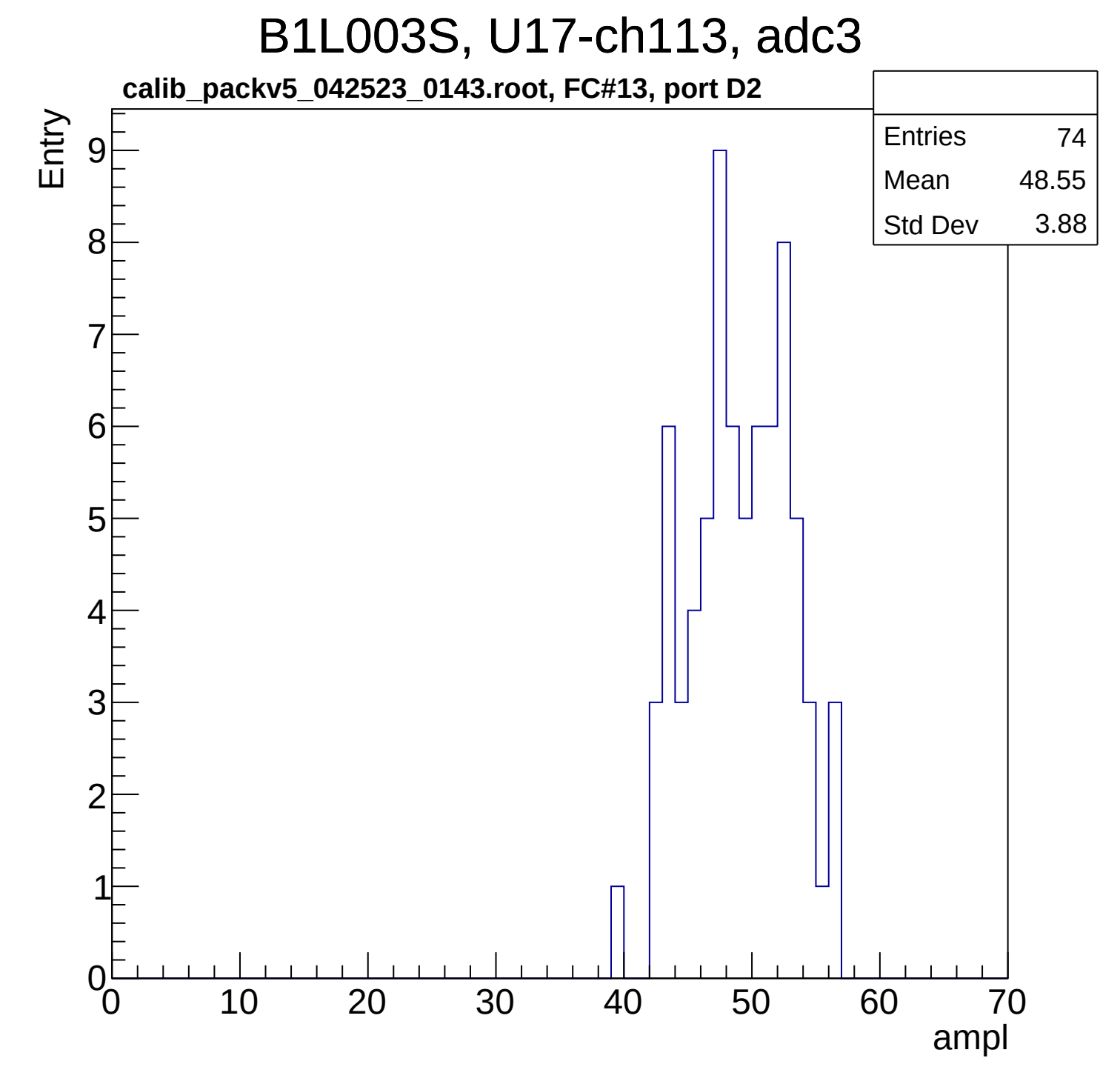
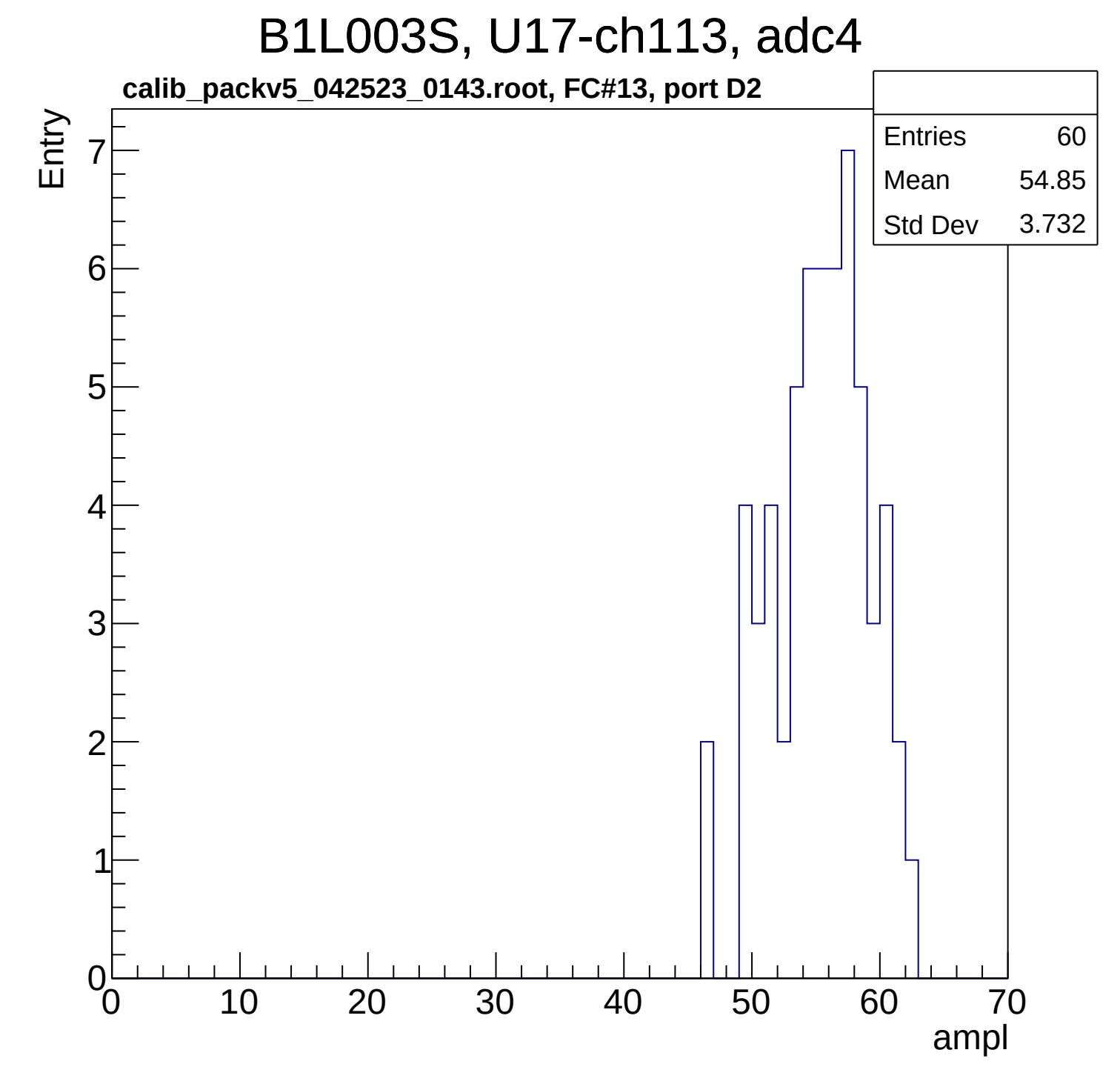
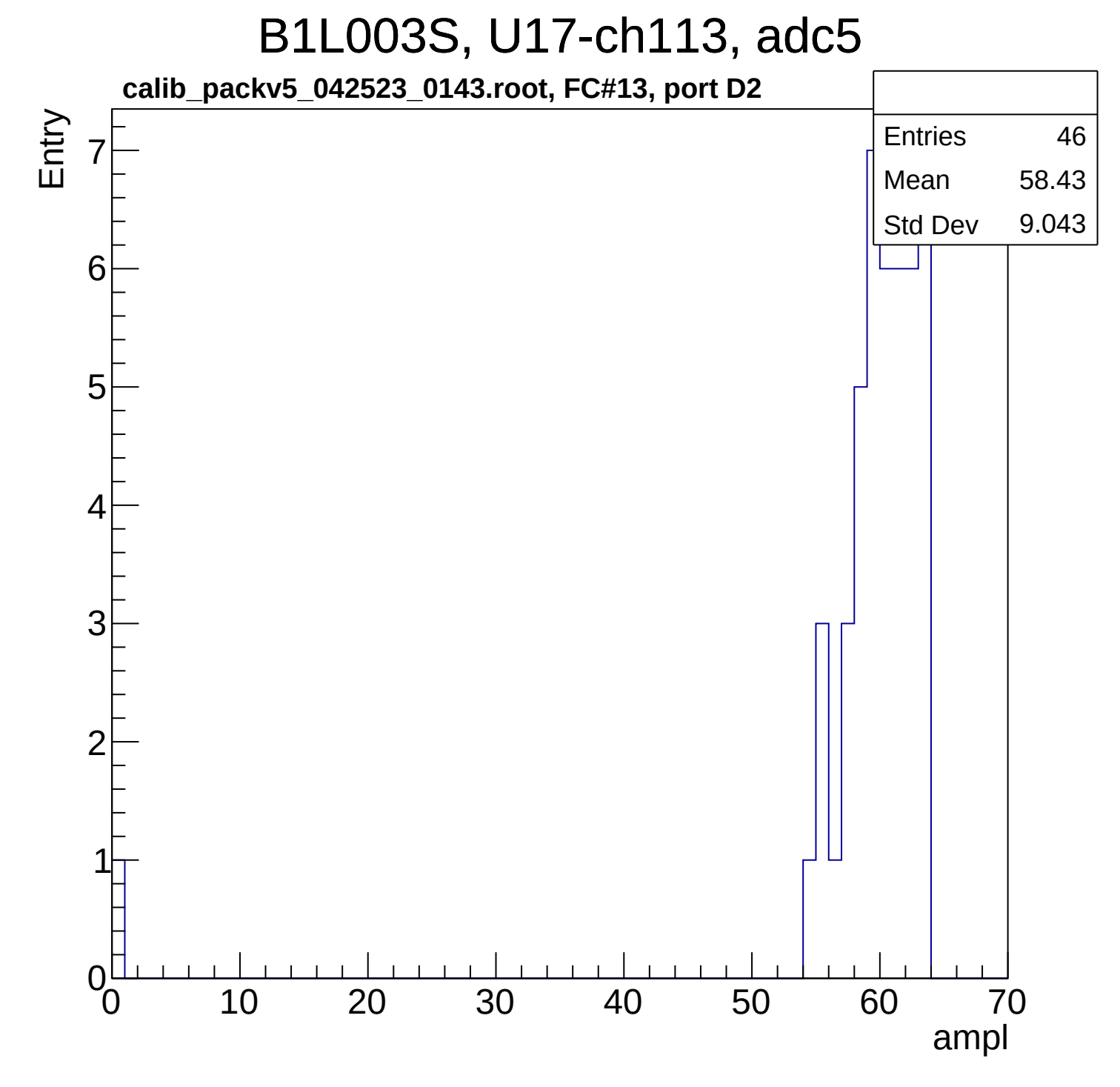

B1L003S, U17-ch113, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 36.64
Std Dev 3.544
Gaus mean : 37.5090
Gaus Width: 3.3319
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch113, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 42.74
Std Dev 3.387
Gaus mean : 42.6527
Gaus Width: 4.0434
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch113, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.55
Std Dev 3.88
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch113, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.85
Std Dev 3.732
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch113, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.43
Std Dev 9.043
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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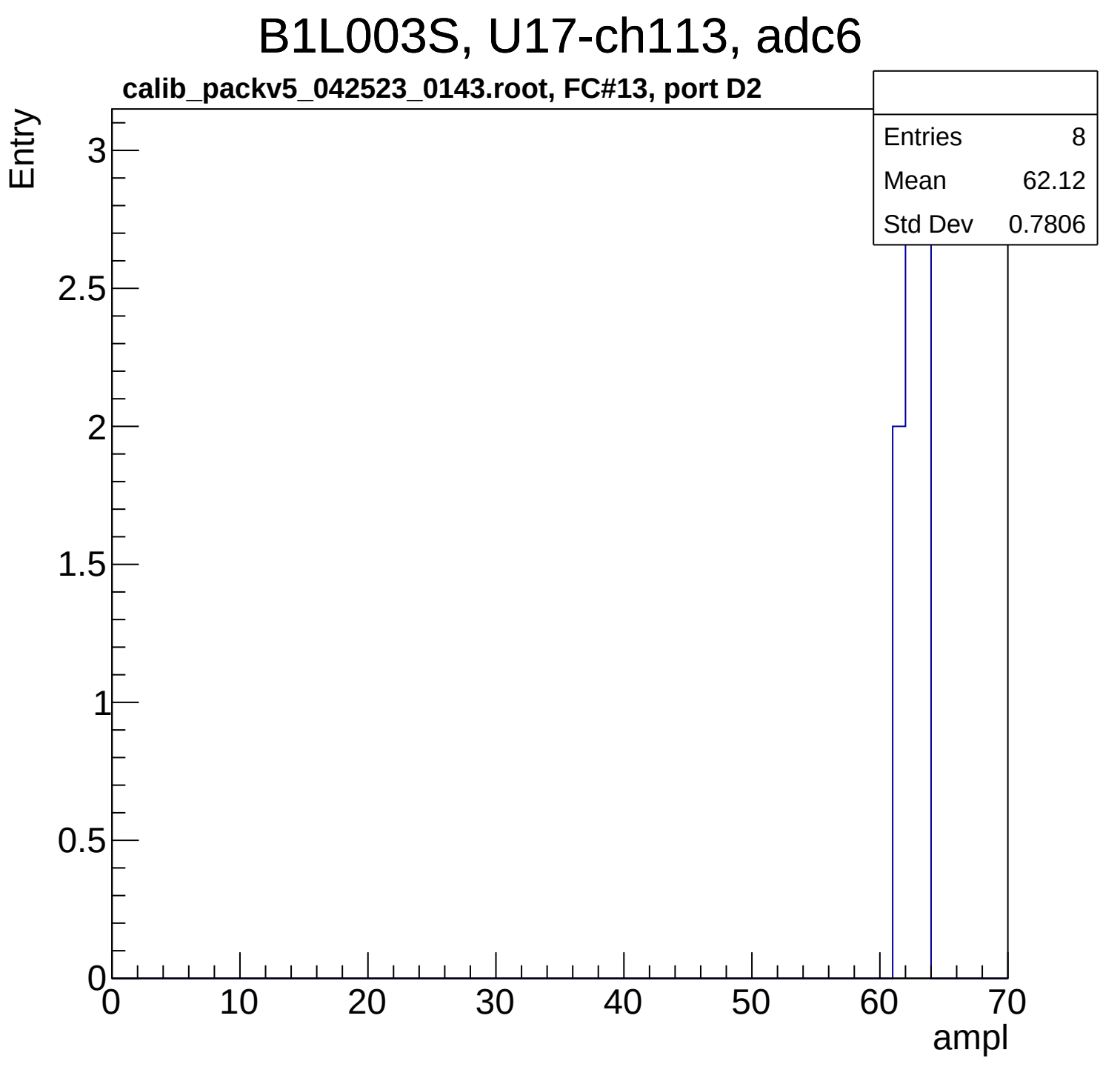
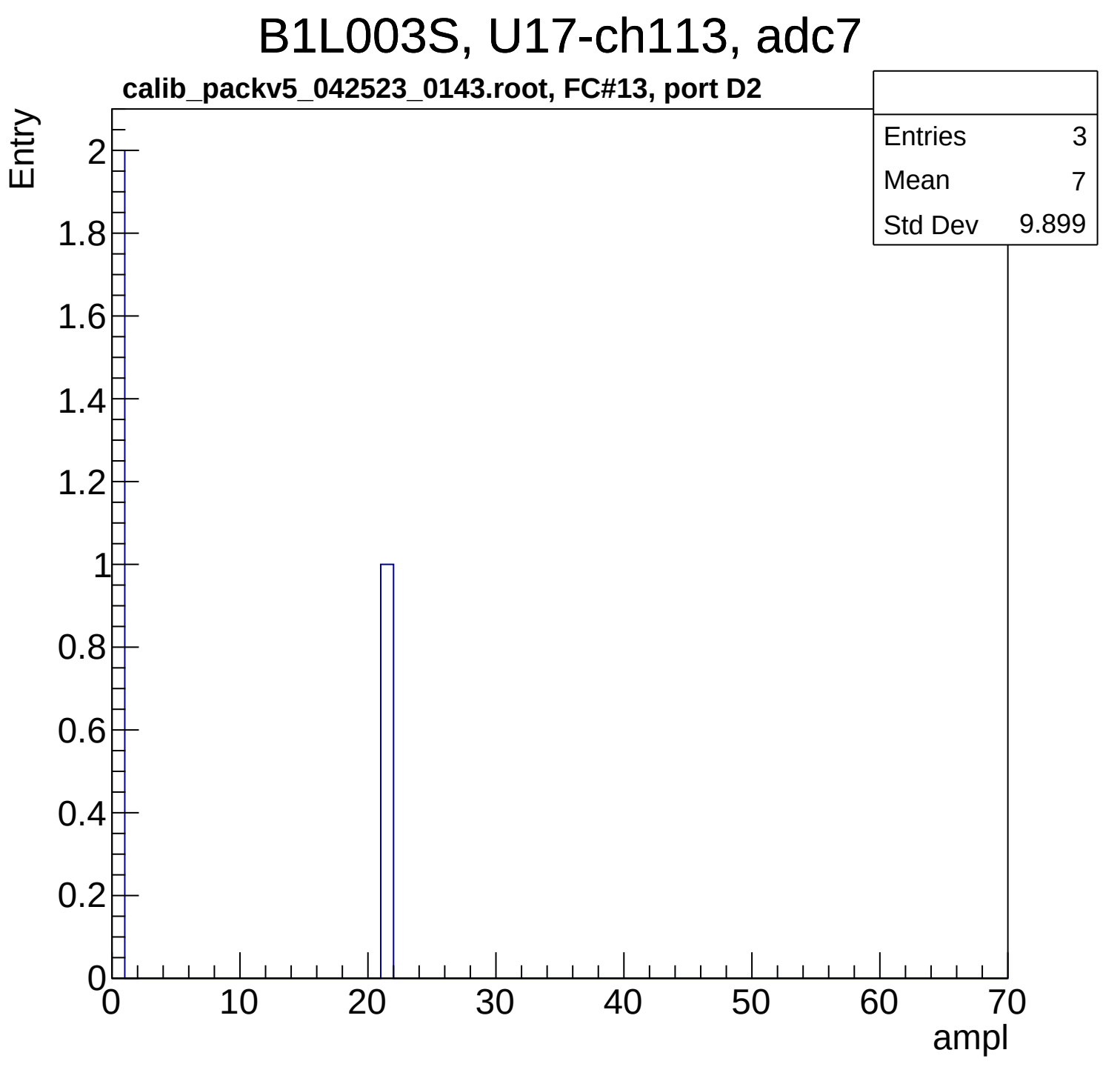

B1L003S, U17-ch113, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.12
Std Dev 0.7806
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch113, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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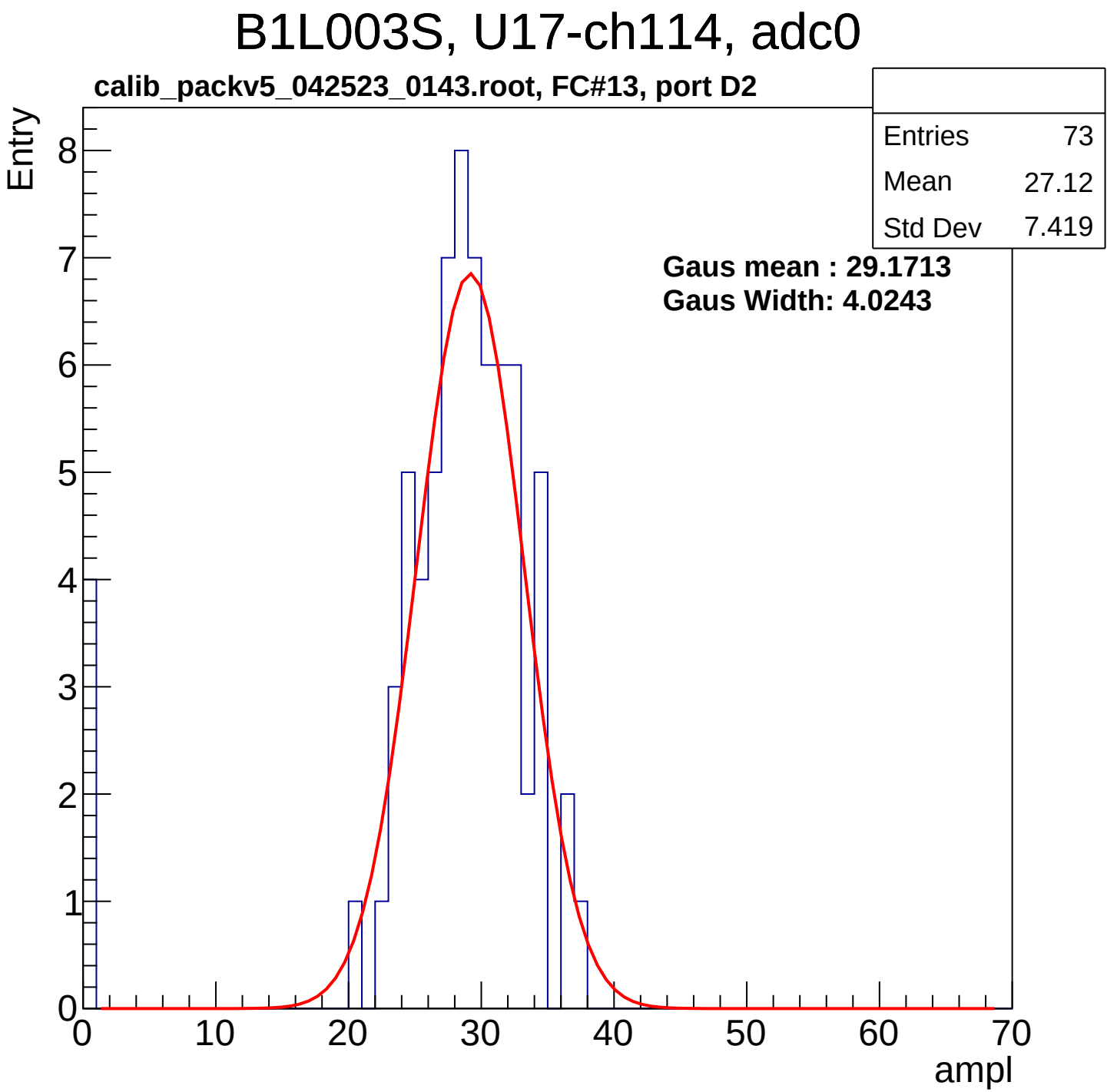
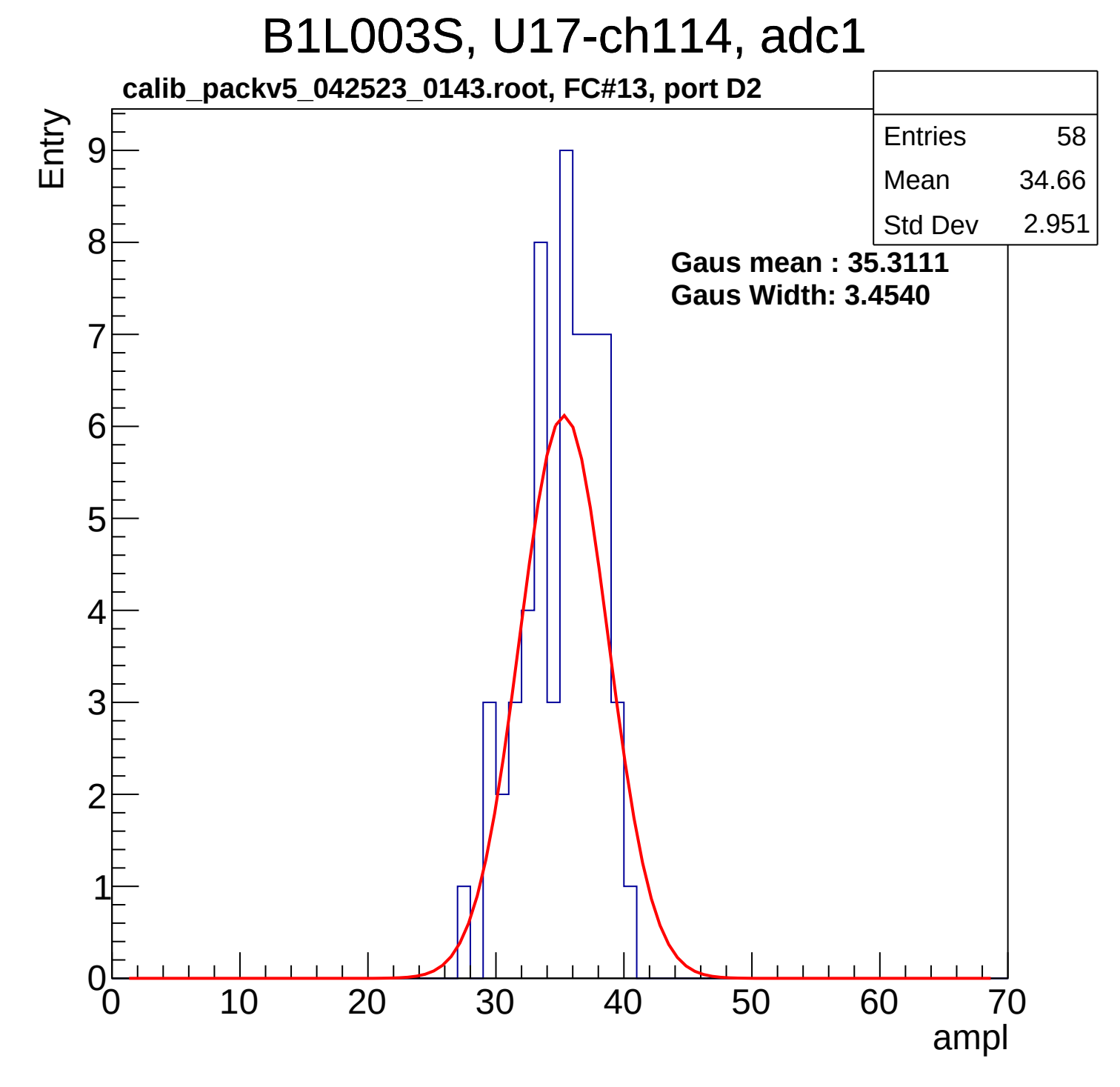
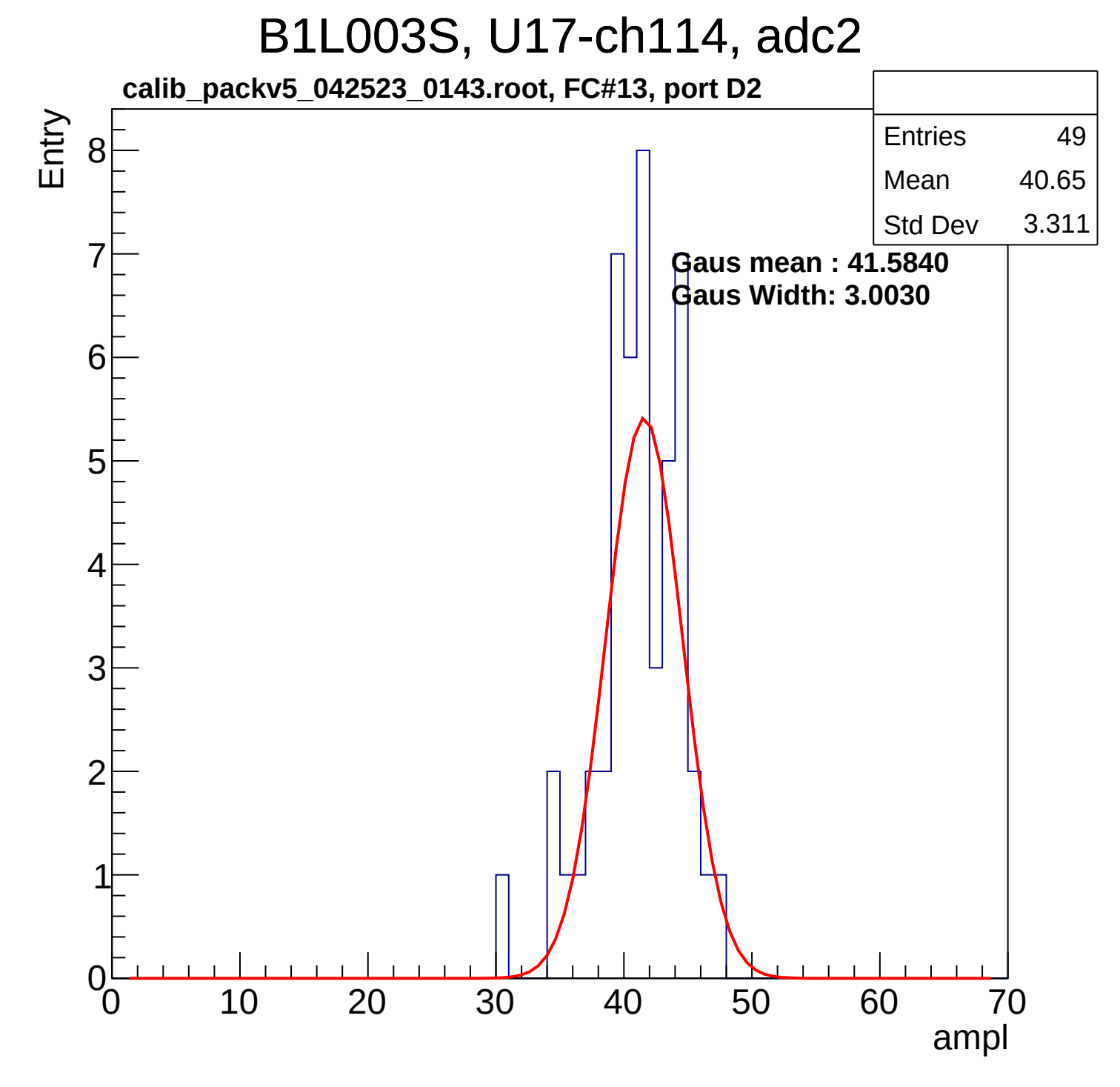
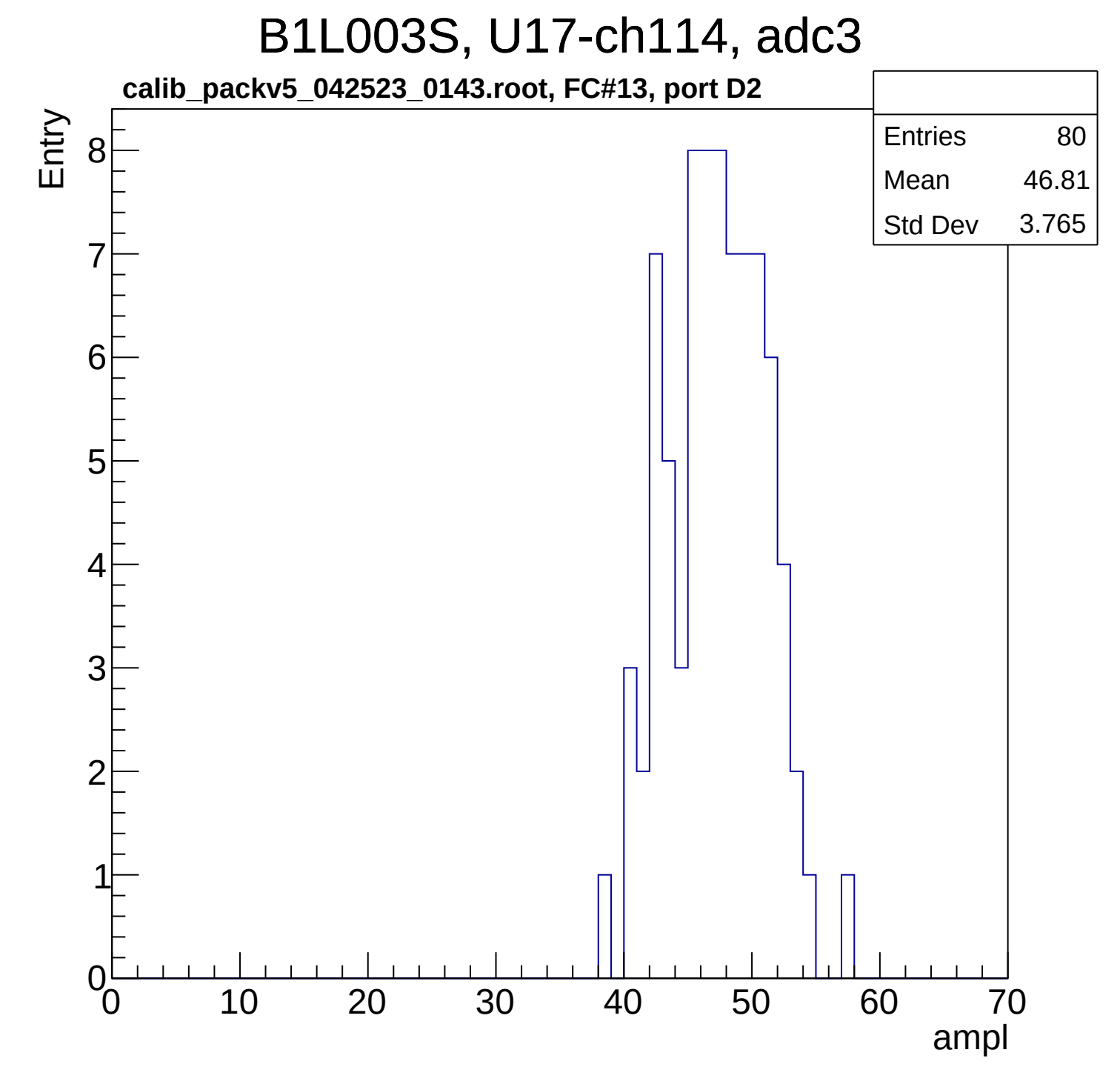
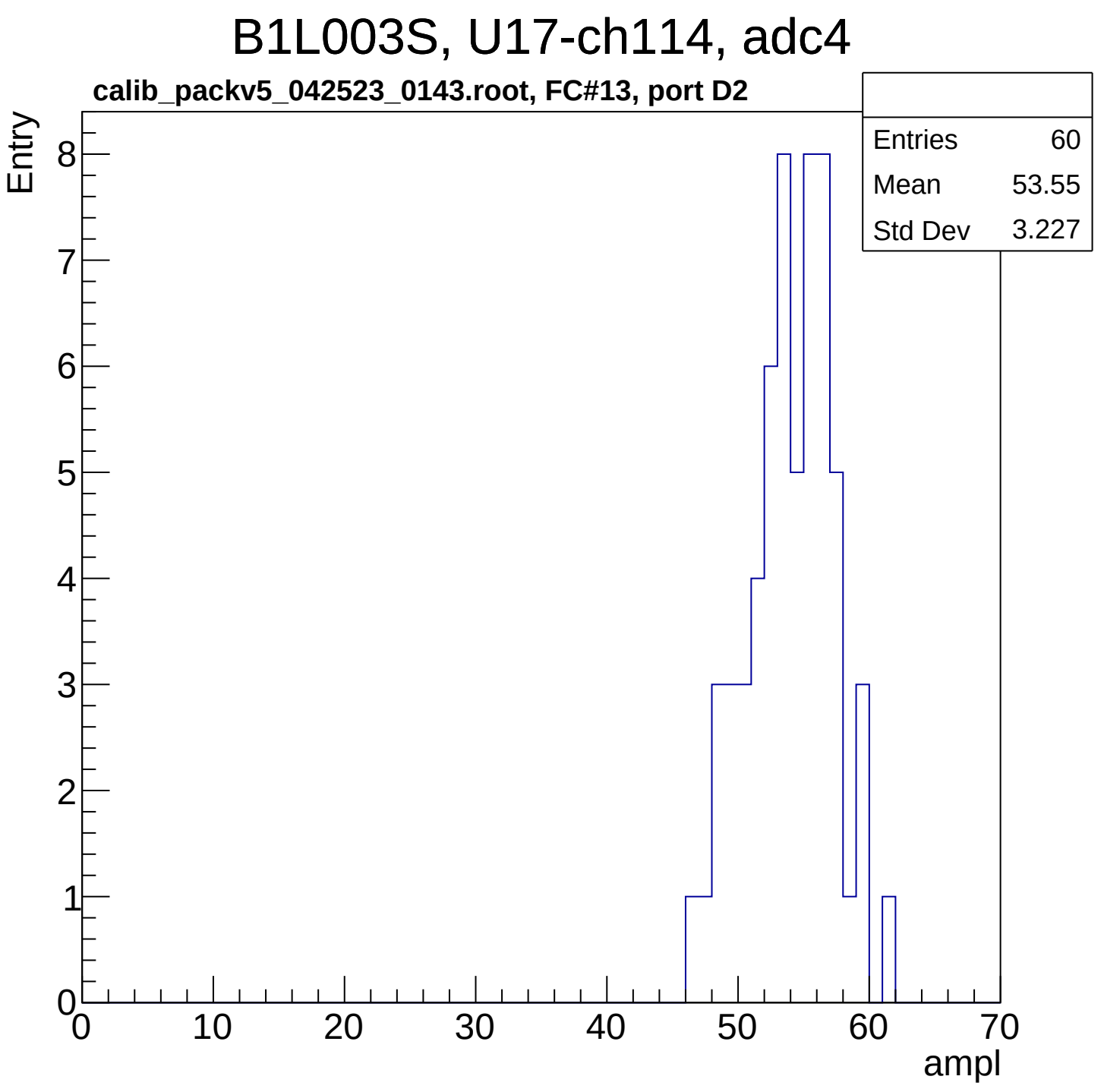

B1L003S, U17-ch114, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 27.12
Std Dev 7.419
Gaus mean : 29.1713
Gaus Width: 4.0243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch114, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 34.66
Std Dev 2.951
Gaus mean : 35.3111
Gaus Width: 3.4540
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch114, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 40.65
Std Dev 3.311
Gaus mean : 41.5840
Gaus Width: 3.0030
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch114, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 46.81
Std Dev 3.765
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch114, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 53.55
Std Dev 3.227
Entry
ampl
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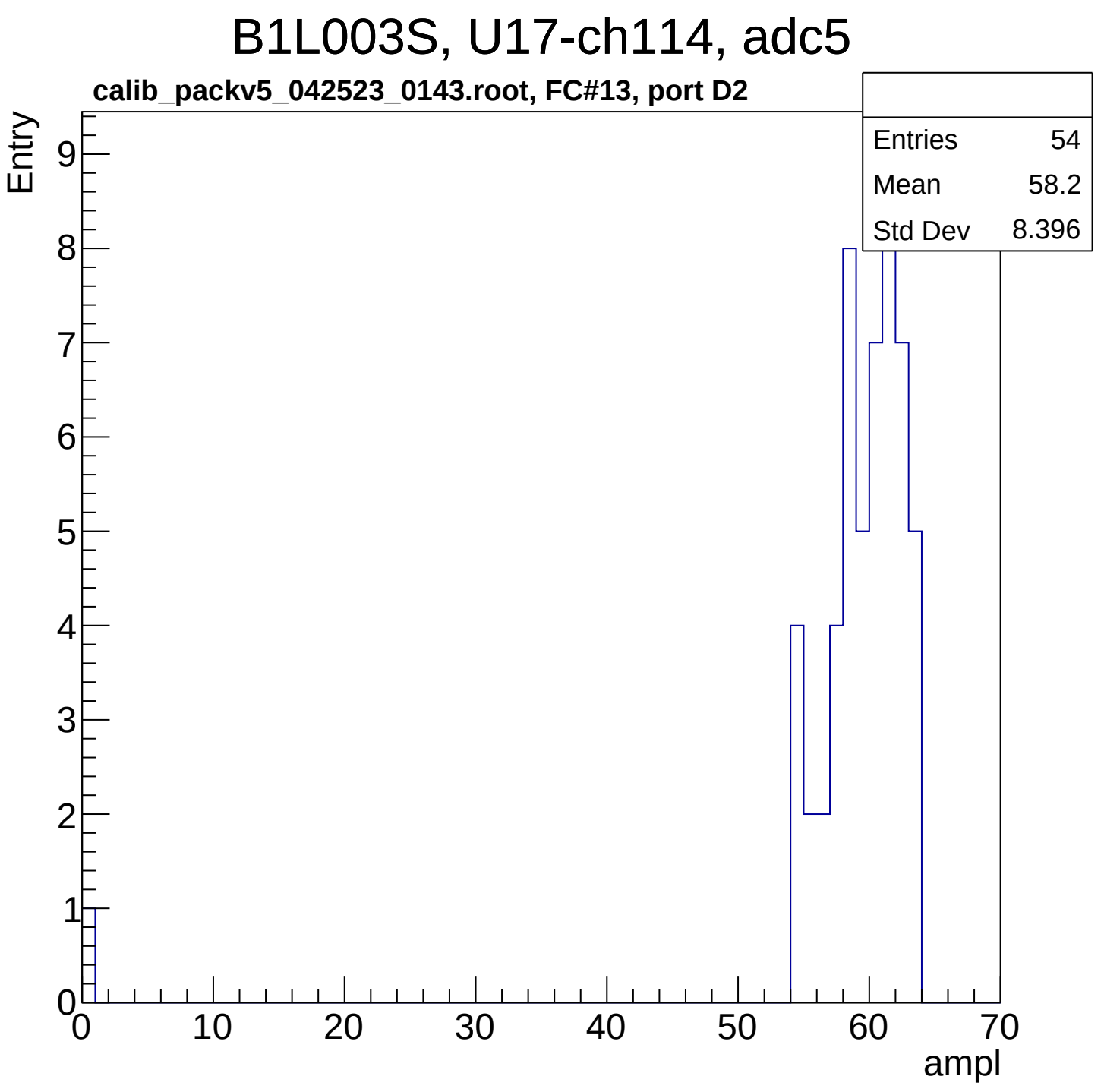
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch114, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.2
Std Dev 8.396
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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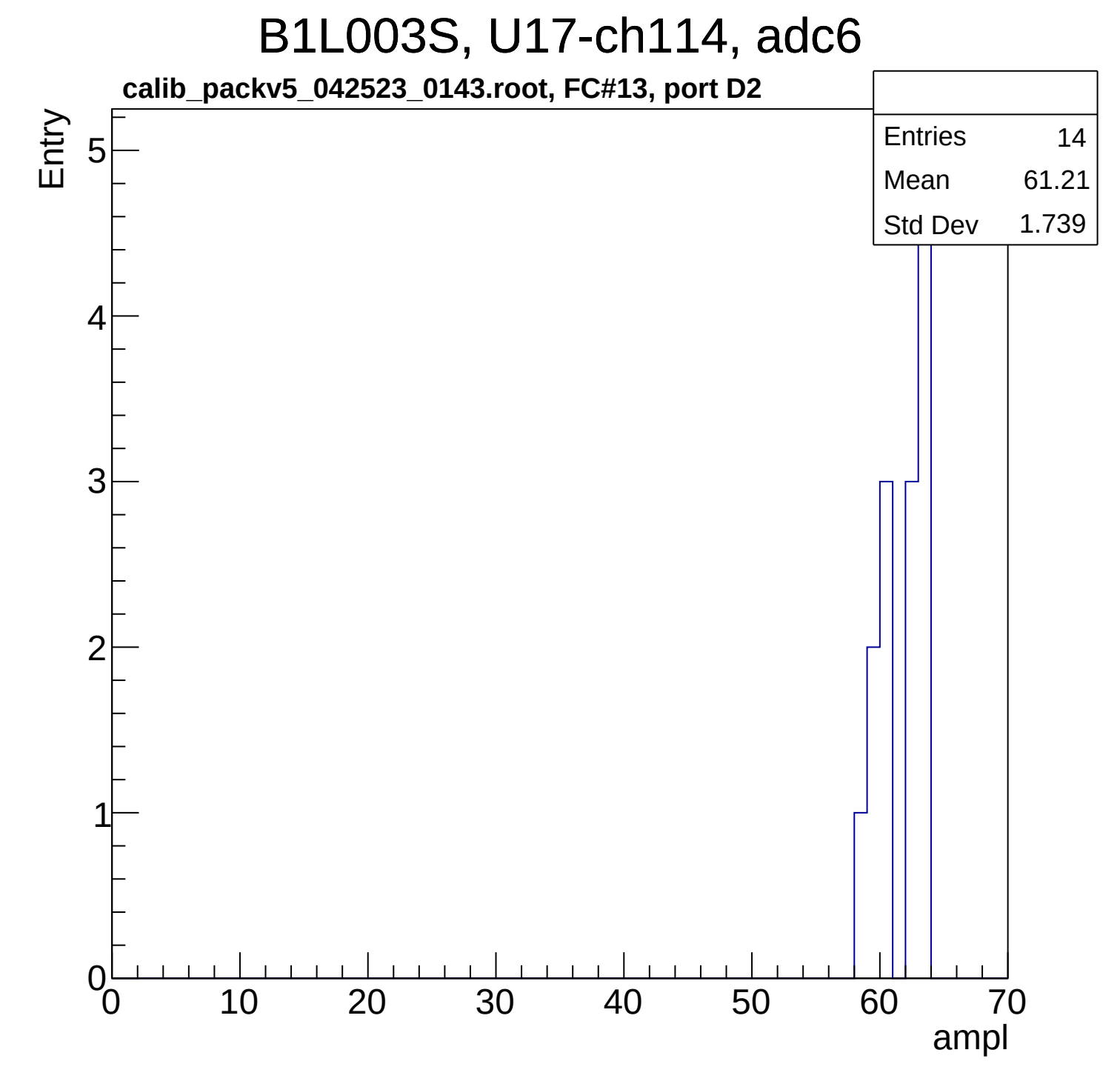
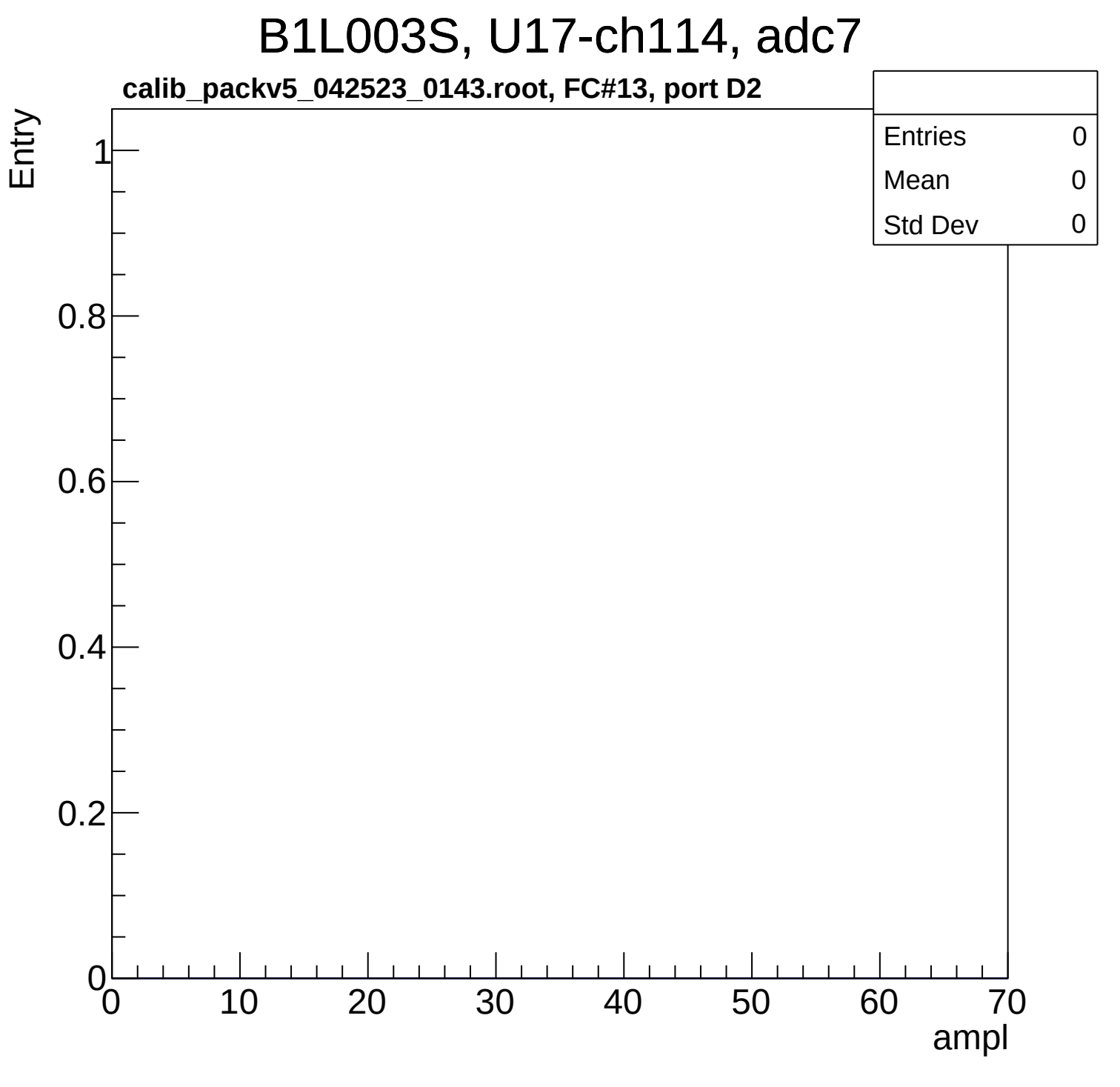
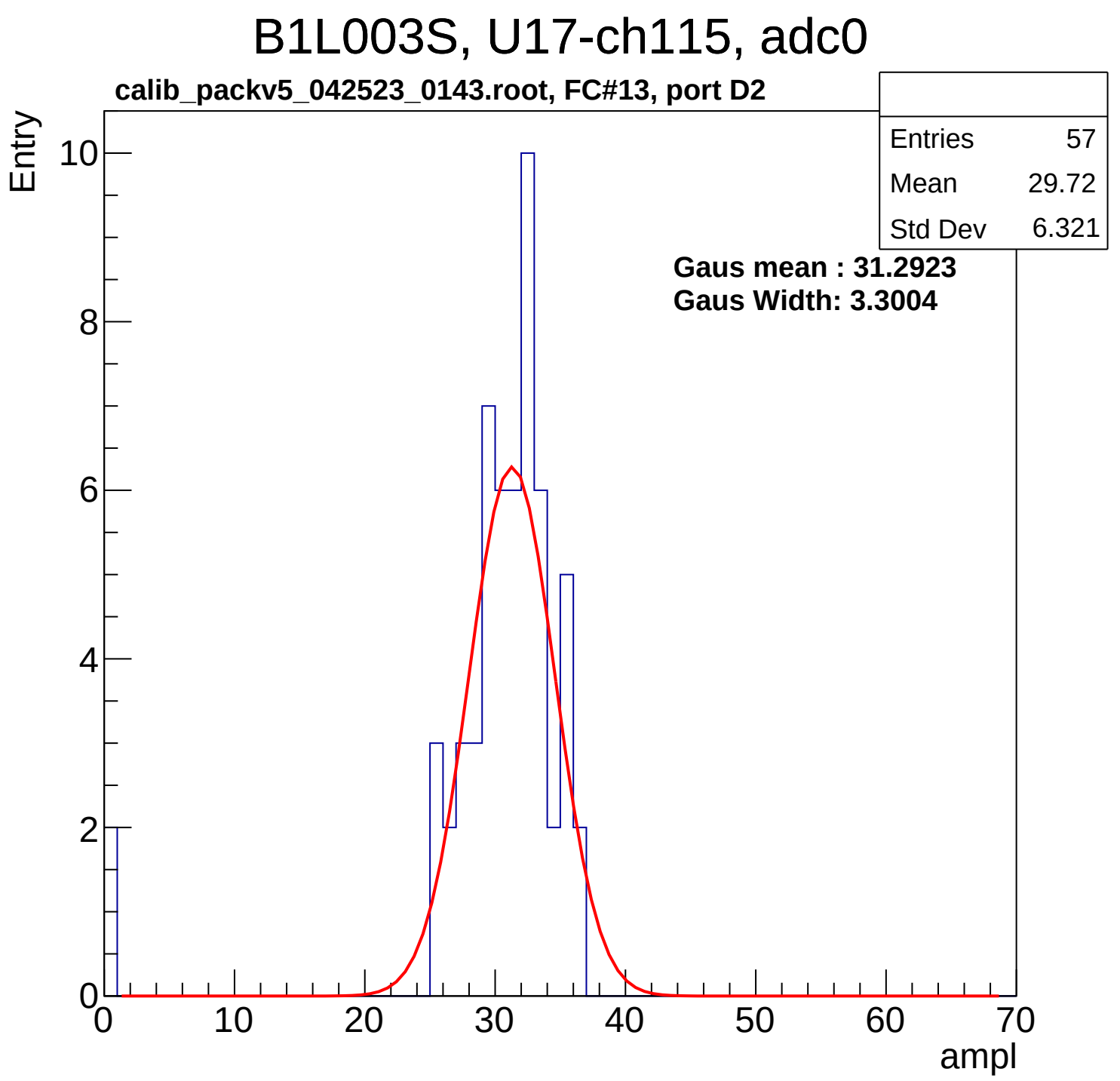
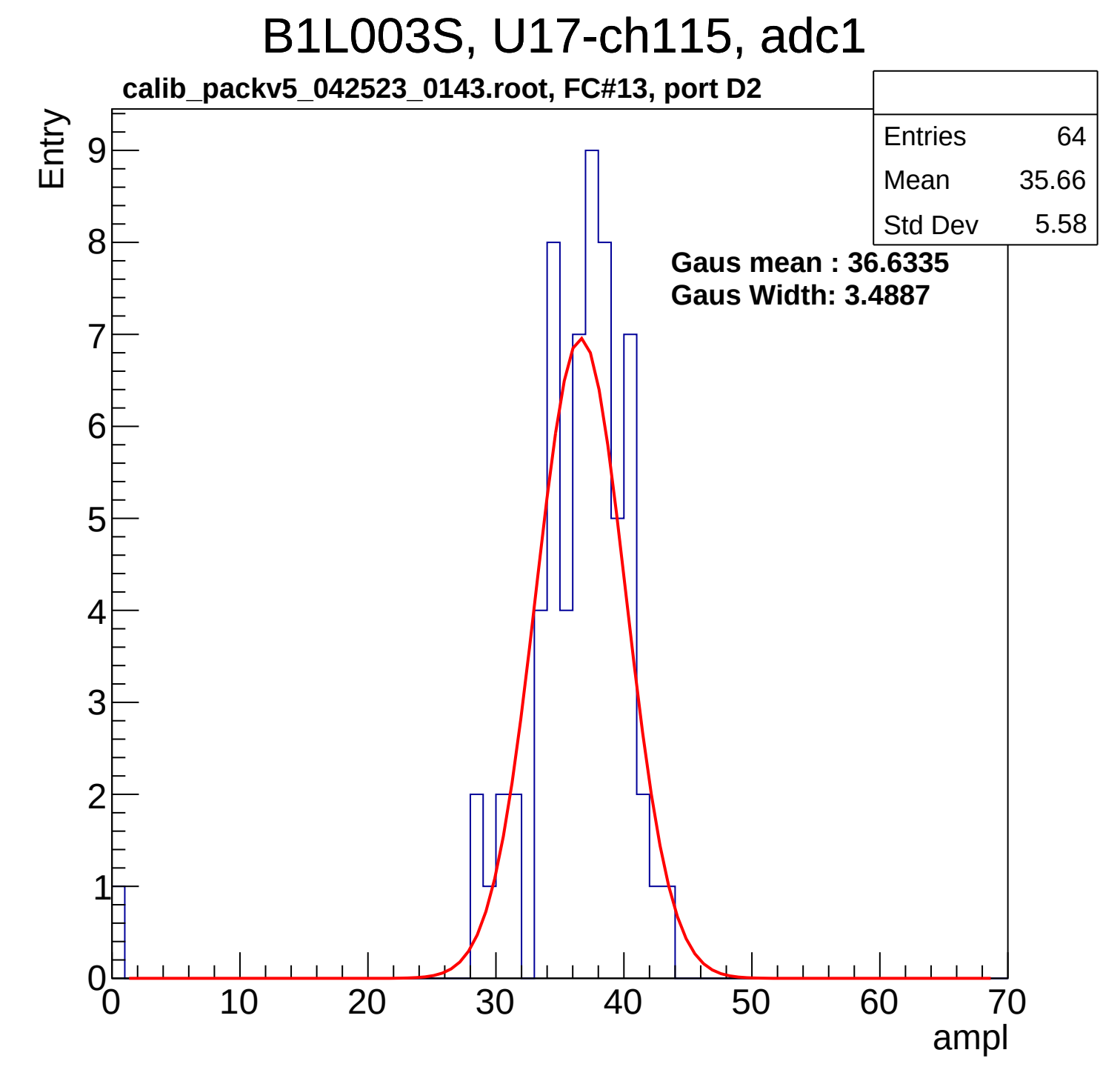

B1L003S, U17-ch114, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.21
Std Dev 1.739
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch114, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch115, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 29.72
Std Dev 6.321
Gaus mean : 31.2923
Gaus Width: 3.3004
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch115, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 35.66
Std Dev 5.58
Gaus mean : 36.6335
Gaus Width: 3.4887
Entry
ampl
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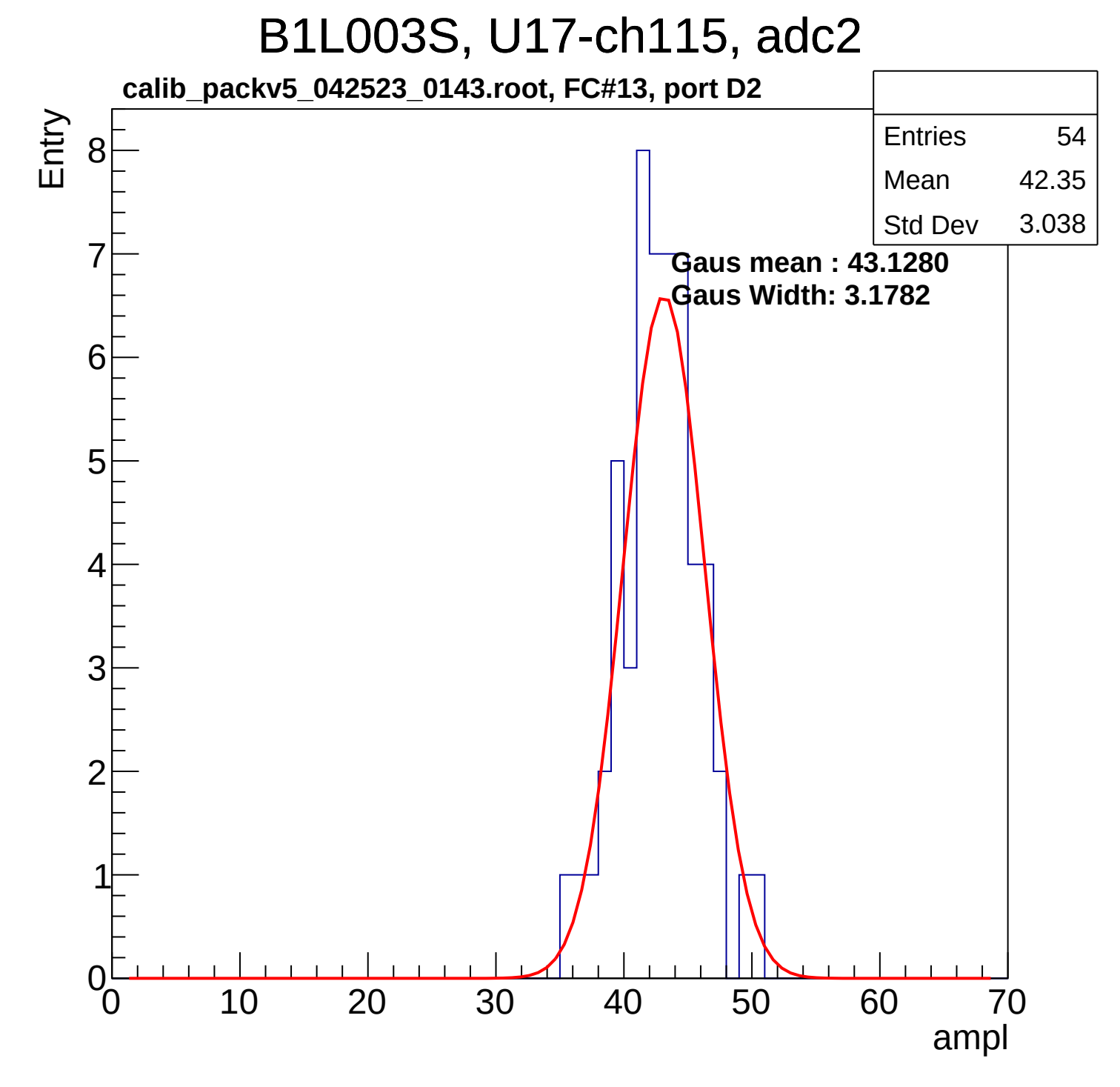
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch115, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.35
Std Dev 3.038
Gaus mean : 43.1280
Gaus Width: 3.1782
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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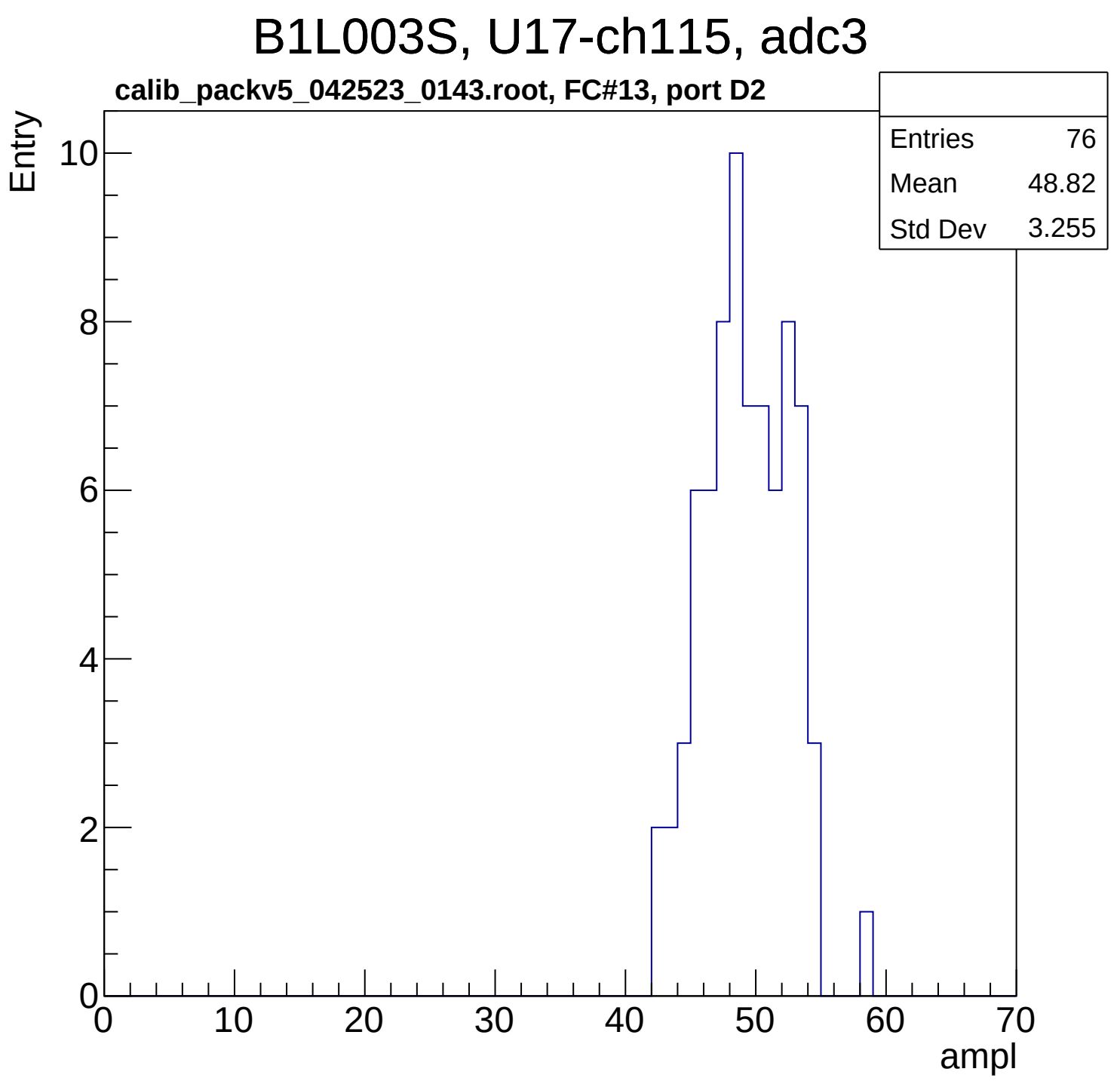
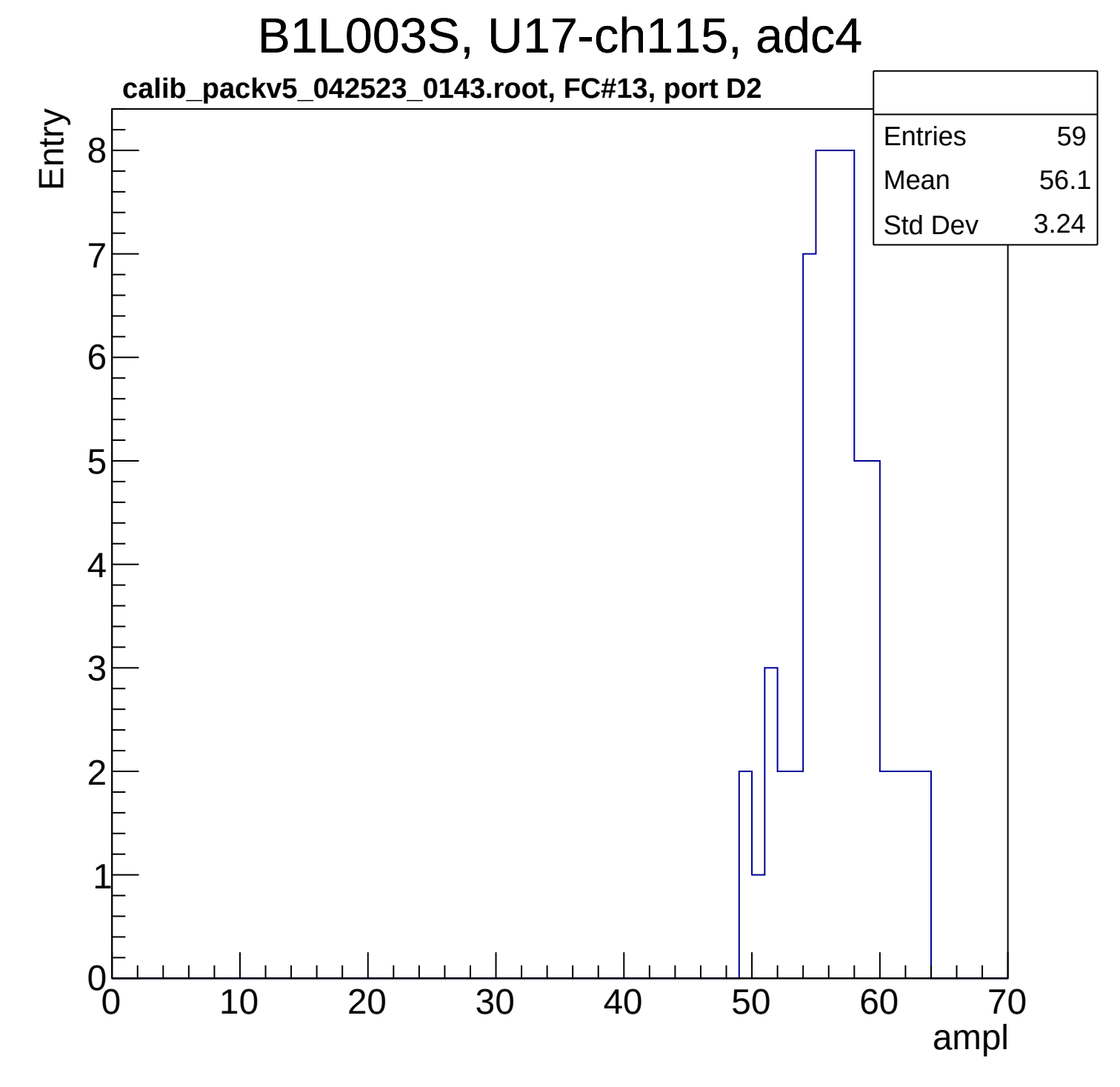
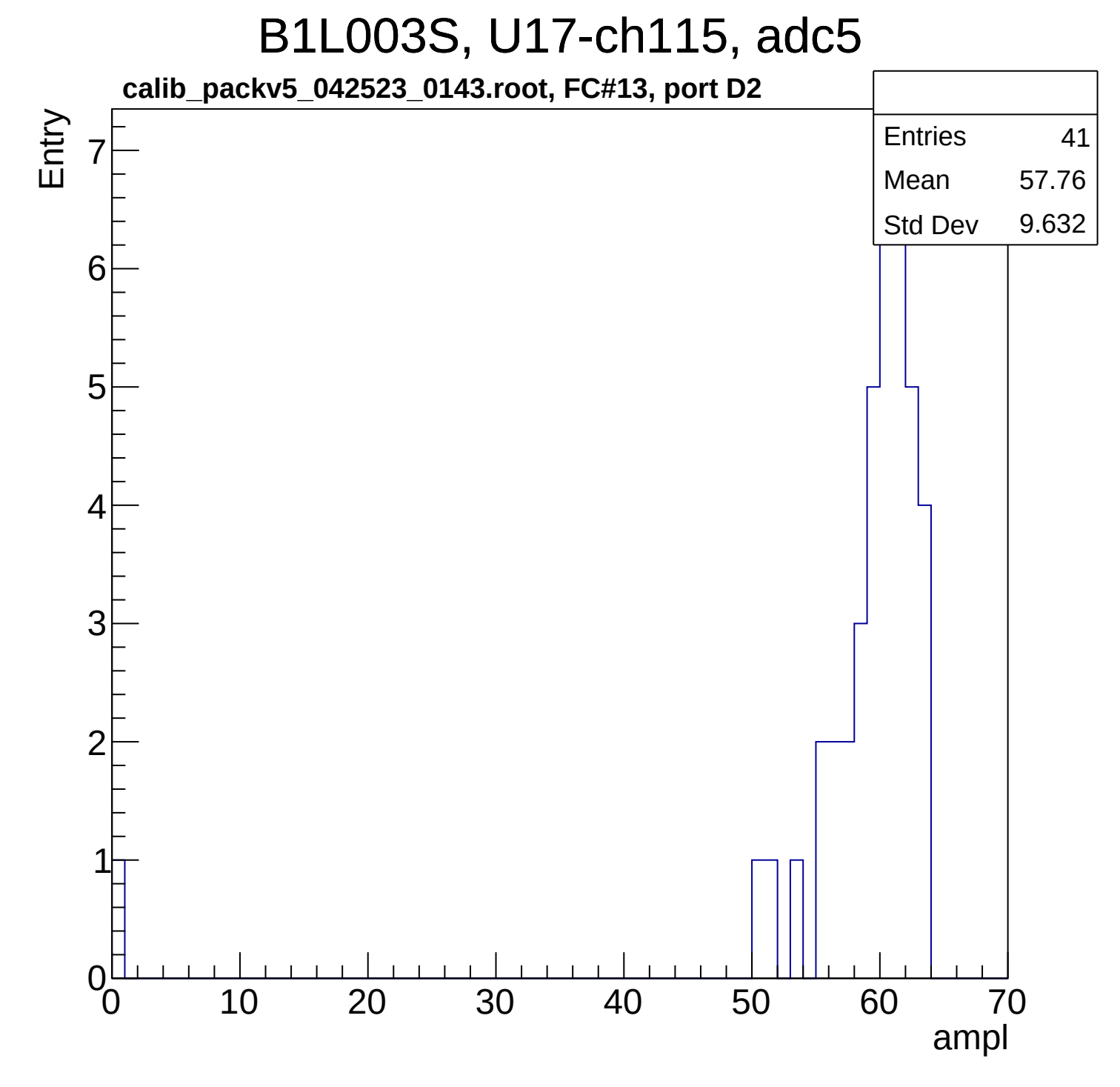
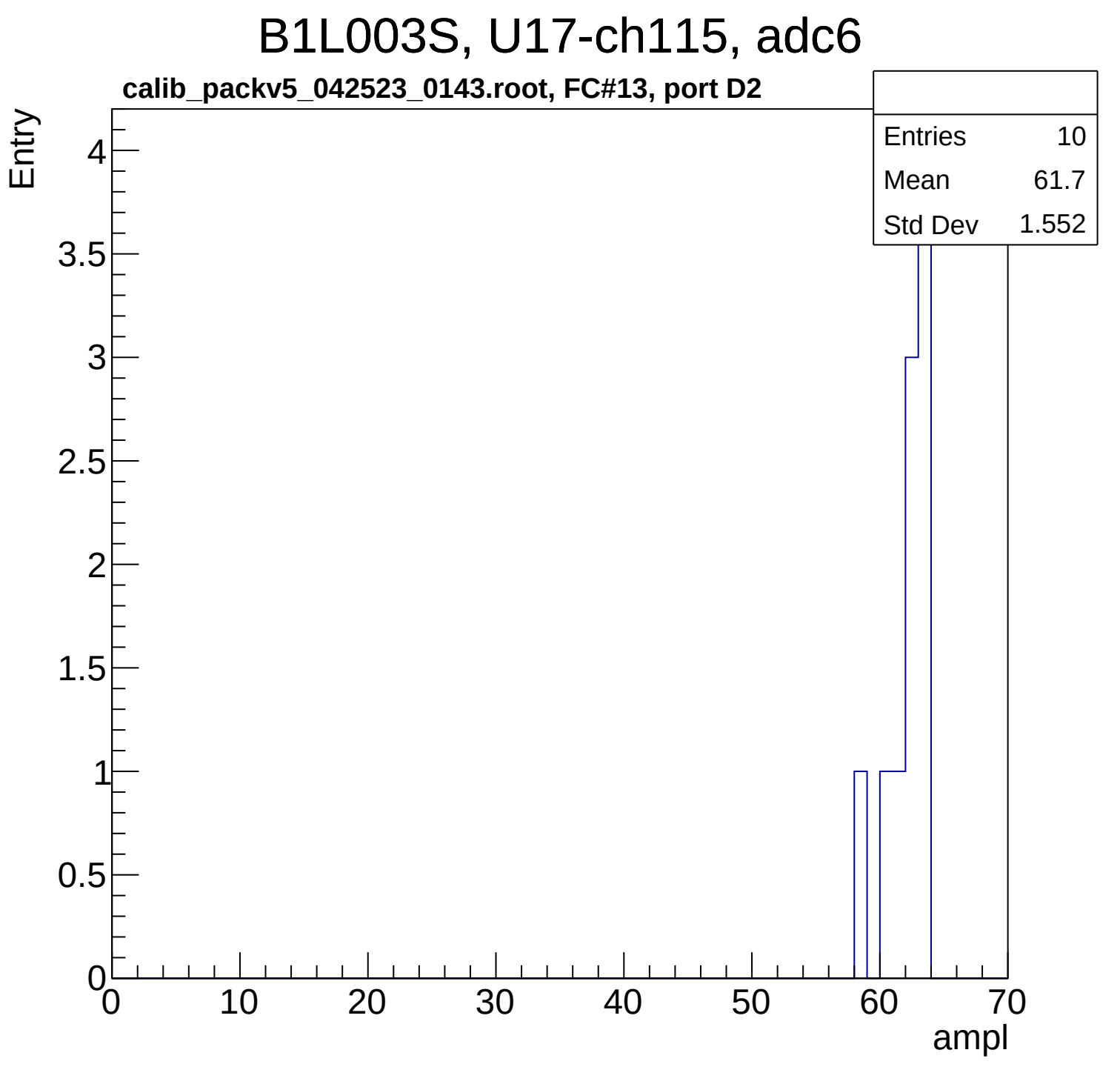

B1L003S, U17-ch115, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 48.82
Std Dev 3.255
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch115, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 56.1
Std Dev 3.24
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch115, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 57.76
Std Dev 9.632
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch115, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.7
Std Dev 1.552
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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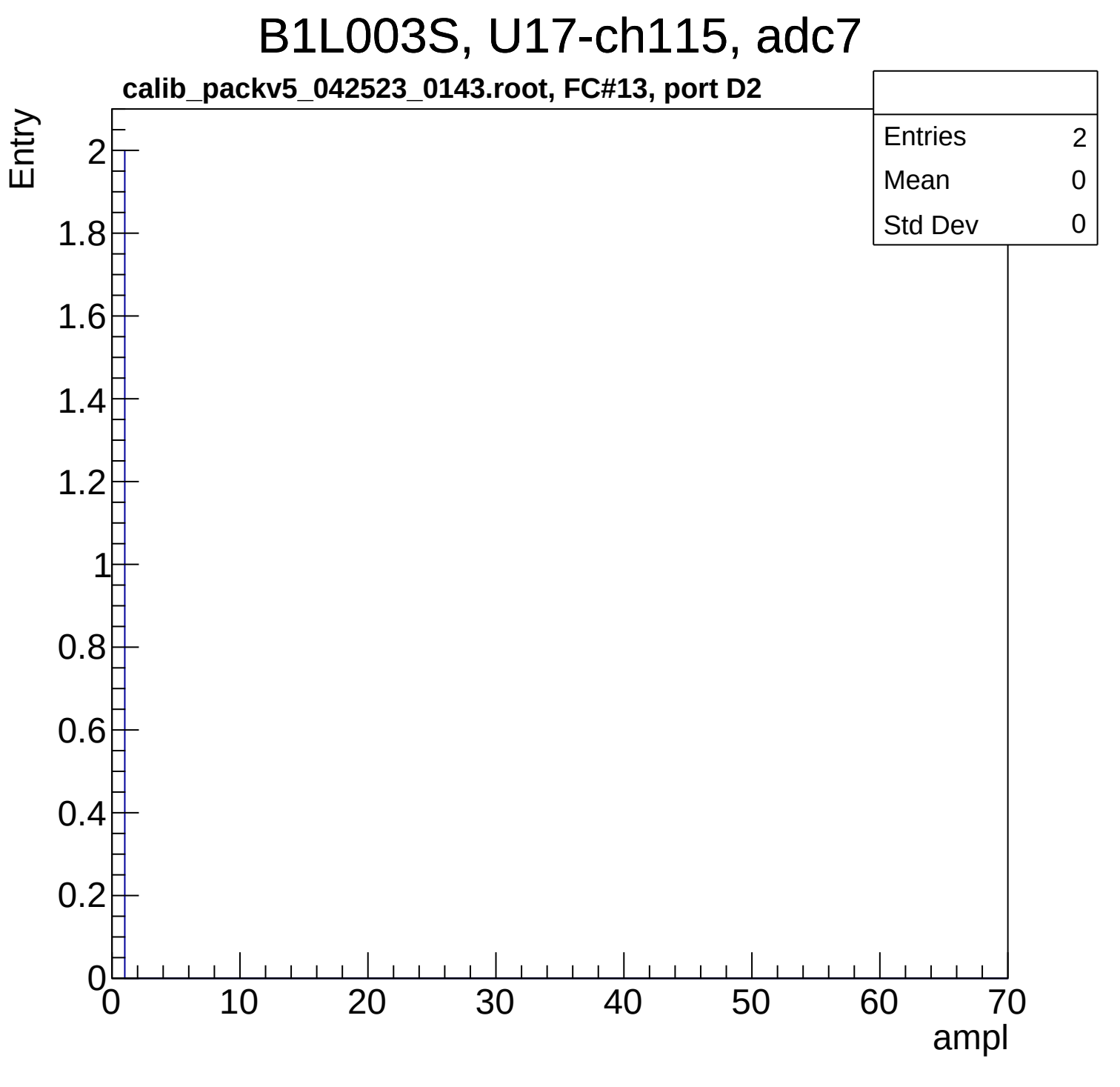
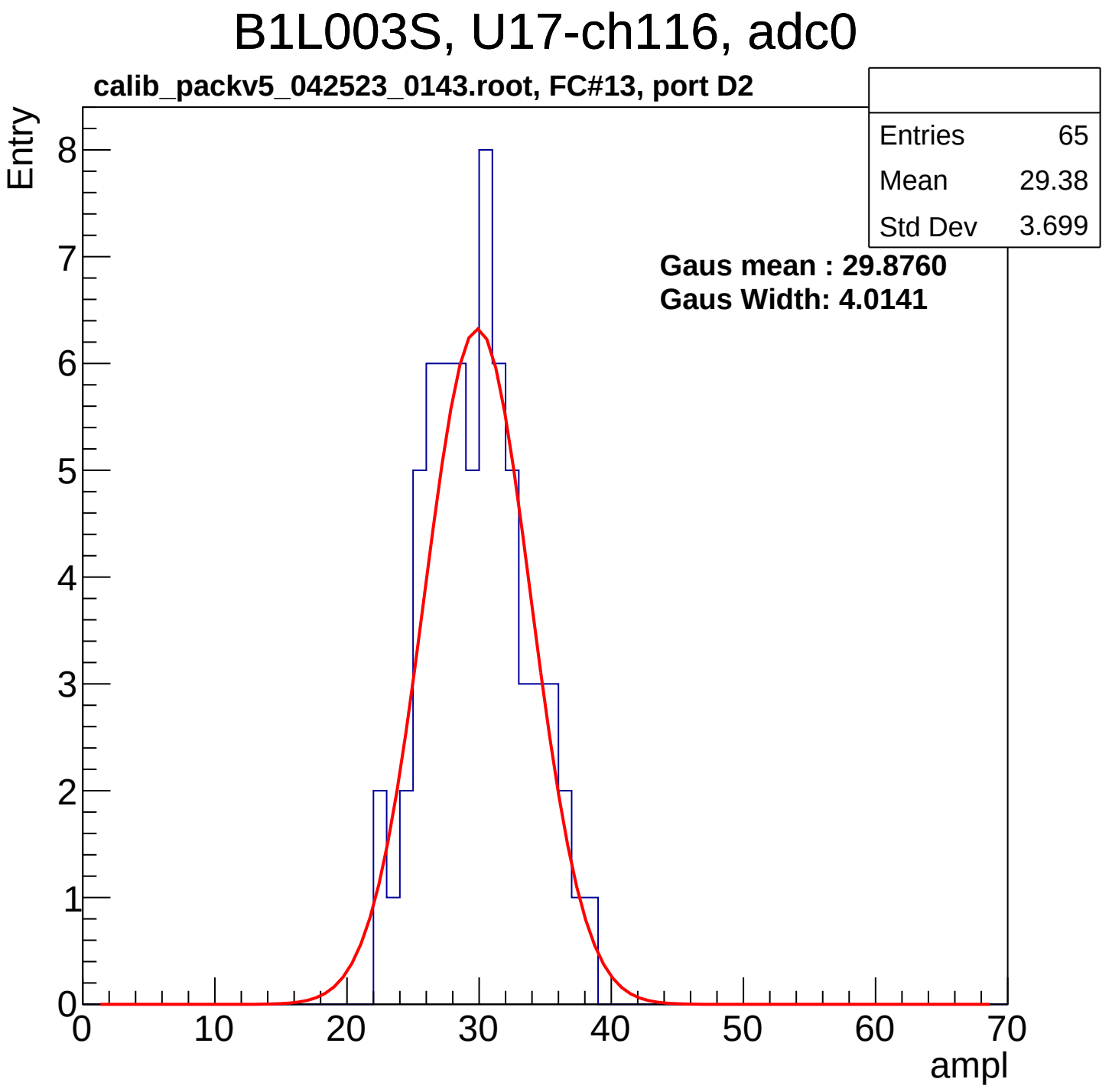
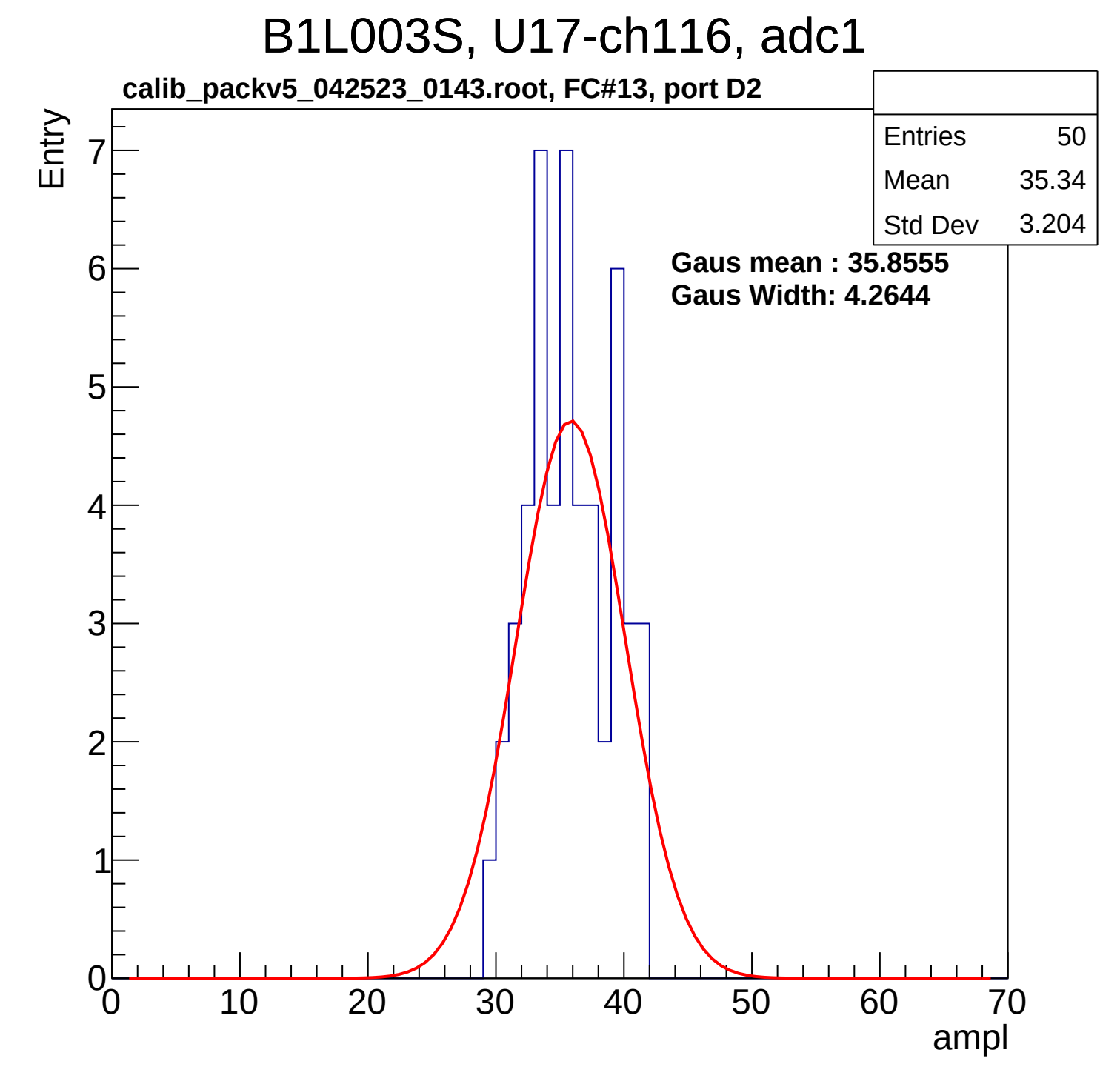

B1L003S, U17-ch115, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U17-ch116, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.38
Std Dev 3.699
Gaus mean : 29.8760
Gaus Width: 4.0141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch116, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 35.34
Std Dev 3.204
Gaus mean : 35.8555
Gaus Width: 4.2644
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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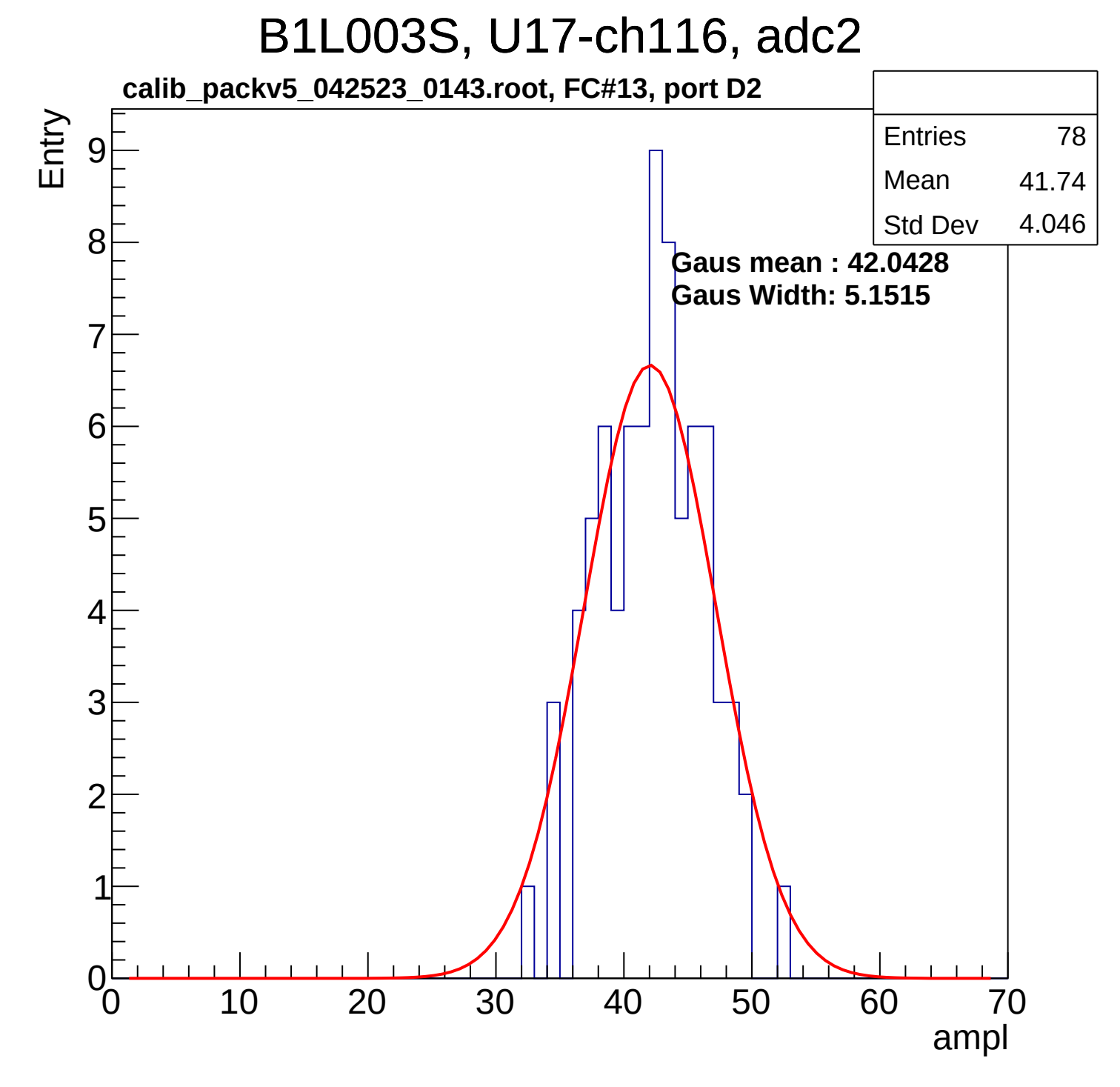
B1L003S, U17-ch116, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 41.74
Std Dev 4.046
Gaus mean : 42.0428
Gaus Width: 5.1515
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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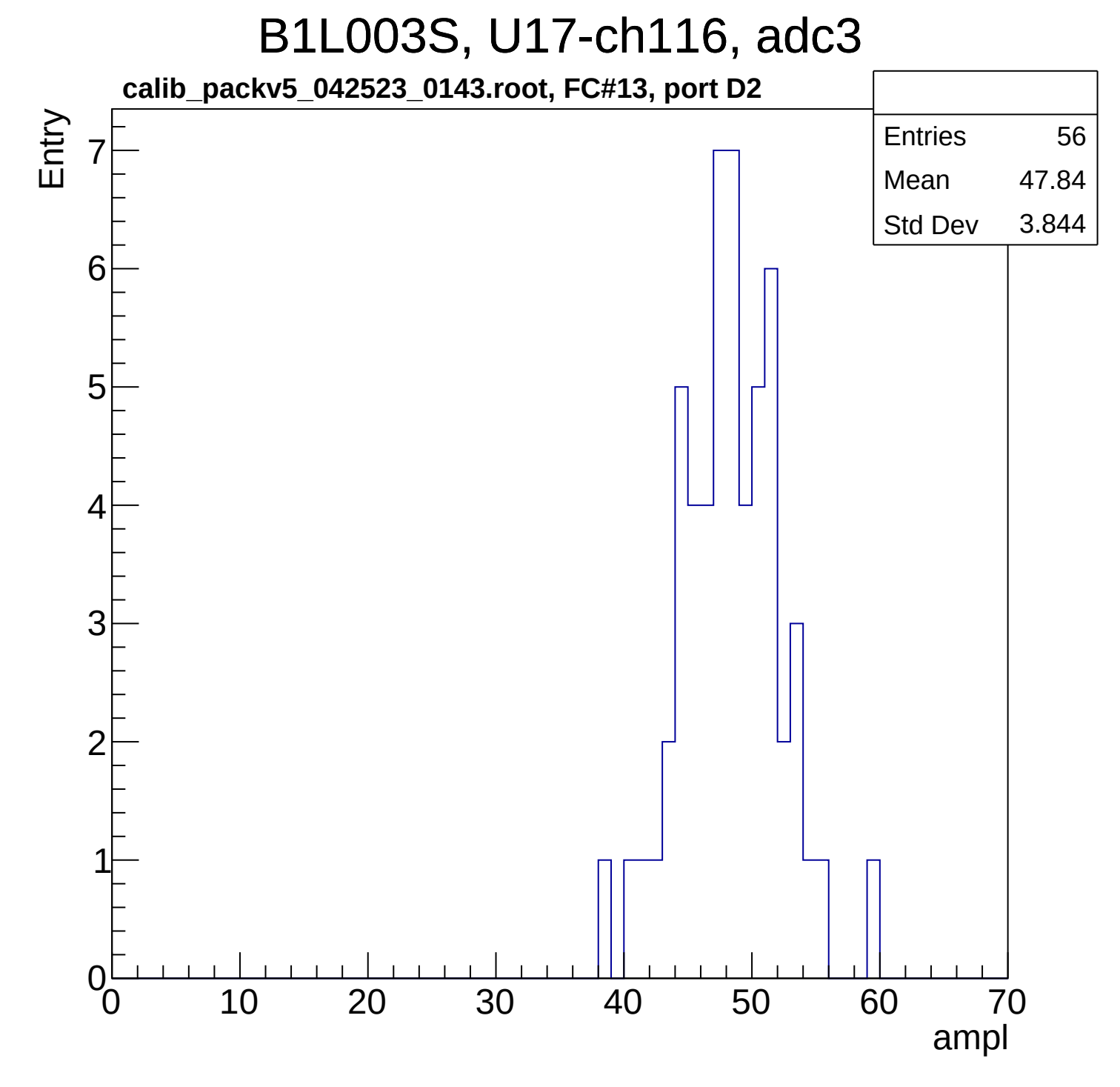
B1L003S, U17-ch116, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 47.84
Std Dev 3.844
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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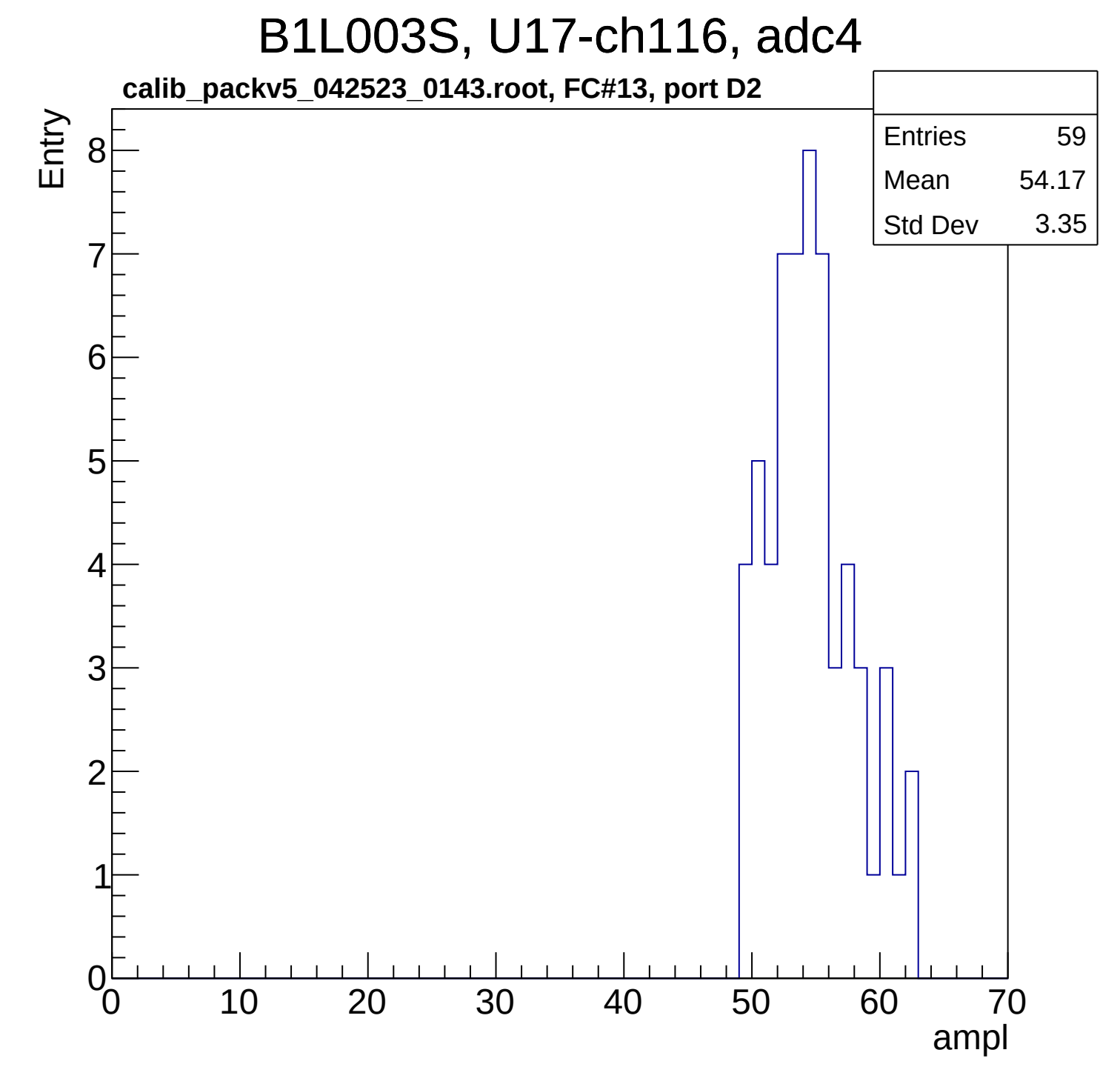
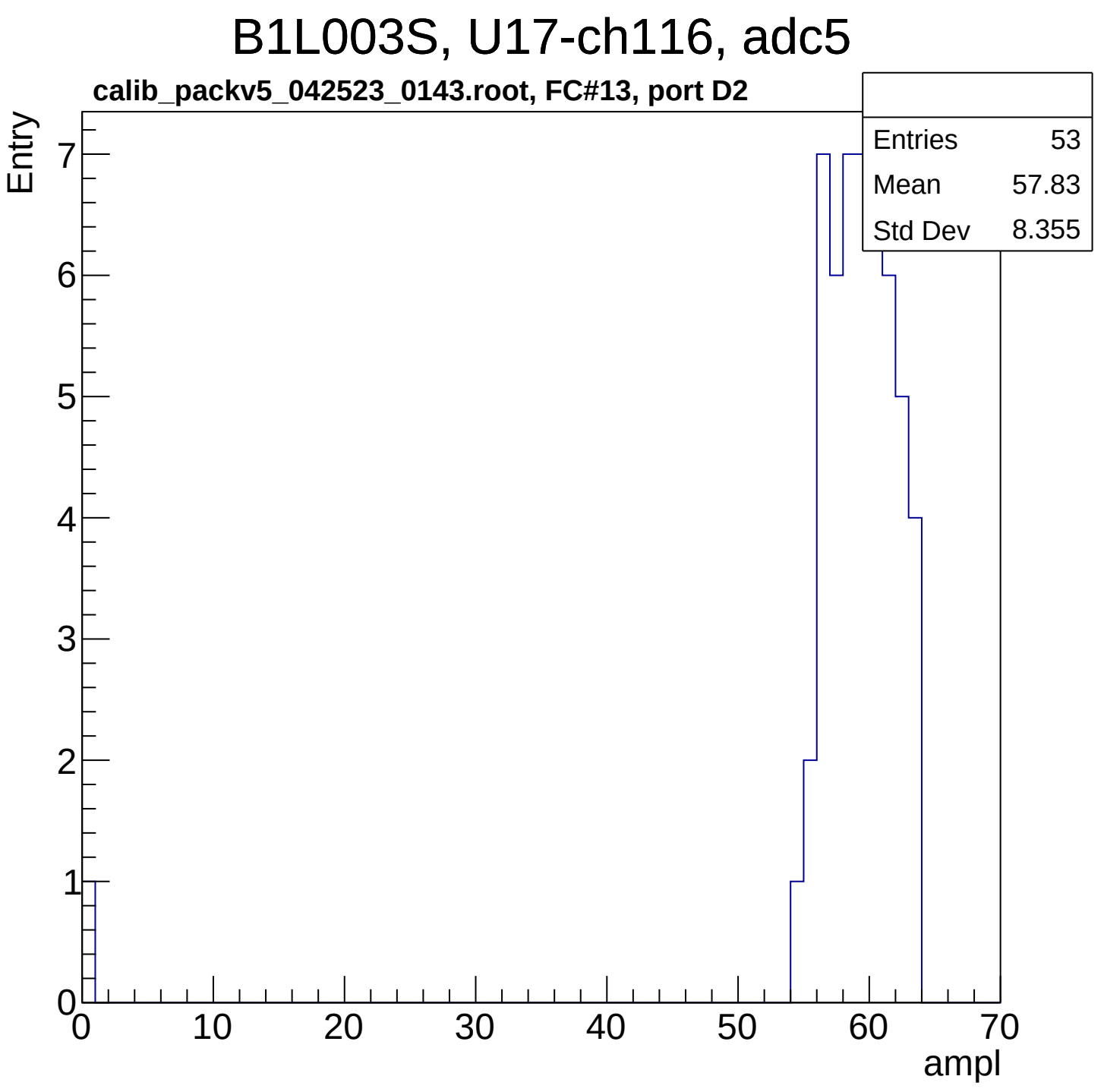

B1L003S, U17-ch116, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.17
Std Dev 3.35
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch116, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 57.83
Std Dev 8.355
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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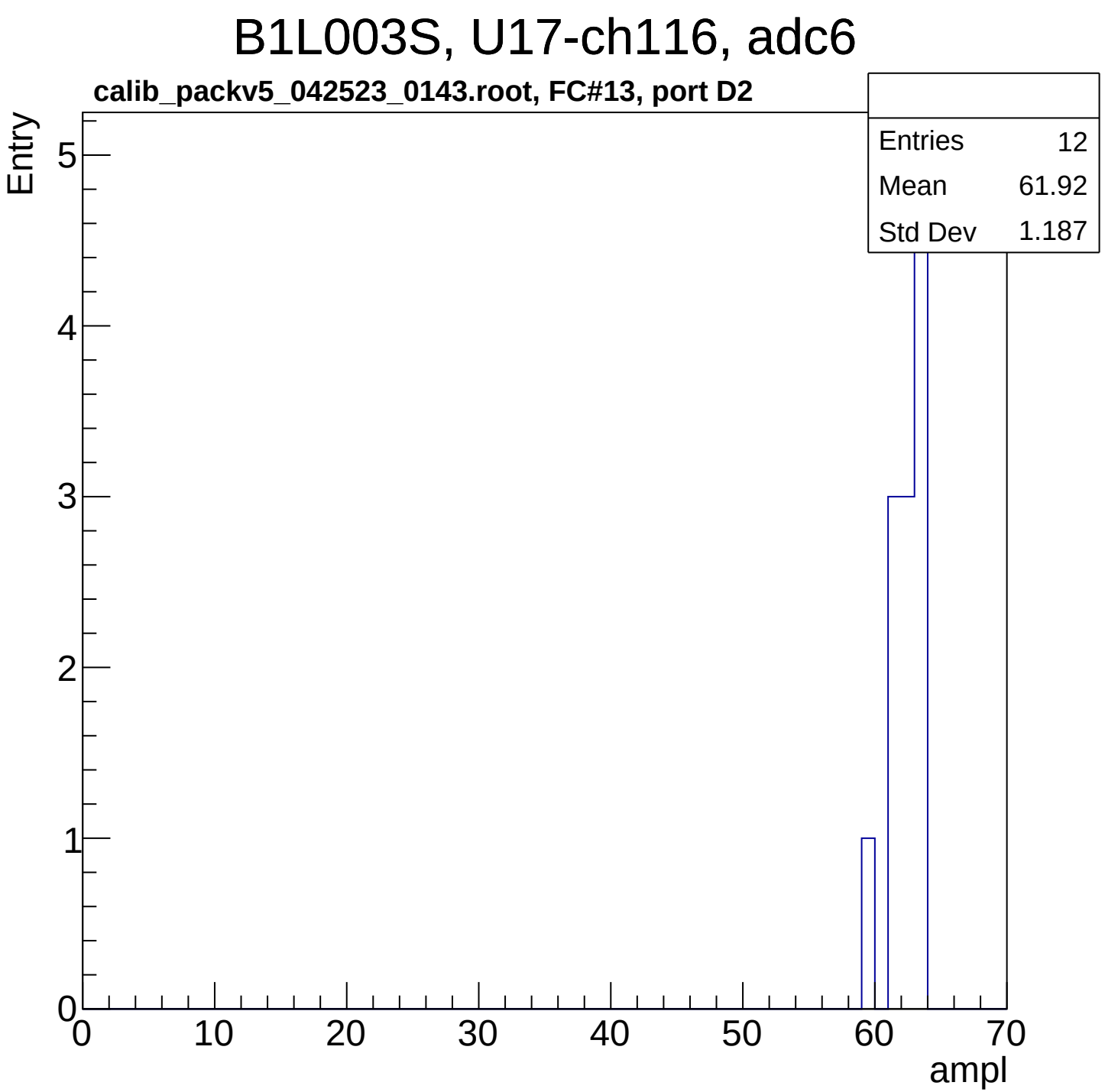
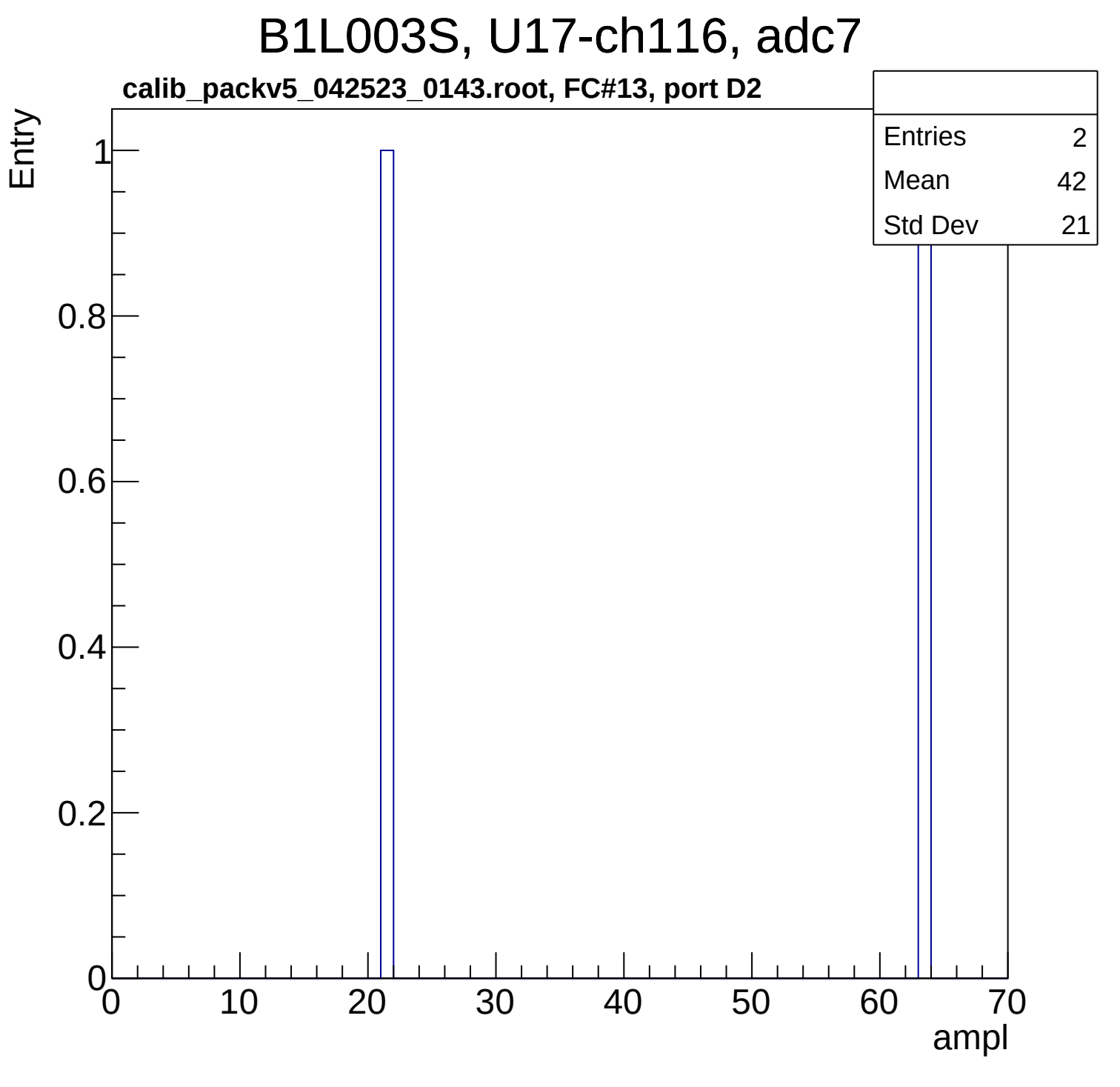
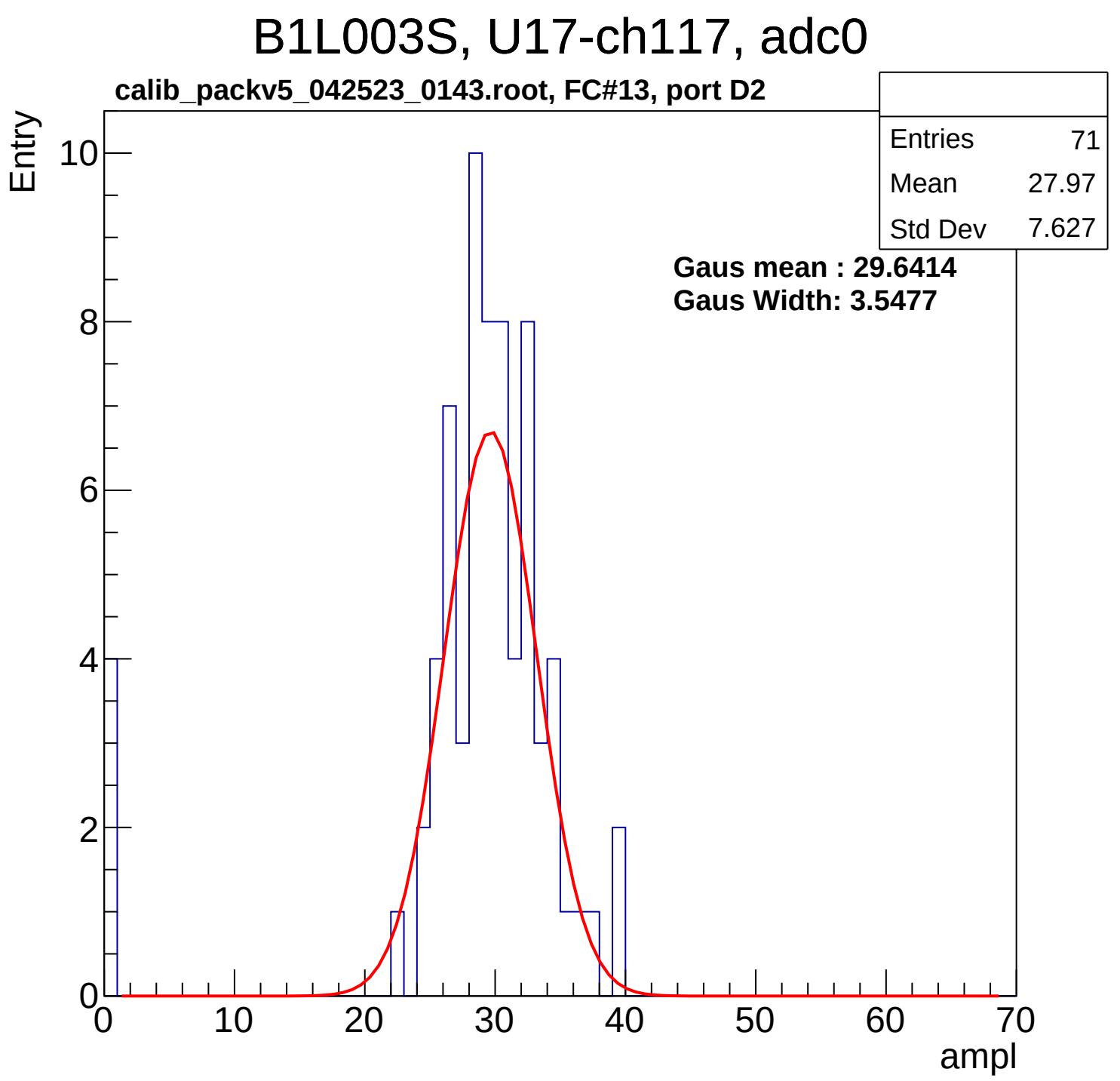
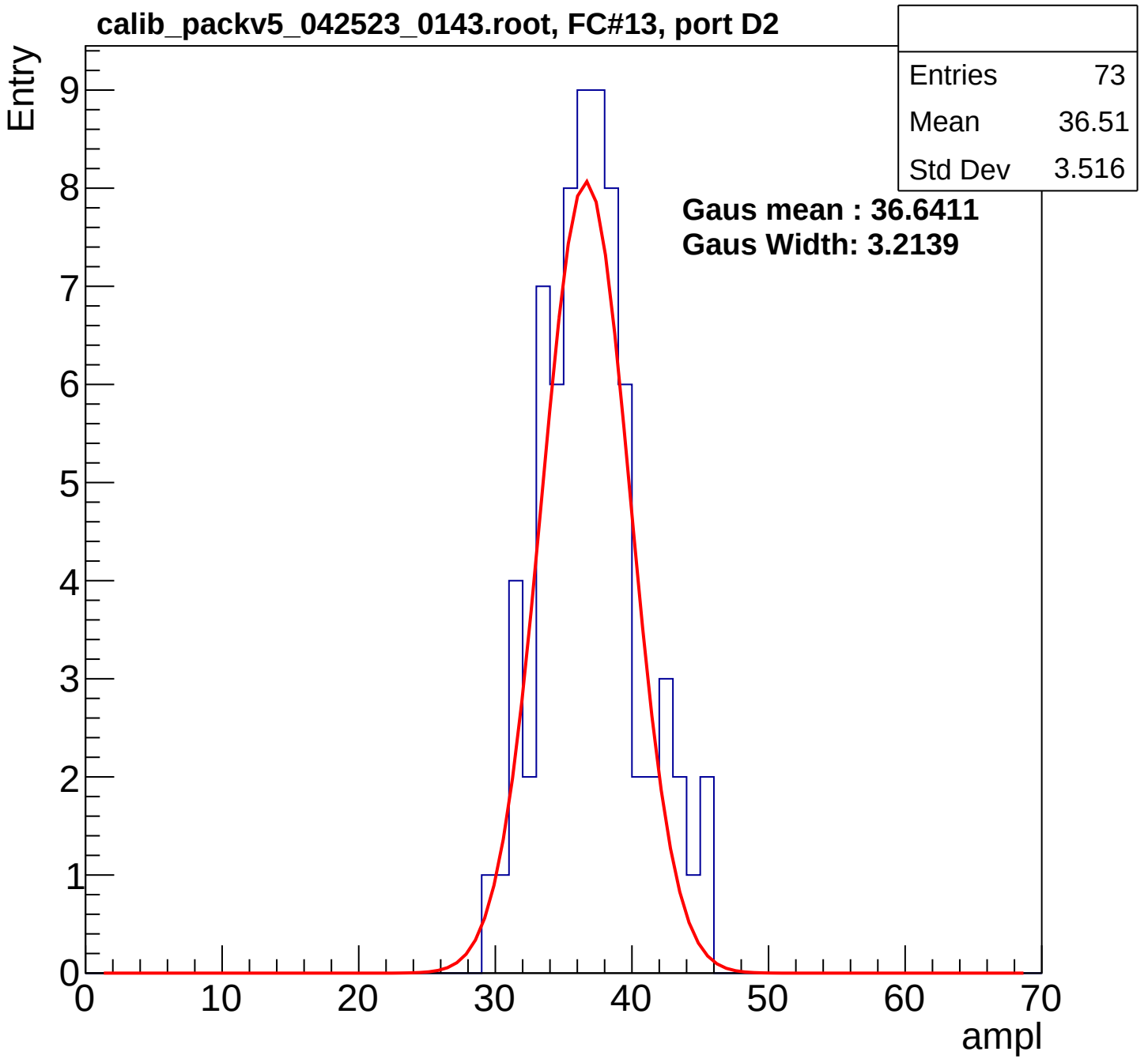
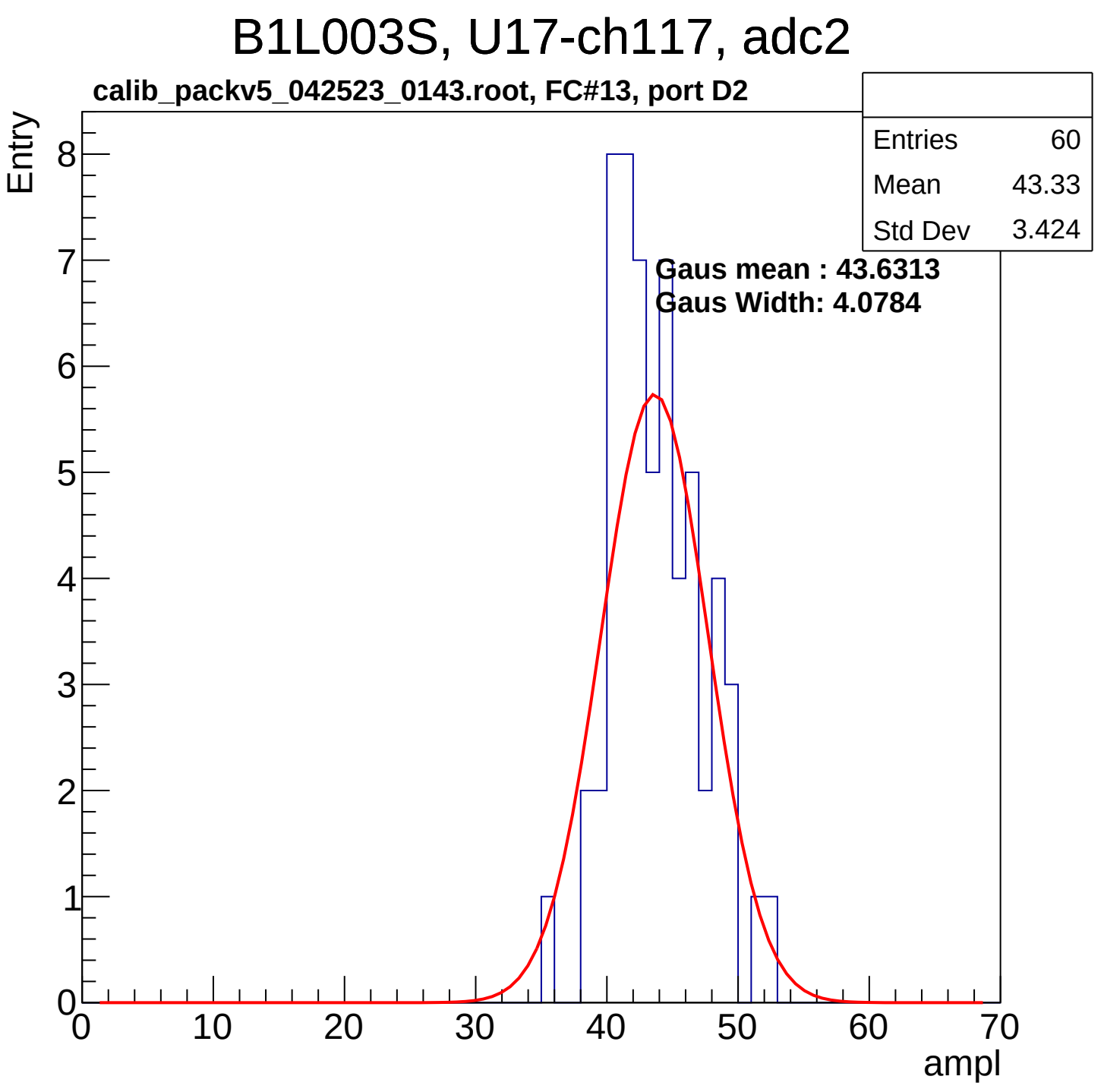
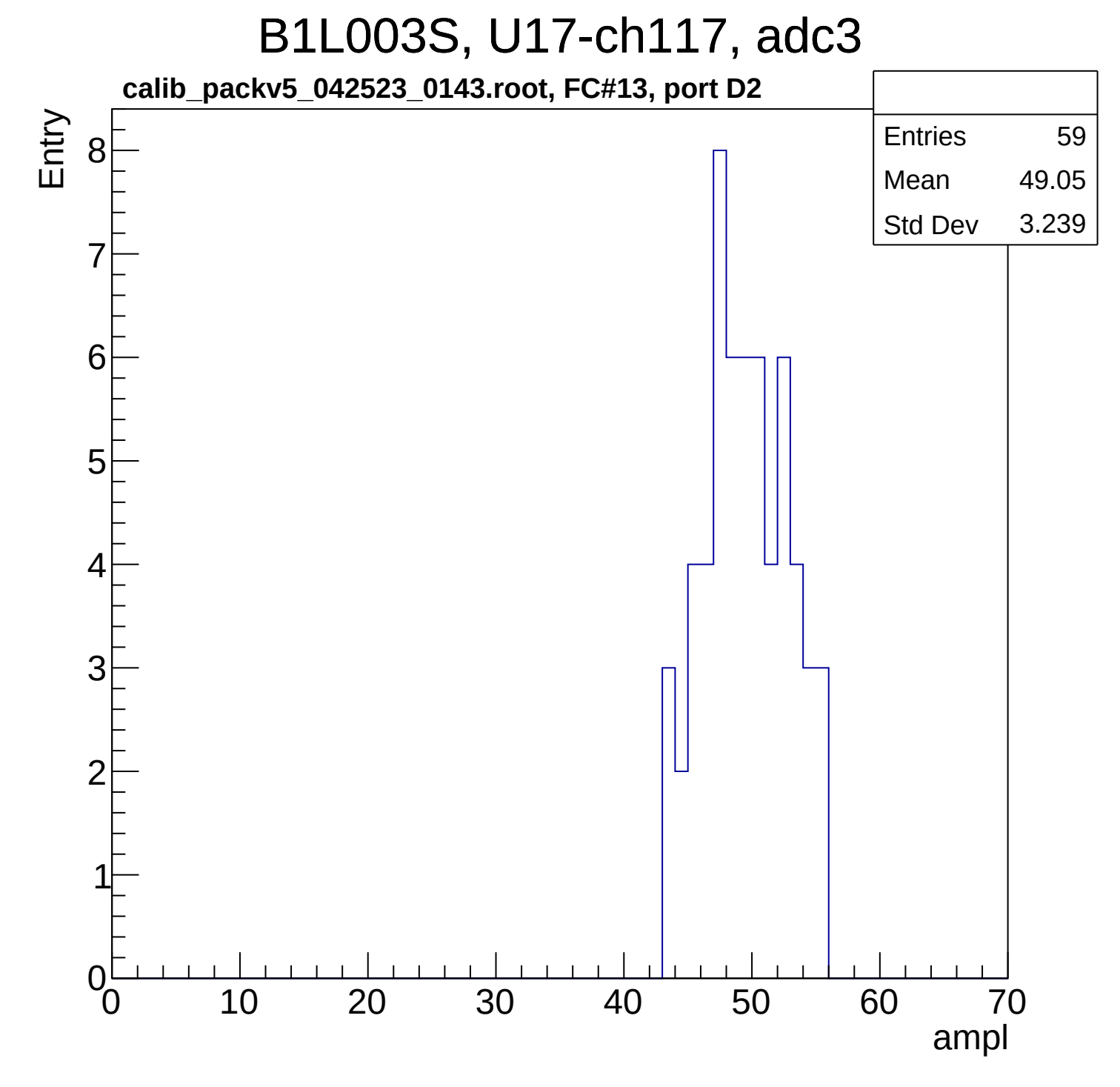

B1L003S, U17-ch116, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.92
Std Dev 1.187
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

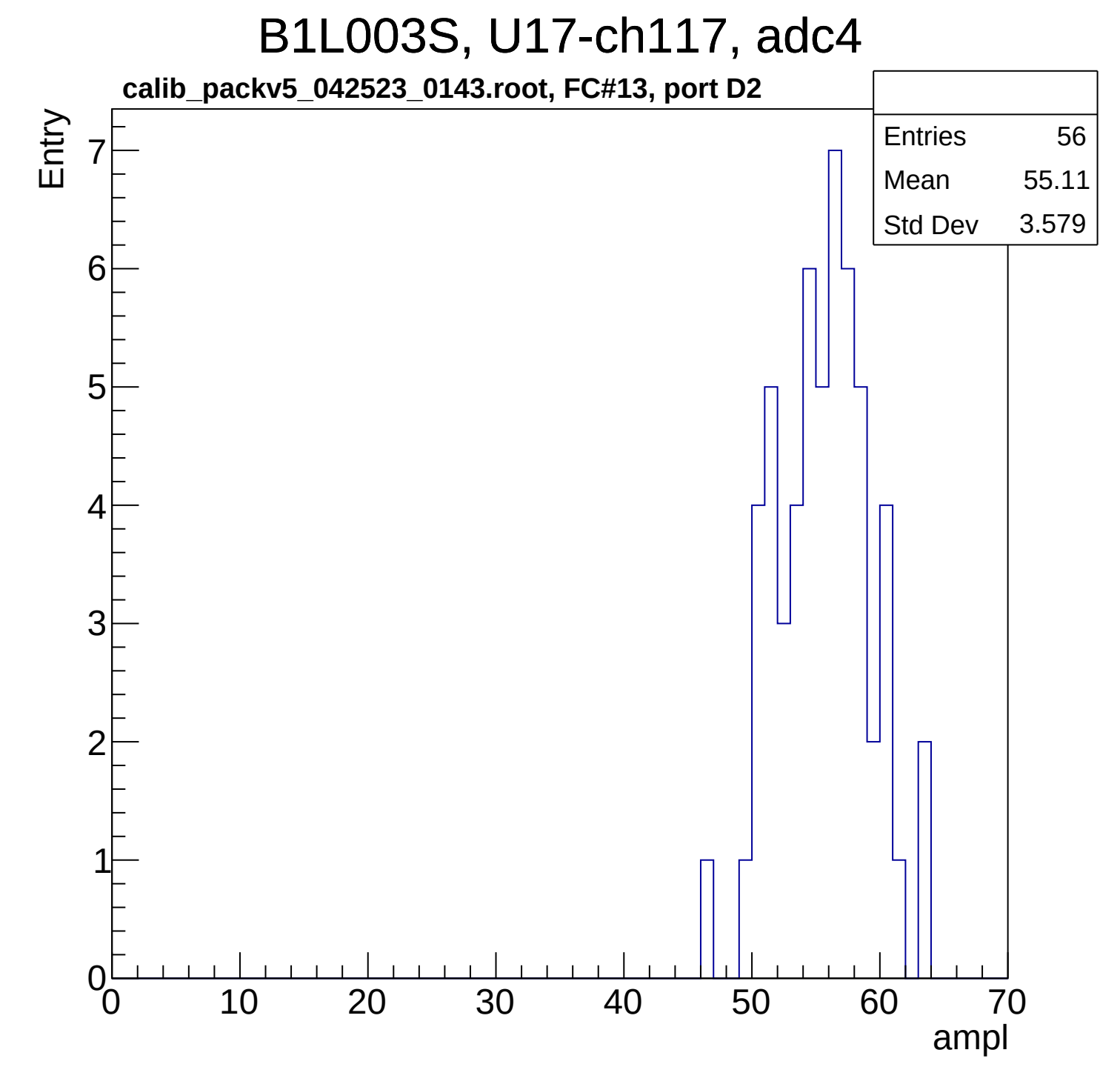

B1L003S, U17-ch116, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 42
Std Dev 21
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

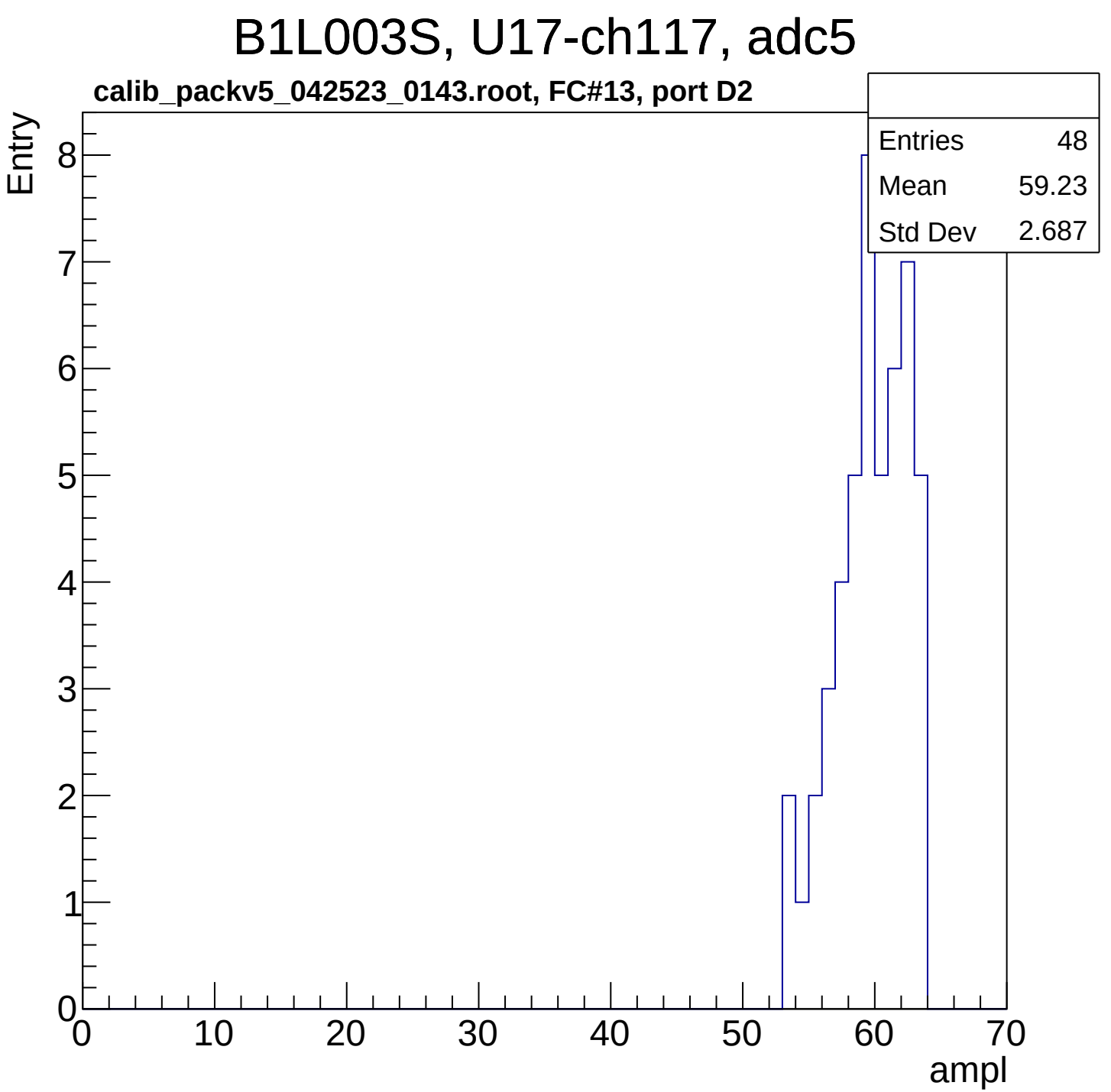

B1L003S, U17-ch117, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 27.97
Std Dev 7.627
Gaus mean : 29.6414
Gaus Width: 3.5477
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

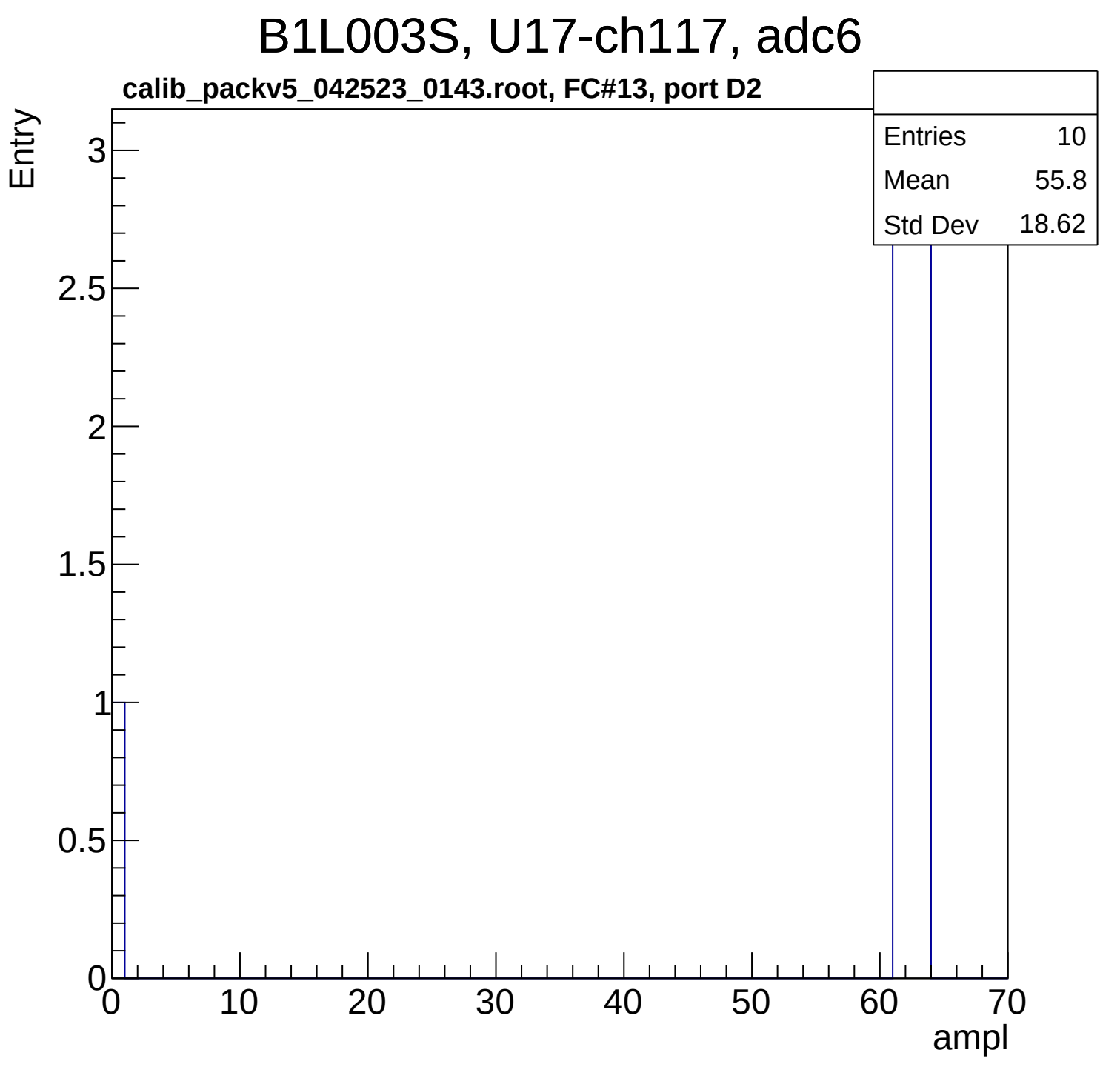

B1L003S, U17-ch117, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.51
Std Dev 3.516
Gaus mean : 36.6411
Gaus Width: 3.2139
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

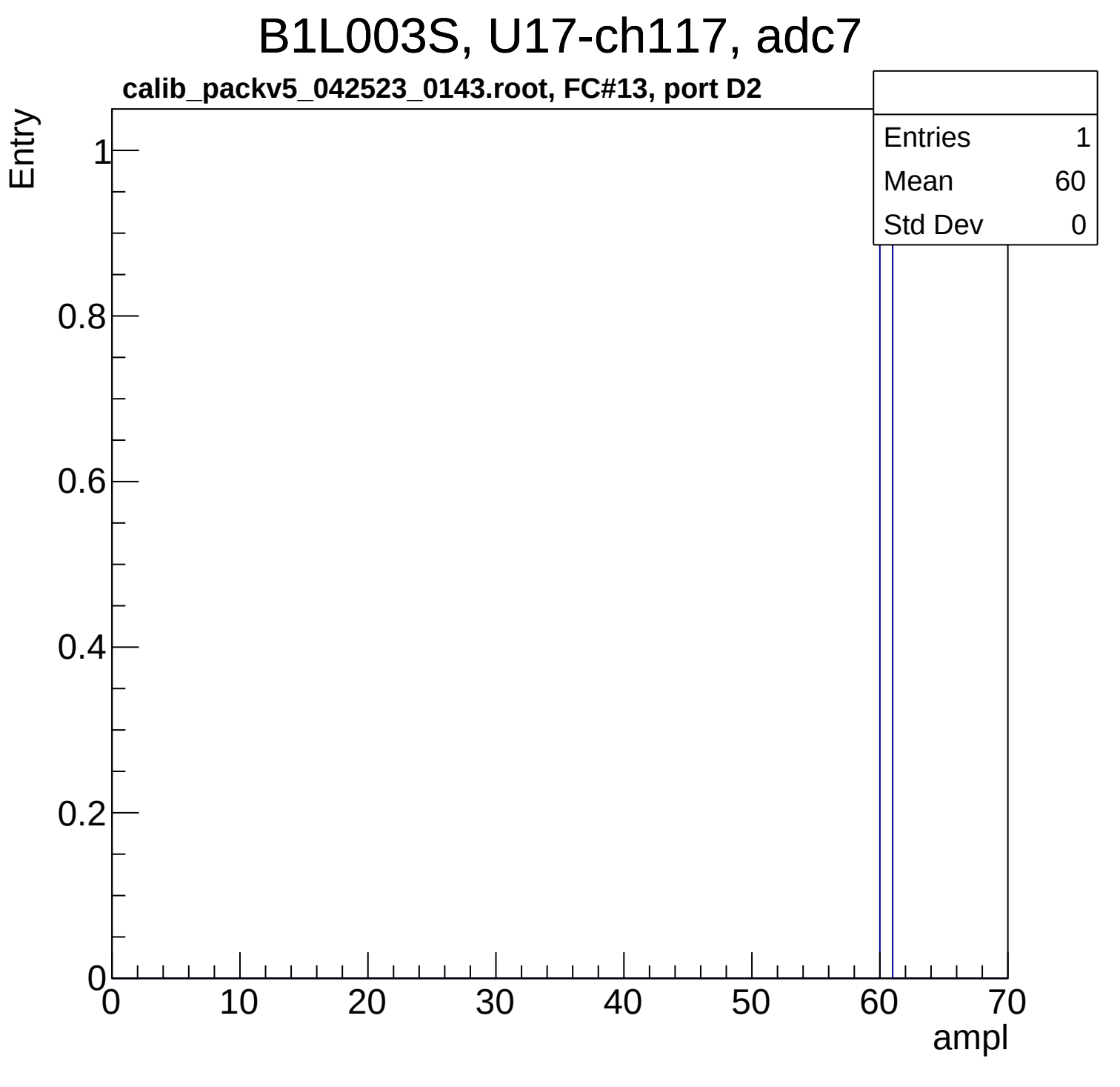

B1L003S, U17-ch117, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.33
Std Dev 3.424
Gaus mean : 43.6313
Gaus Width: 4.0784
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

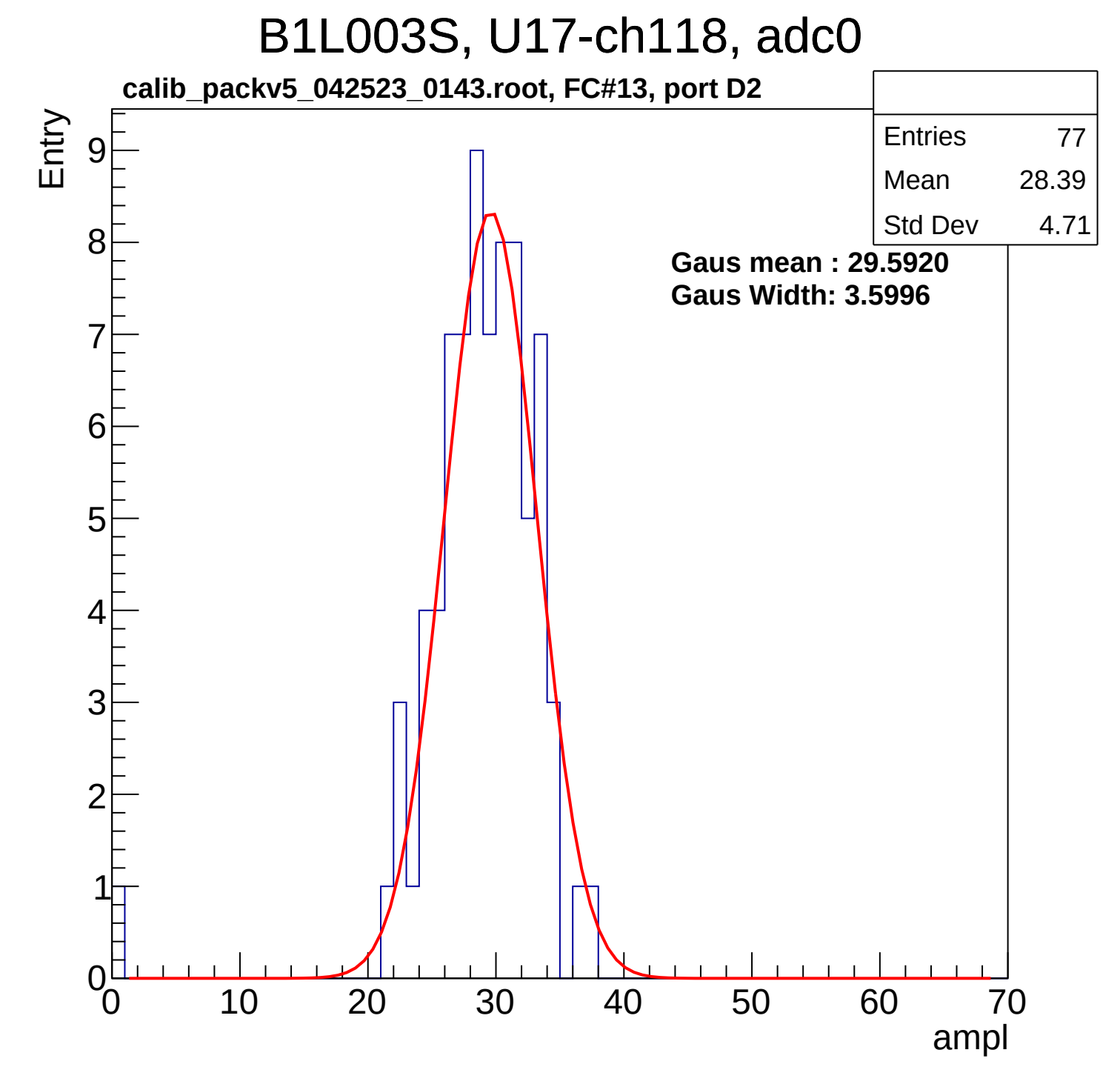

B1L003S, U17-ch117, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.05
Std Dev 3.239
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

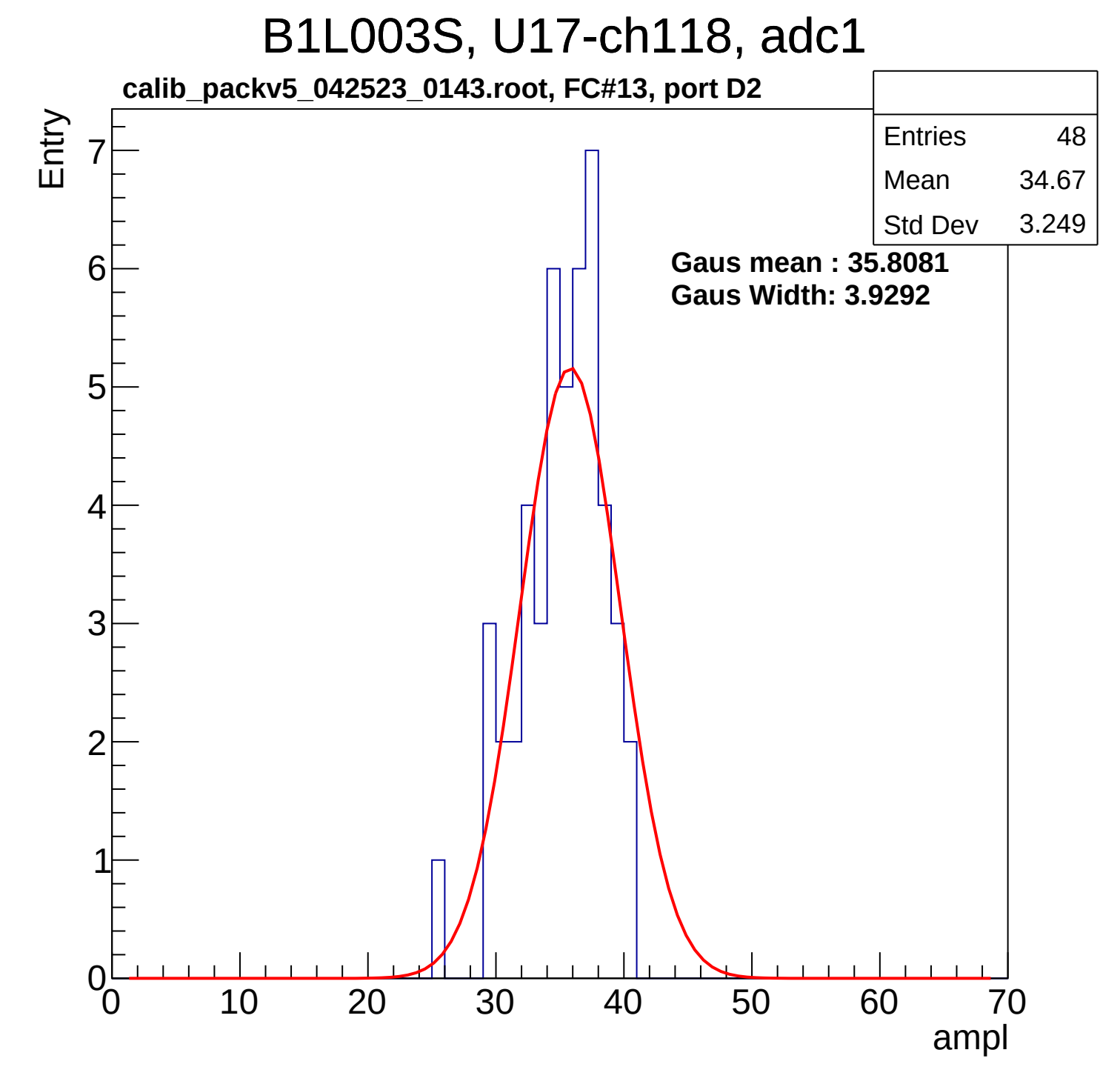

B1L003S, U17-ch117, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.11
Std Dev 3.579
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

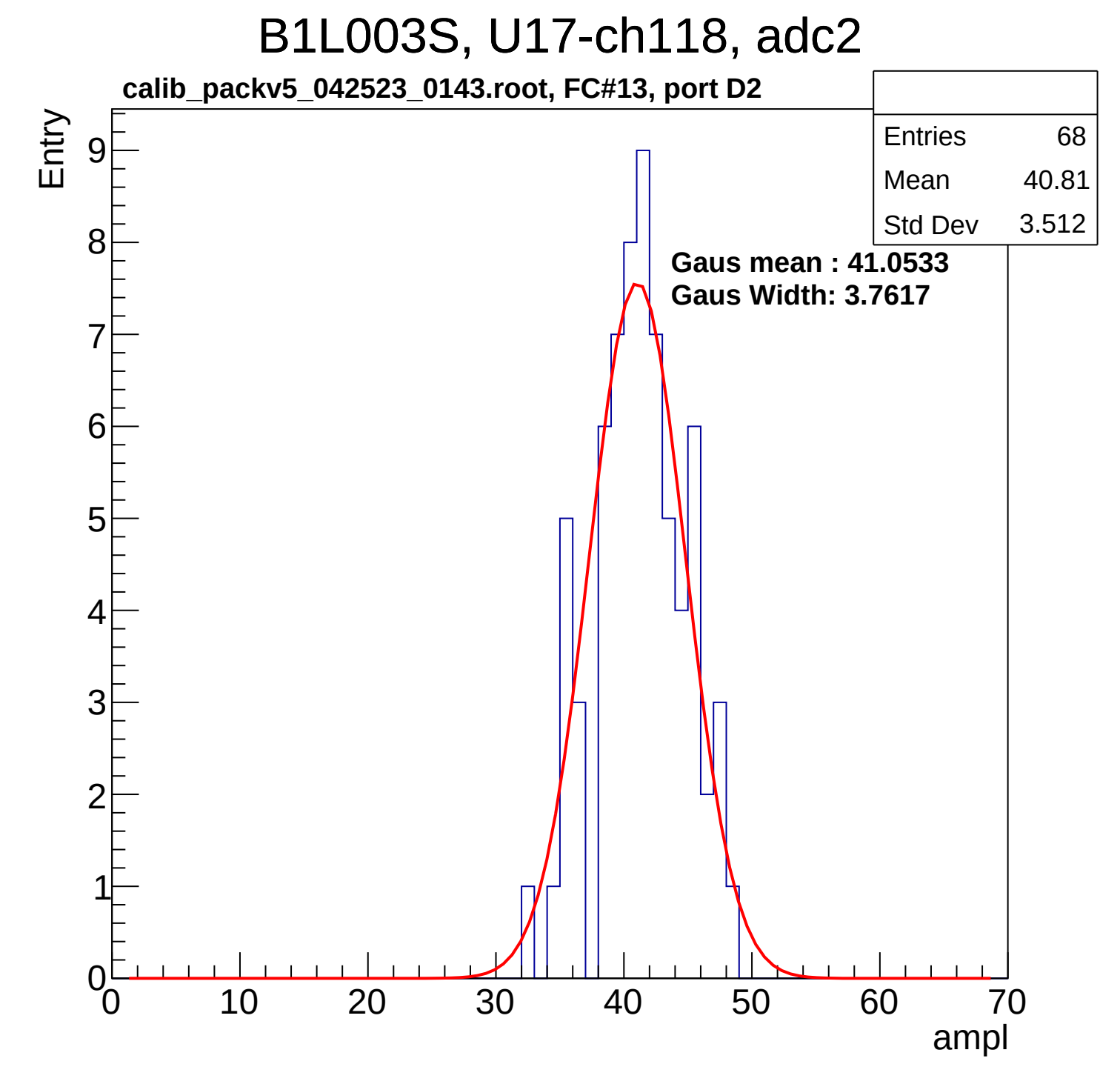

B1L003S, U17-ch117, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.23
Std Dev 2.687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

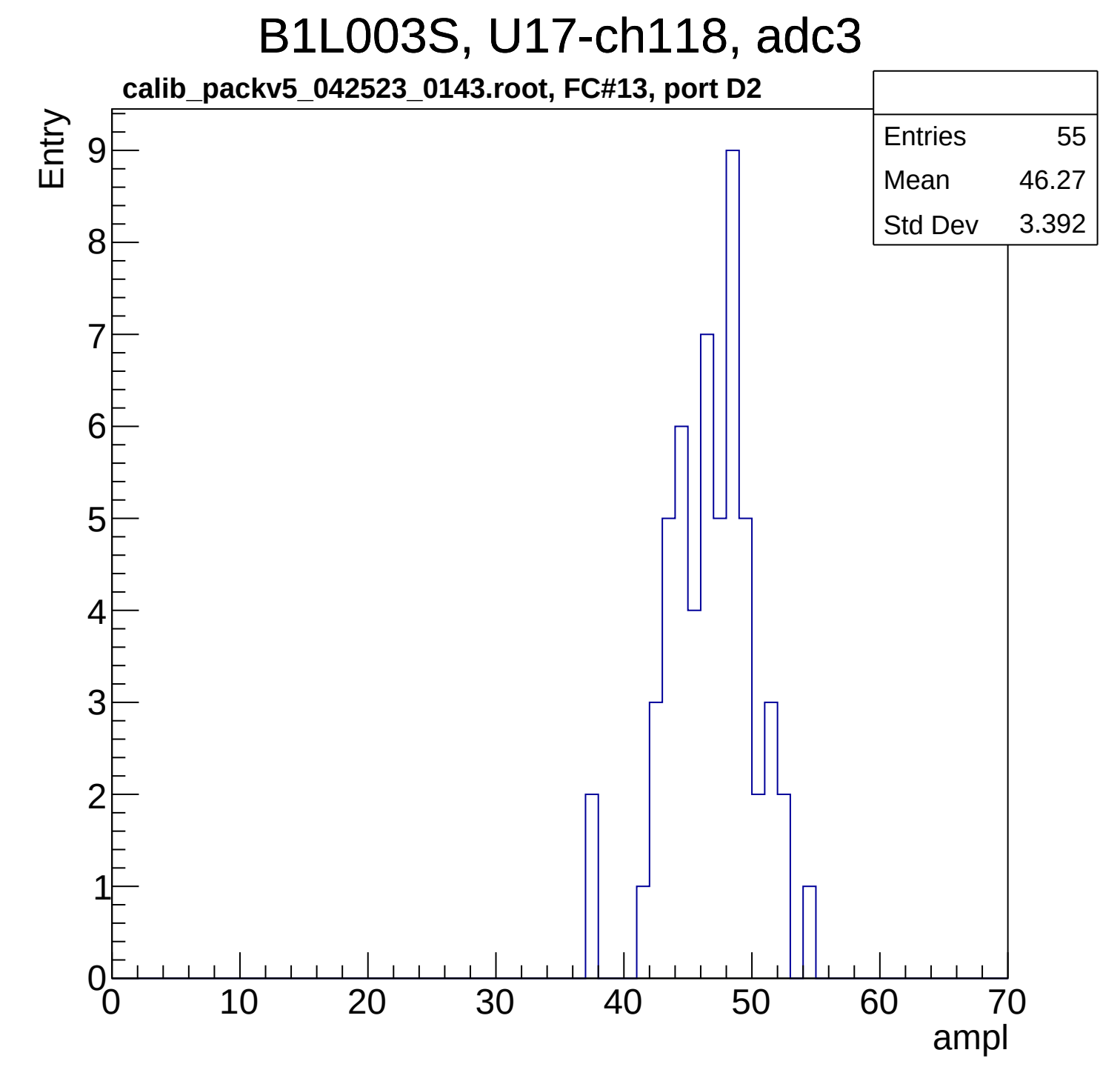

B1L003S, U17-ch117, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.8
Std Dev 18.62
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

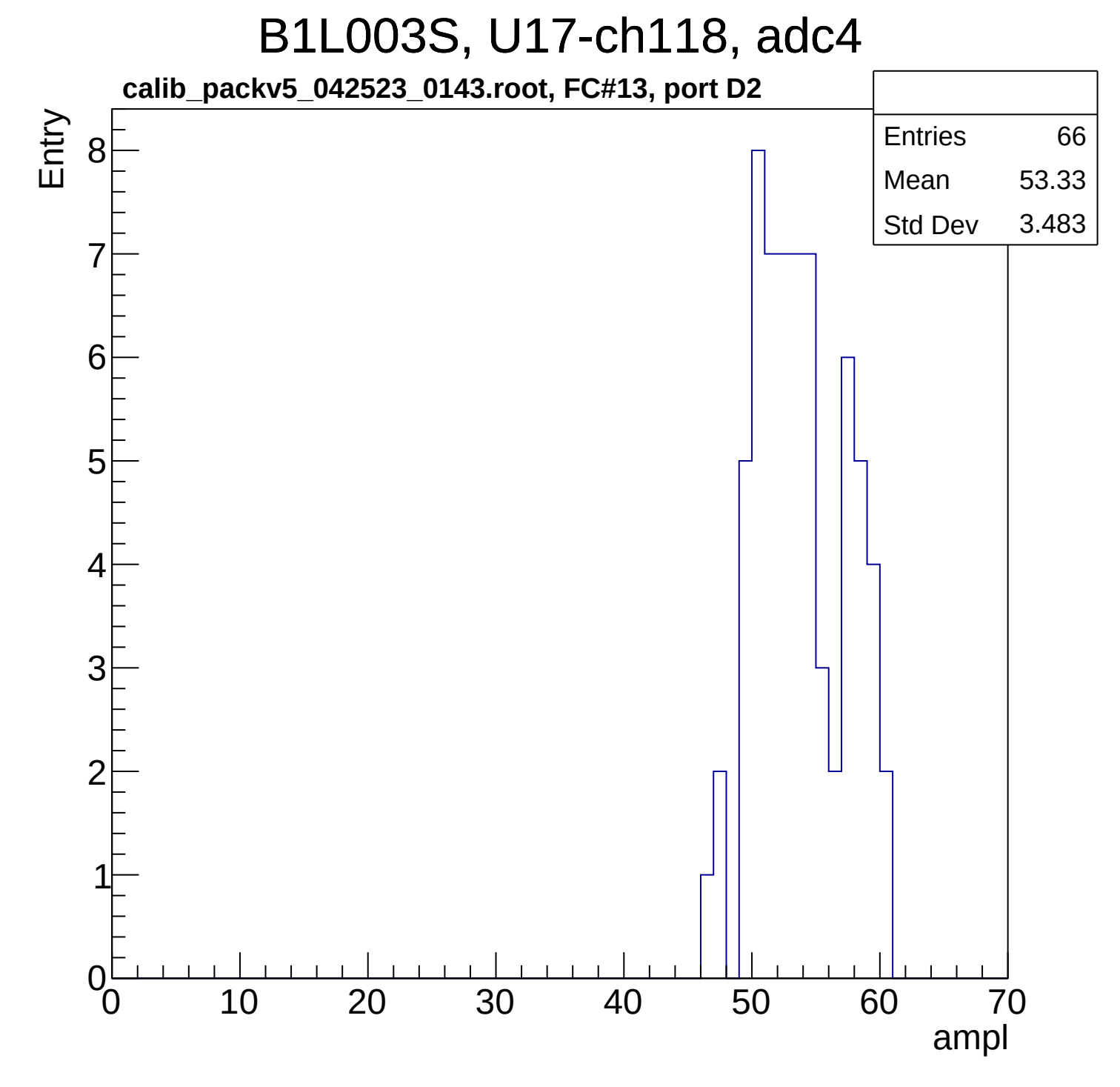

B1L003S, U17-ch117, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch118, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.39
Std Dev 4.71
Gaus mean : 29.5920
Gaus Width: 3.5996
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch118, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 34.67
Std Dev 3.249
Gaus mean : 35.8081
Gaus Width: 3.9292
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch118, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 40.81
Std Dev 3.512
Gaus mean : 41.0533
Gaus Width: 3.7617
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch118, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 46.27
Std Dev 3.392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch118, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 53.33
Std Dev 3.483
Entry
ampl
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1
2
3
4
5
6
7
8
0
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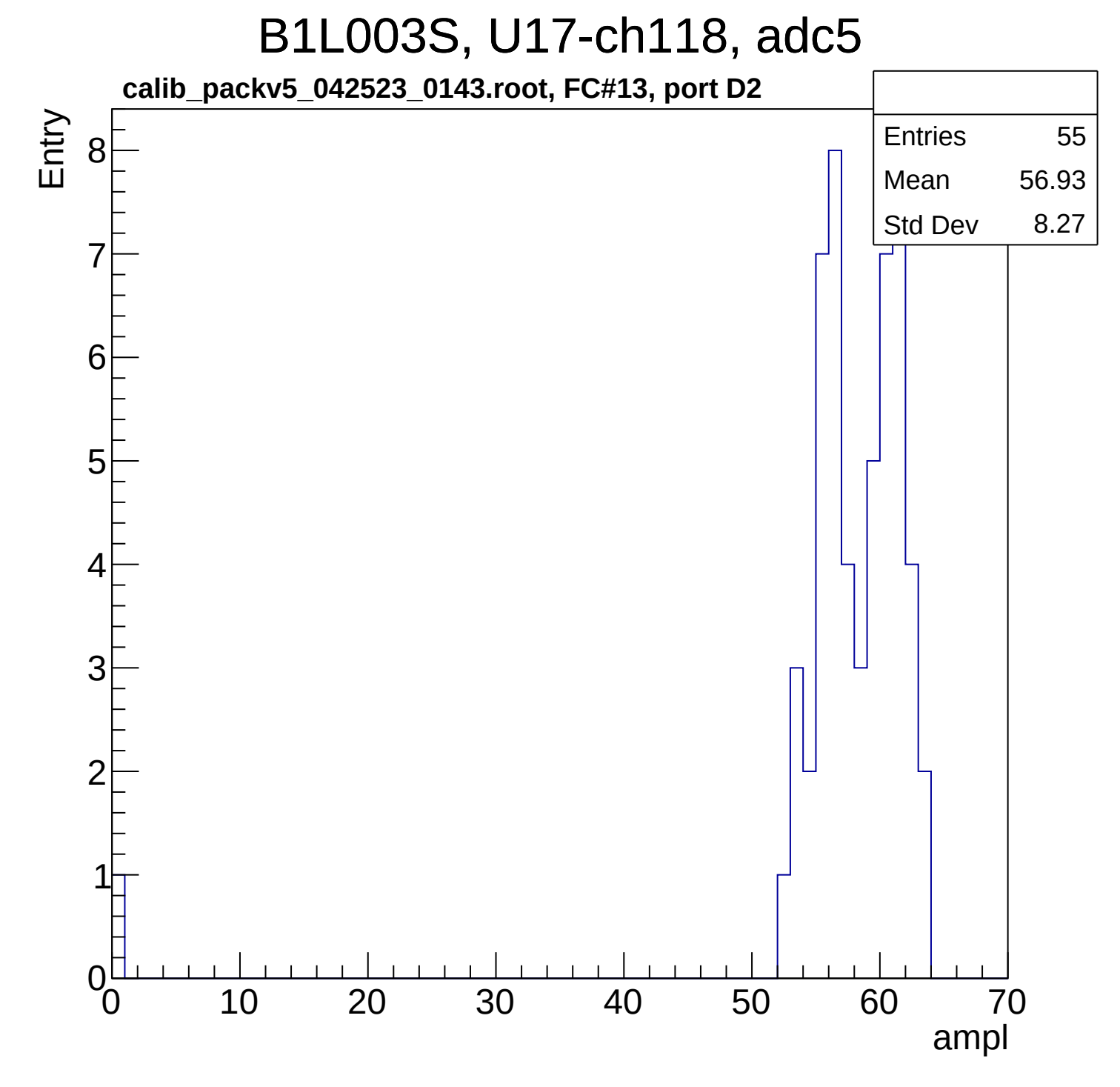
20
30
40
50
60
70


B1L003S, U17-ch118, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 56.93
Std Dev 8.27
Entry
ampl
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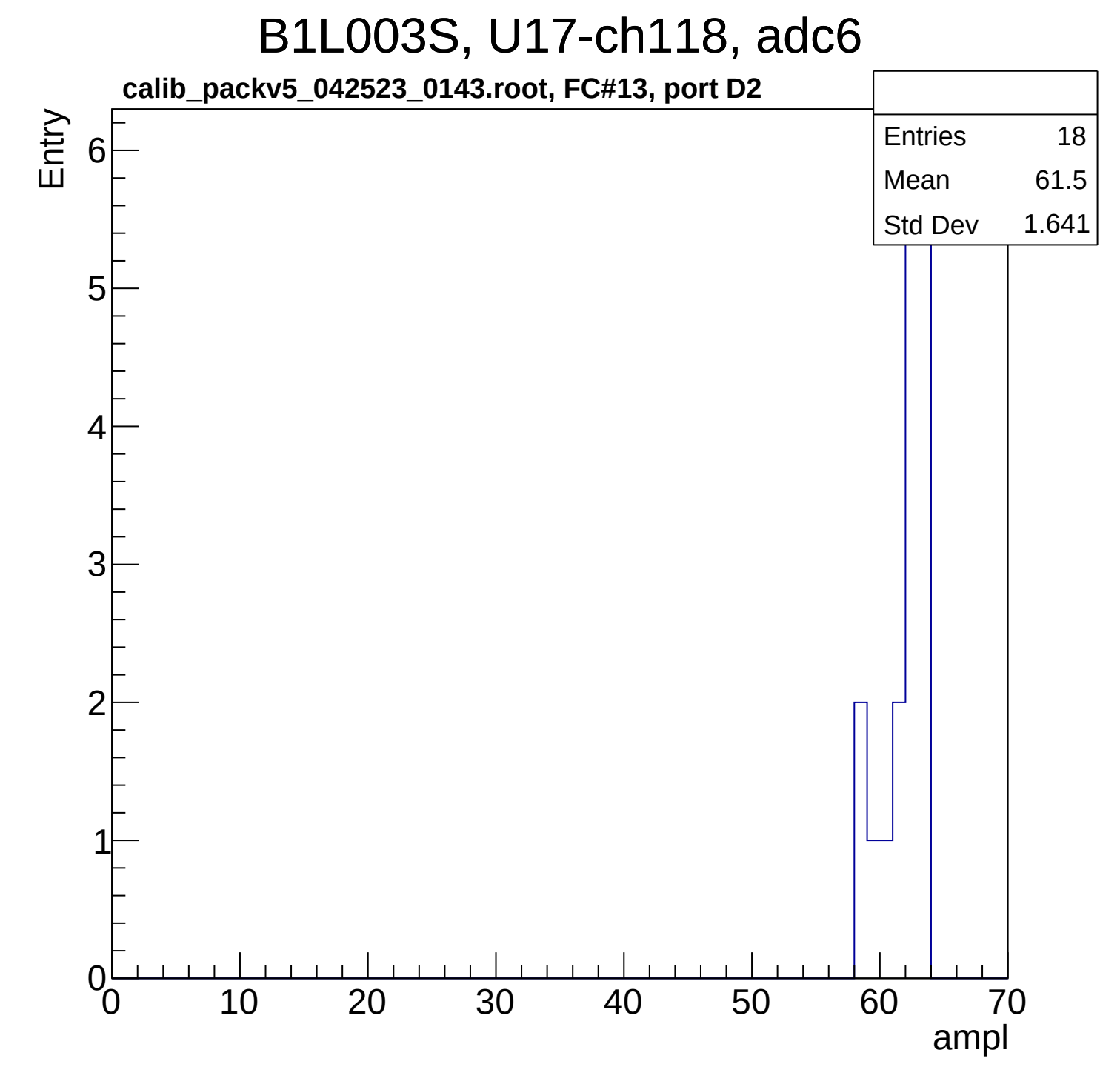
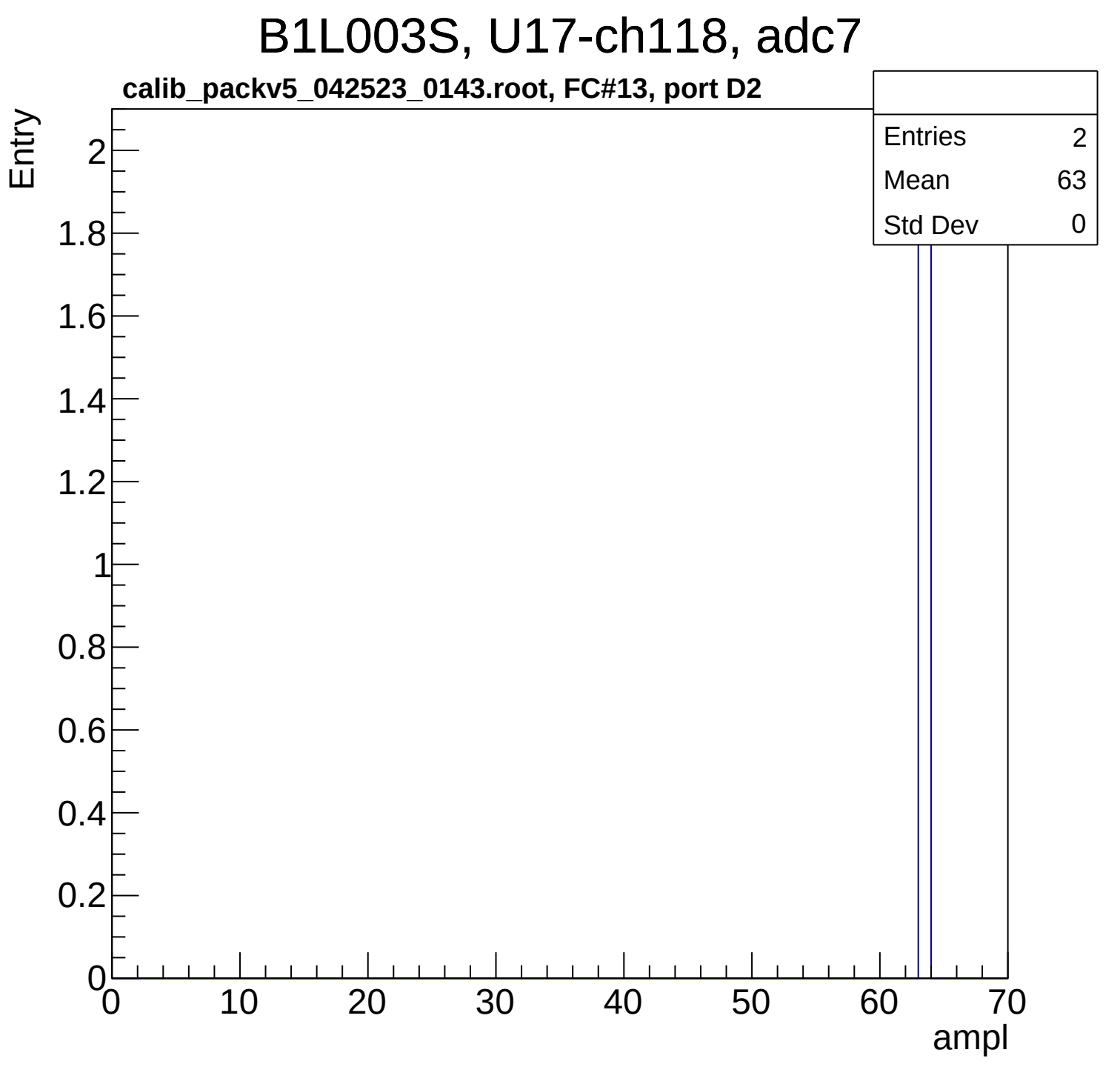
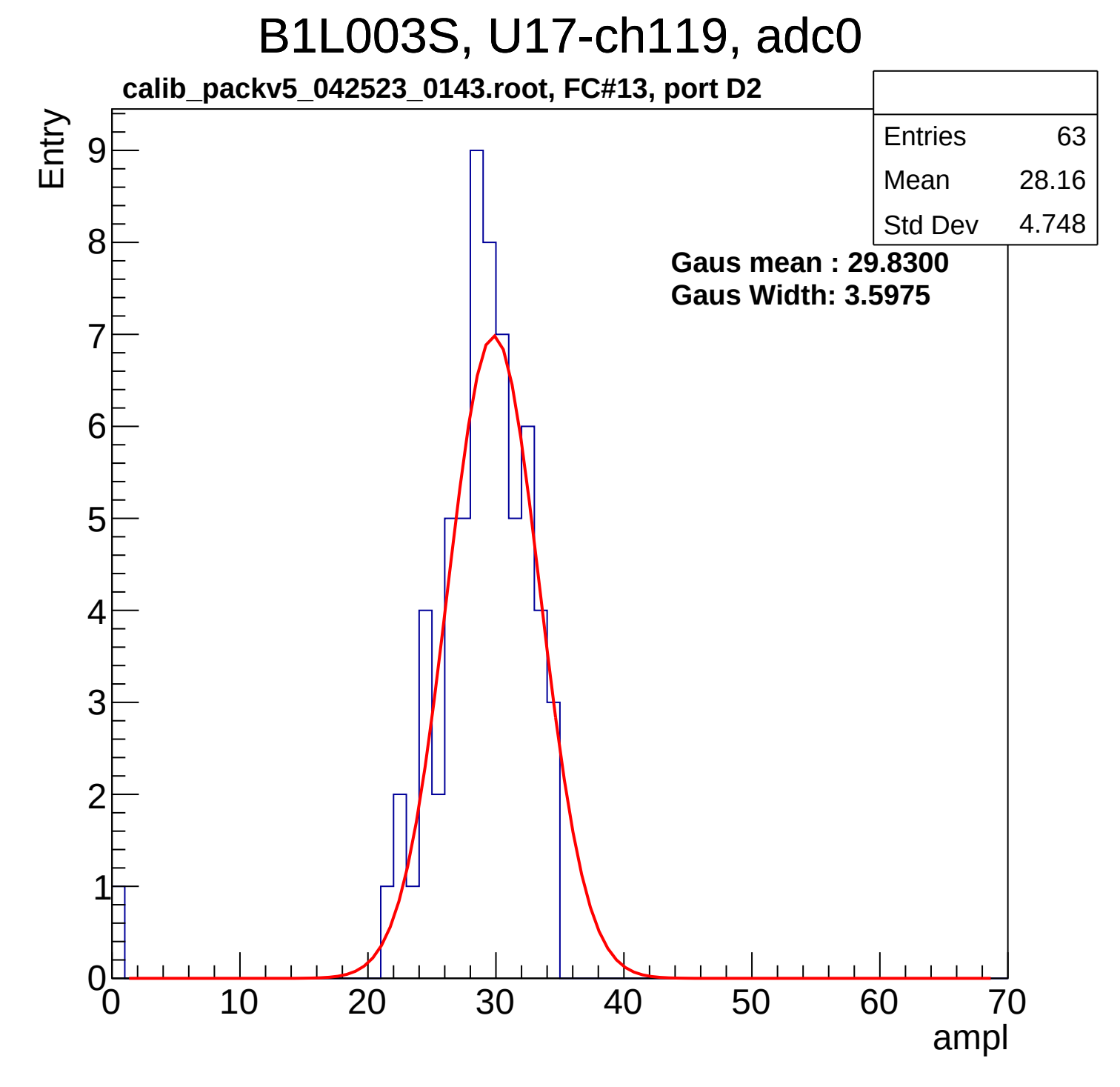
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch118, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 61.5
Std Dev 1.641
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch118, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U17-ch119, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.16
Std Dev 4.748
Gaus mean : 29.8300
Gaus Width: 3.5975
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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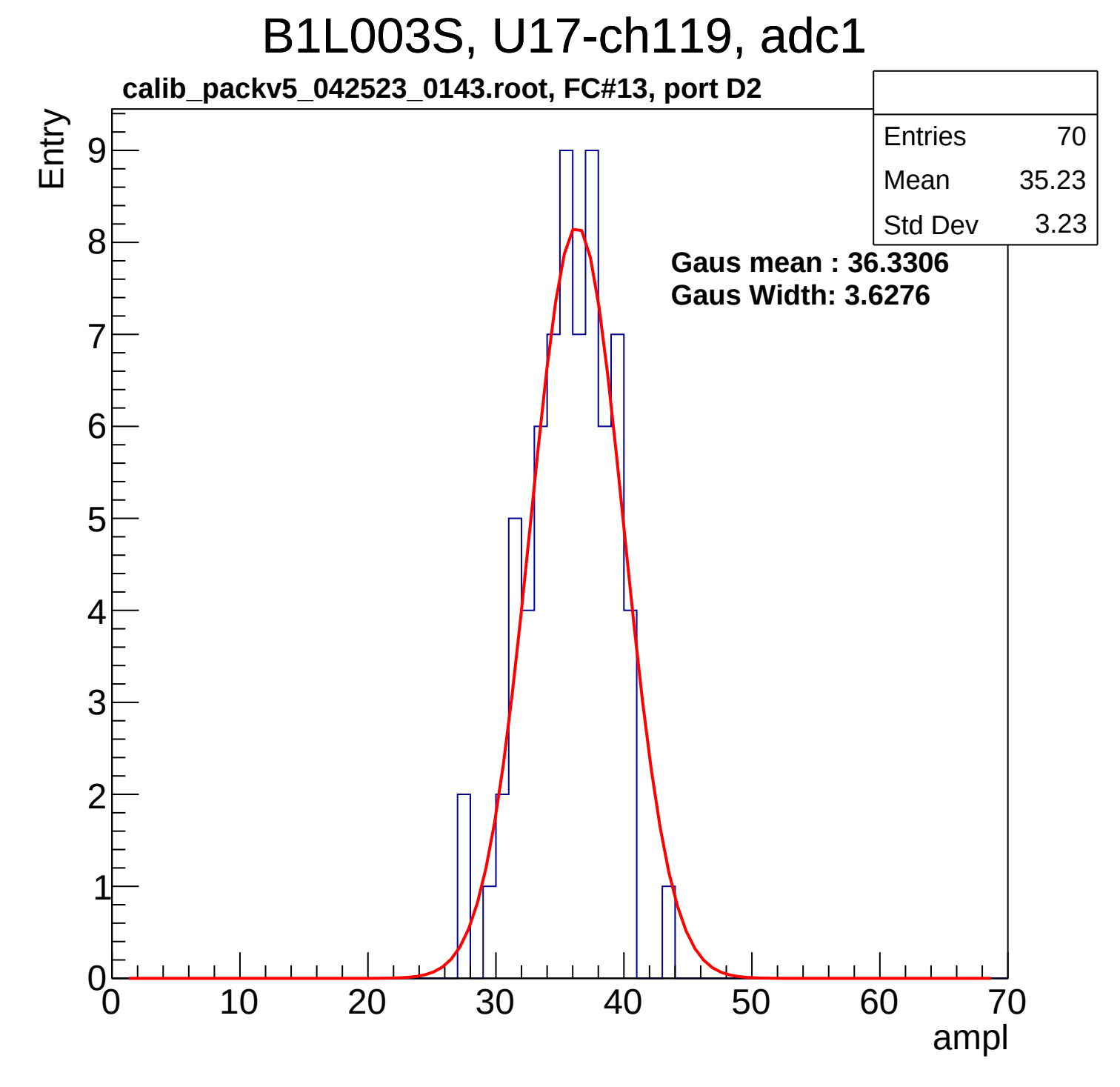
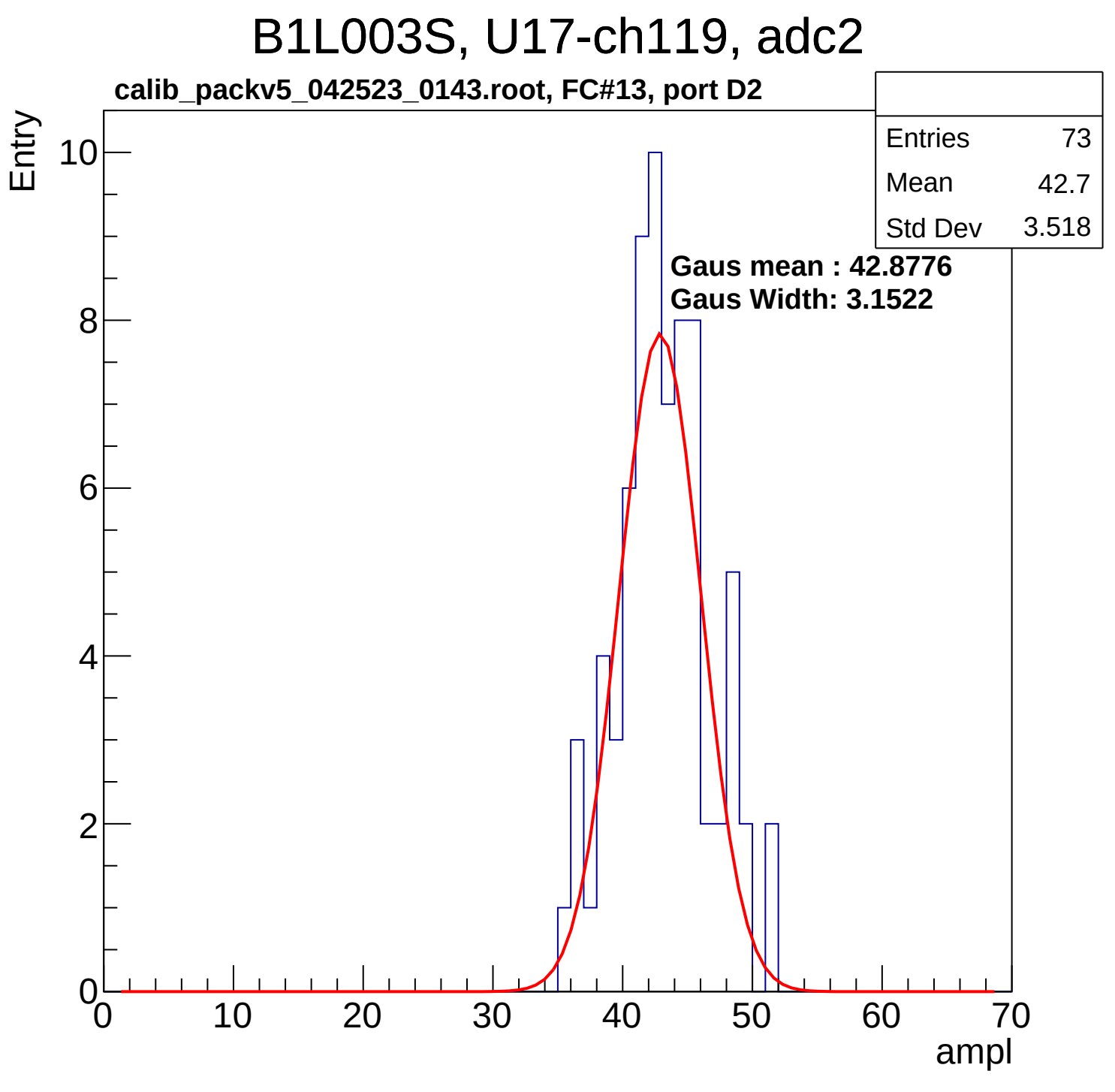
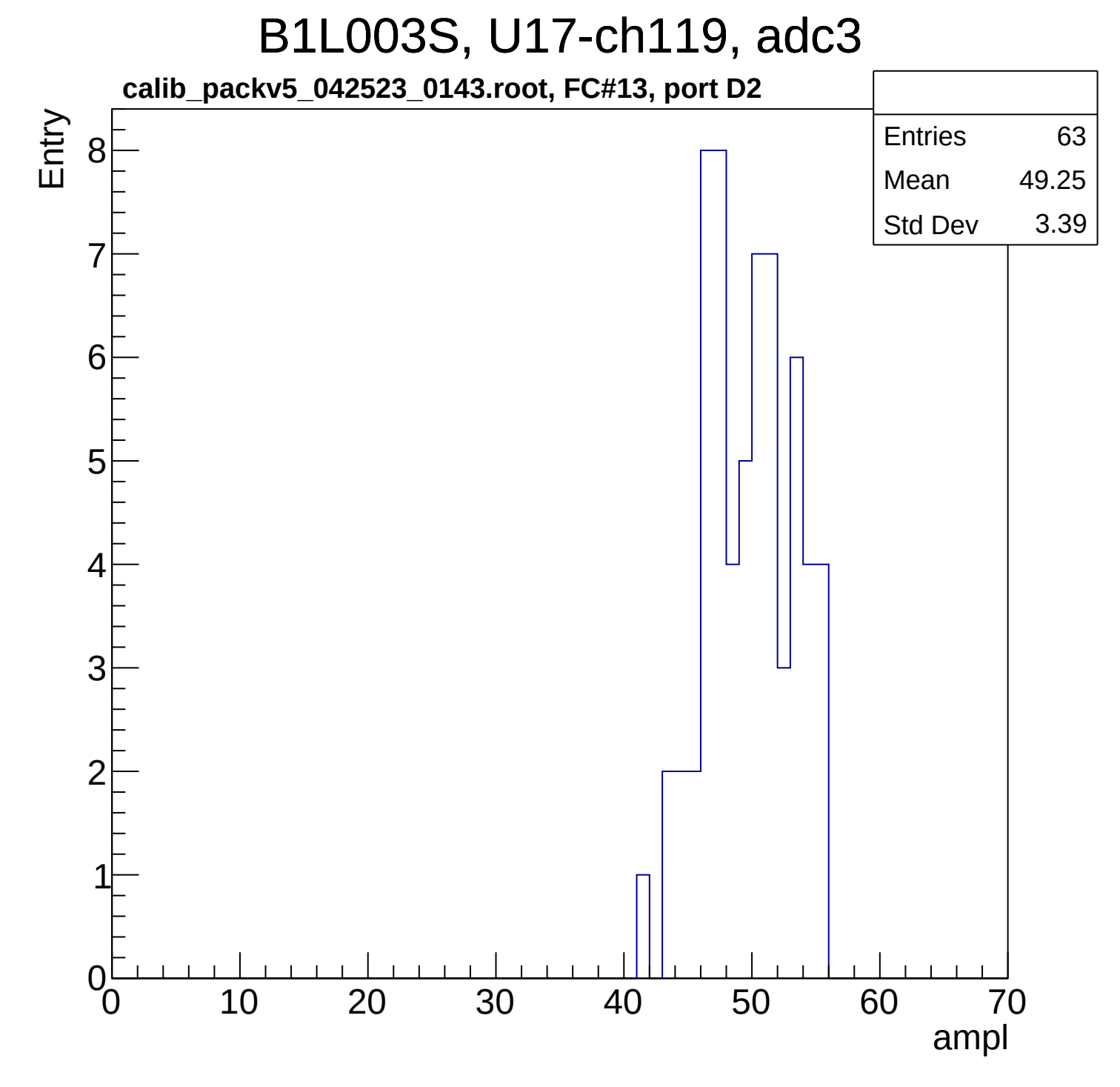

B1L003S, U17-ch119, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 35.23
Std Dev 3.23
Gaus mean : 36.3306
Gaus Width: 3.6276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch119, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 42.7
Std Dev 3.518
Gaus mean : 42.8776
Gaus Width: 3.1522
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch119, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.25
Std Dev 3.39
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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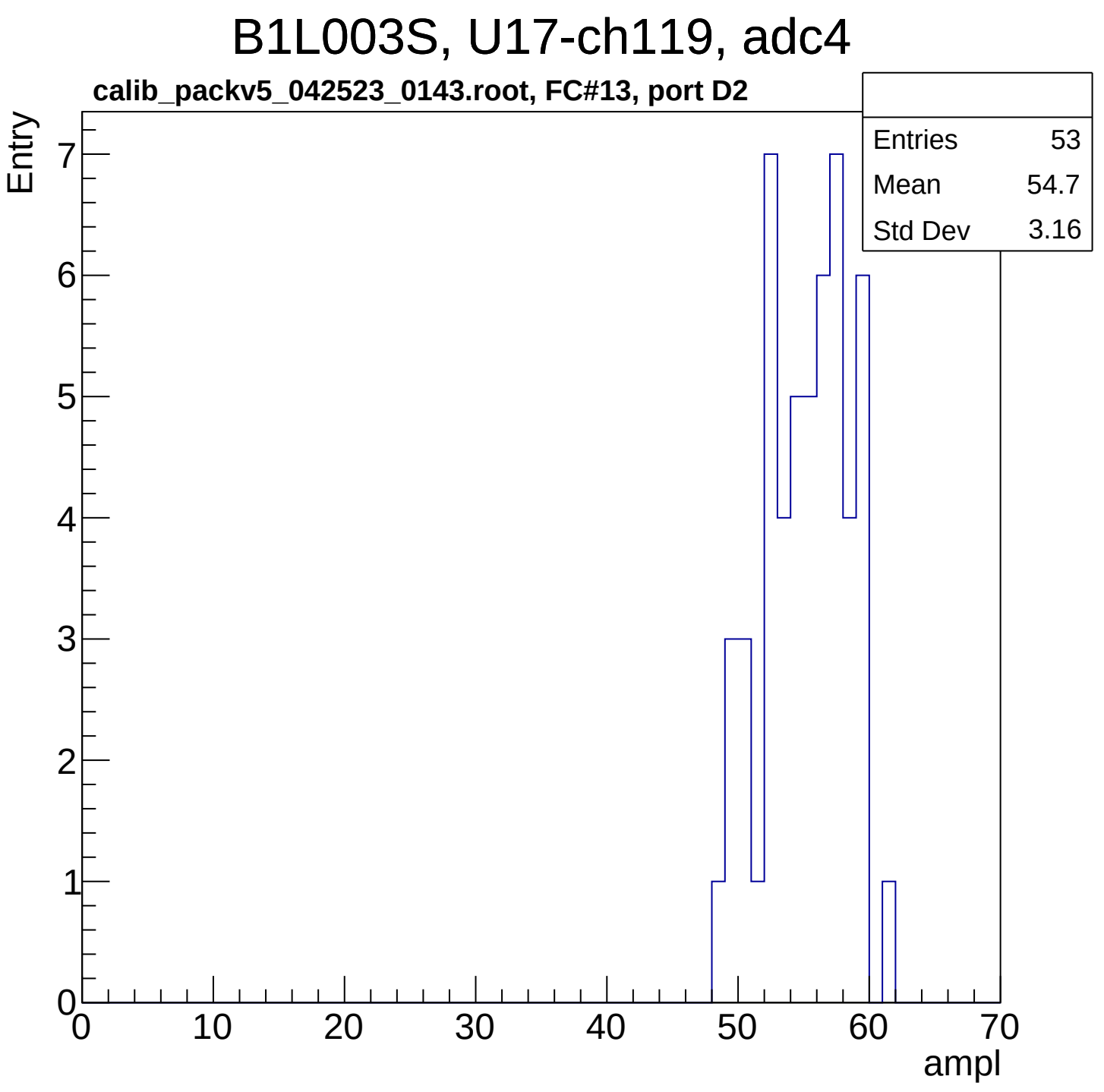
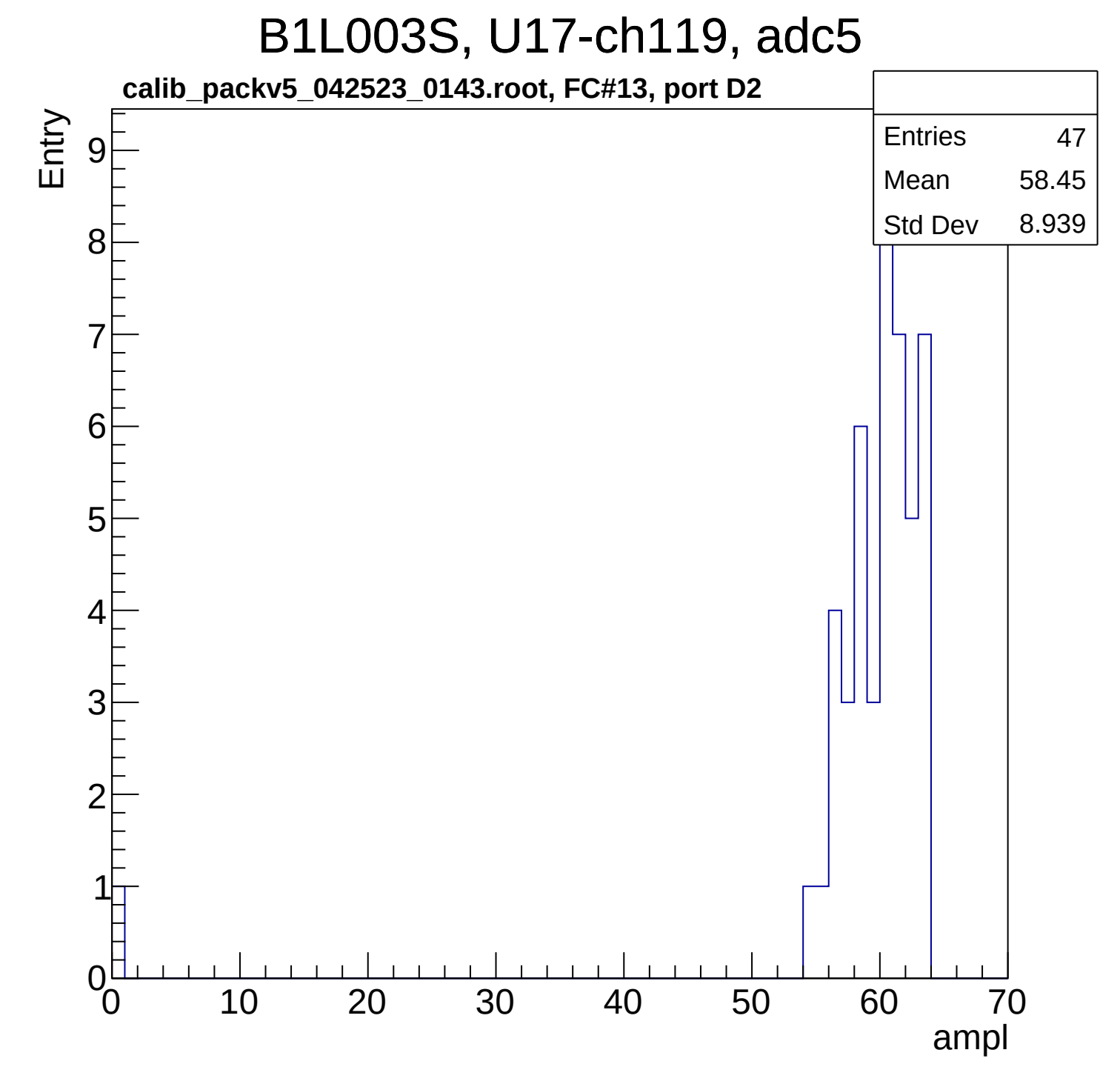

B1L003S, U17-ch119, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.7
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch119, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.45
Std Dev 8.939
Entry
ampl
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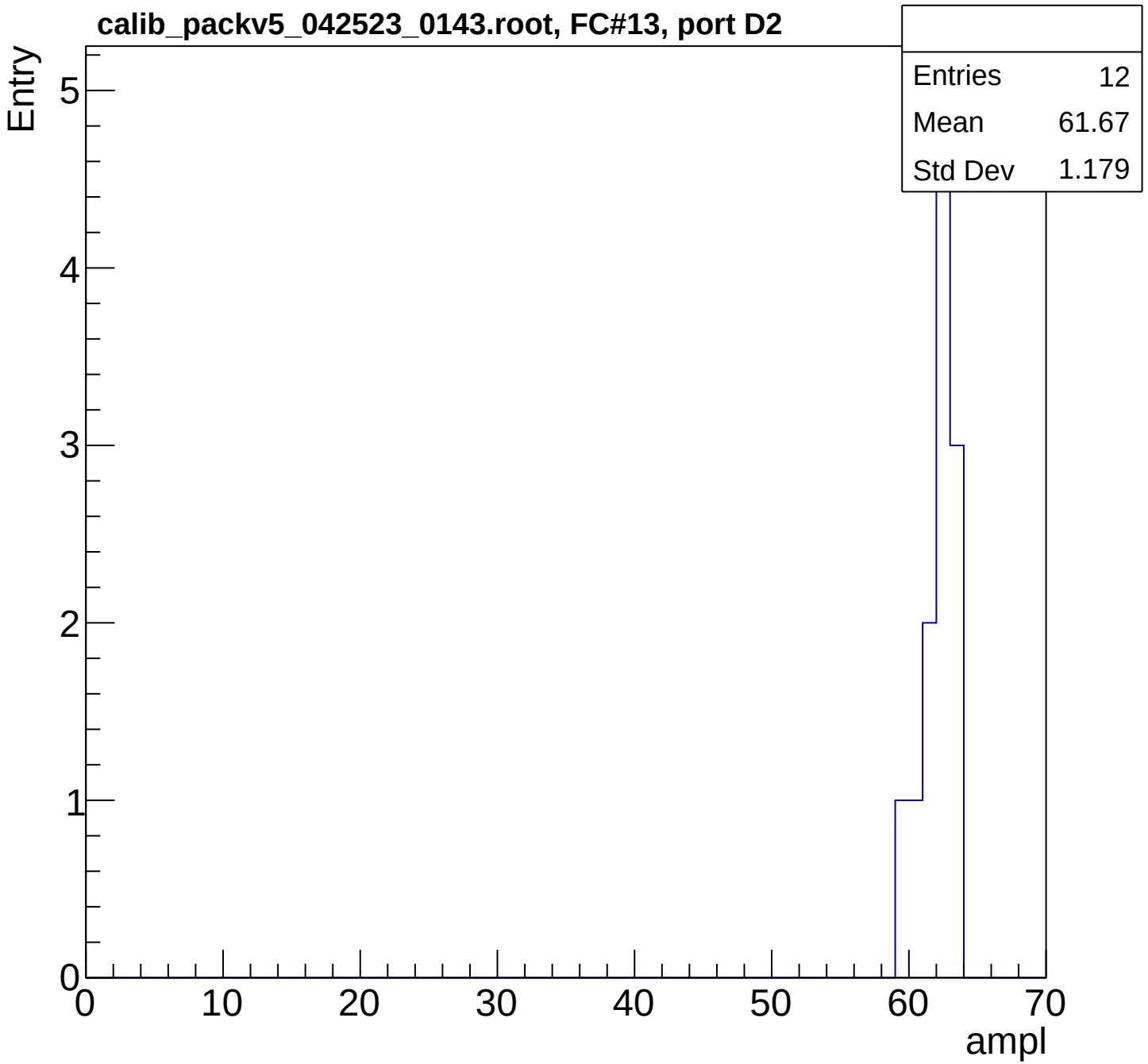
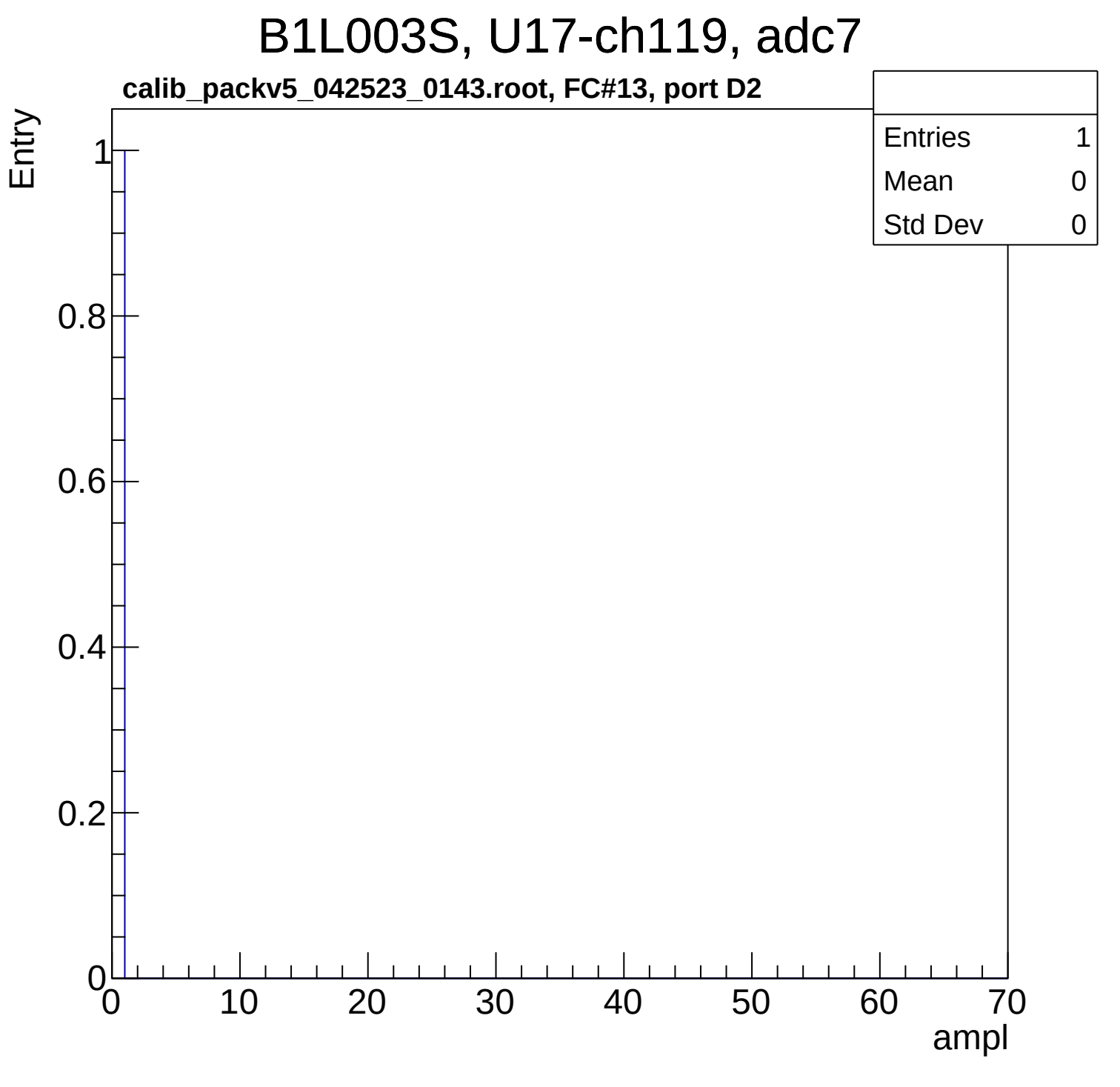
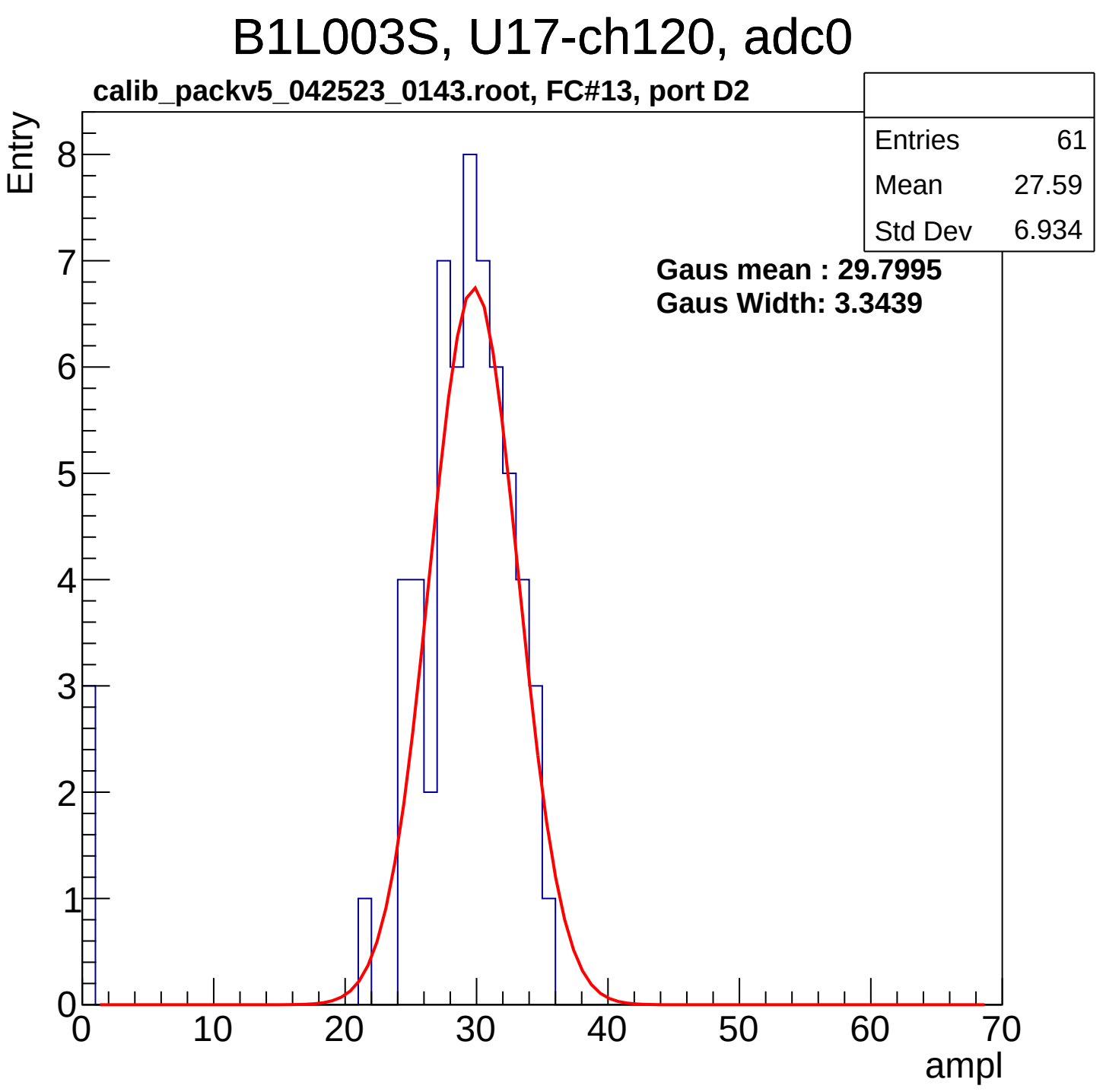
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch119, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.67
Std Dev 1.179
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch119, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch120, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 27.59
Std Dev 6.934
Gaus mean : 29.7995
Gaus Width: 3.3439
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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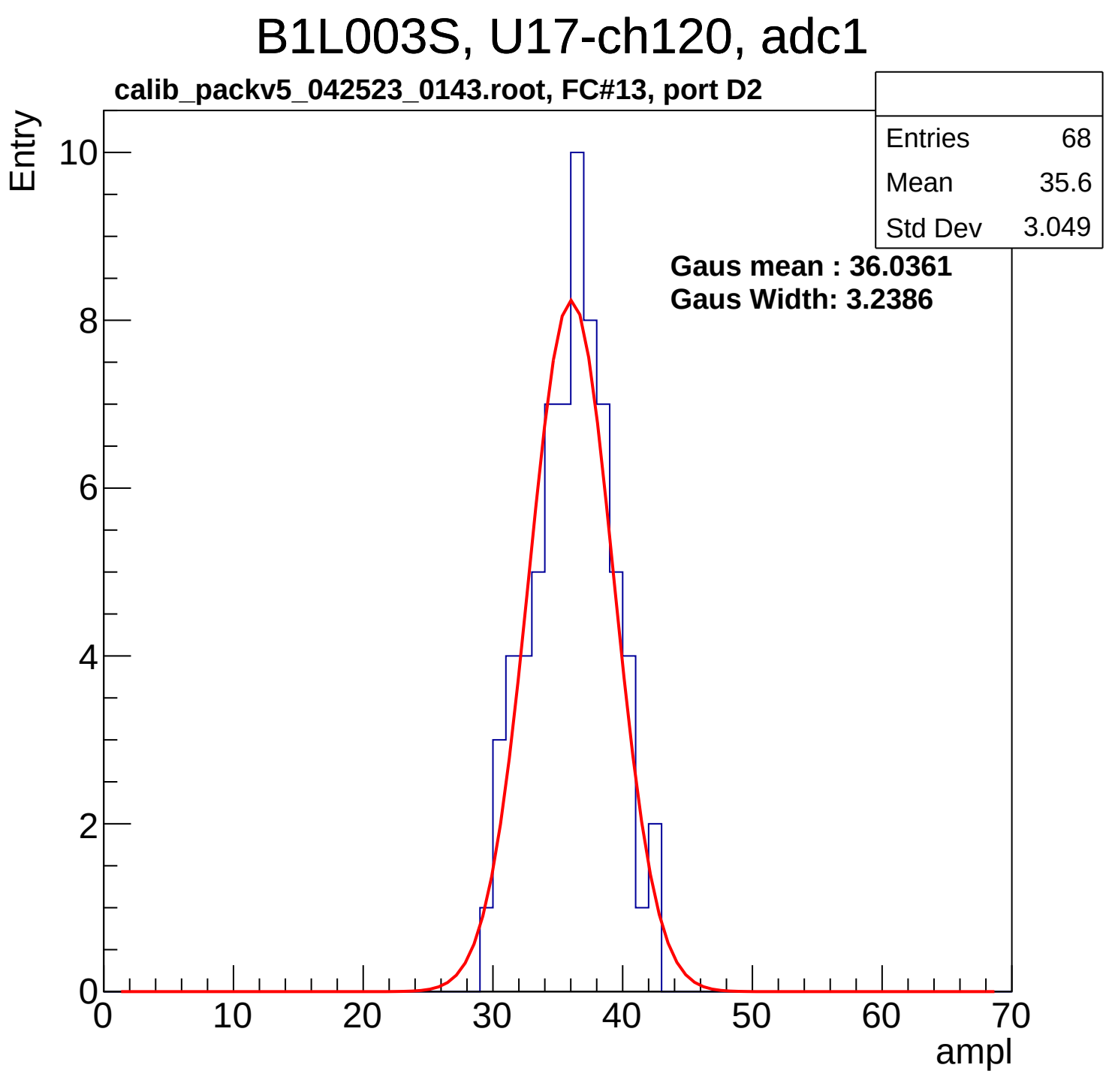
B1L003S, U17-ch120, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.6
Std Dev 3.049
Gaus mean : 36.0361
Gaus Width: 3.2386
Entry
ampl
0
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4
6
8
10
0
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20
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40
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60
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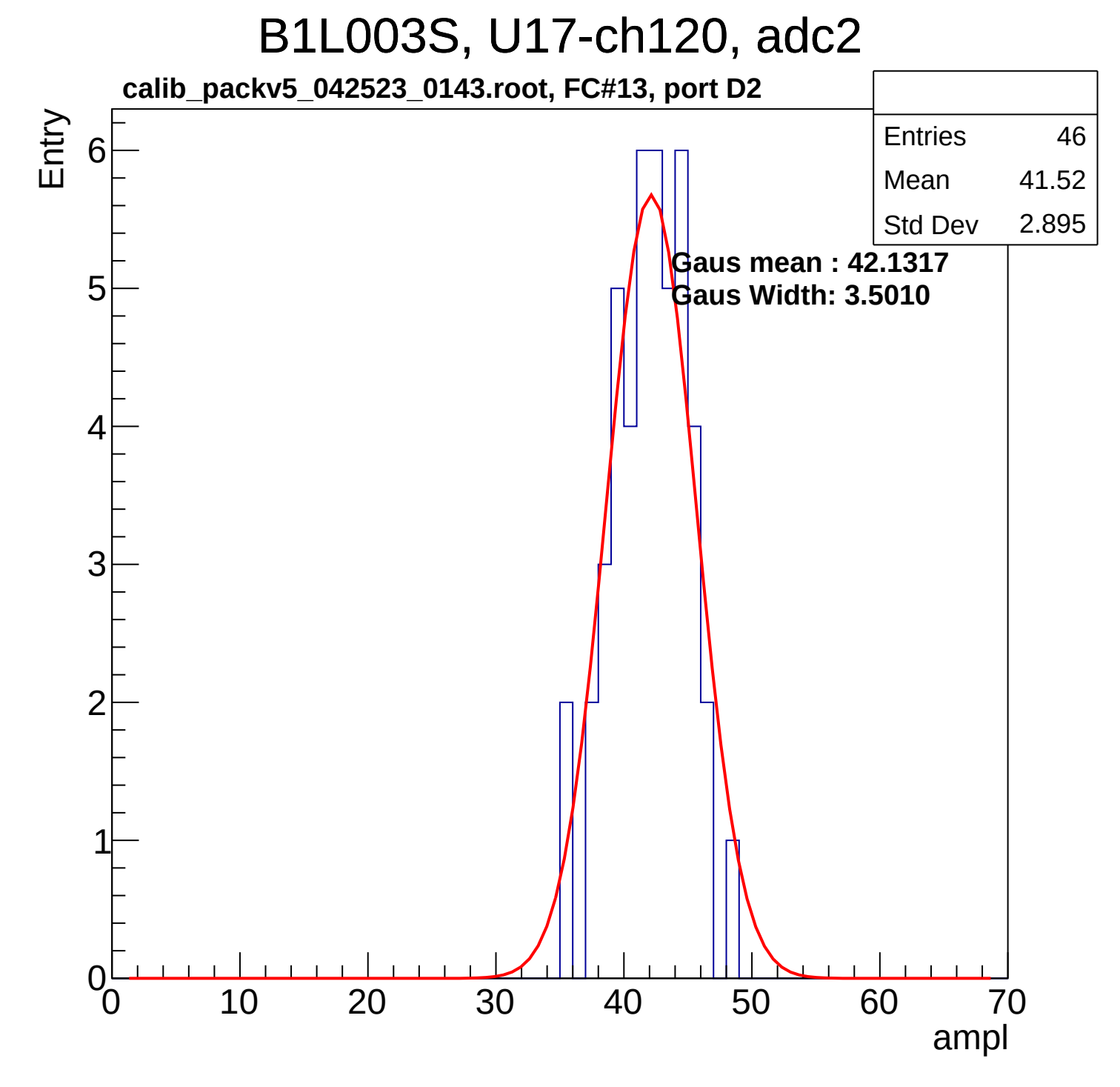
B1L003S, U17-ch120, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 41.52
Std Dev 2.895
Gaus mean : 42.1317
Gaus Width: 3.5010
Entry
ampl
0
1
2
3
4
5
6
0
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60
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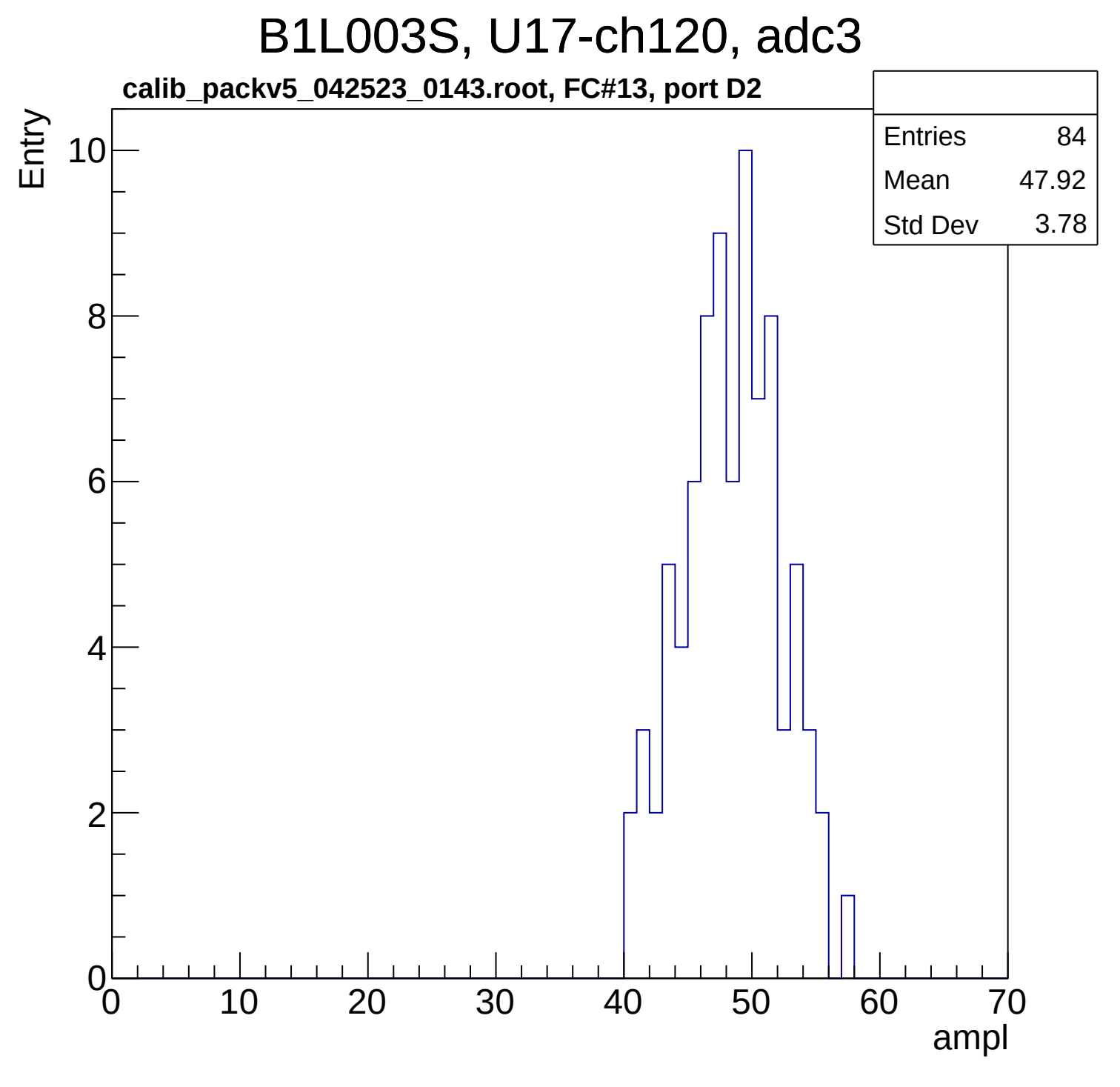
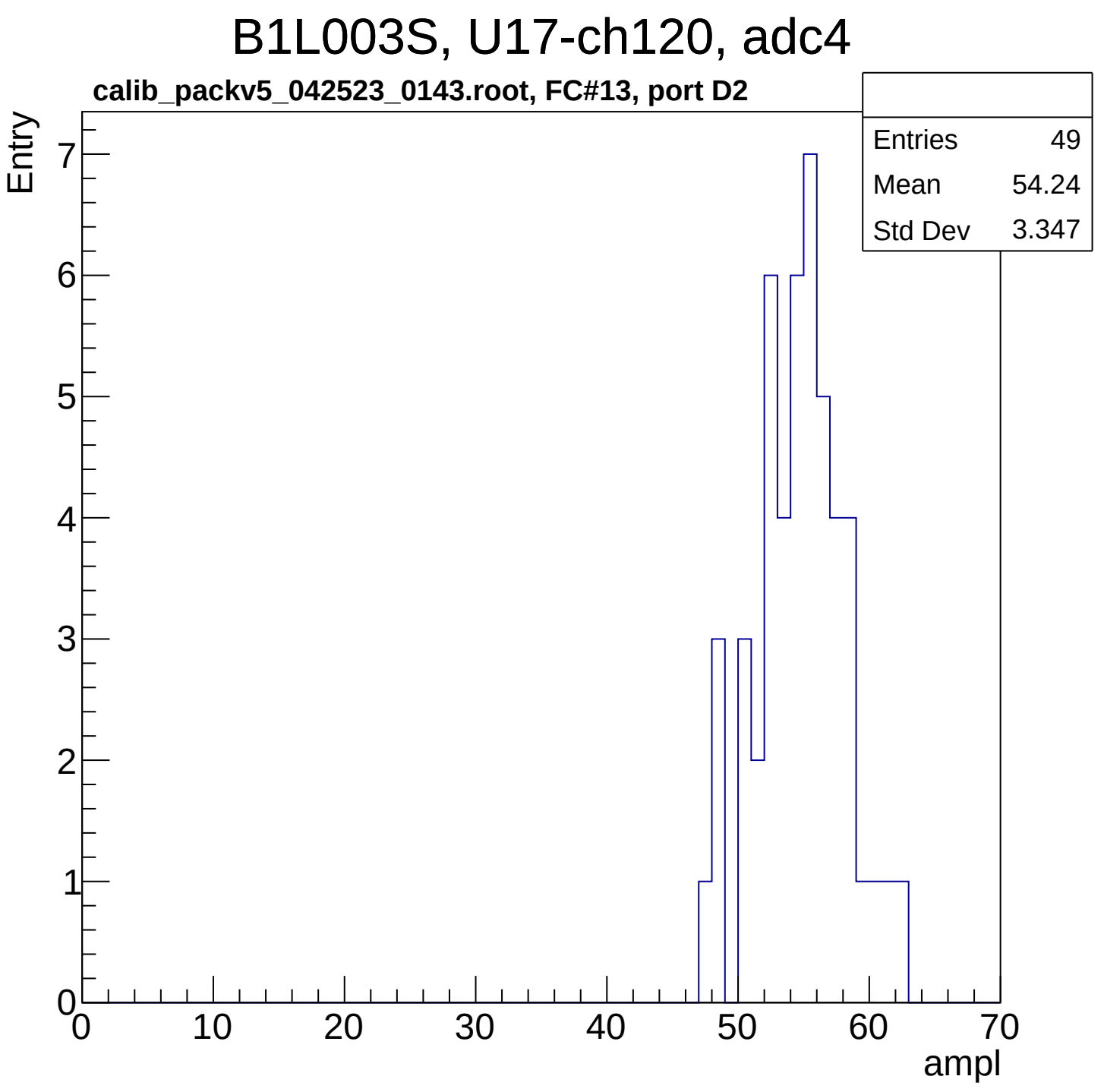

B1L003S, U17-ch120, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 47.92
Std Dev 3.78
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch120, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.24
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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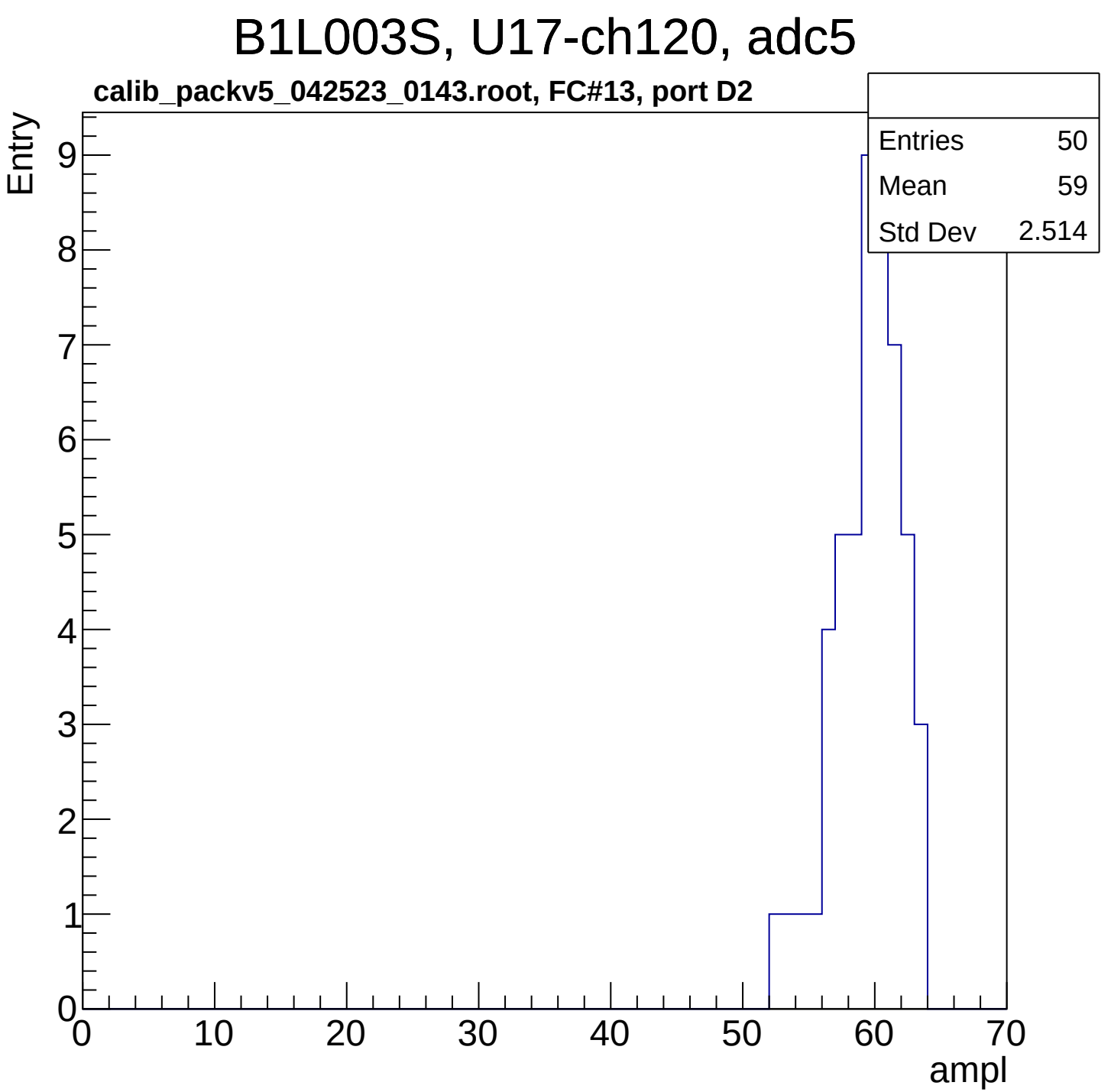
B1L003S, U17-ch120, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59
Std Dev 2.514
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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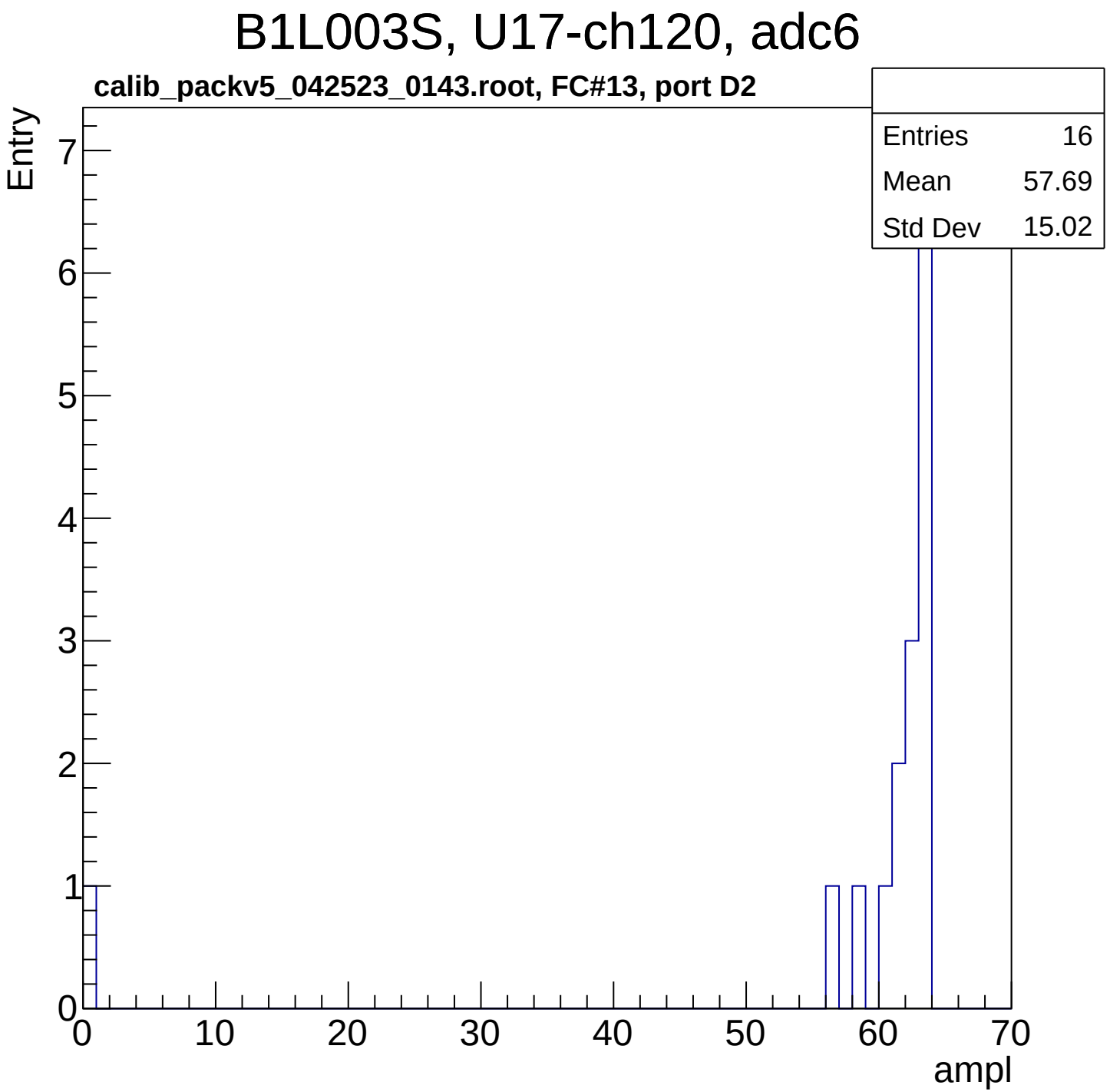
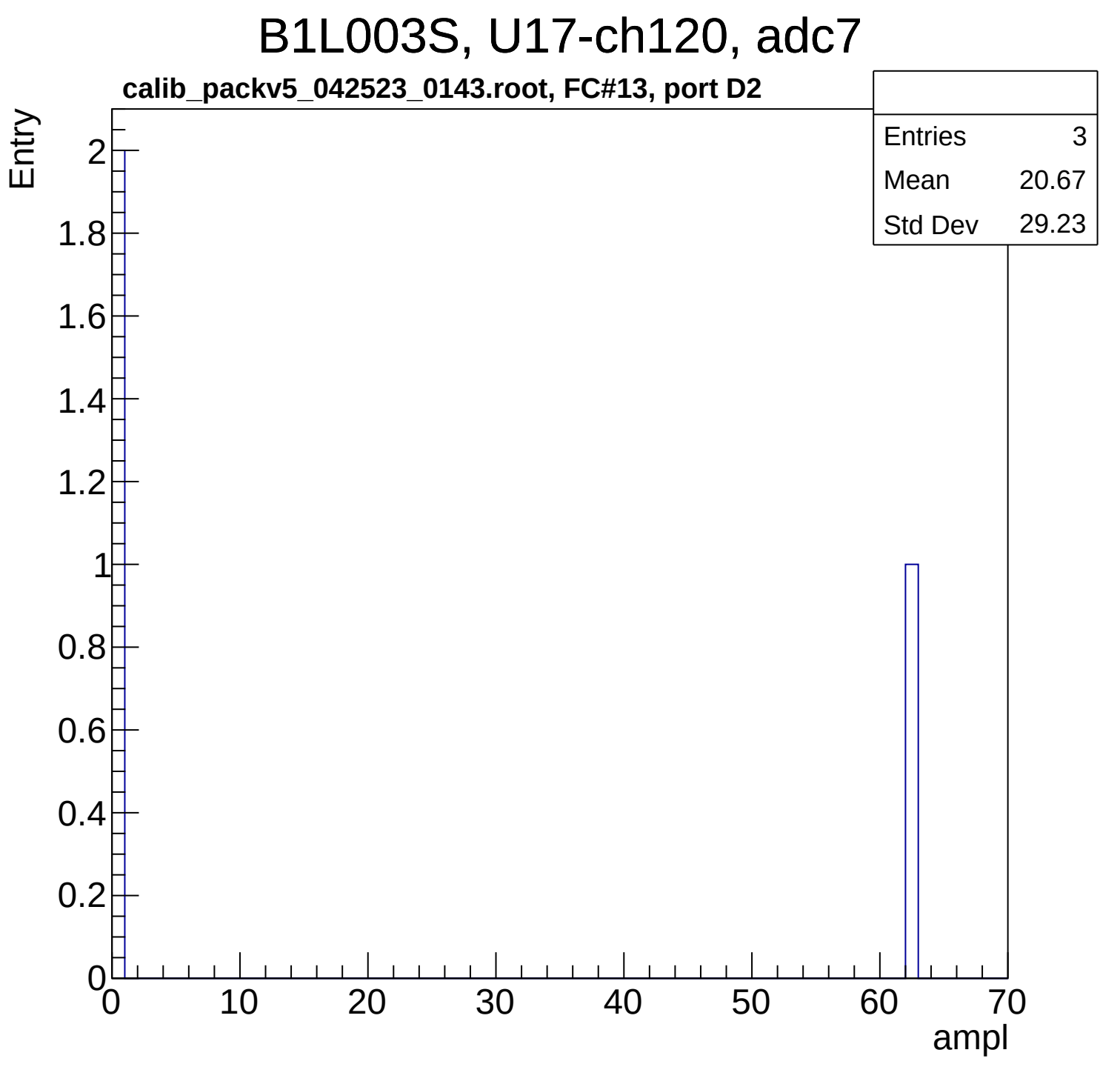

B1L003S, U17-ch120, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 57.69
Std Dev 15.02
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch120, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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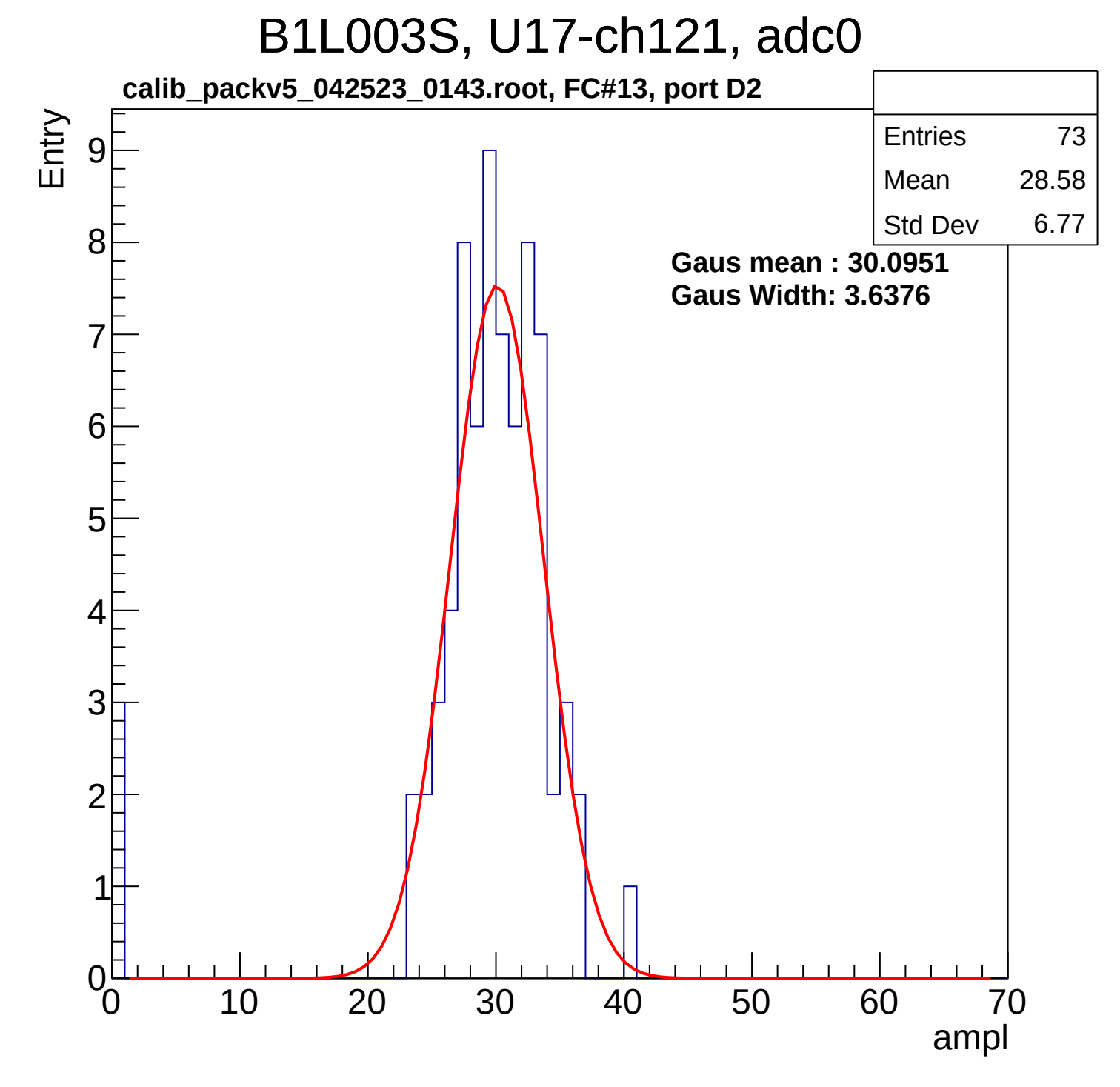
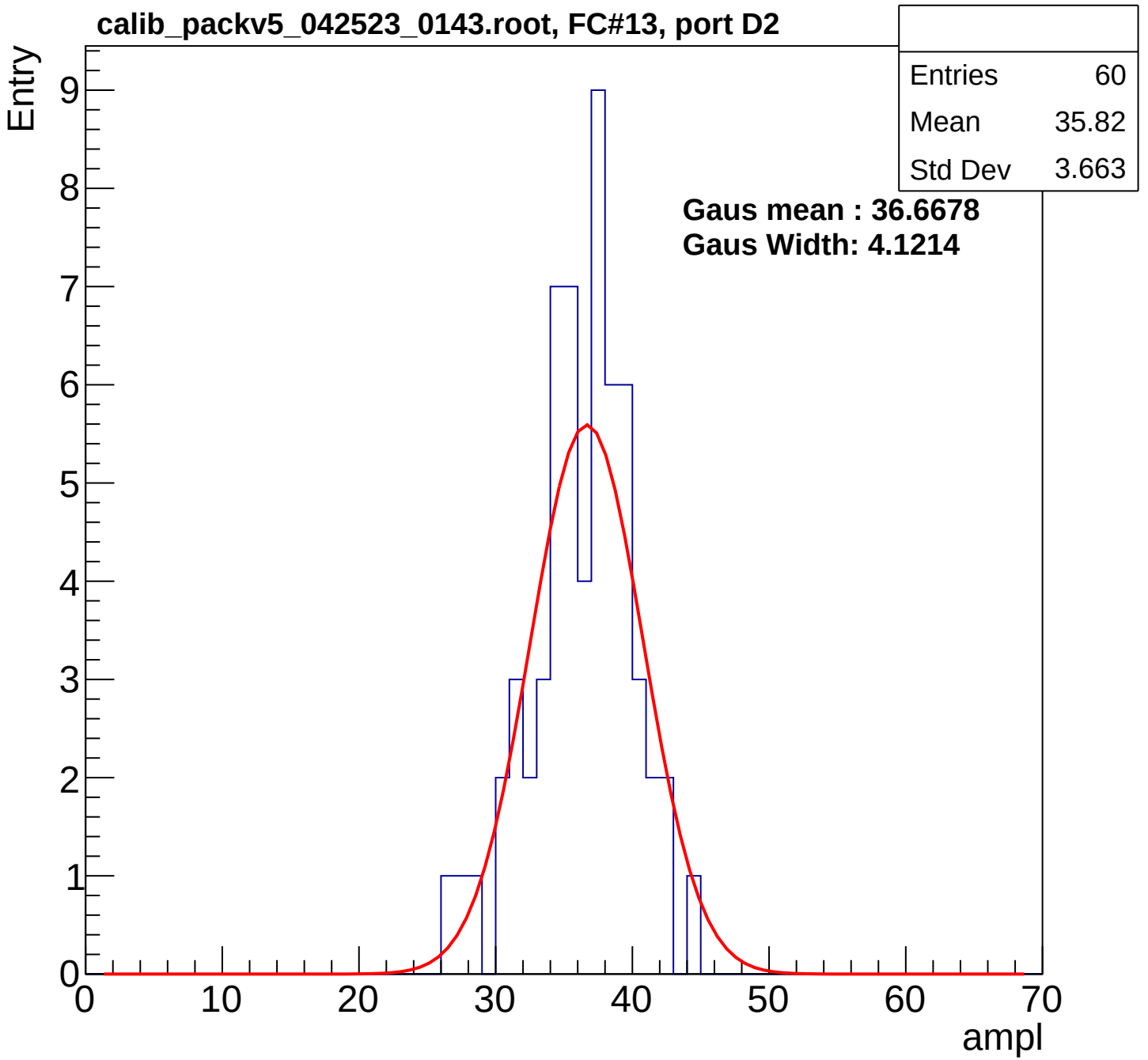
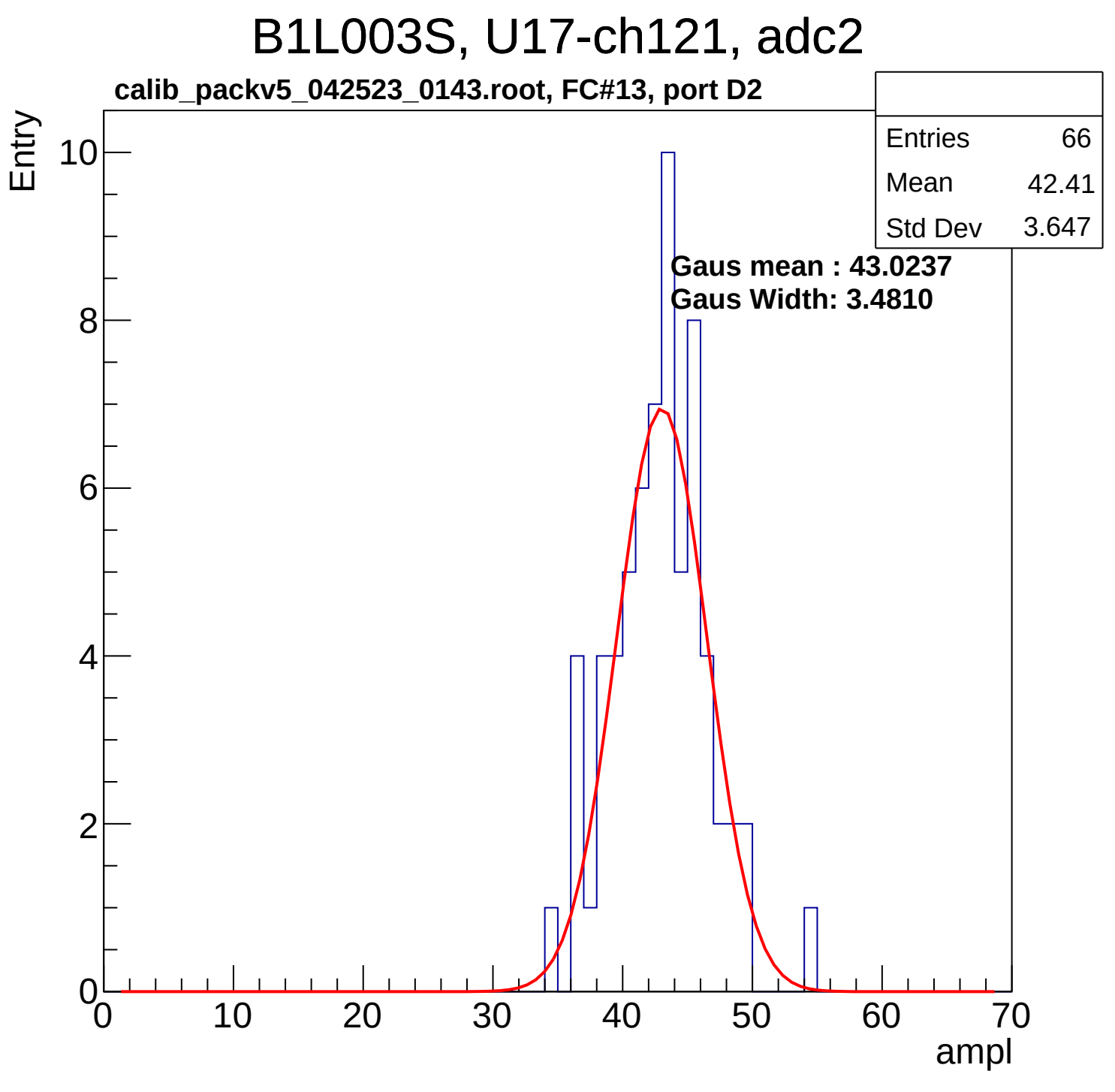
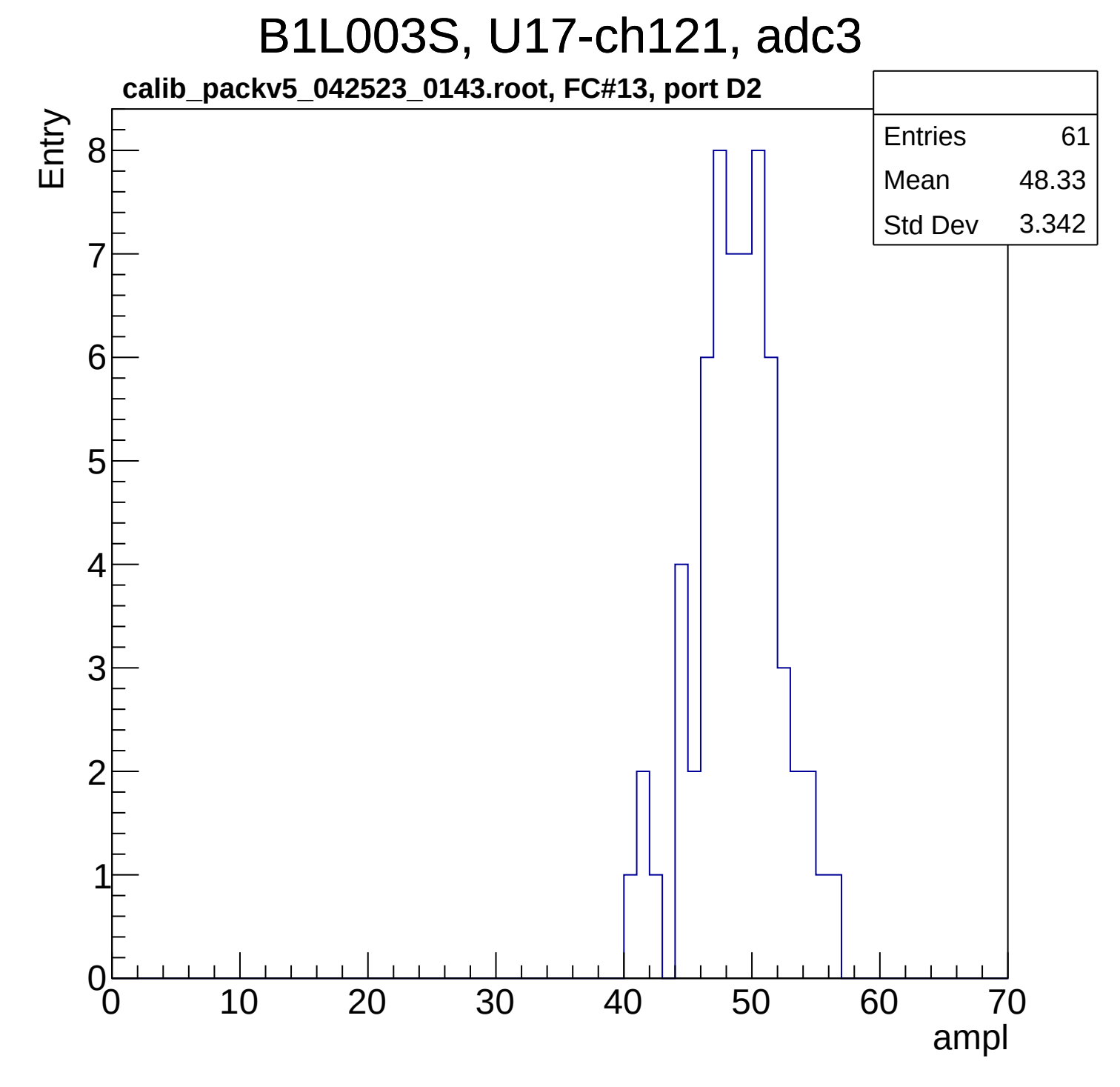
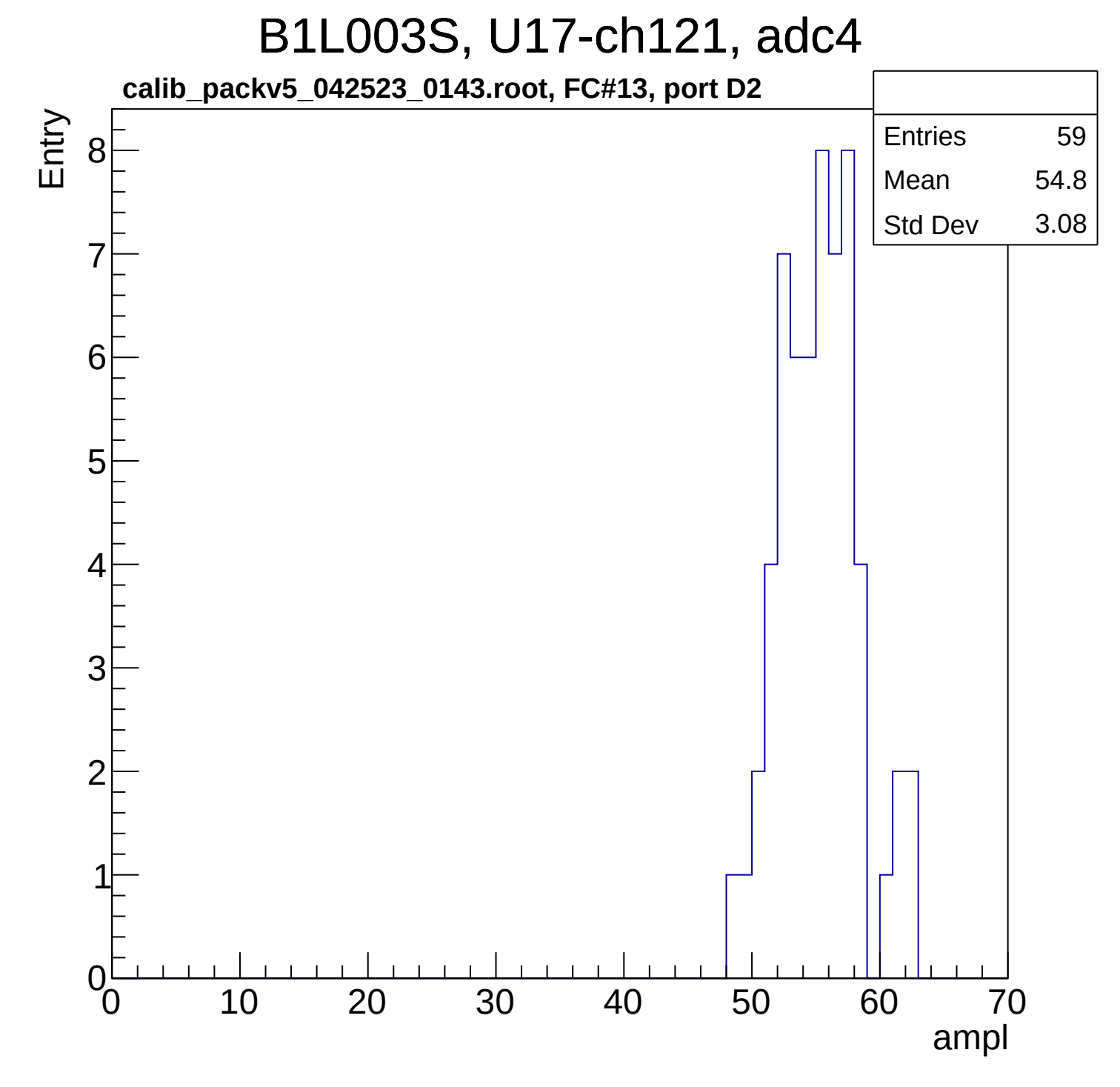
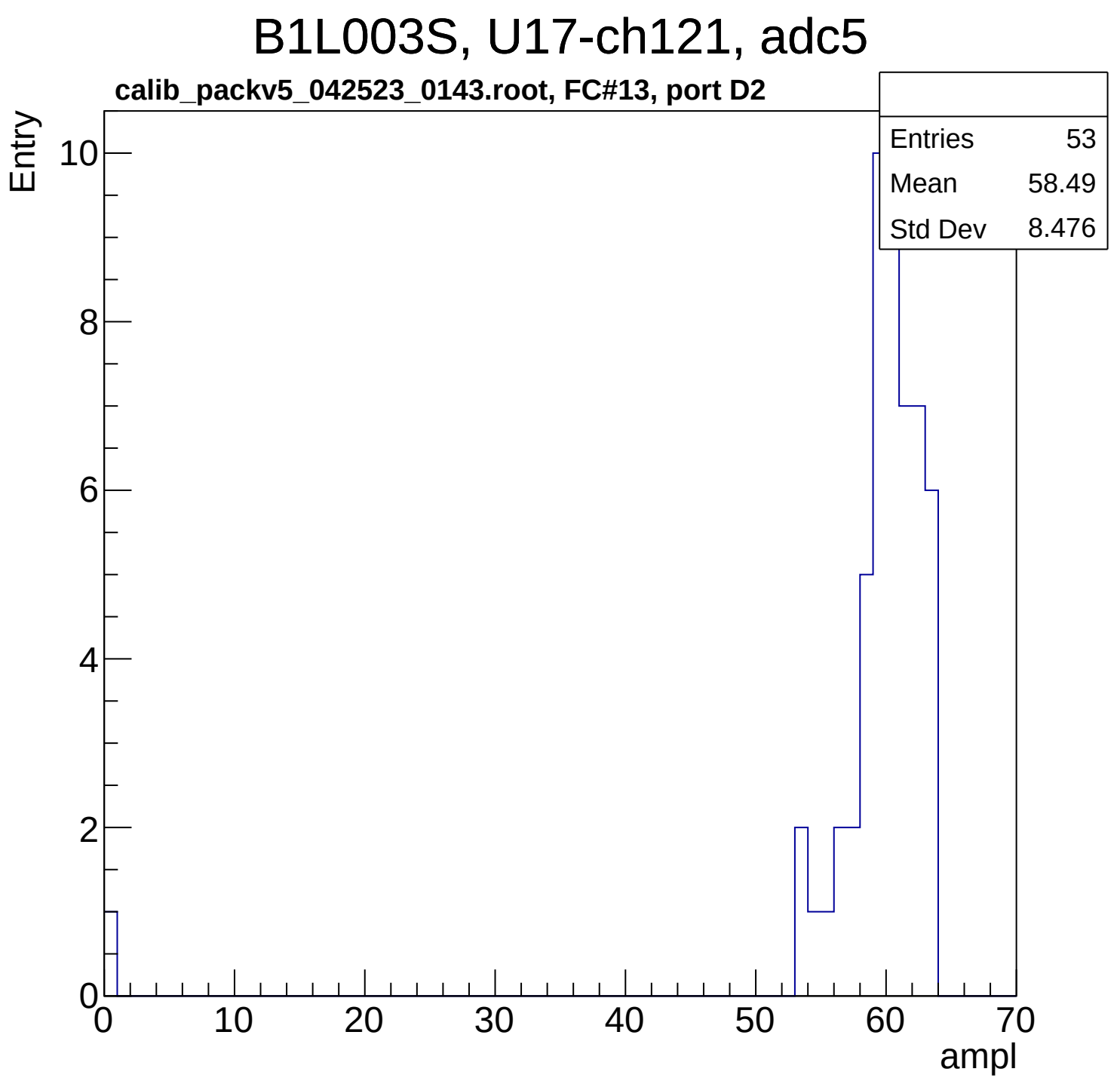

B1L003S, U17-ch121, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 28.58
Std Dev 6.77
Gaus mean : 30.0951
Gaus Width: 3.6376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

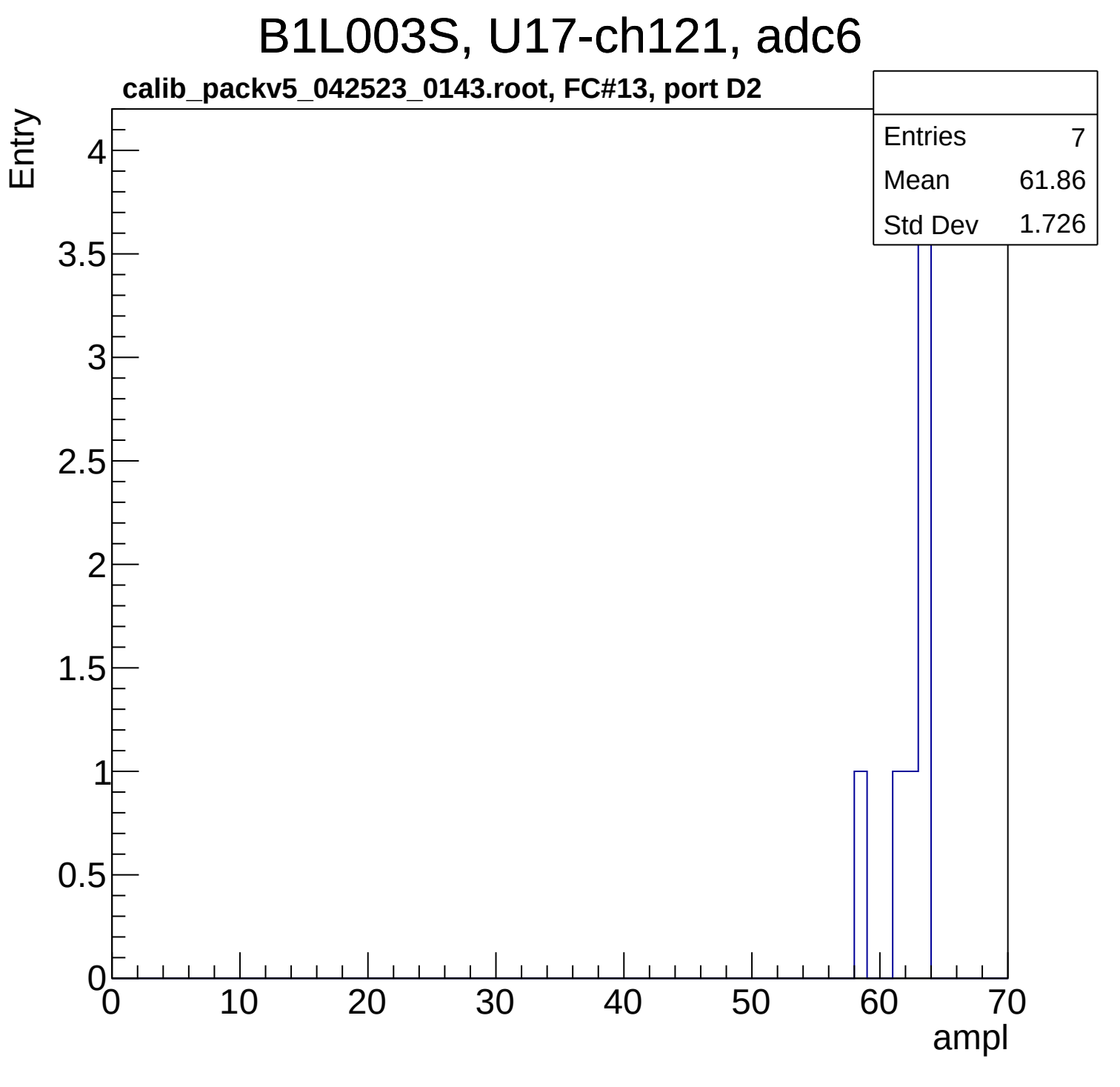

B1L003S, U17-ch121, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 35.82
Std Dev 3.663
Gaus mean : 36.6678
Gaus Width: 4.1214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

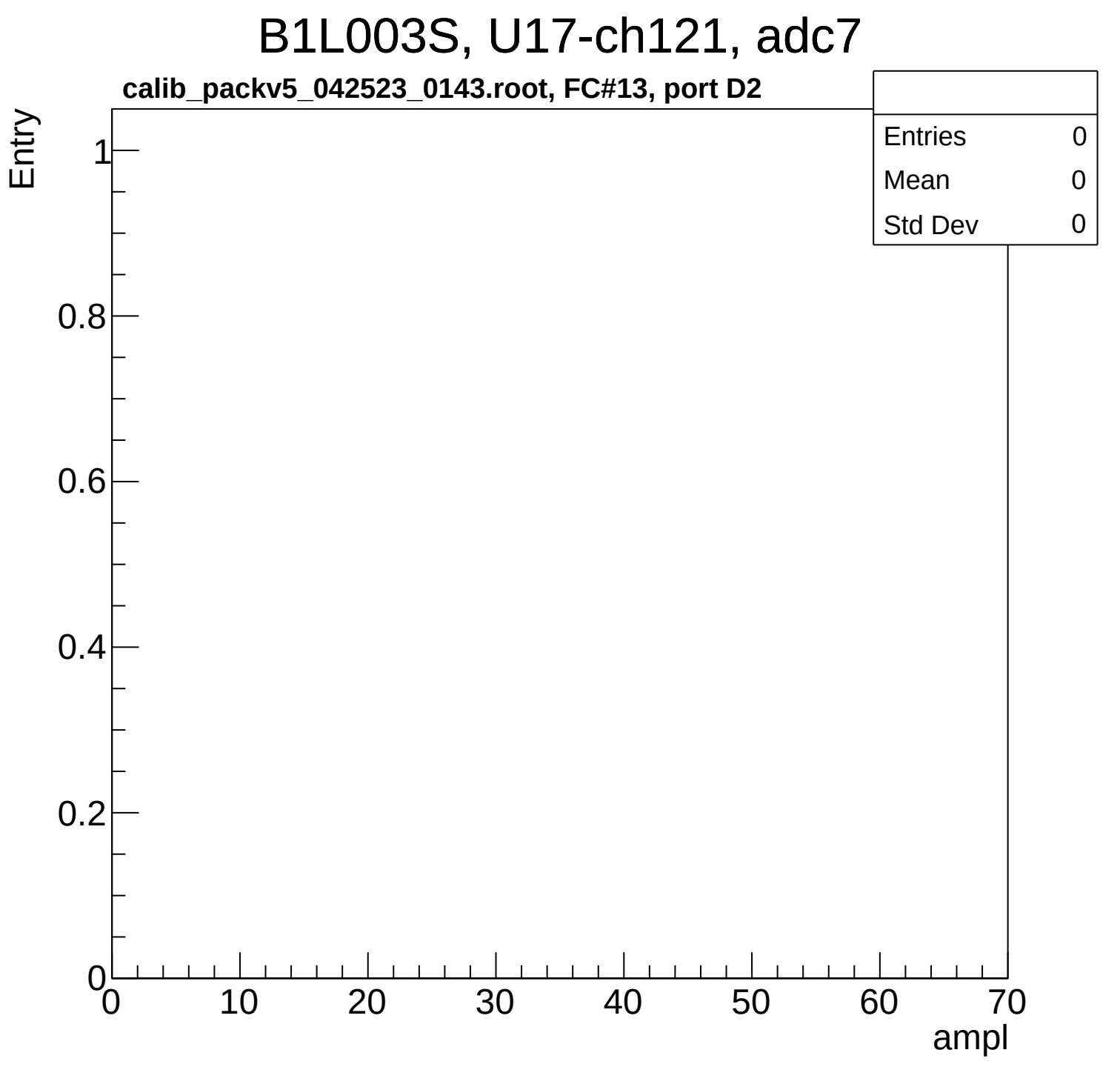

B1L003S, U17-ch121, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.41
Std Dev 3.647
Gaus mean : 43.0237
Gaus Width: 3.4810
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

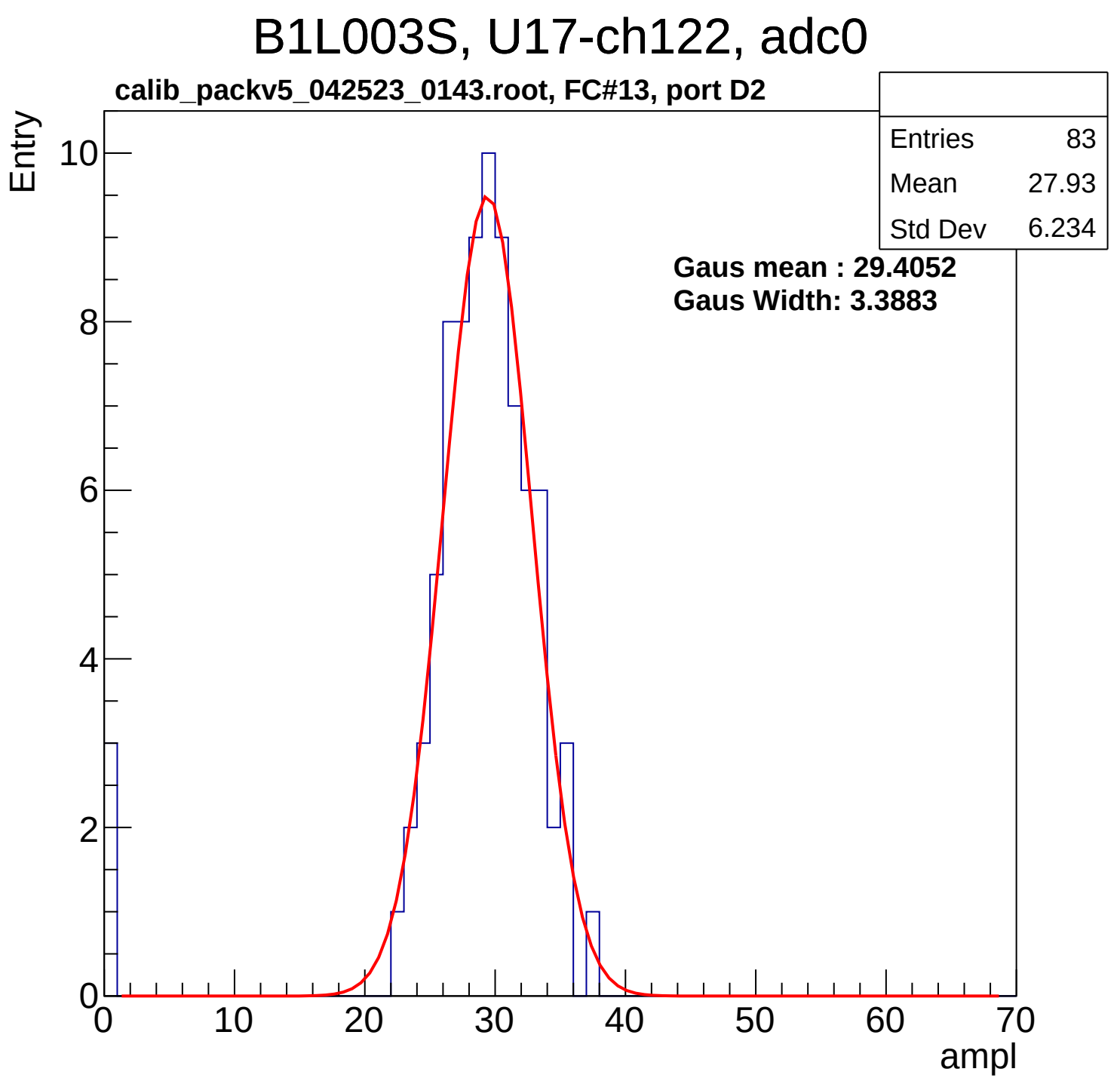

B1L003S, U17-ch121, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.33
Std Dev 3.342
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

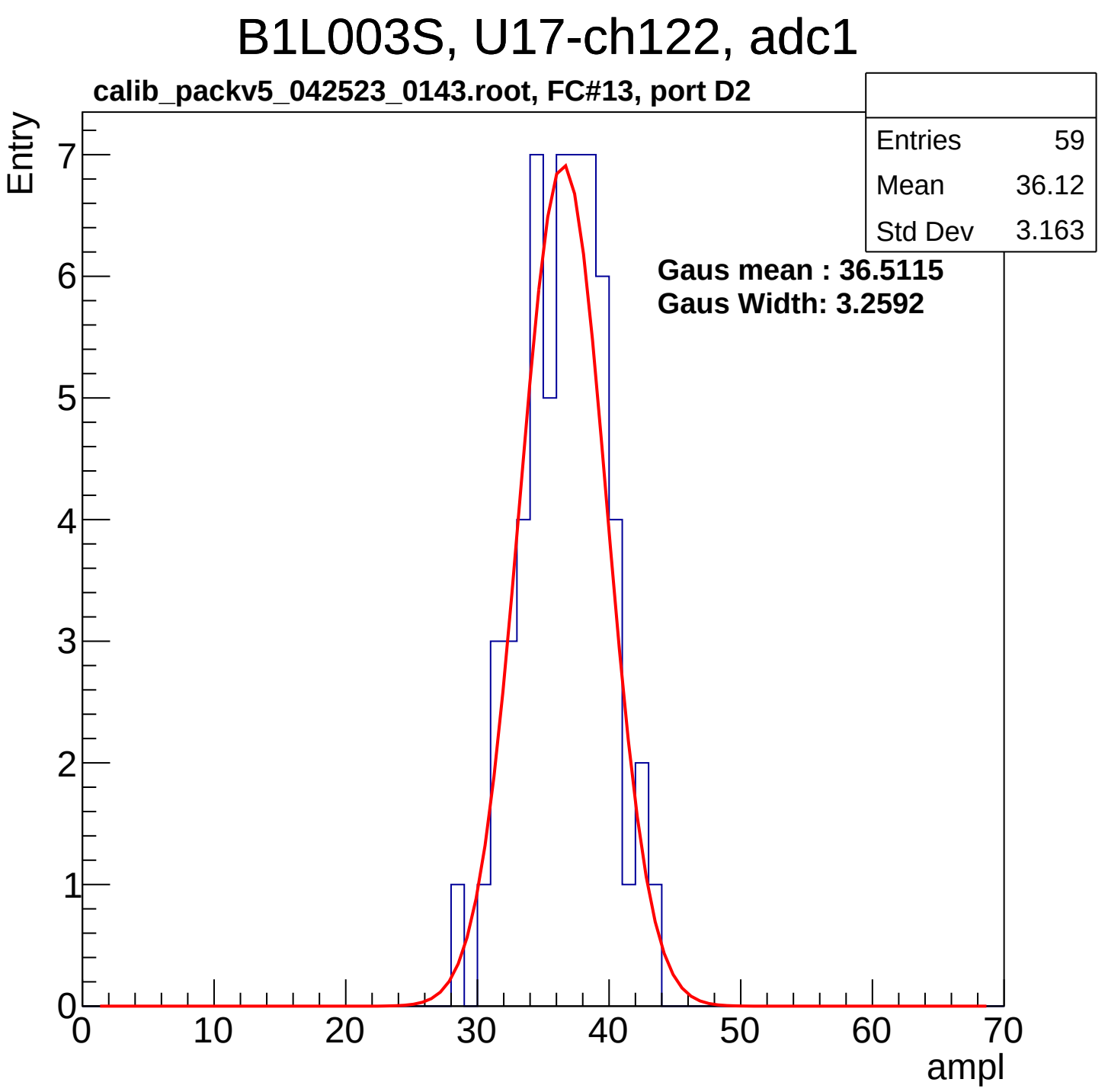

B1L003S, U17-ch121, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.8
Std Dev 3.08
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

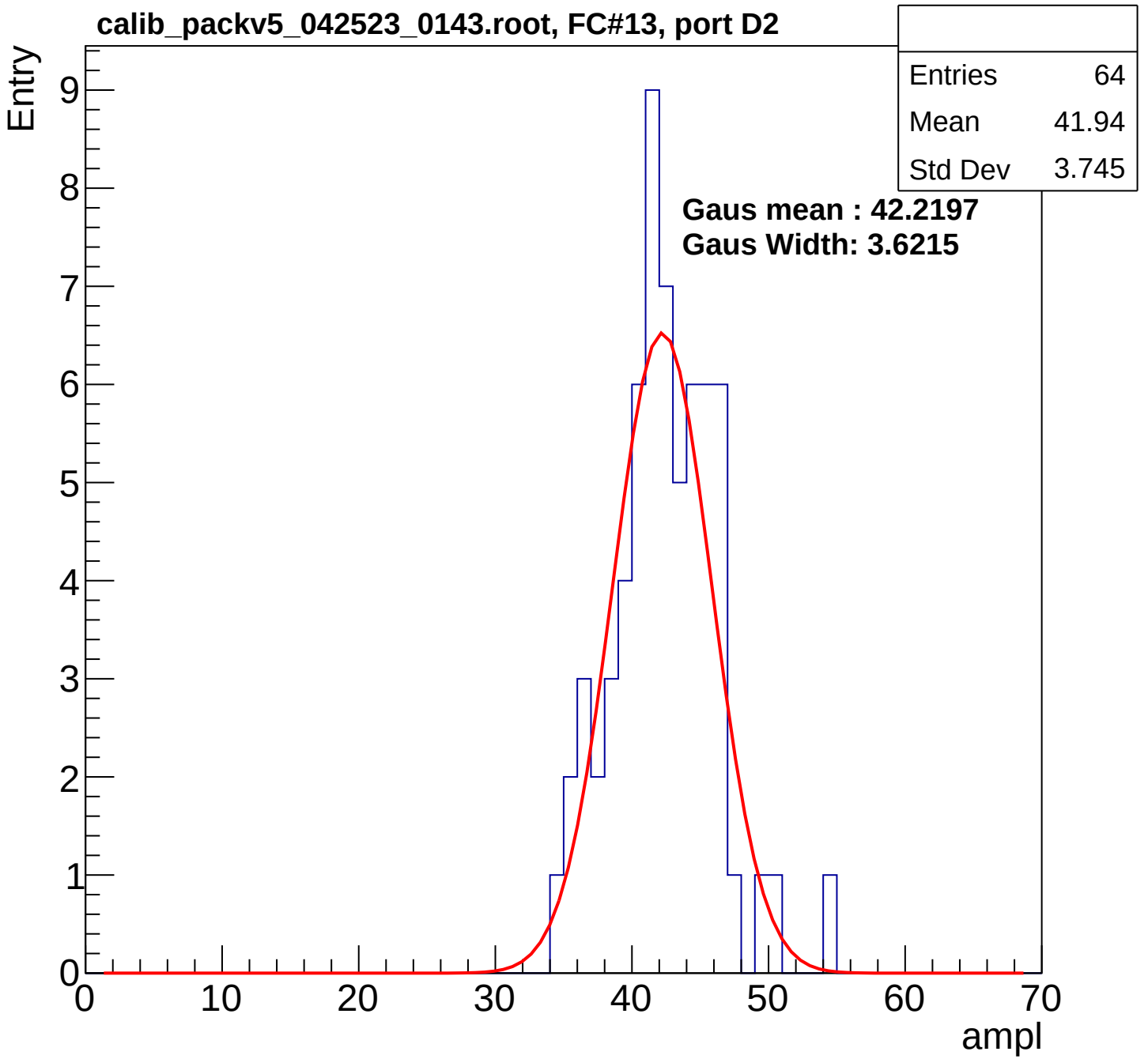

B1L003S, U17-ch121, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.49
Std Dev 8.476
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch121, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.86
Std Dev 1.726
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U17-ch121, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch122, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 27.93
Std Dev 6.234
Gaus mean : 29.4052
Gaus Width: 3.3883
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch122, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.12
Std Dev 3.163
Gaus mean : 36.5115
Gaus Width: 3.2592
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch122, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 41.94
Std Dev 3.745
Gaus mean : 42.2197
Gaus Width: 3.6215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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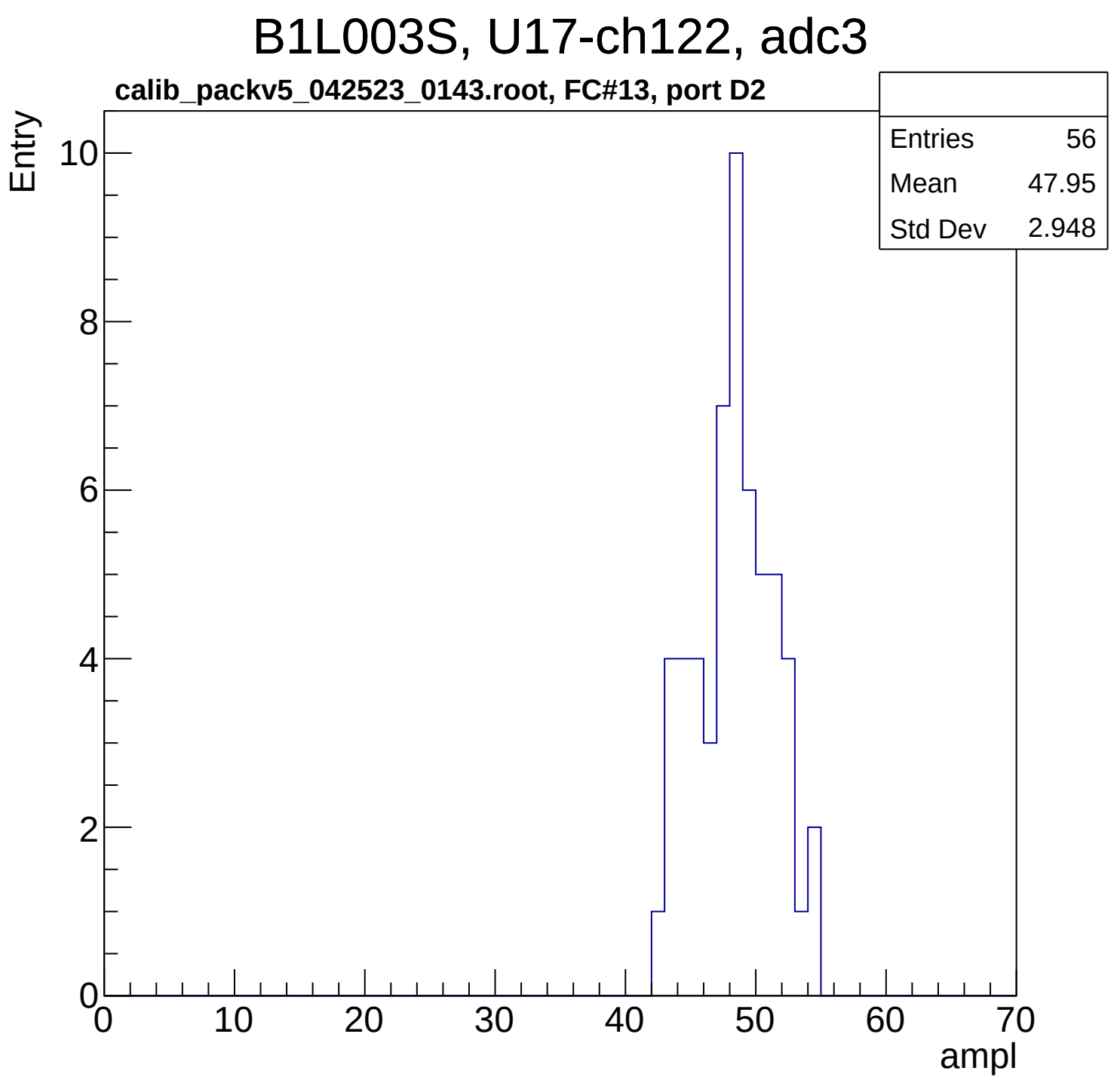
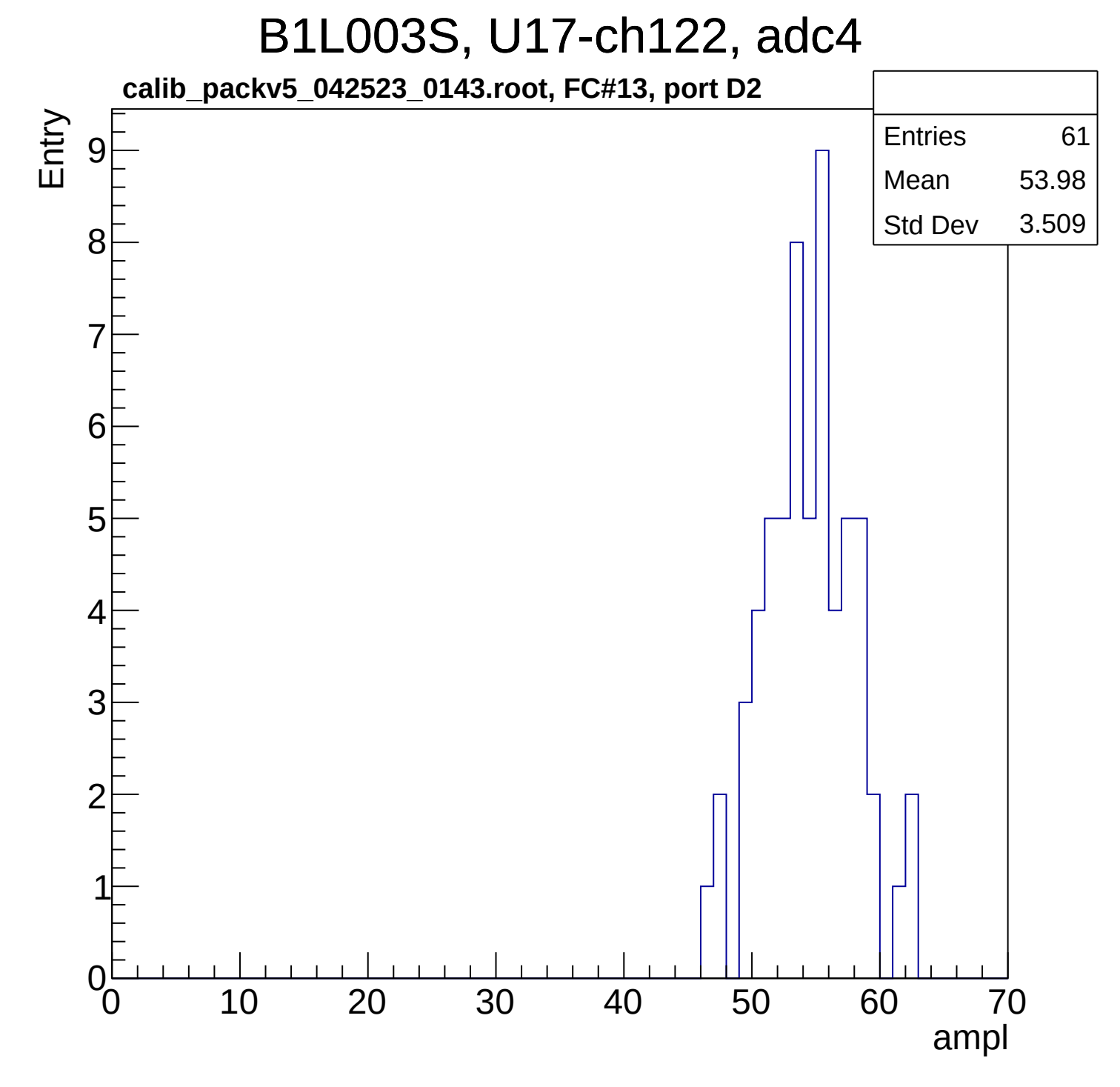

B1L003S, U17-ch122, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 47.95
Std Dev 2.948
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch122, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 53.98
Std Dev 3.509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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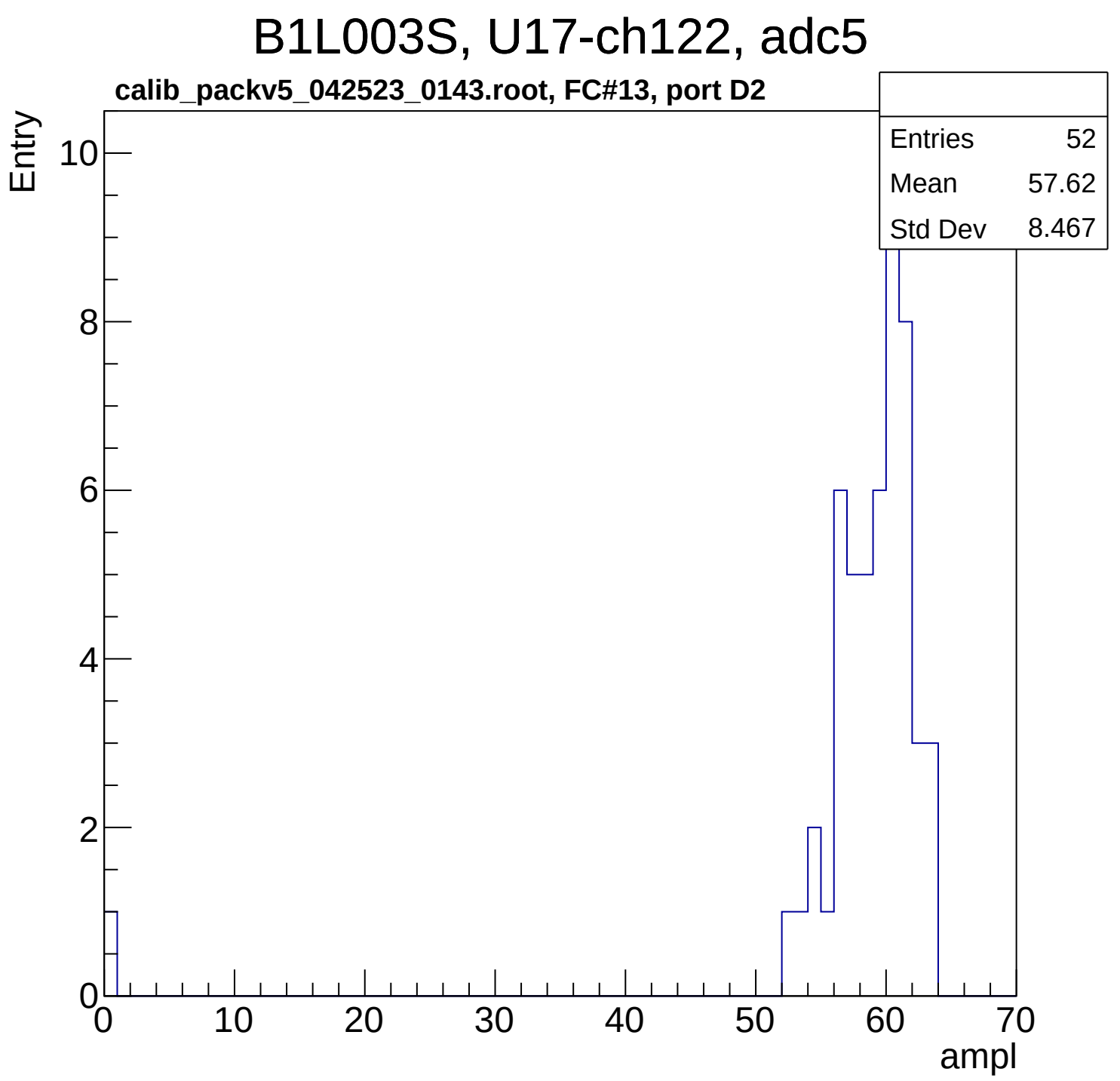
B1L003S, U17-ch122, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 57.62
Std Dev 8.467
Entry
ampl
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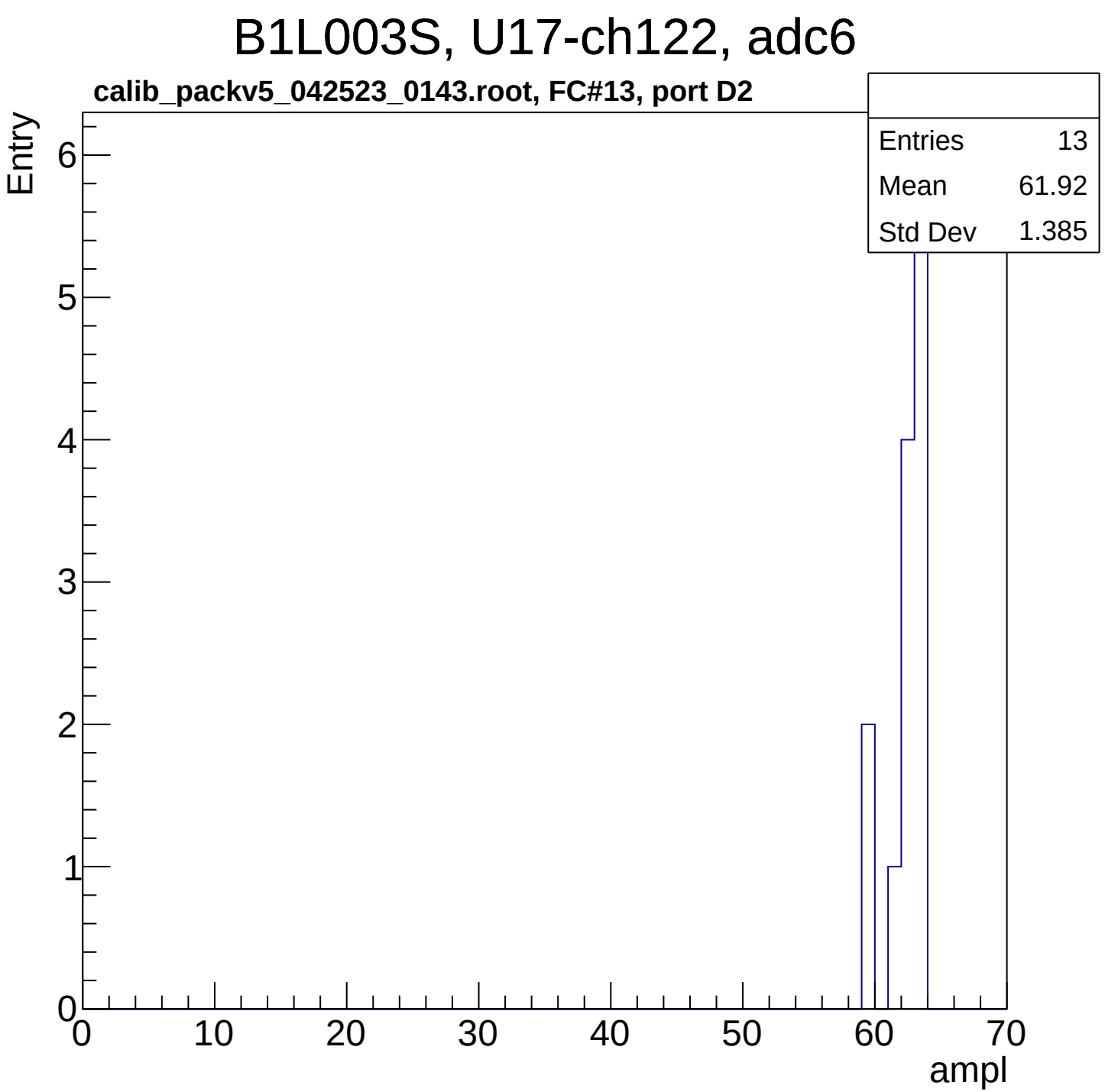
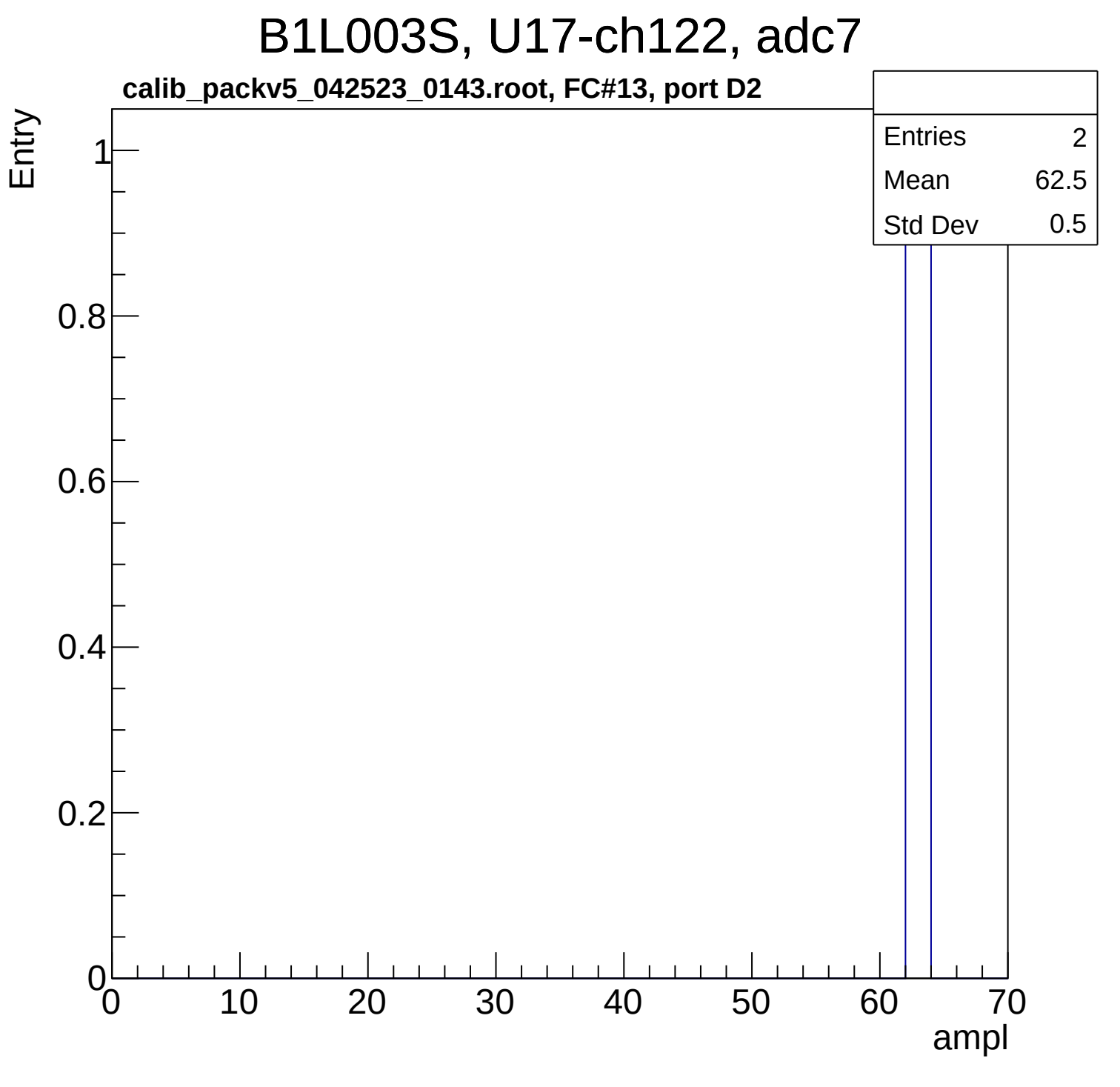
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch122, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.92
Std Dev 1.385
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch122, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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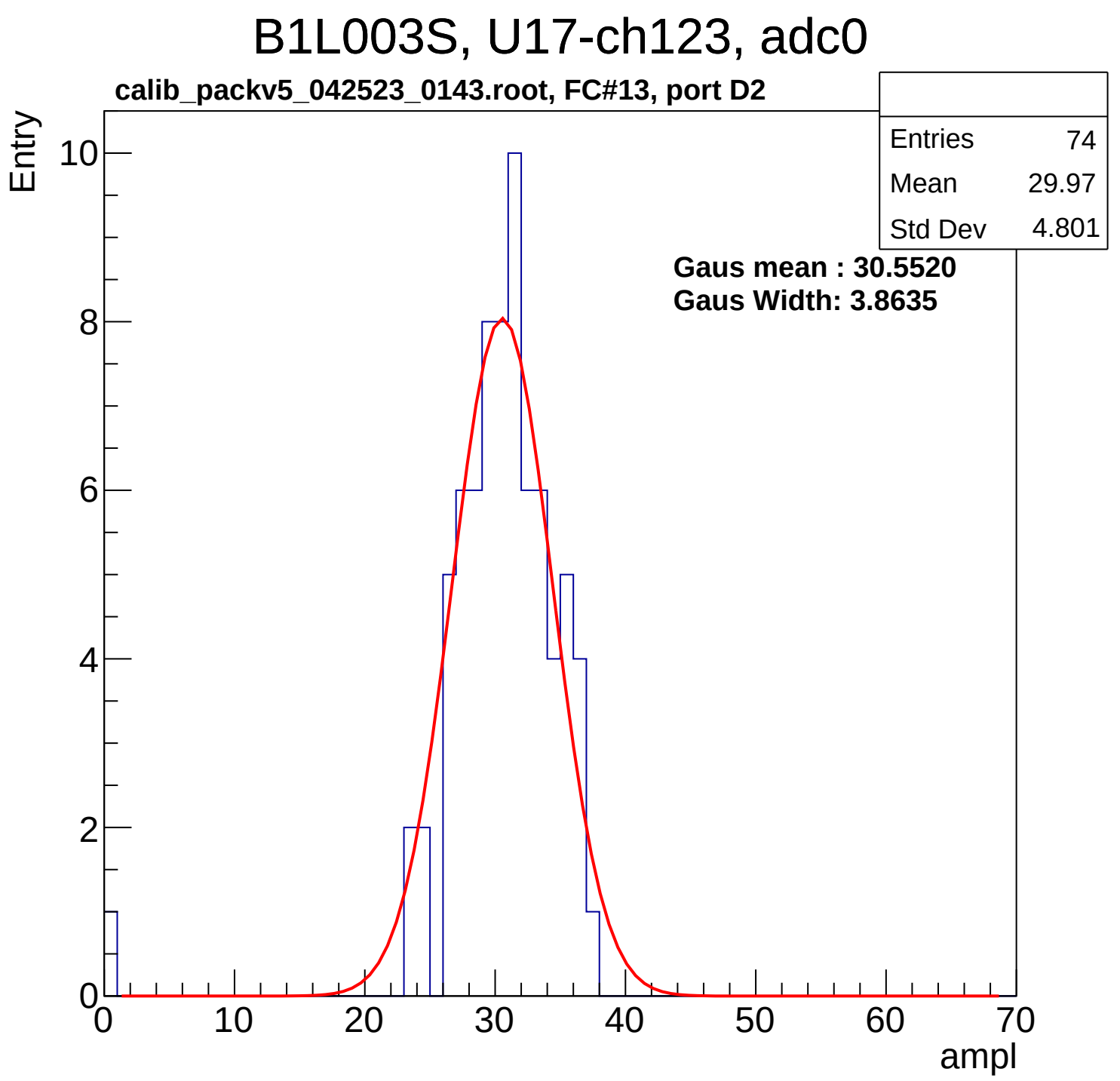
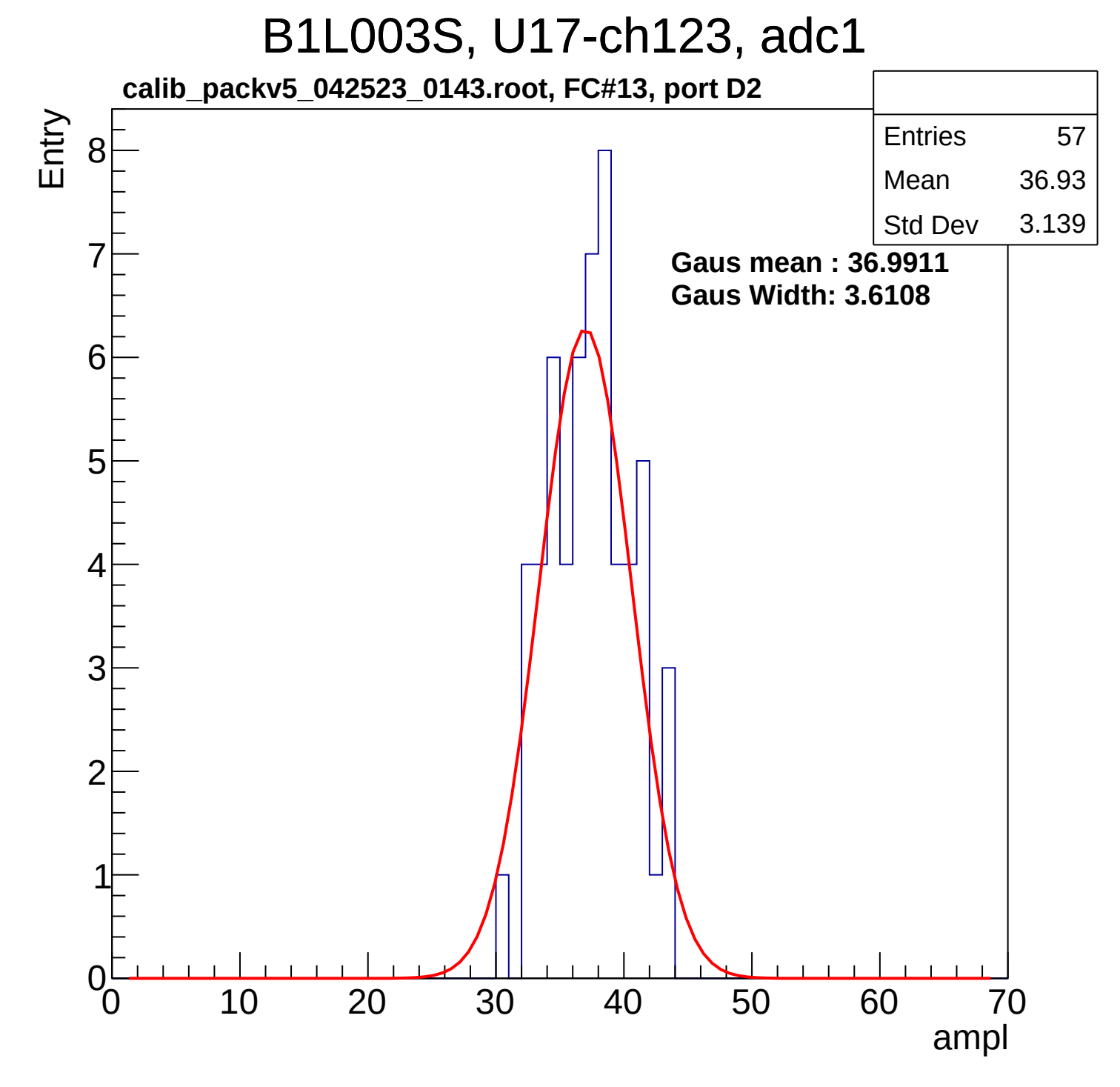
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch123, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.97
Std Dev 4.801
Gaus mean : 30.5520
Gaus Width: 3.8635
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U17-ch123, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 36.93
Std Dev 3.139
Gaus mean : 36.9911
Gaus Width: 3.6108
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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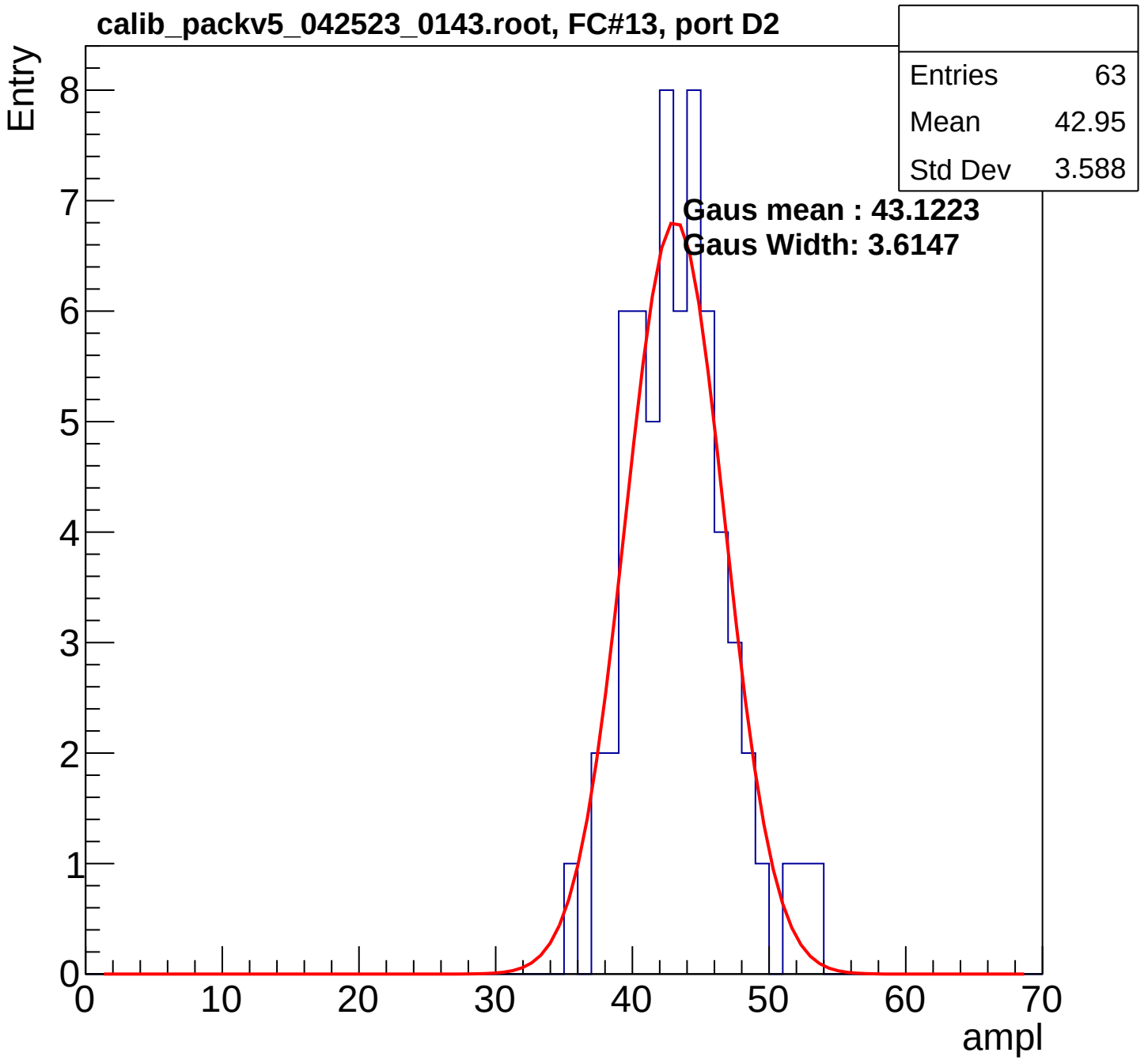
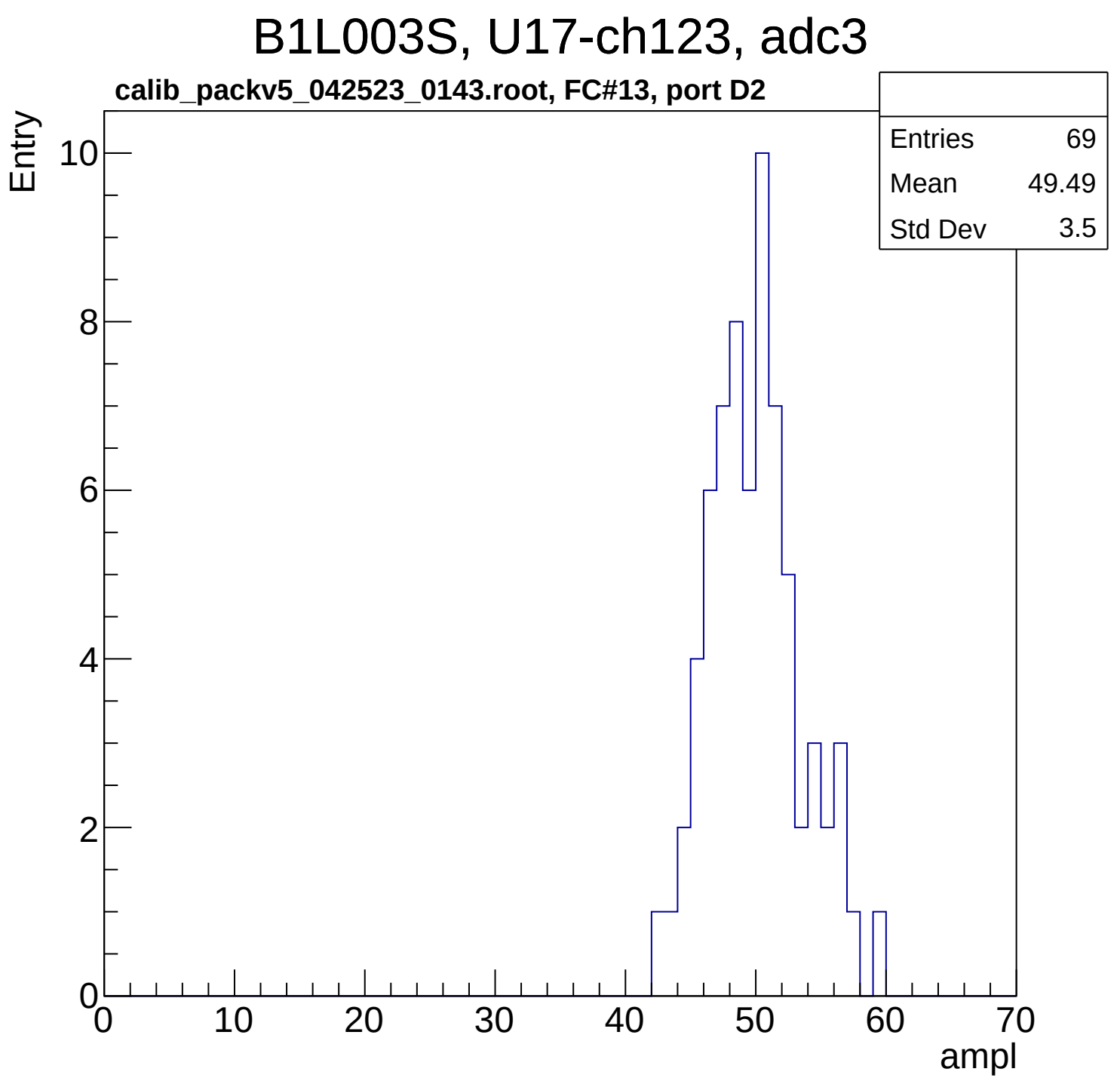
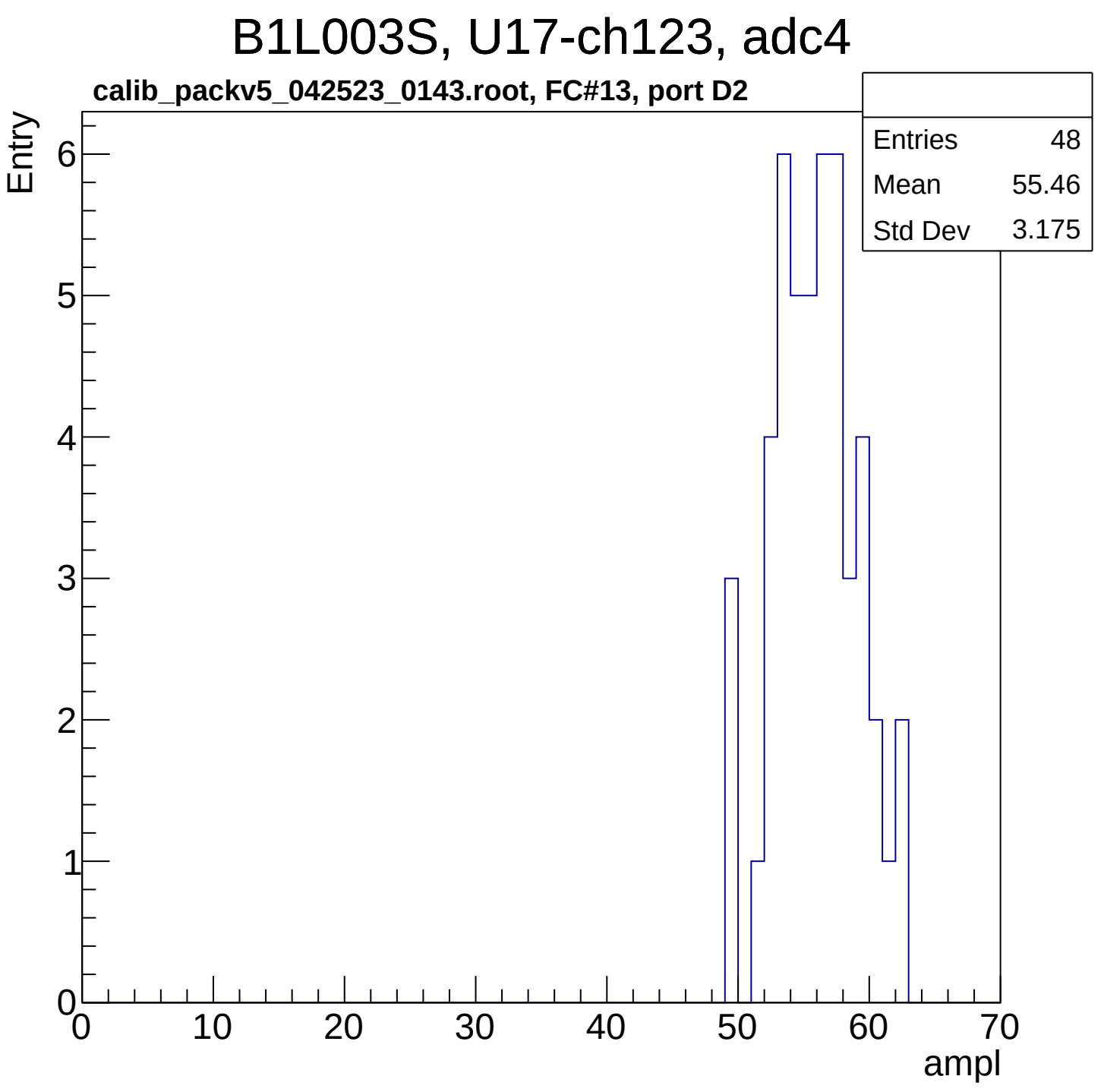

B1L003S, U17-ch123, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 42.95
Std Dev 3.588
Gaus mean : 43.1223
Gaus Width: 3.6147
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch123, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 49.49
Std Dev 3.5
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch123, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.46
Std Dev 3.175
Entry
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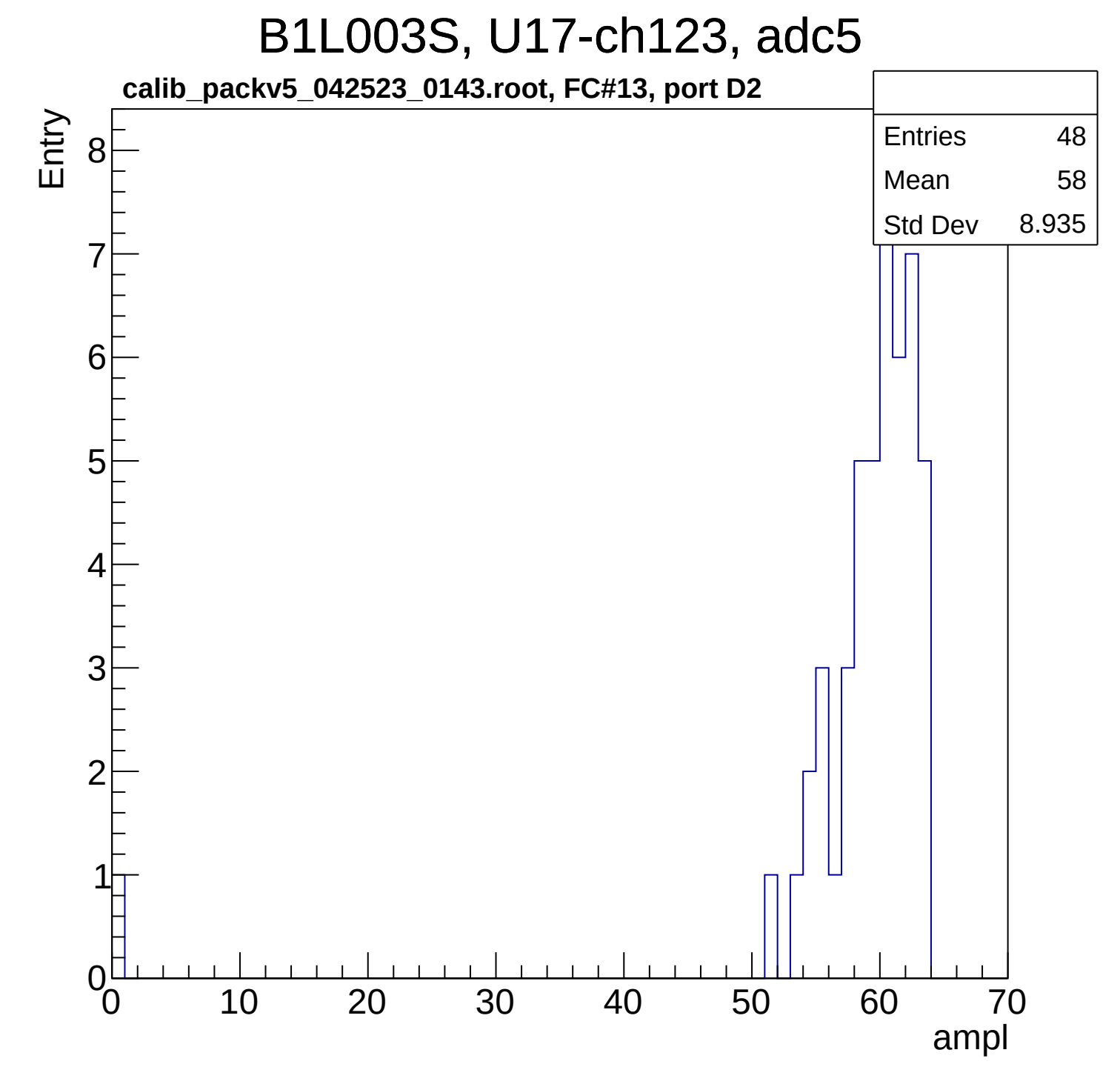
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch123, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58
Std Dev 8.935
Entry
ampl
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4
5
6
7
8
0
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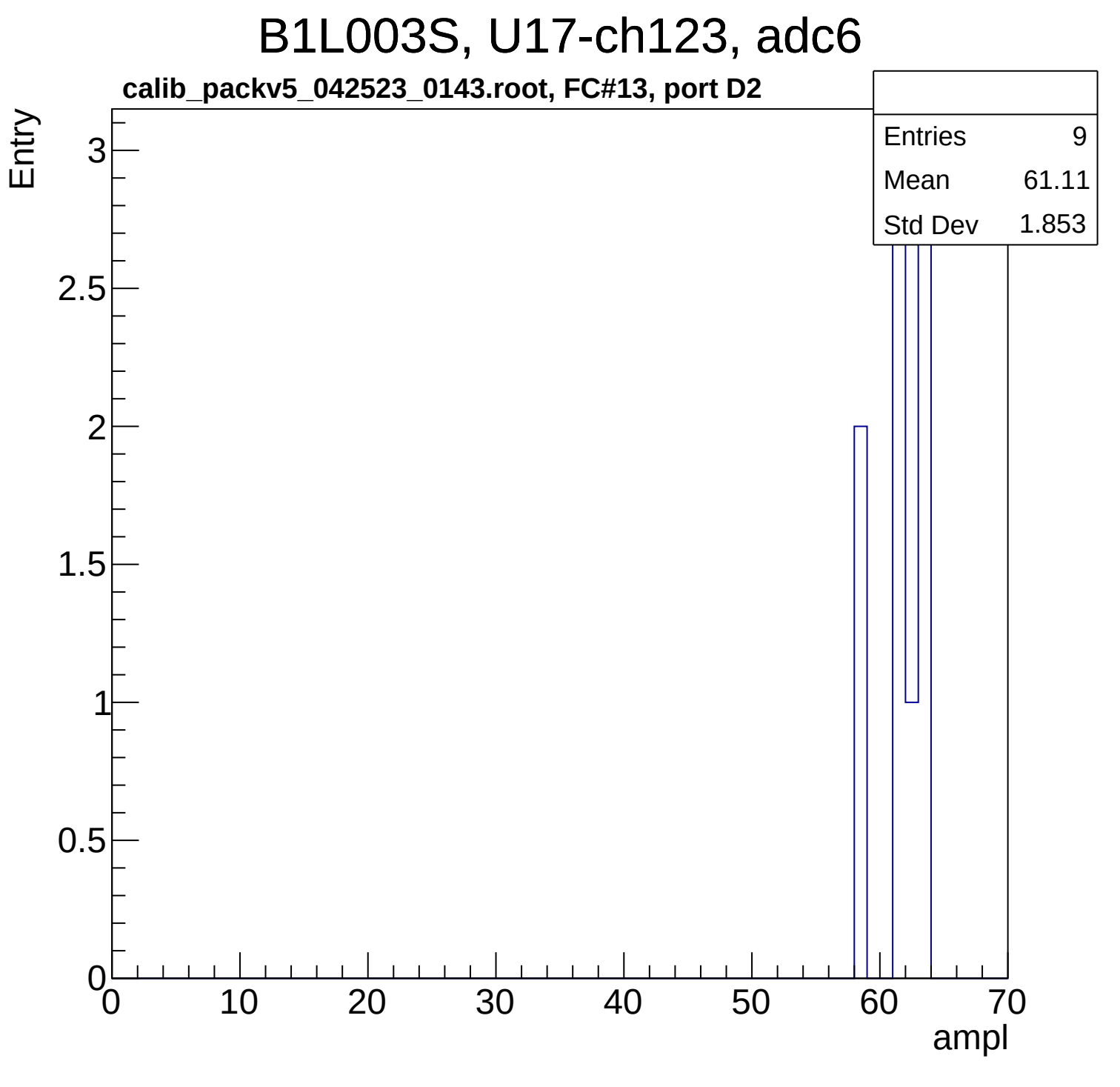
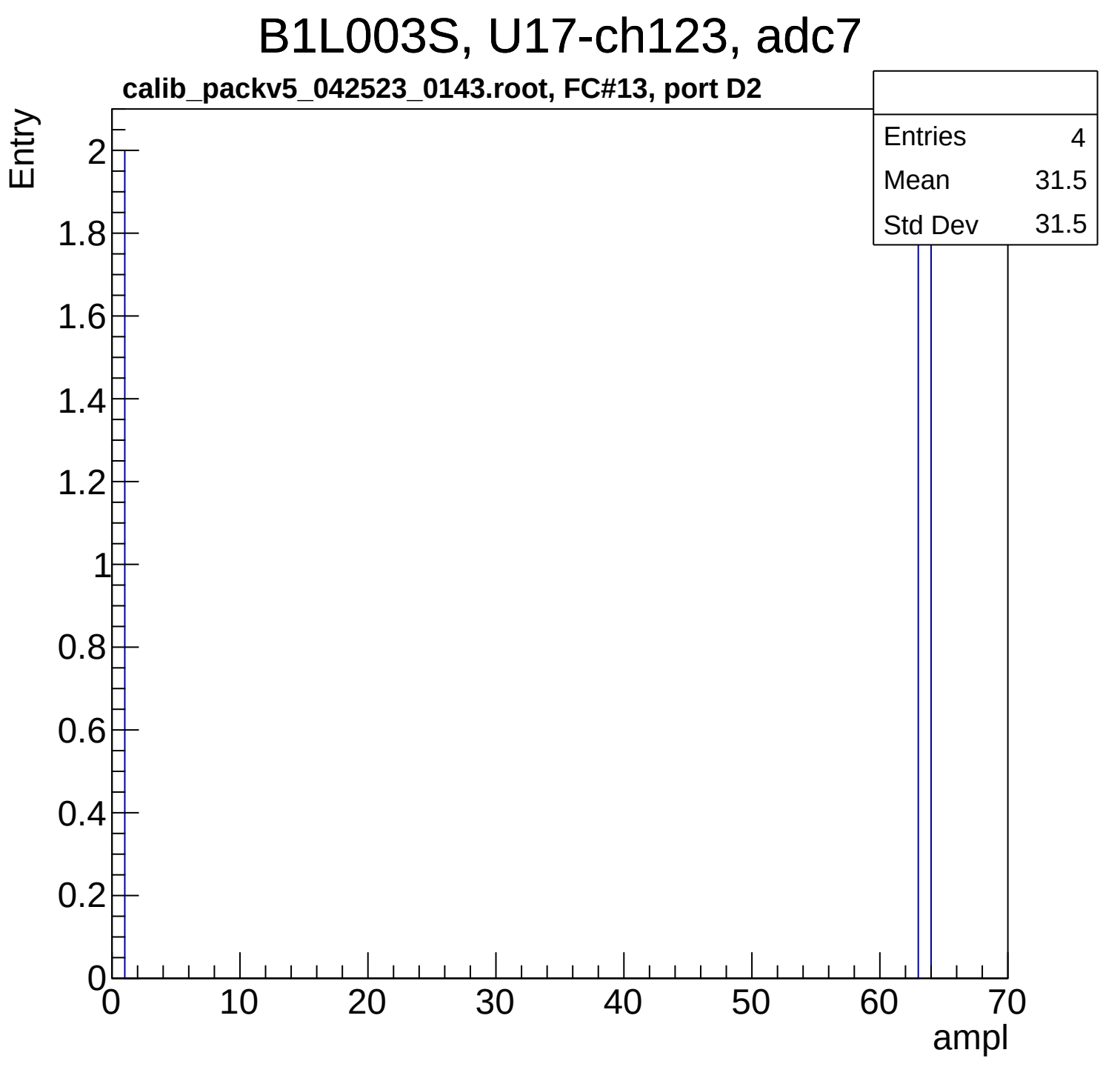

B1L003S, U17-ch123, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.11
Std Dev 1.853
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U17-ch123, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
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1.4
1.6
1.8
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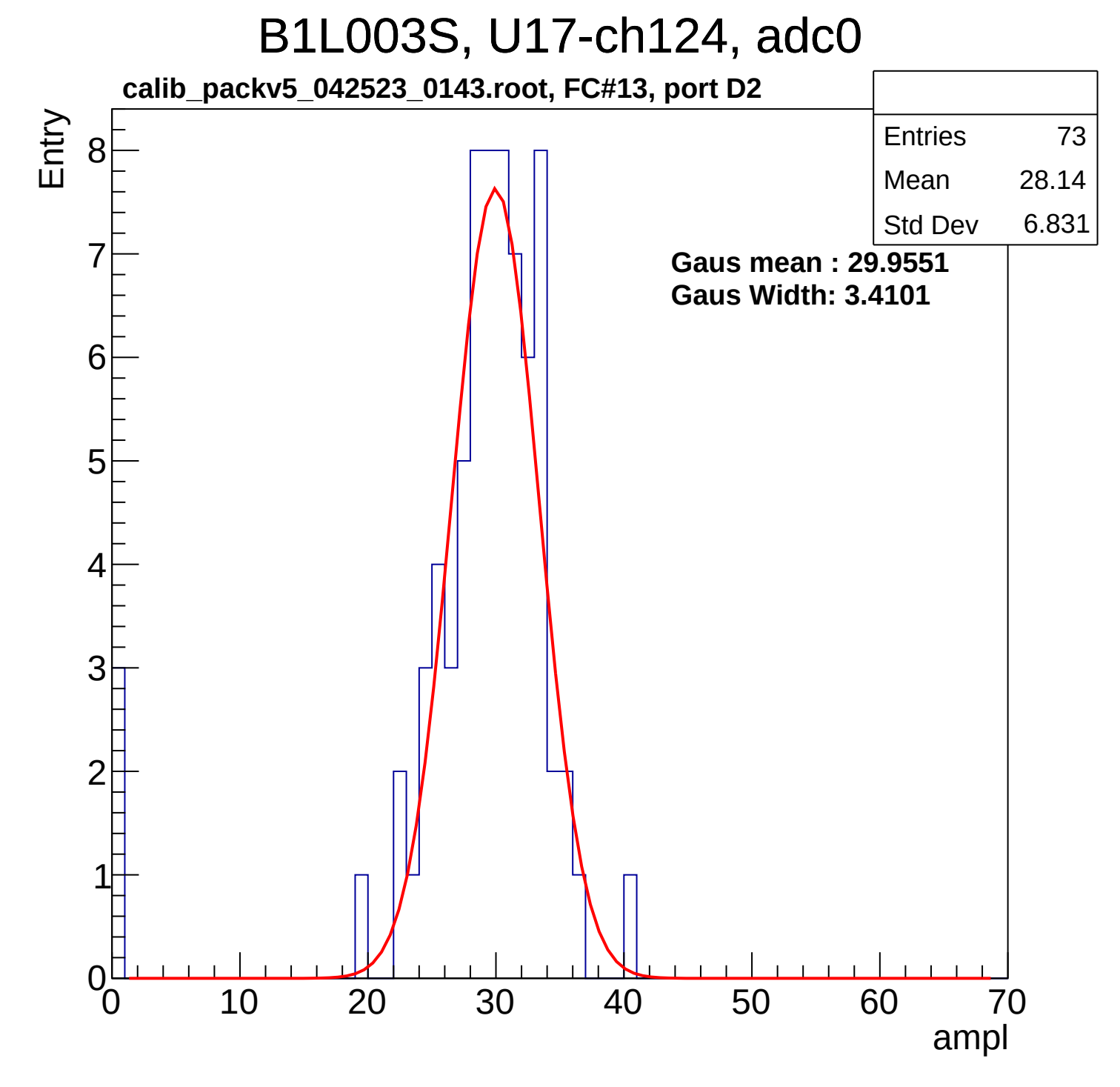
B1L003S, U17-ch124, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 28.14
Std Dev 6.831
Gaus mean : 29.9551
Gaus Width: 3.4101
Entry
ampl
0
1
2
3
4
5
6
7
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0
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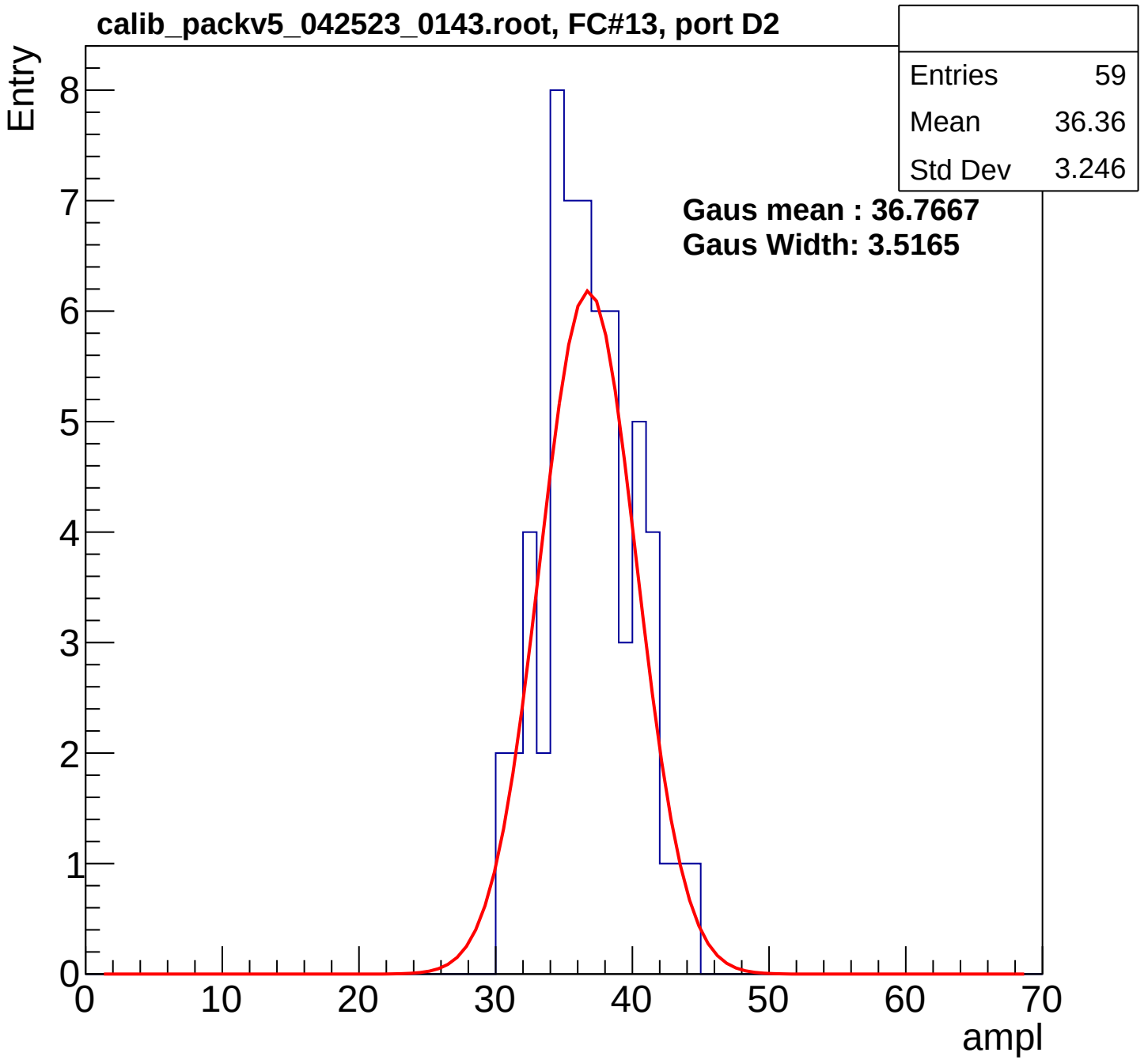
B1L003S, U17-ch124, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.36
Std Dev 3.246
Gaus mean : 36.7667
Gaus Width: 3.5165
Entry
ampl
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2
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4
5
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0
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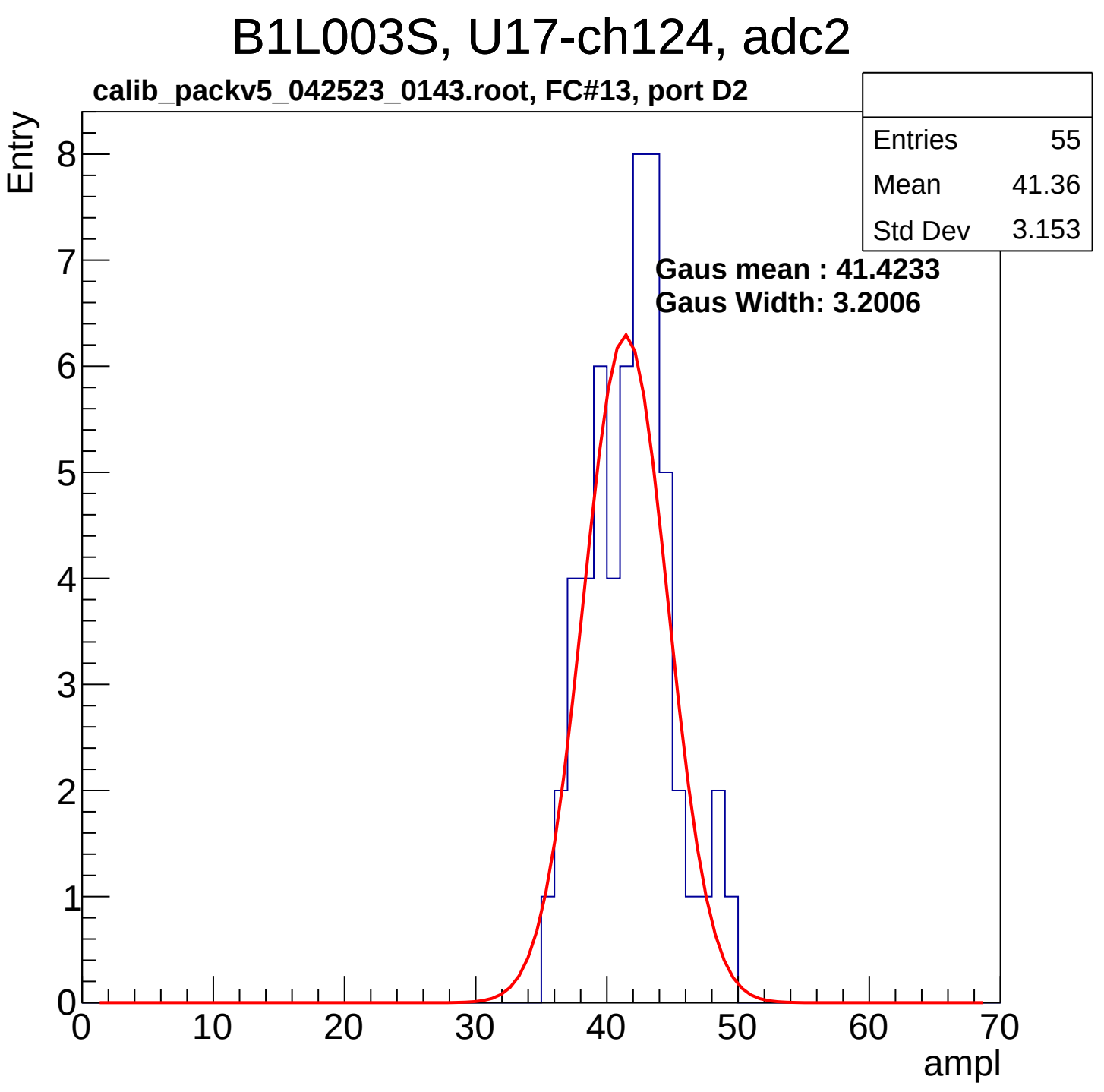
B1L003S, U17-ch124, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.36
Std Dev 3.153
Gaus mean : 41.4233
Gaus Width: 3.2006
Entry
ampl
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2
3
4
5
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7
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0
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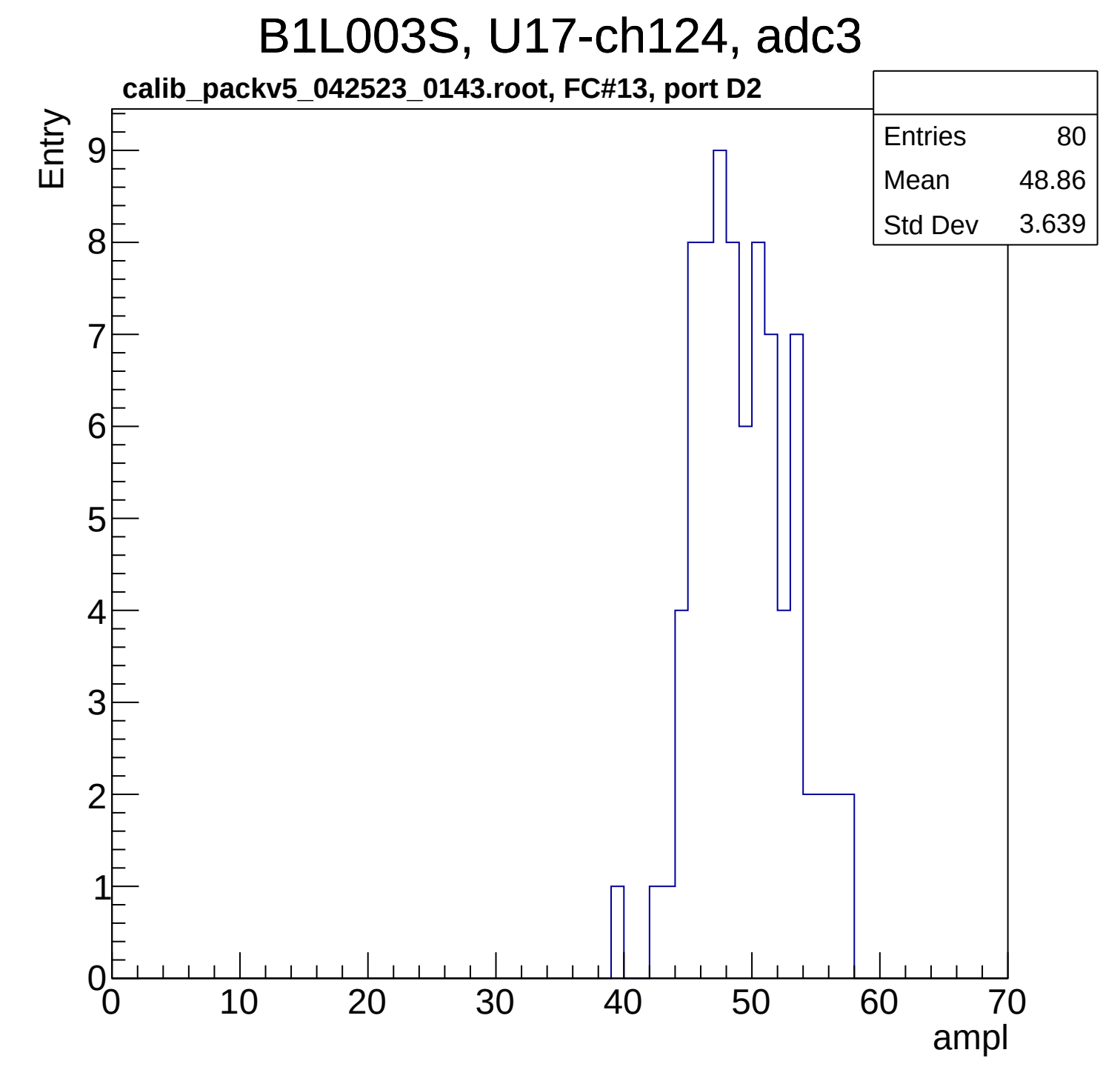
B1L003S, U17-ch124, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 48.86
Std Dev 3.639
Entry
ampl
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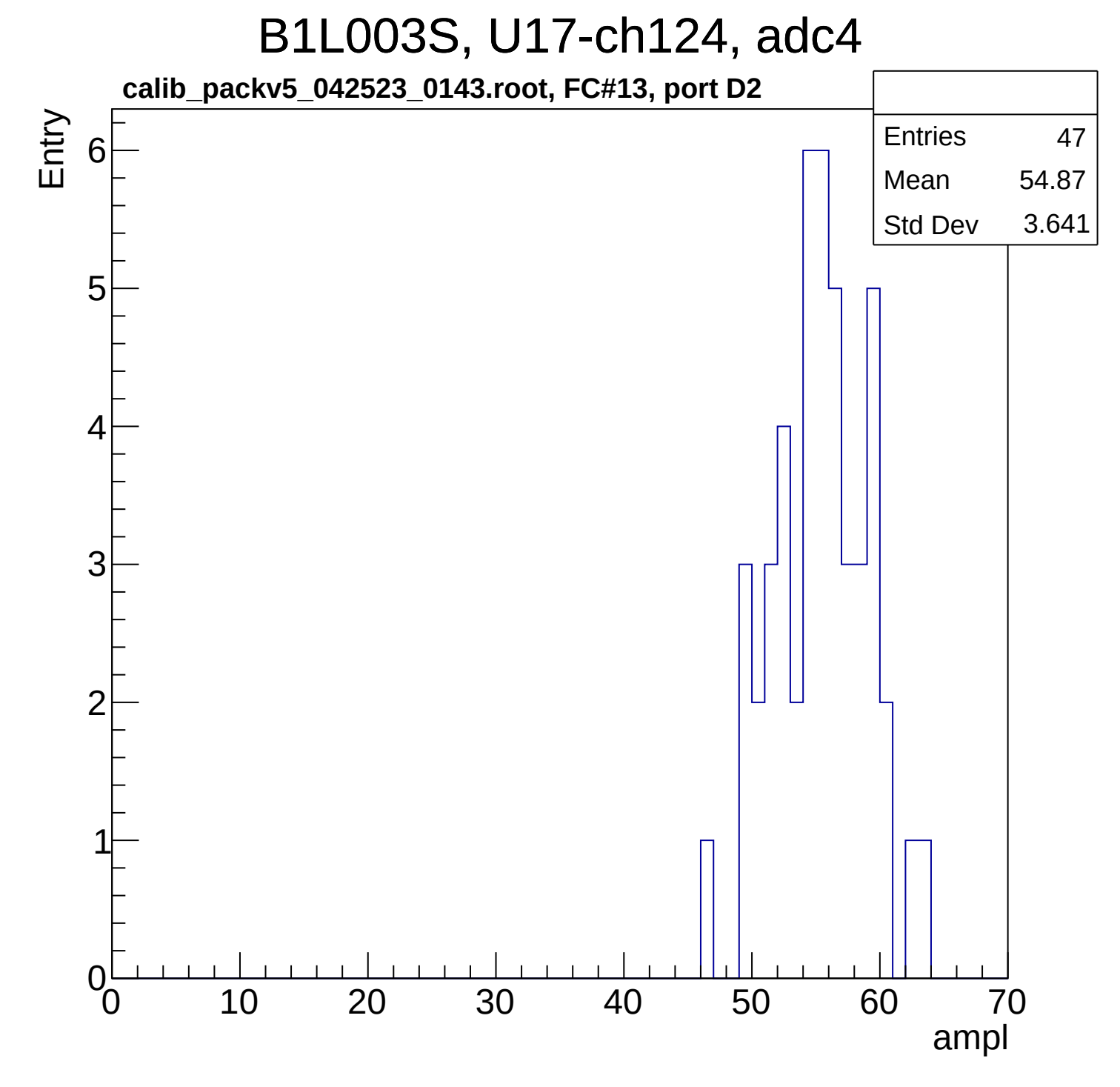
B1L003S, U17-ch124, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.87
Std Dev 3.641
Entry
ampl
0
1
2
3
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0
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40
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60
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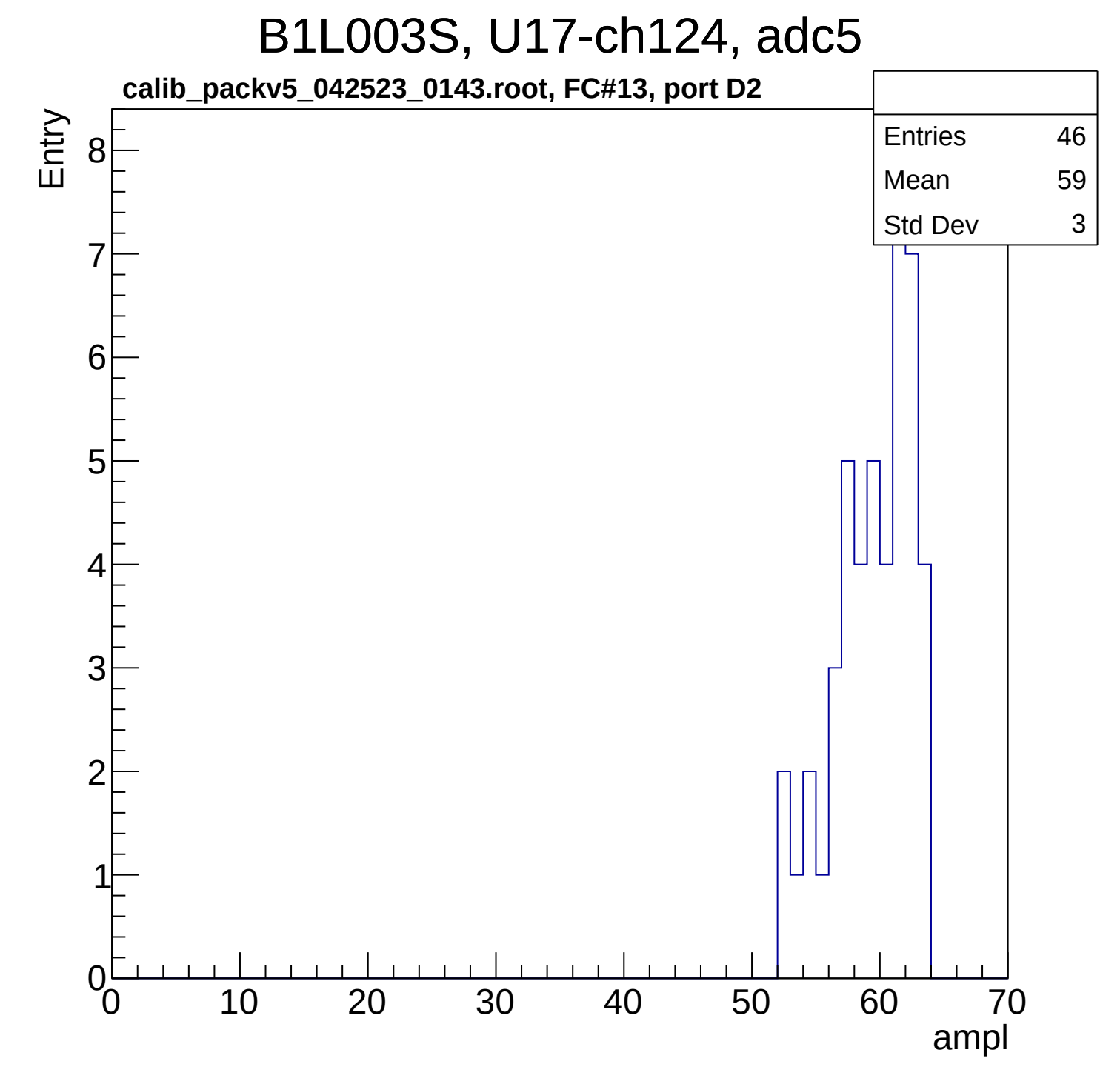
B1L003S, U17-ch124, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59
Std Dev 3
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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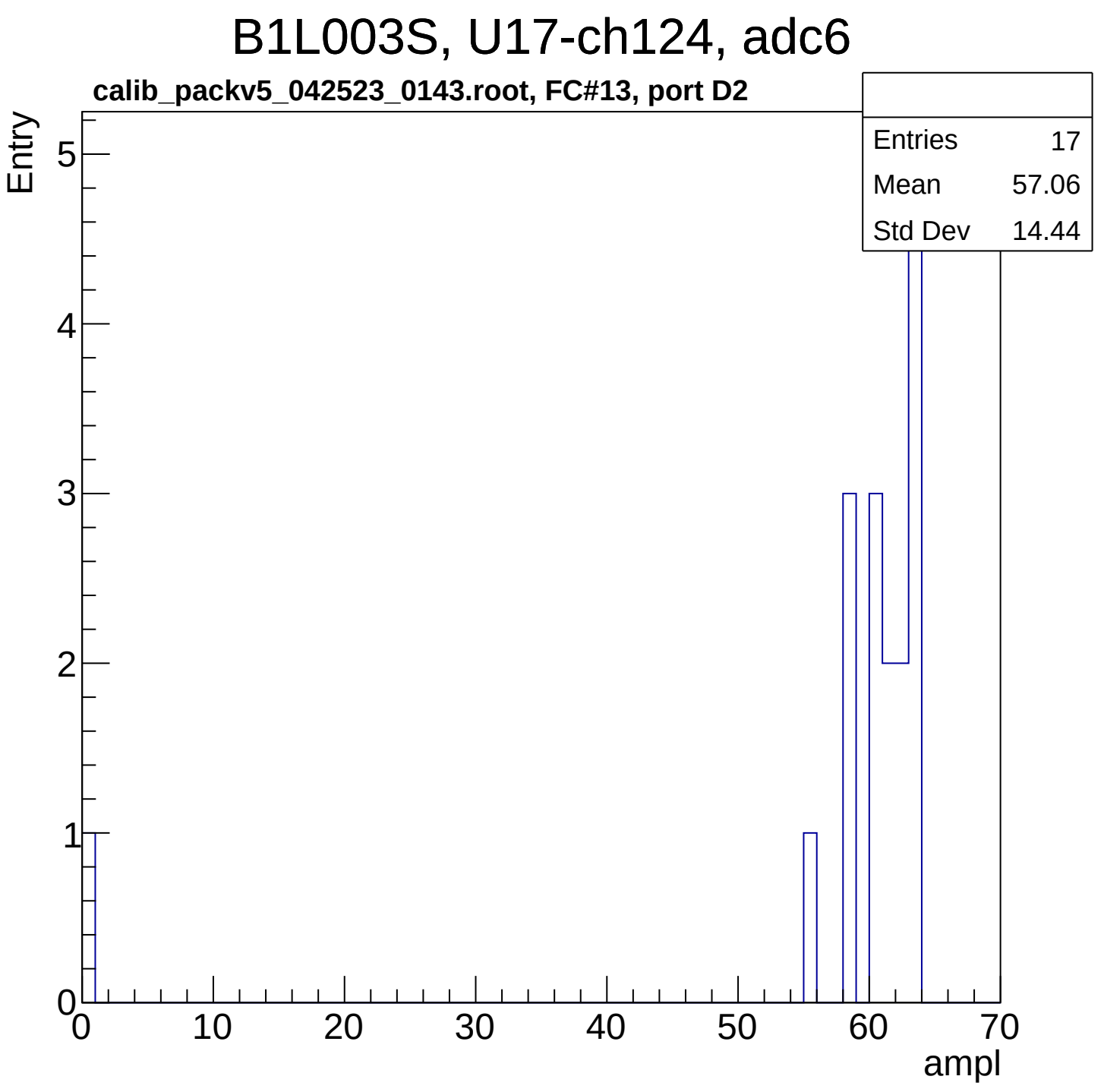
B1L003S, U17-ch124, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.06
Std Dev 14.44
Entry
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1
2
3
4
5
0
10
20
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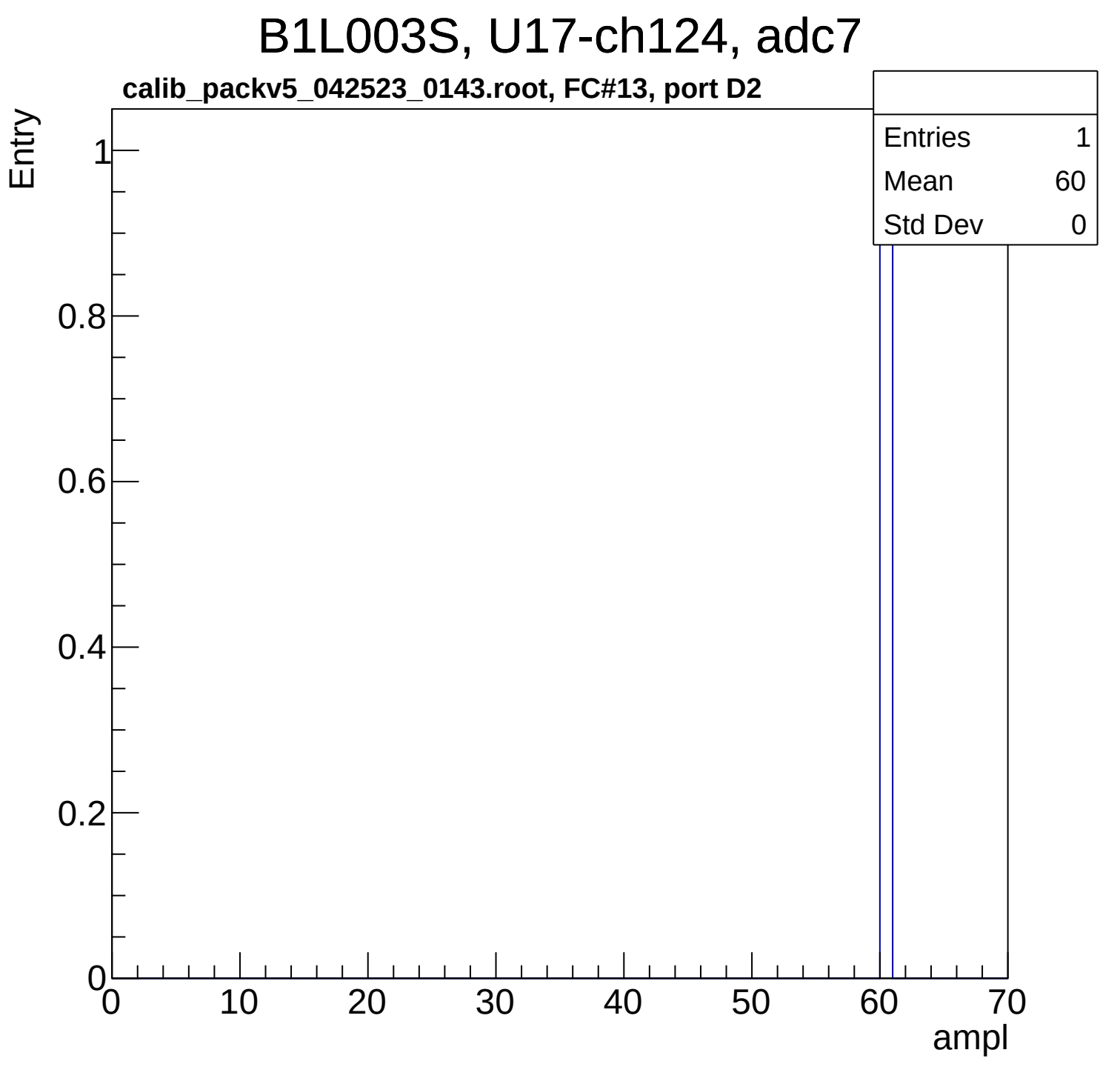
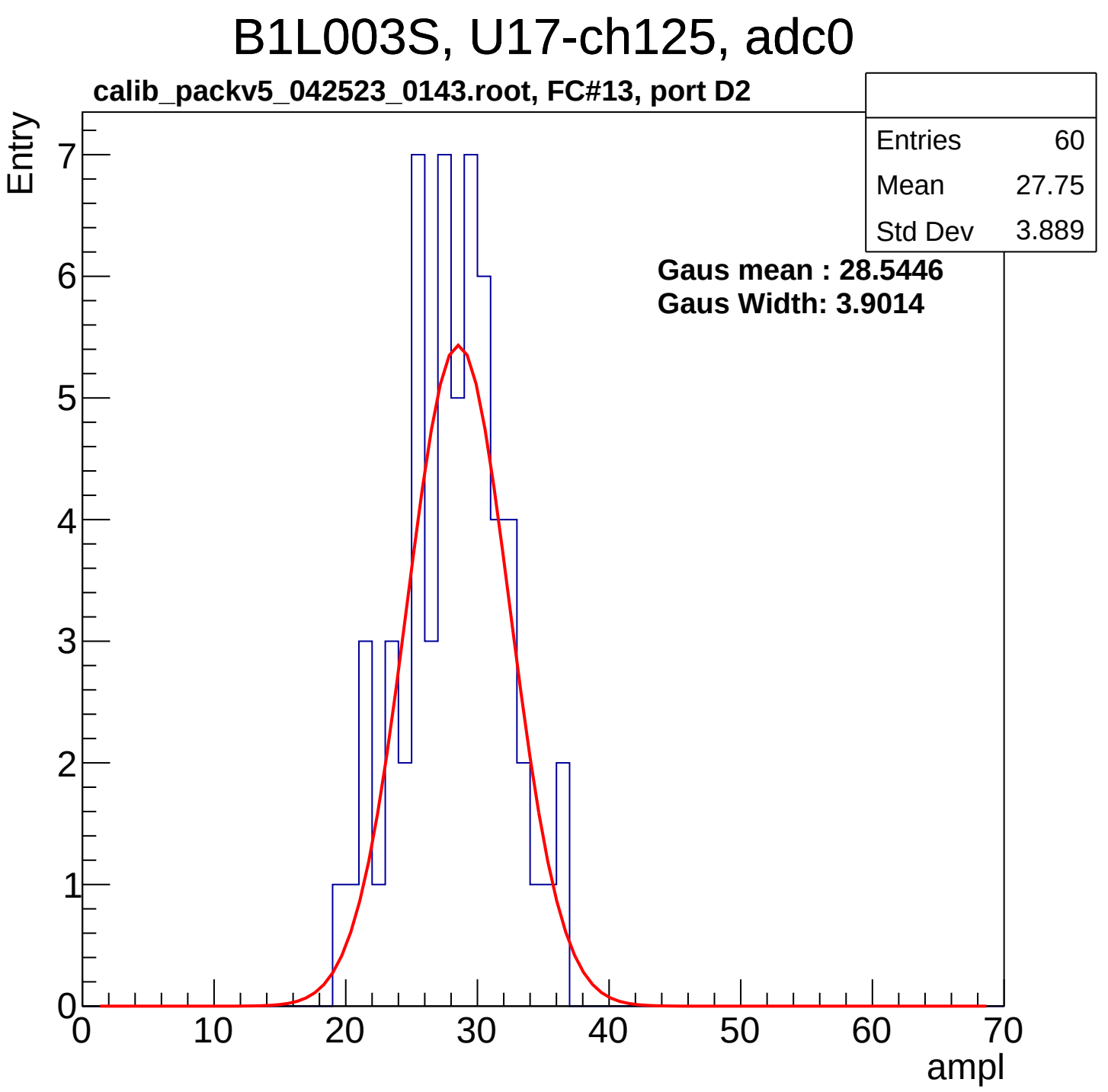
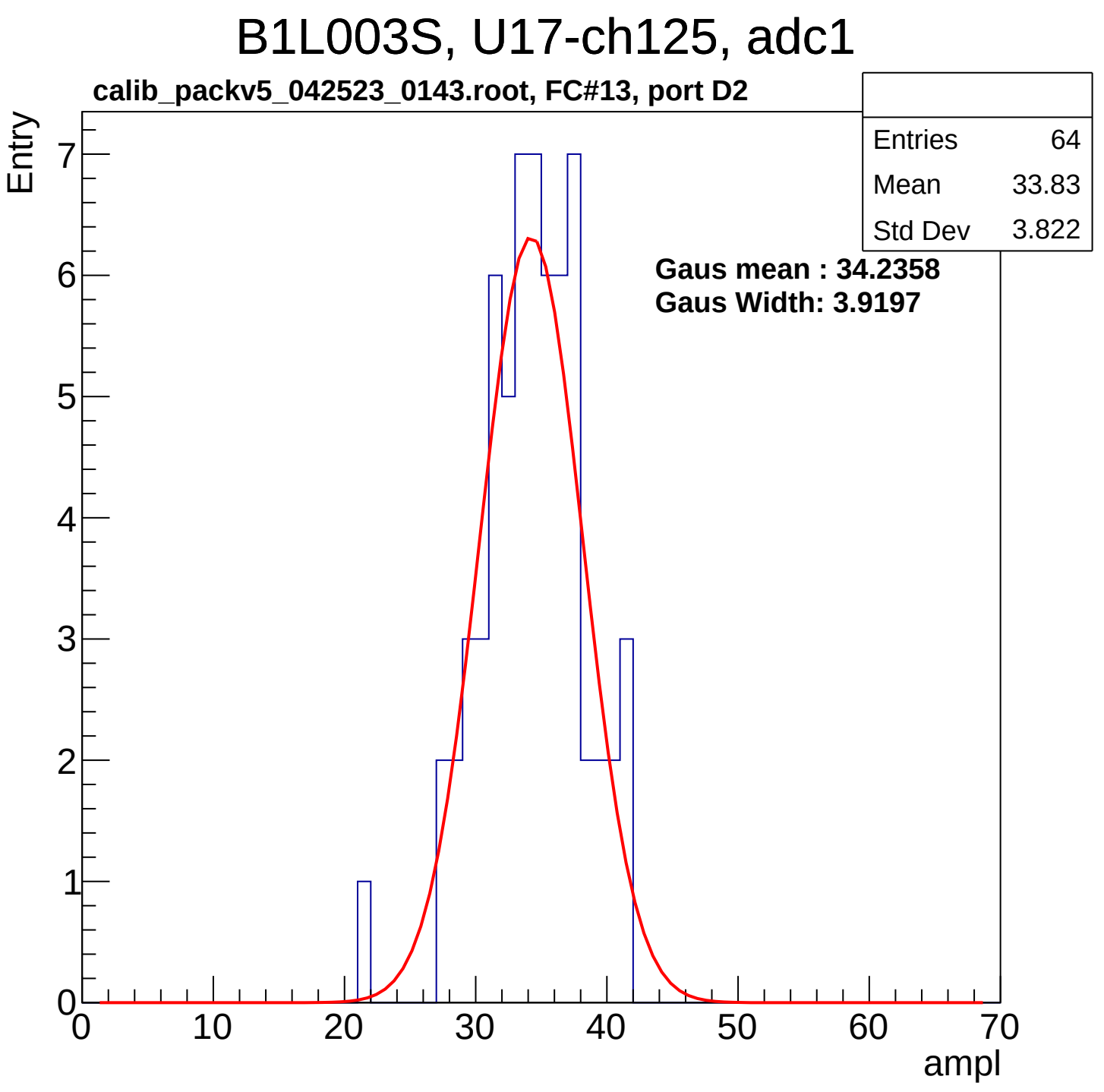
50
60
70
ampl


B1L003S, U17-ch124, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch125, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 27.75
Std Dev 3.889
Gaus mean : 28.5446
Gaus Width: 3.9014
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U17-ch125, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 33.83
Std Dev 3.822
Gaus mean : 34.2358
Gaus Width: 3.9197
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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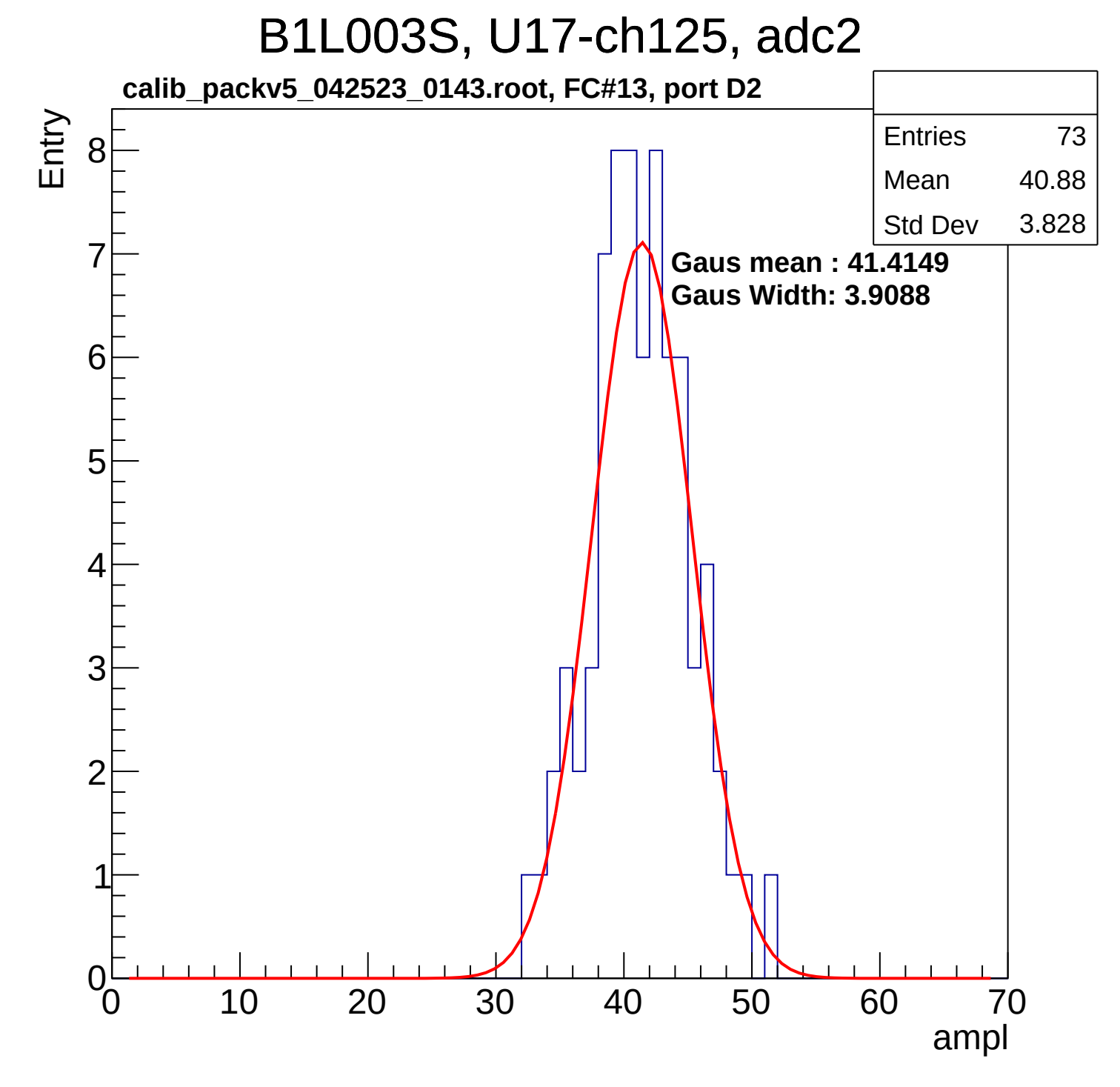
B1L003S, U17-ch125, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 40.88
Std Dev 3.828
Gaus mean : 41.4149
Gaus Width: 3.9088
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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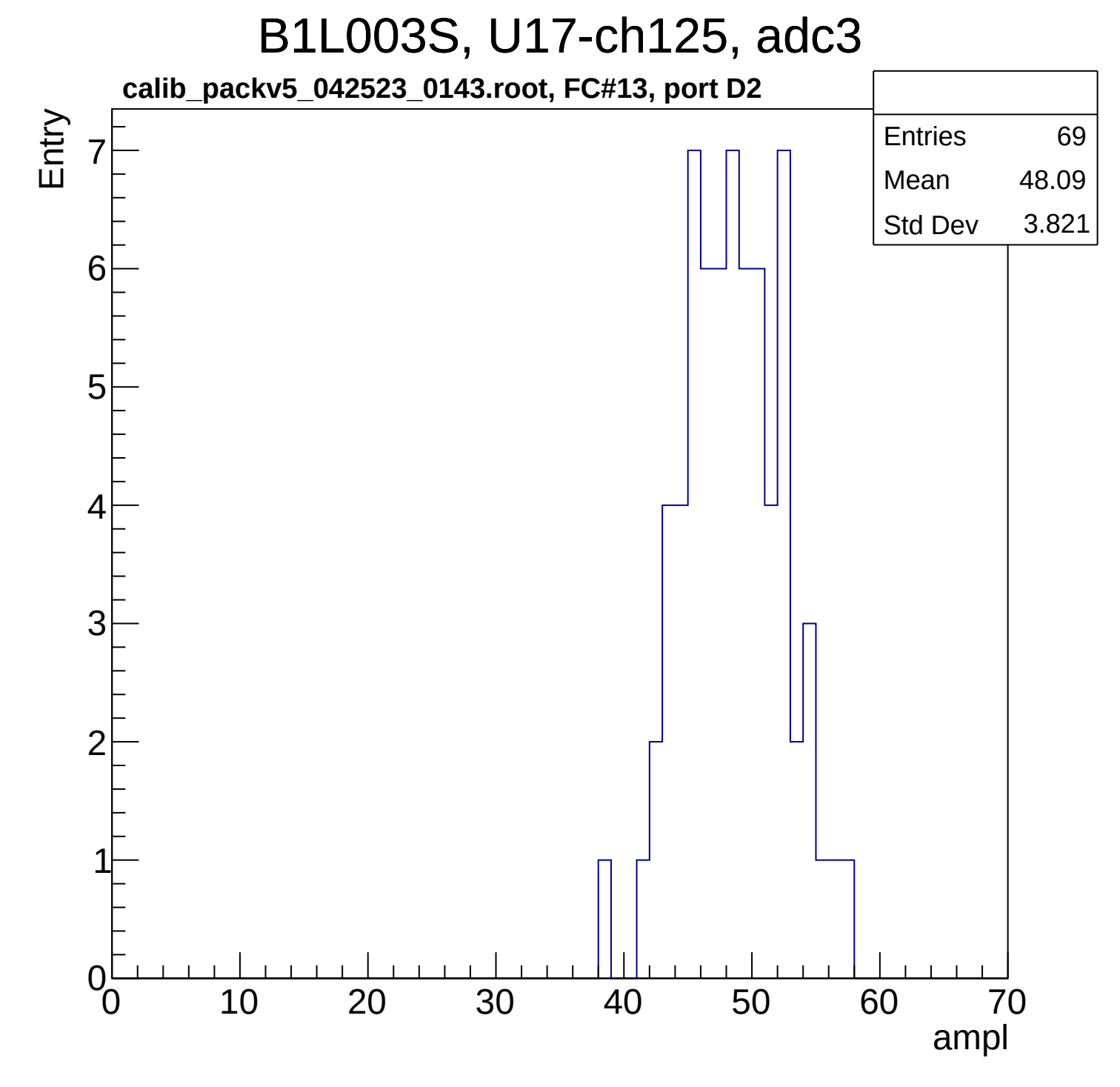
B1L003S, U17-ch125, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.09
Std Dev 3.821
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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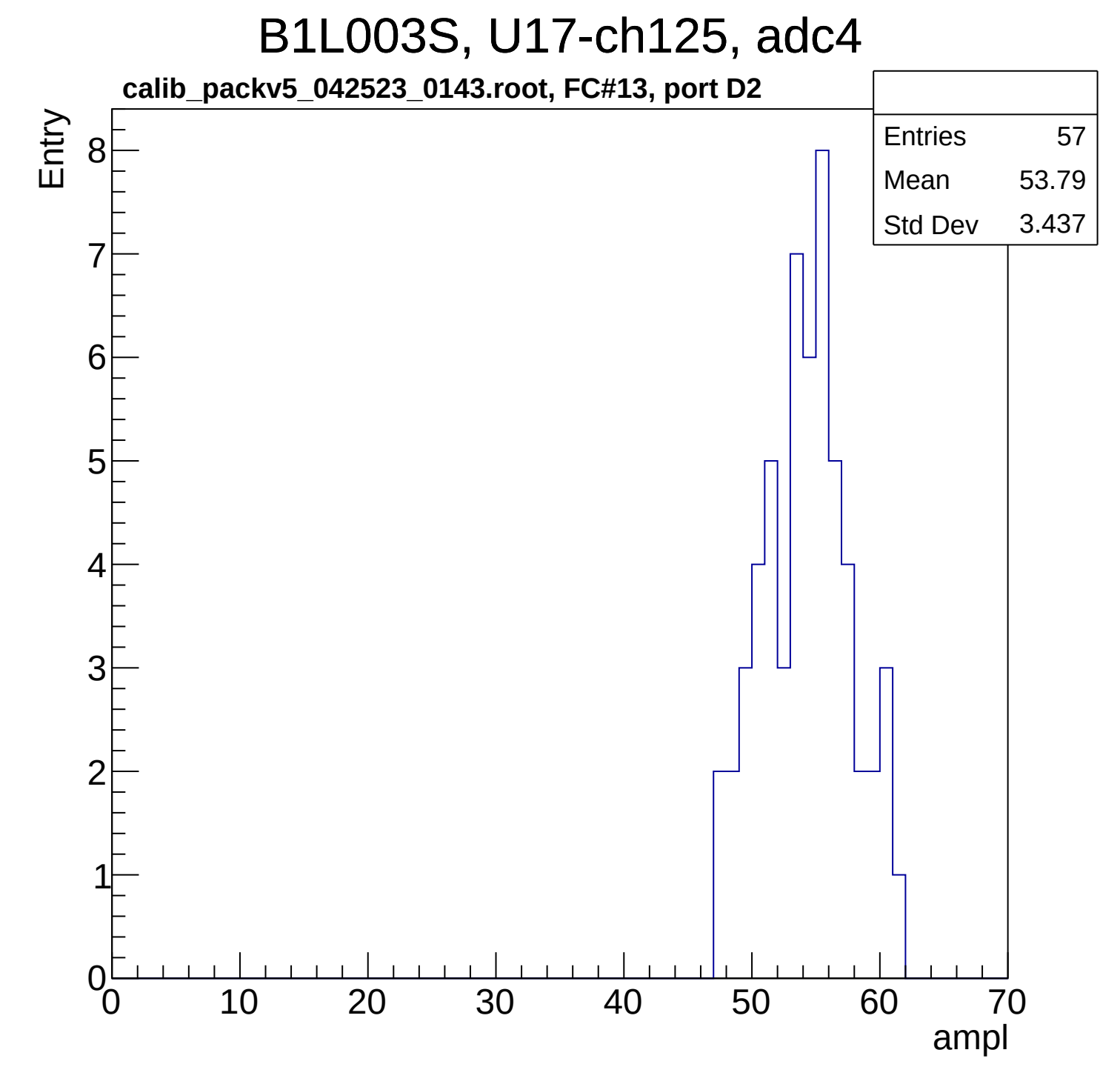
B1L003S, U17-ch125, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 53.79
Std Dev 3.437
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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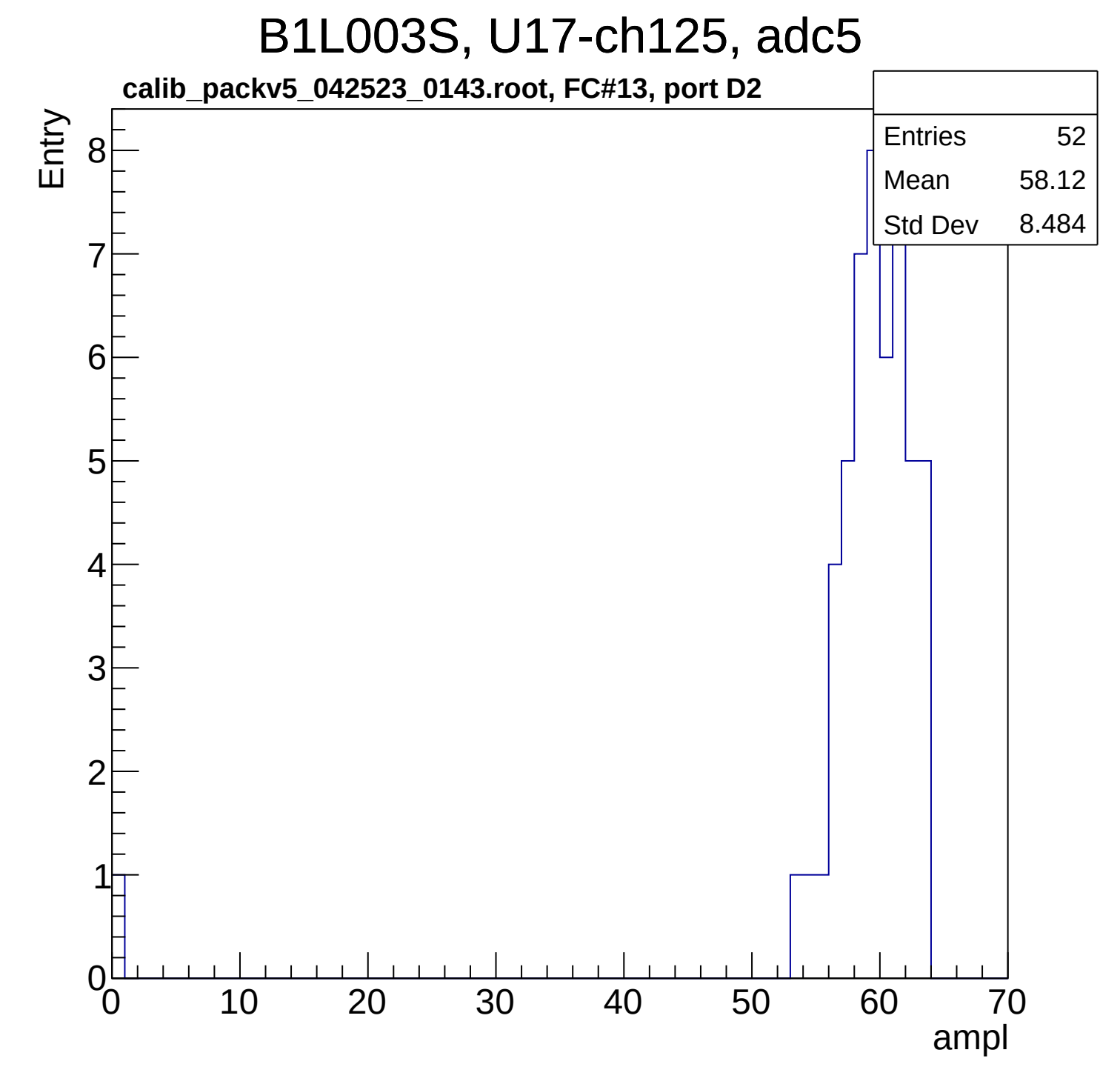
B1L003S, U17-ch125, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.12
Std Dev 8.484
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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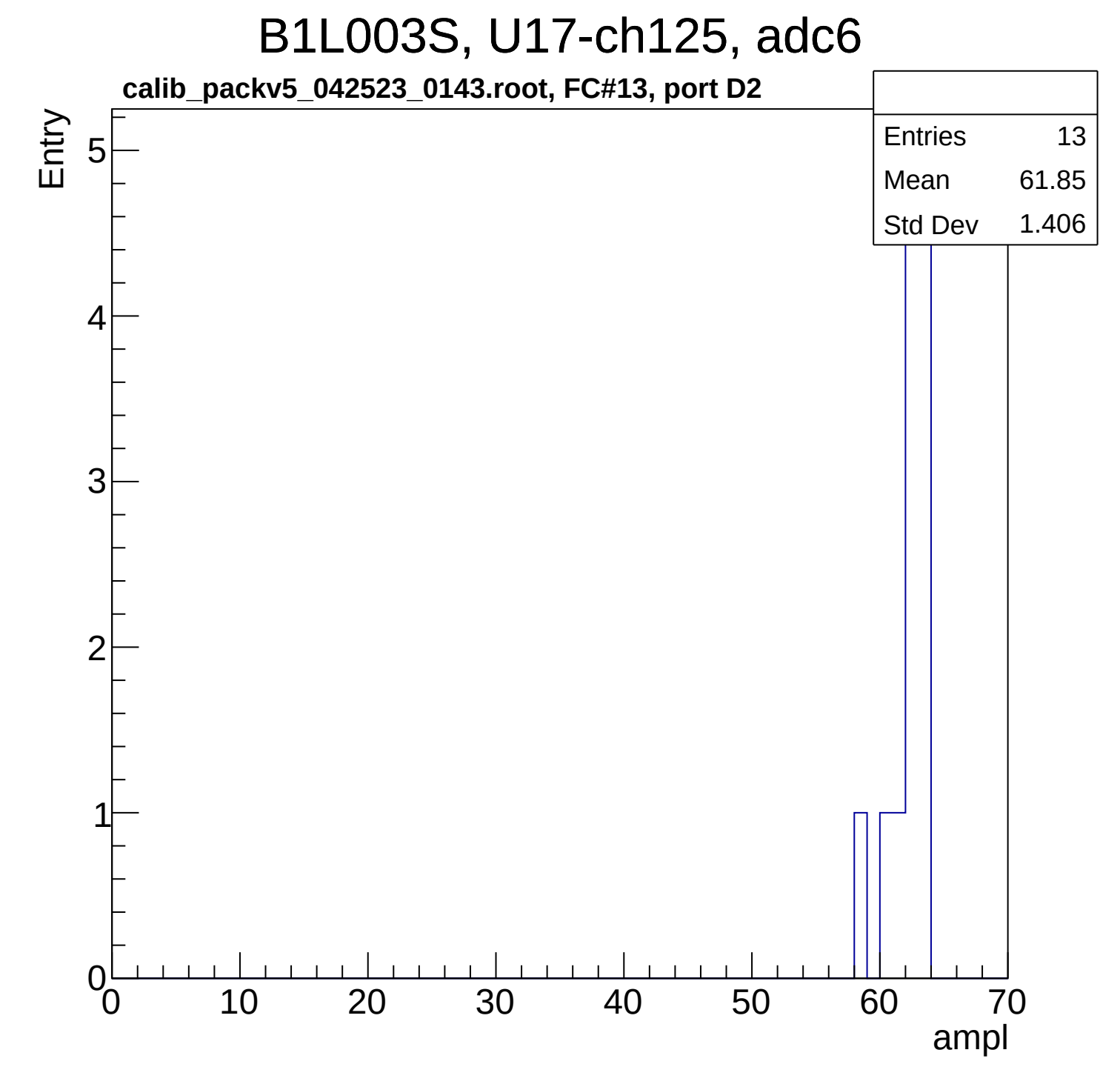
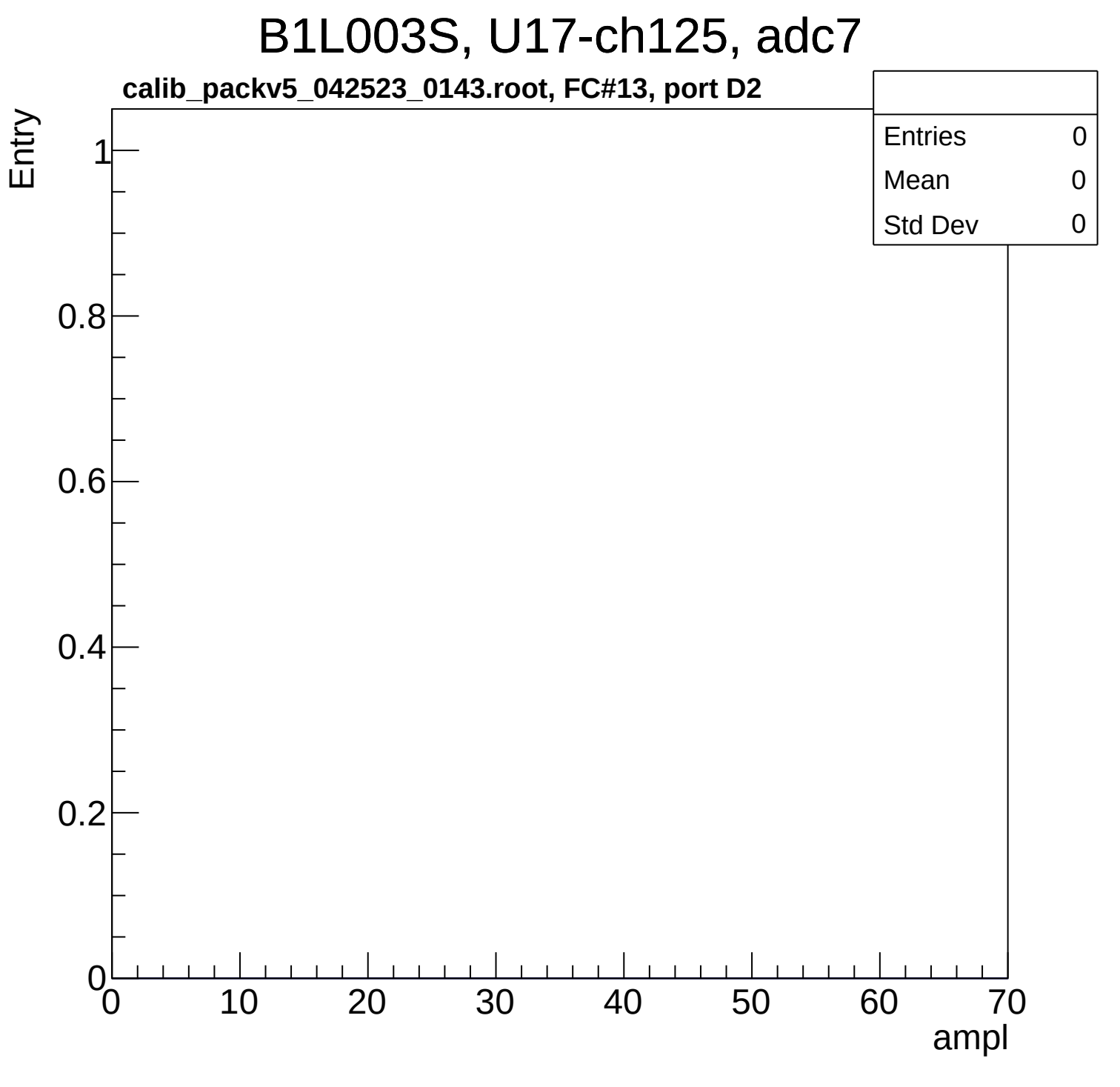
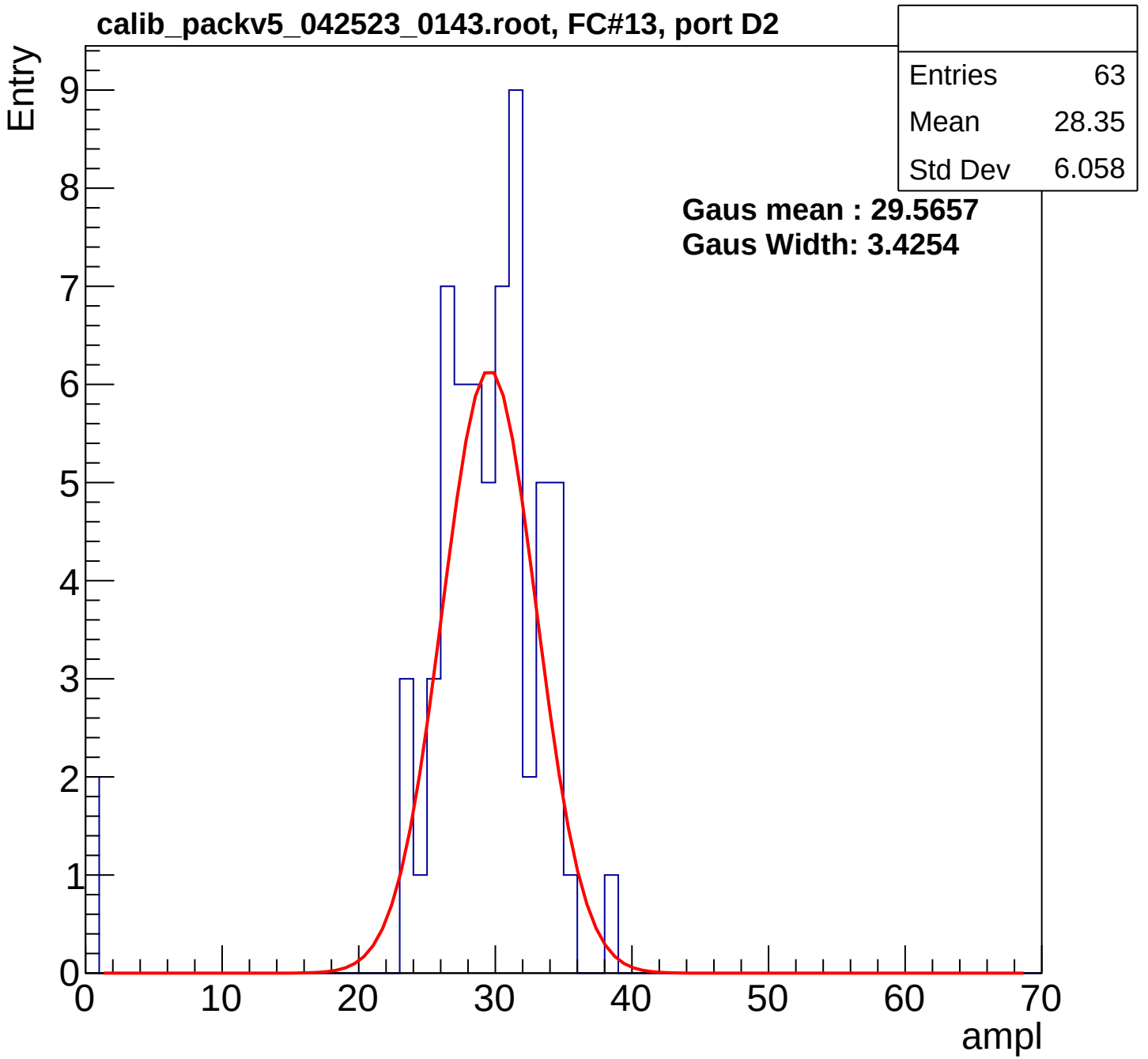

B1L003S, U17-ch125, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.85
Std Dev 1.406
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U17-ch125, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U17-ch126, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.35
Std Dev 6.058
Gaus mean : 29.5657
Gaus Width: 3.4254
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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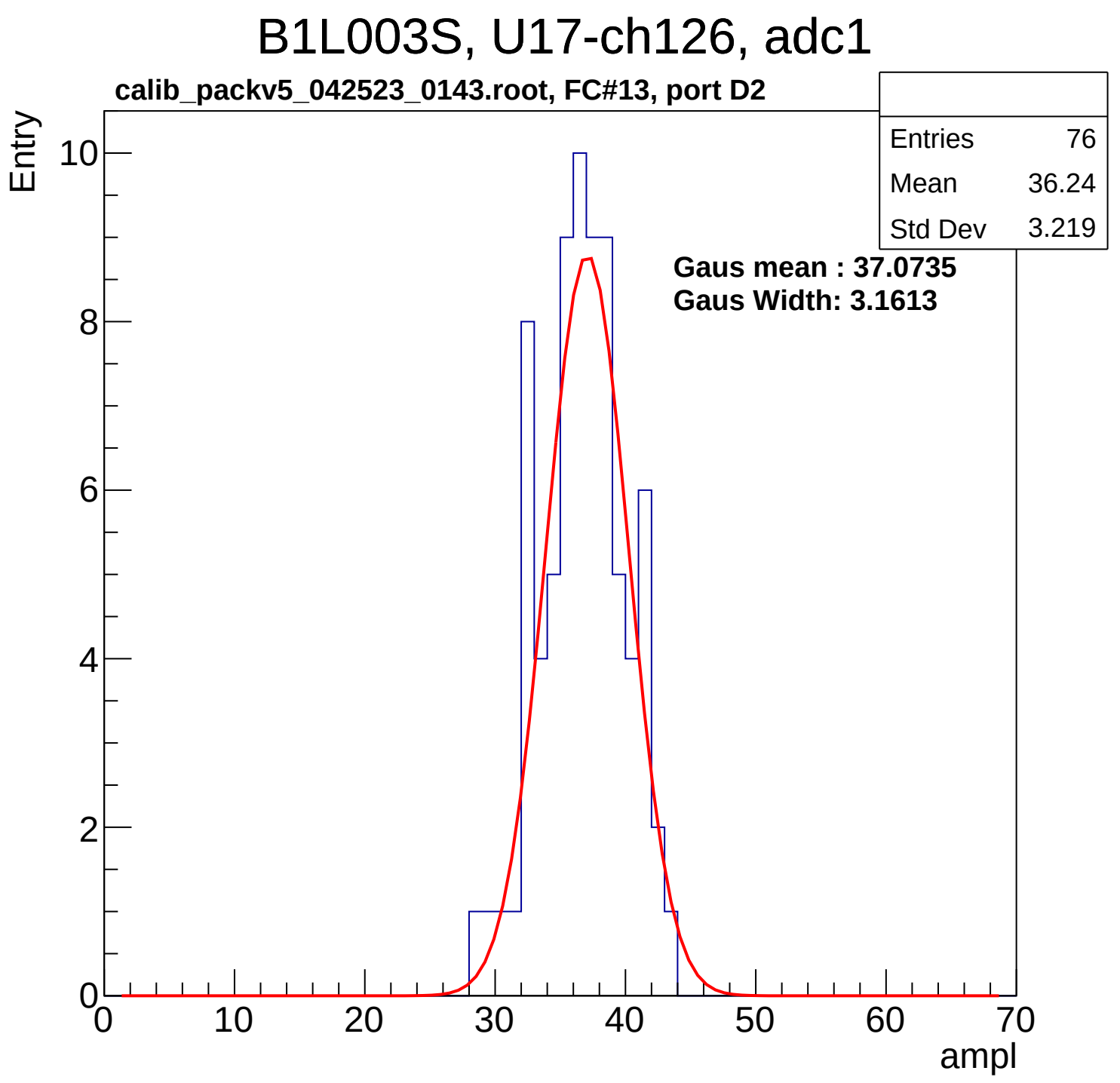
B1L003S, U17-ch126, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.24
Std Dev 3.219
Gaus mean : 37.0735
Gaus Width: 3.1613
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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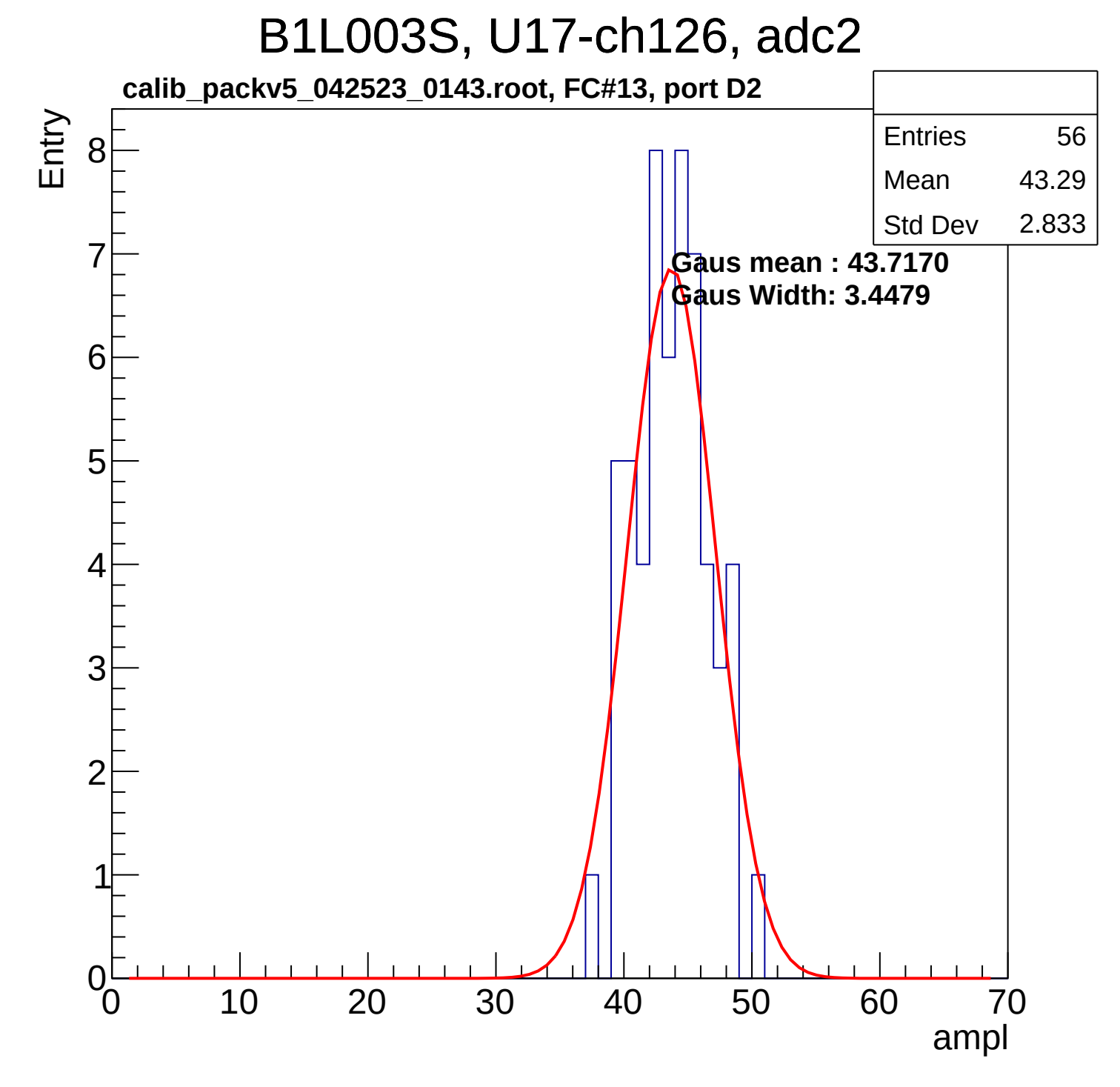
B1L003S, U17-ch126, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 43.29
Std Dev 2.833
Gaus mean : 43.7170
Gaus Width: 3.4479
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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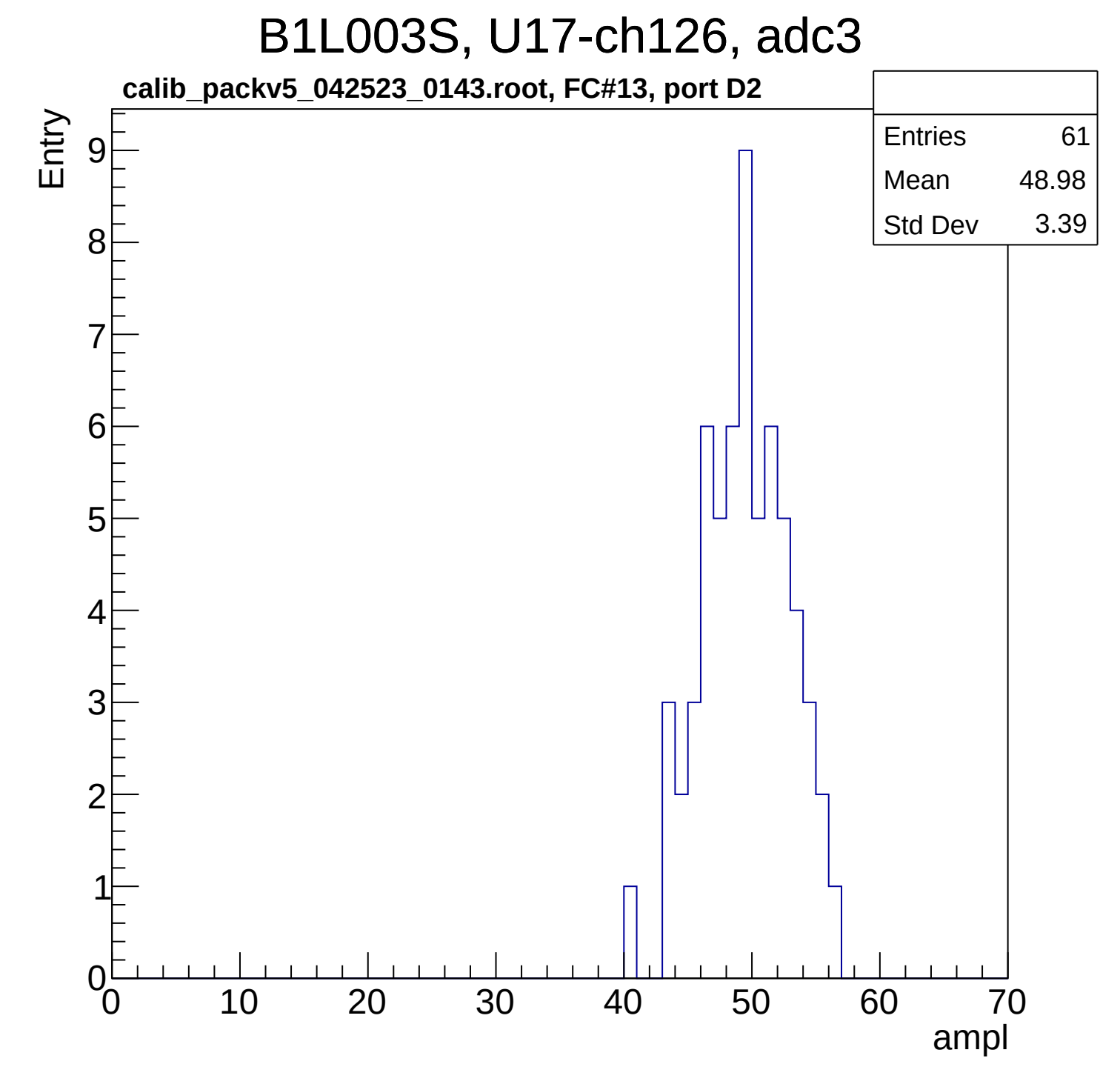
B1L003S, U17-ch126, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.98
Std Dev 3.39
Entry
ampl
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3
4
5
6
7
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0
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20
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60
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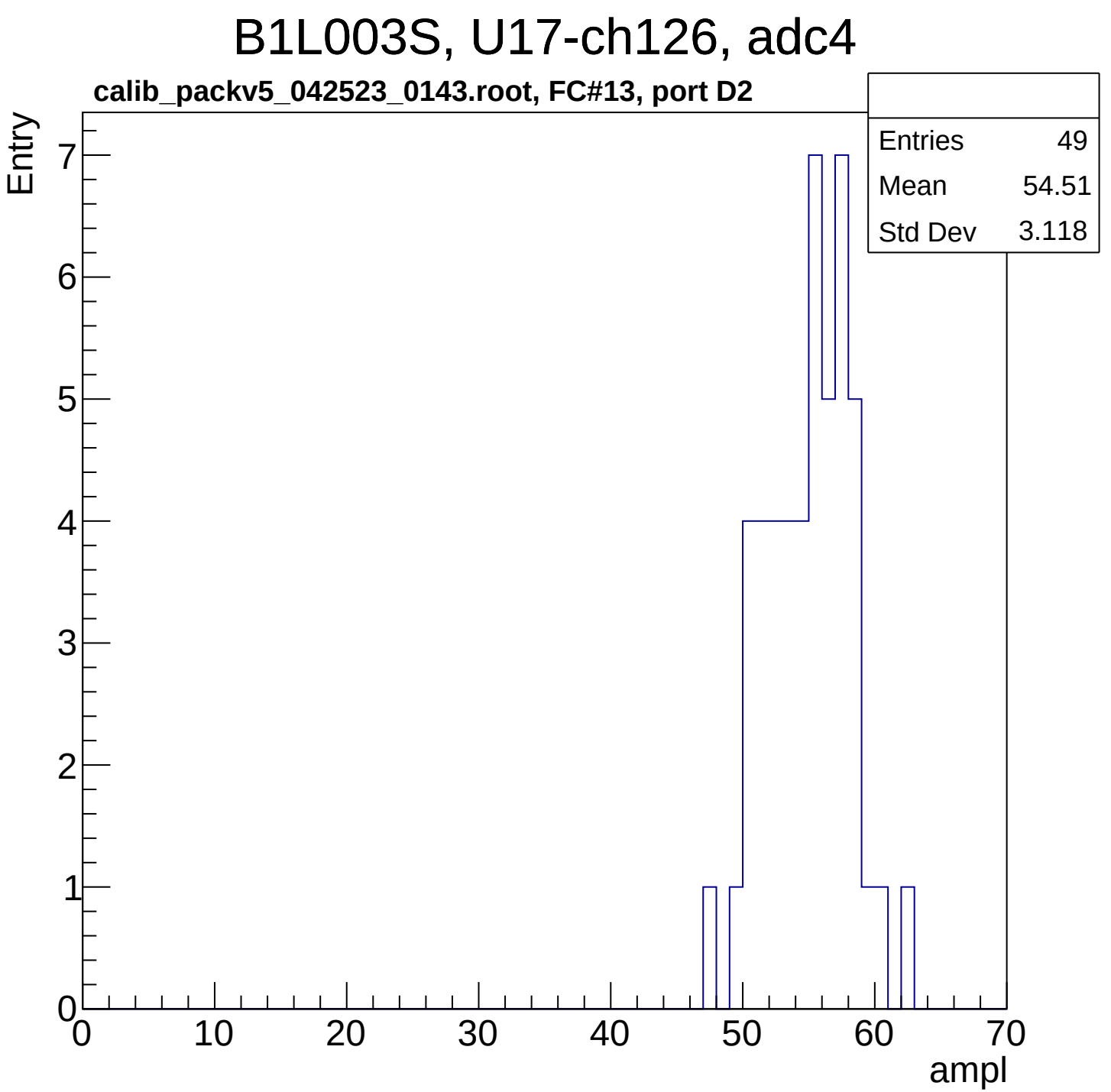
B1L003S, U17-ch126, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.51
Std Dev 3.118
Entry
ampl
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0
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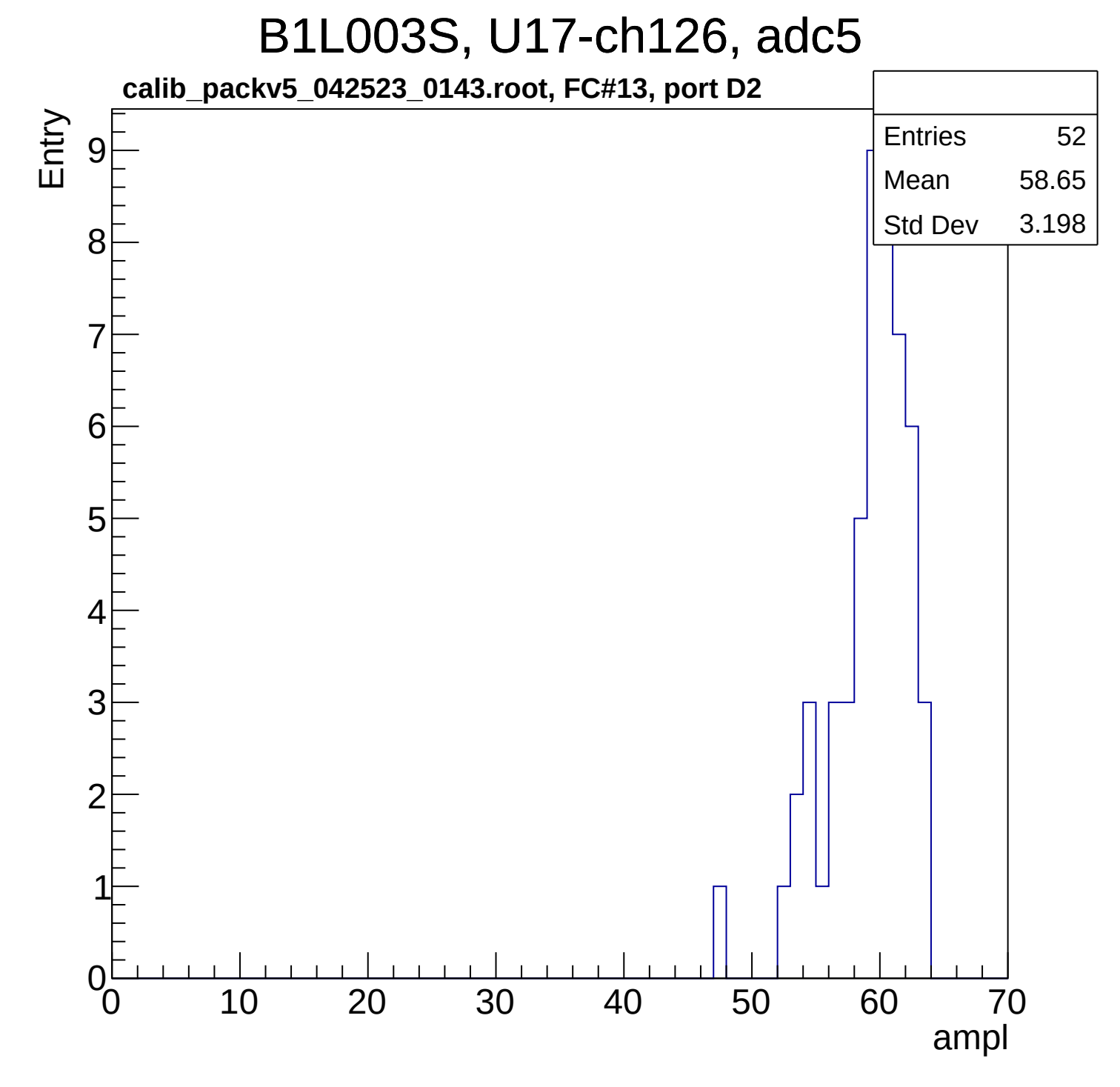
B1L003S, U17-ch126, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.65
Std Dev 3.198
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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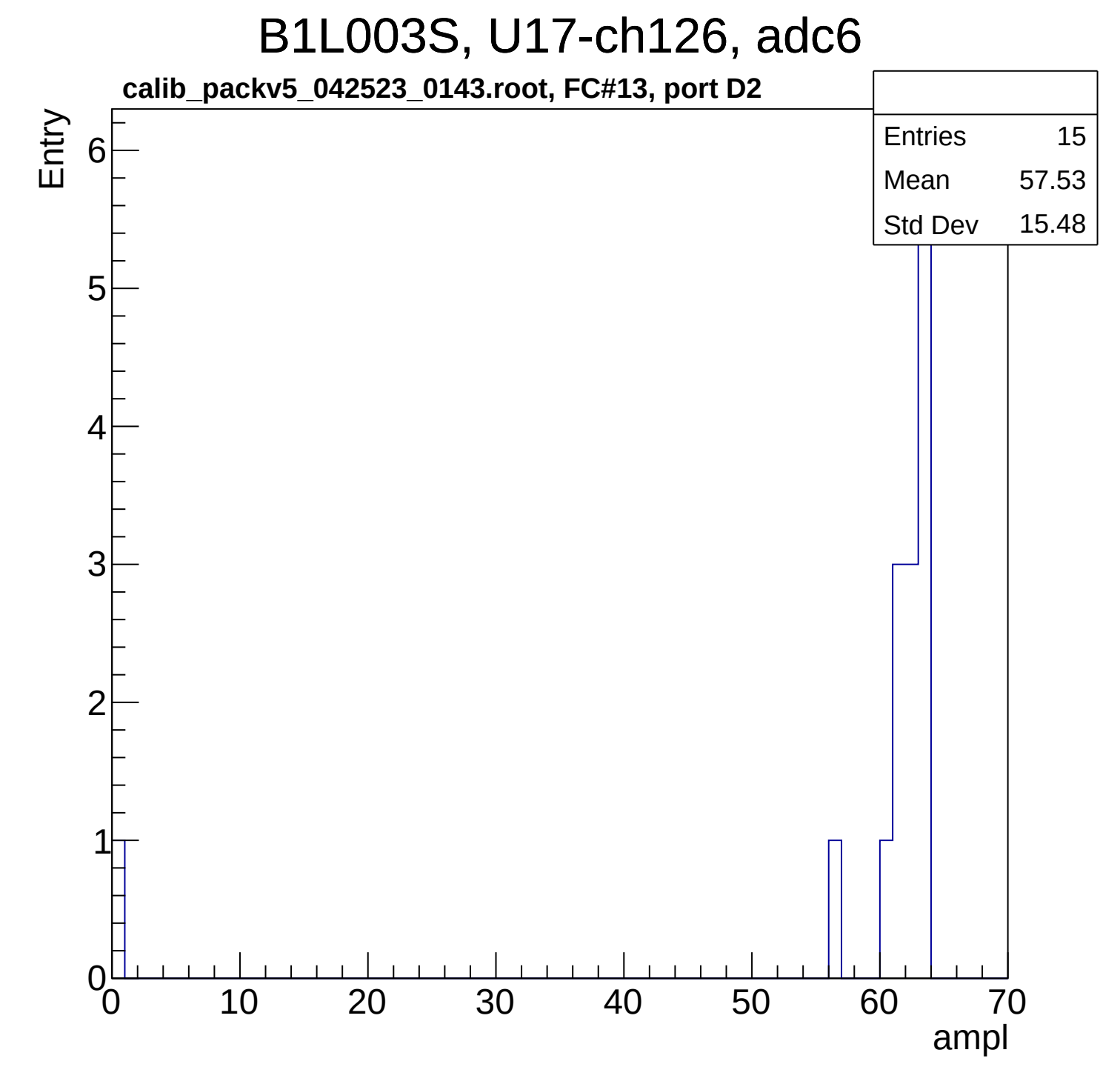
B1L003S, U17-ch126, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.53
Std Dev 15.48
Entry
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4
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10
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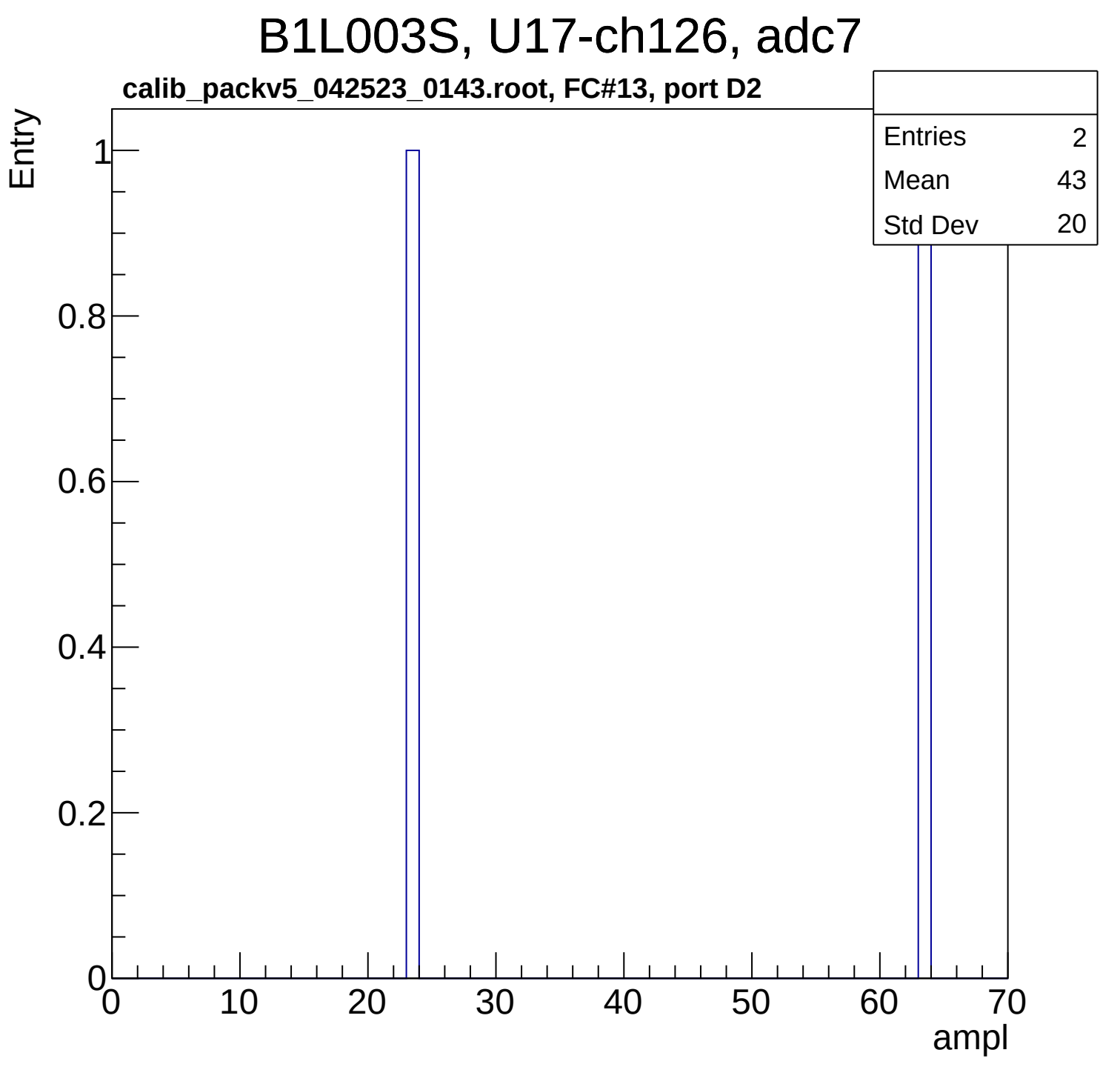
50
60
70
ampl


B1L003S, U17-ch126, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 43
Std Dev 20
Entry
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10
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50
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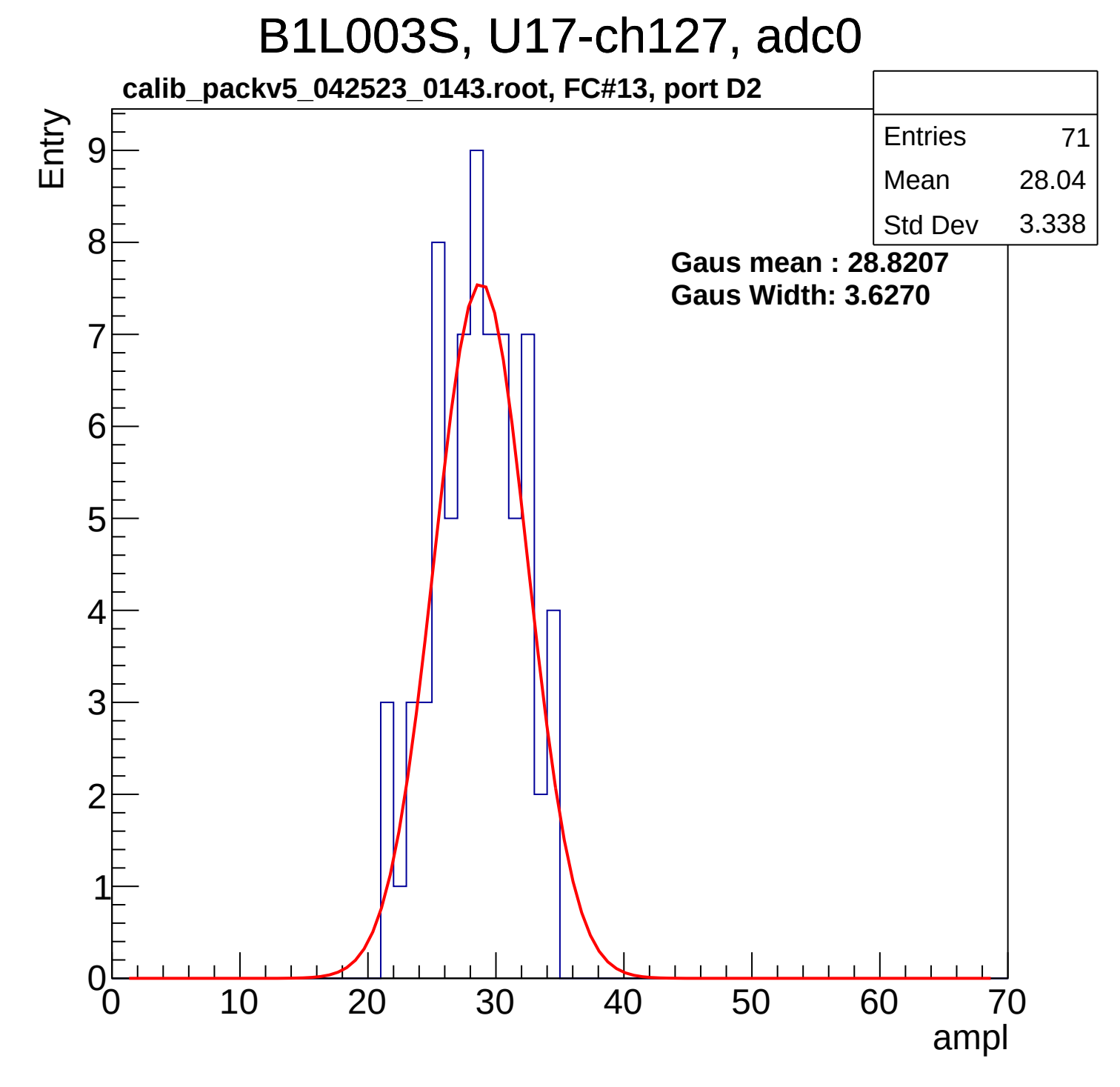
70
ampl


B1L003S, U17-ch127, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 28.04
Std Dev 3.338
Gaus mean : 28.8207
Gaus Width: 3.6270
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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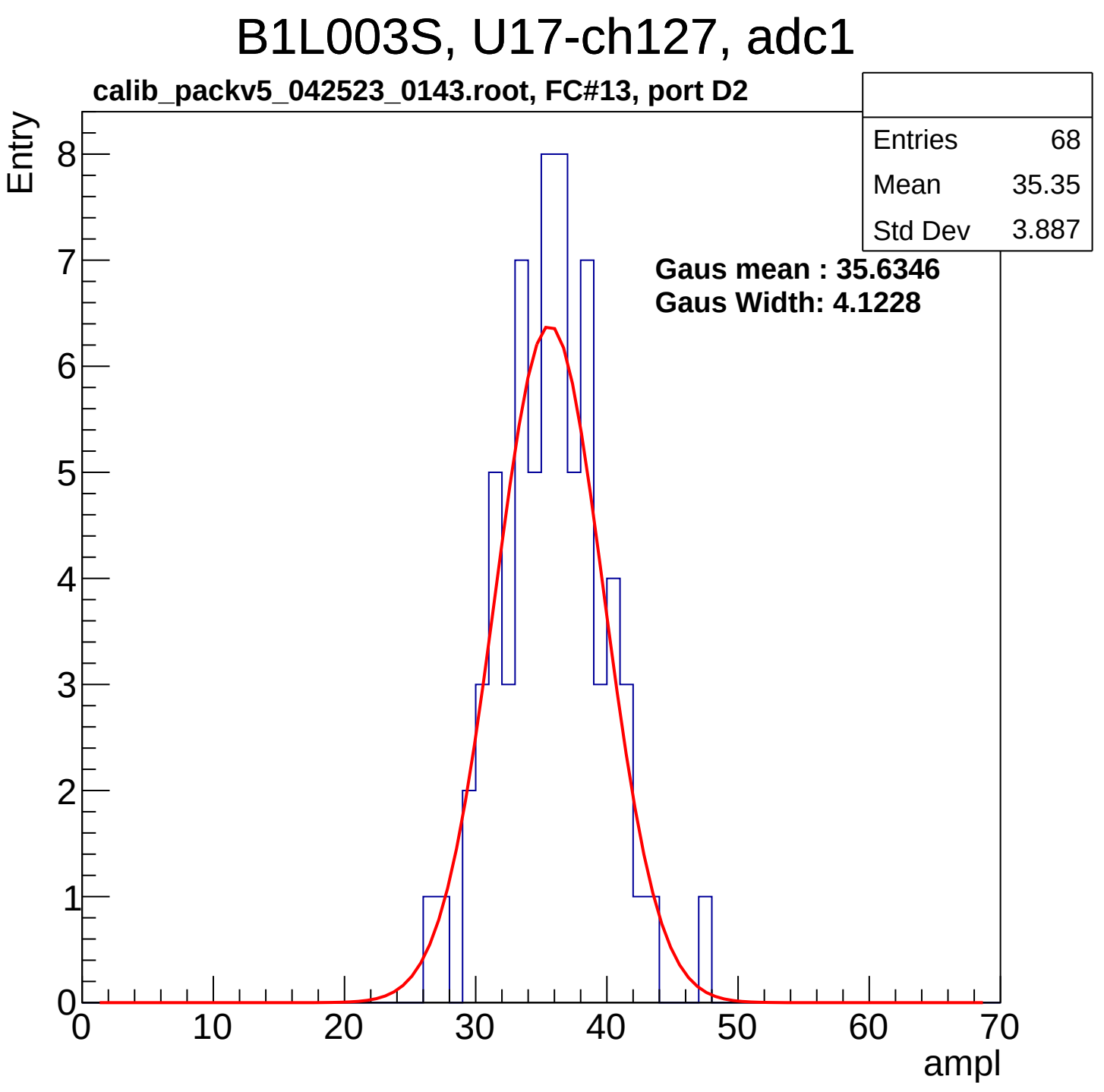
B1L003S, U17-ch127, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.35
Std Dev 3.887
Gaus mean : 35.6346
Gaus Width: 4.1228
Entry
ampl
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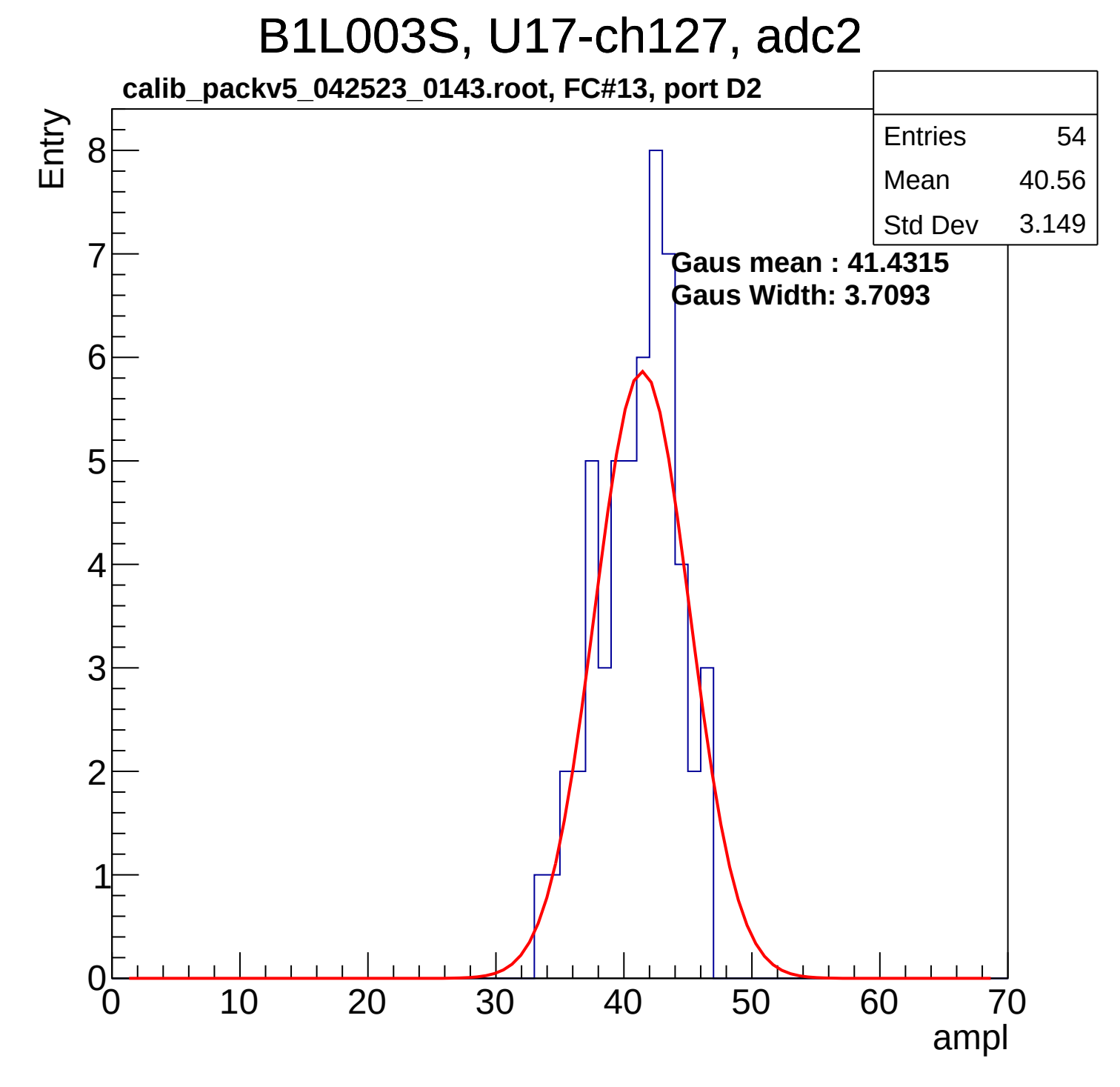
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U17-ch127, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 40.56
Std Dev 3.149
Gaus mean : 41.4315
Gaus Width: 3.7093
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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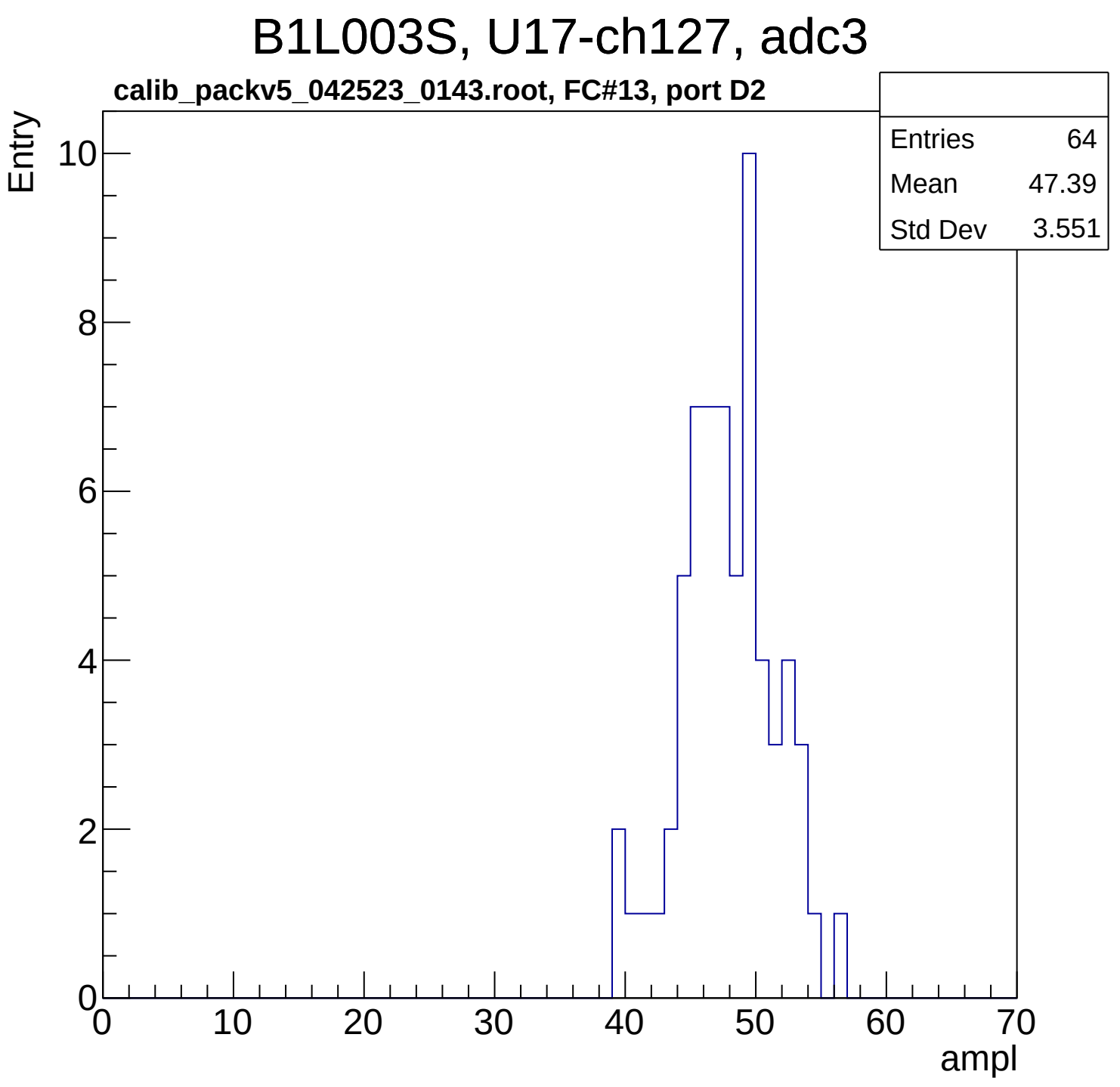
B1L003S, U17-ch127, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.39
Std Dev 3.551
Entry
ampl
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40
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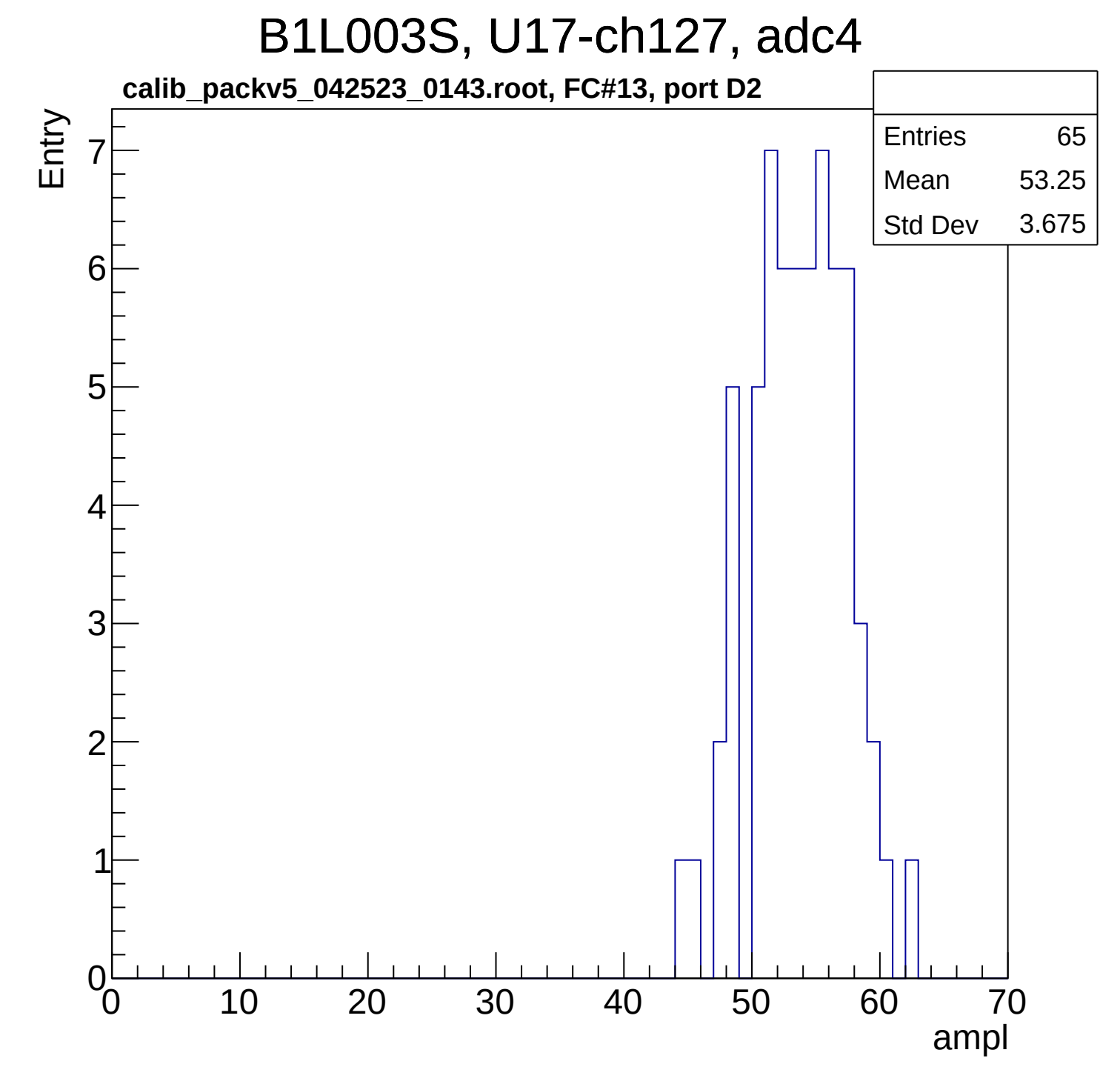
B1L003S, U17-ch127, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 53.25
Std Dev 3.675
Entry
ampl
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3
4
5
6
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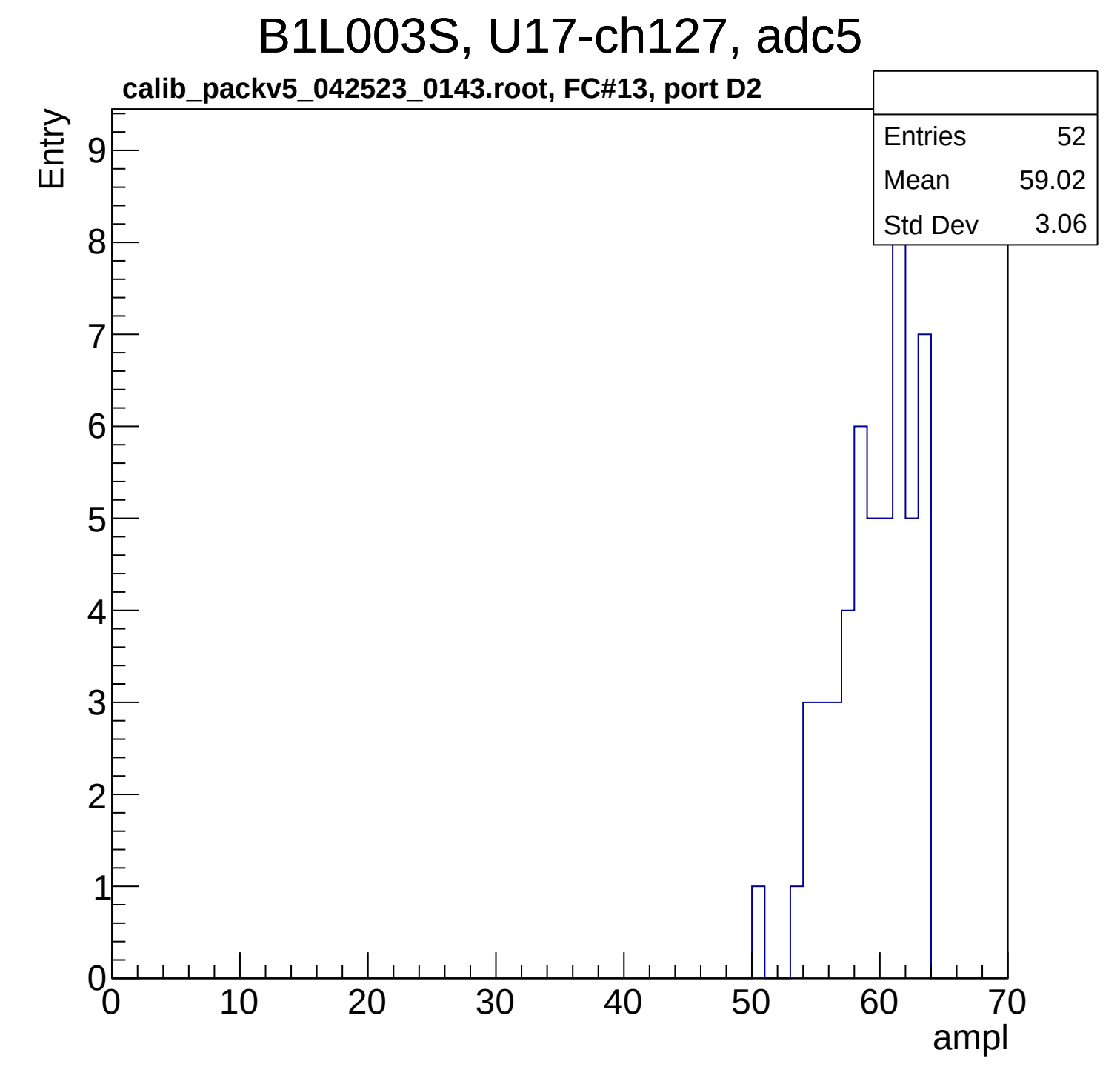
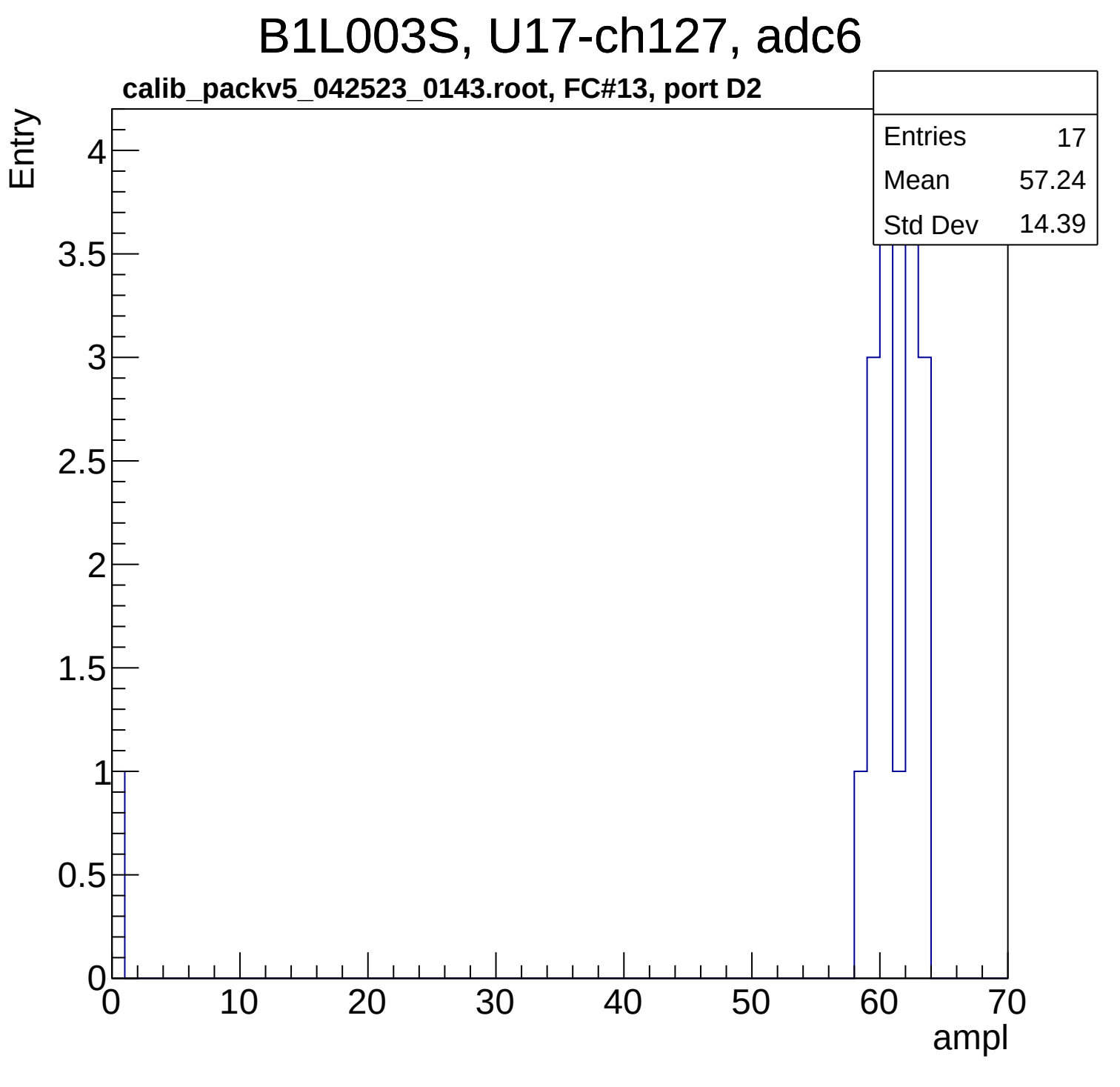
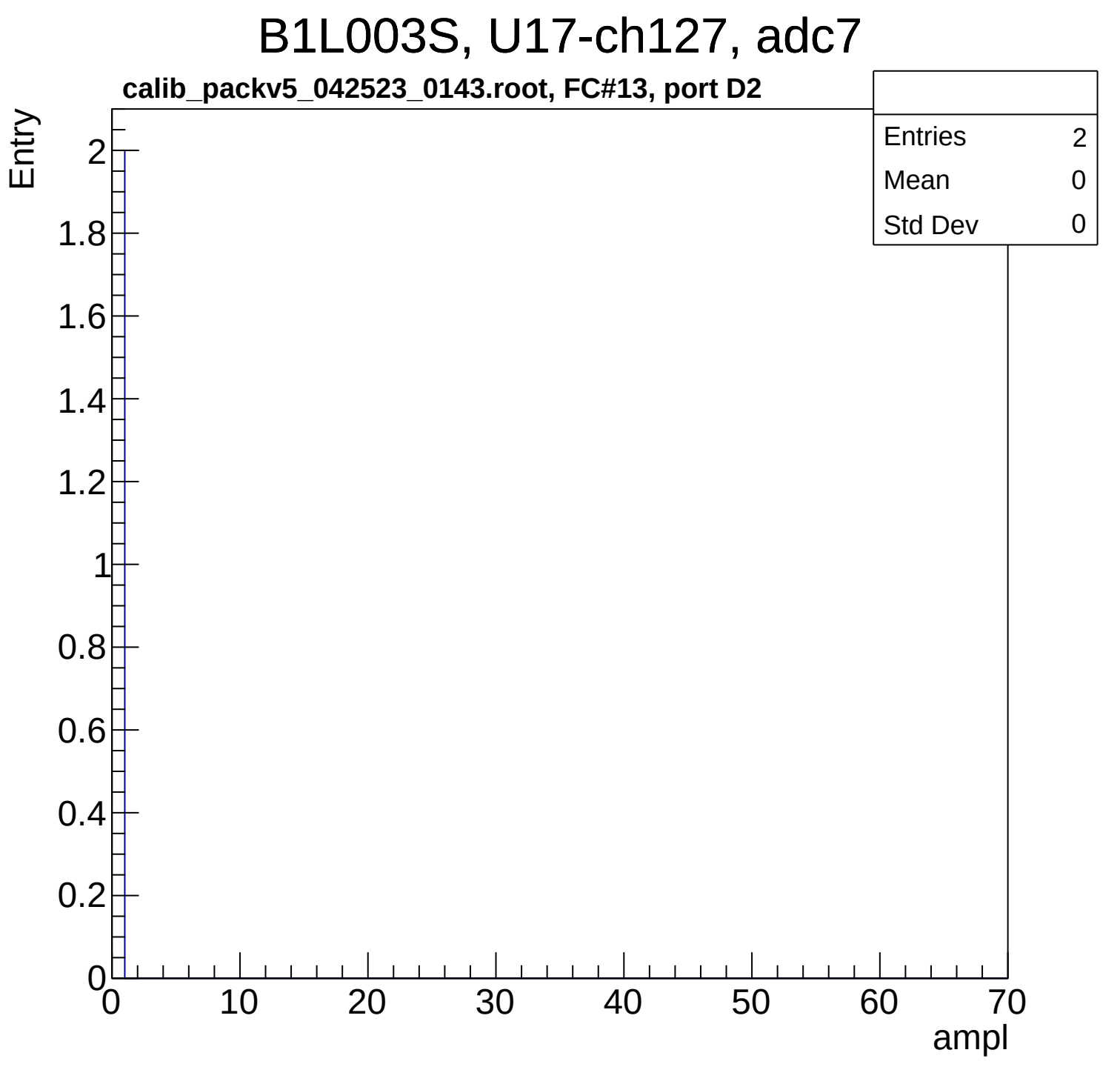
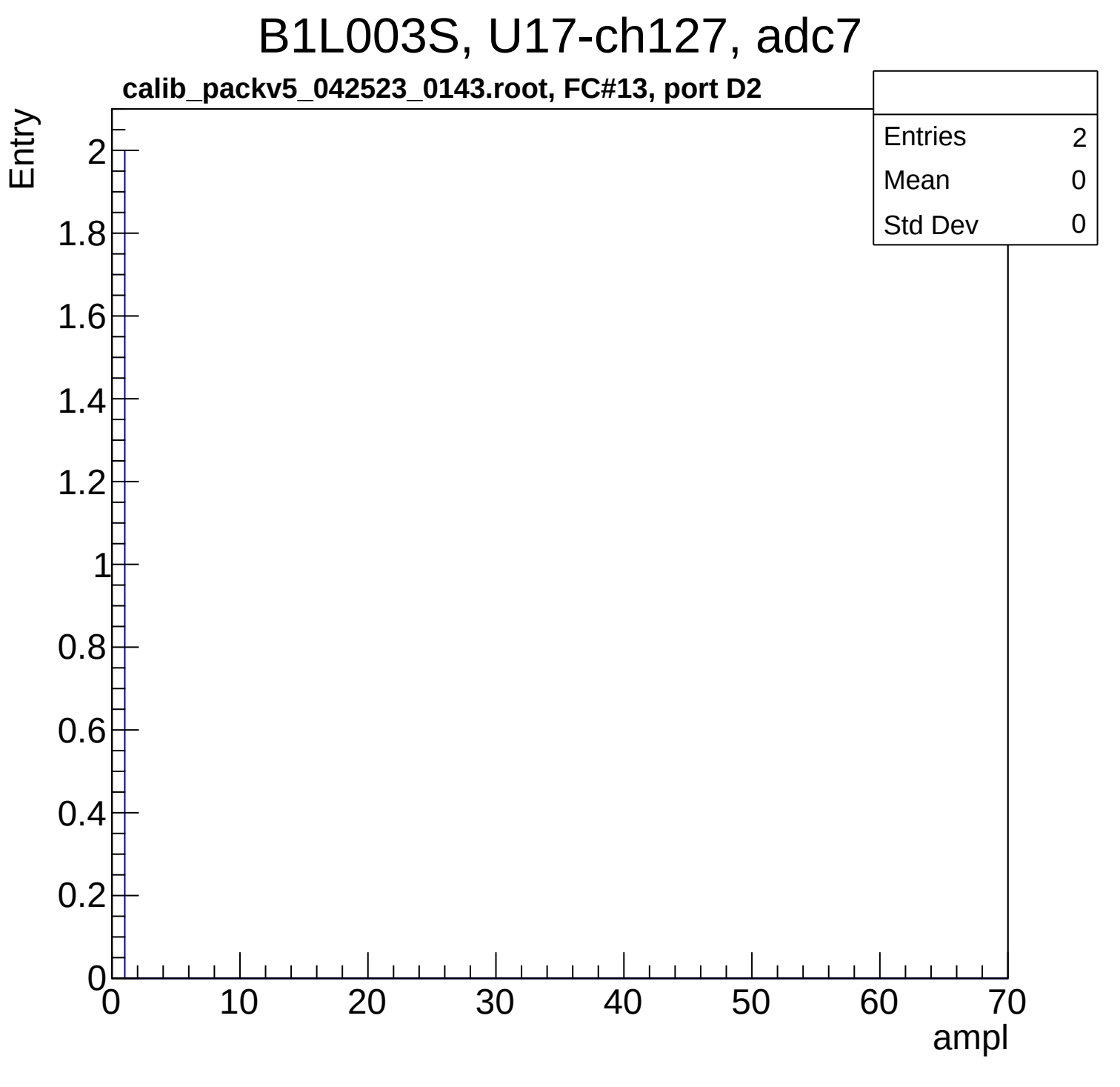
0
10
20
30
40
50
60
70


B1L003S, U17-ch127, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.02
Std Dev 3.06
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U17-ch127, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.24
Std Dev 14.39
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U17-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U17-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70