

B1L000S, U22-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.78
Std Dev	11.87

Turn on : 25.2976

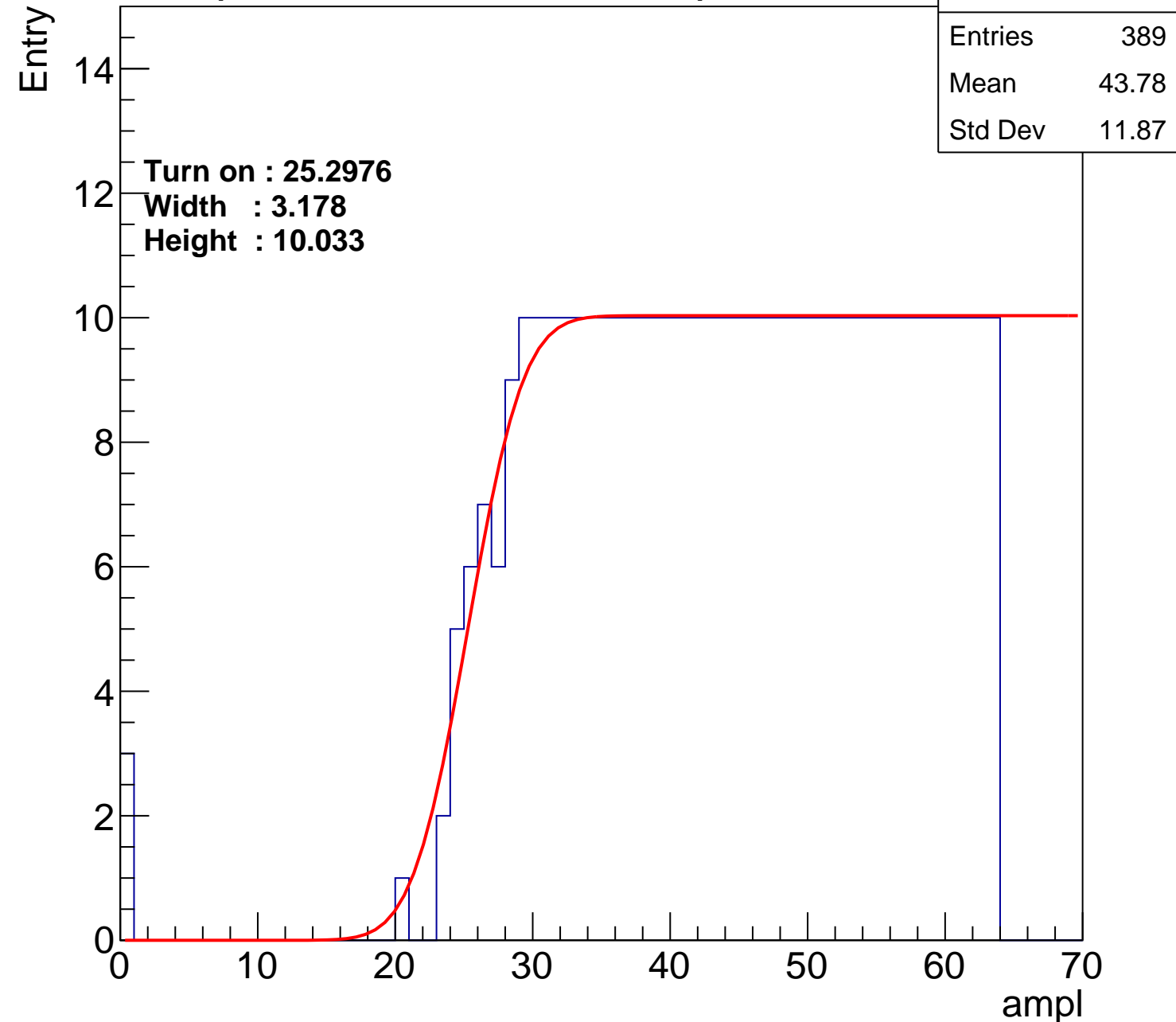
Width : 3.178

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch1

calib_packv5_042523_0143.root, FC#3, port B3

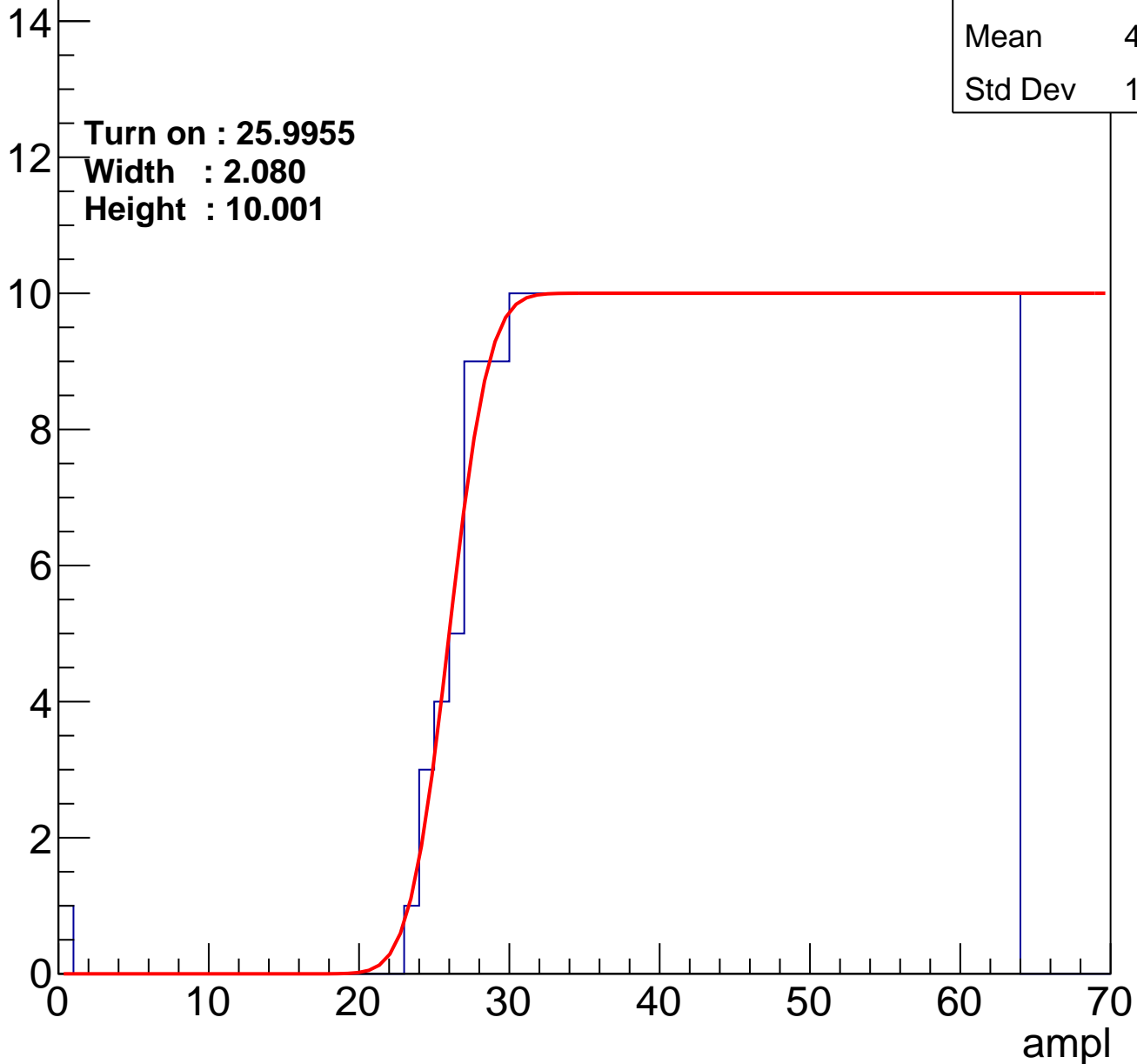
Entries	381
Mean	44.33
Std Dev	11.27

Turn on : 25.9955

Width : 2.080

Height : 10.001

Entry



B1L000S, U22-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.54
Std Dev	11.15

Turn on : 26.5277

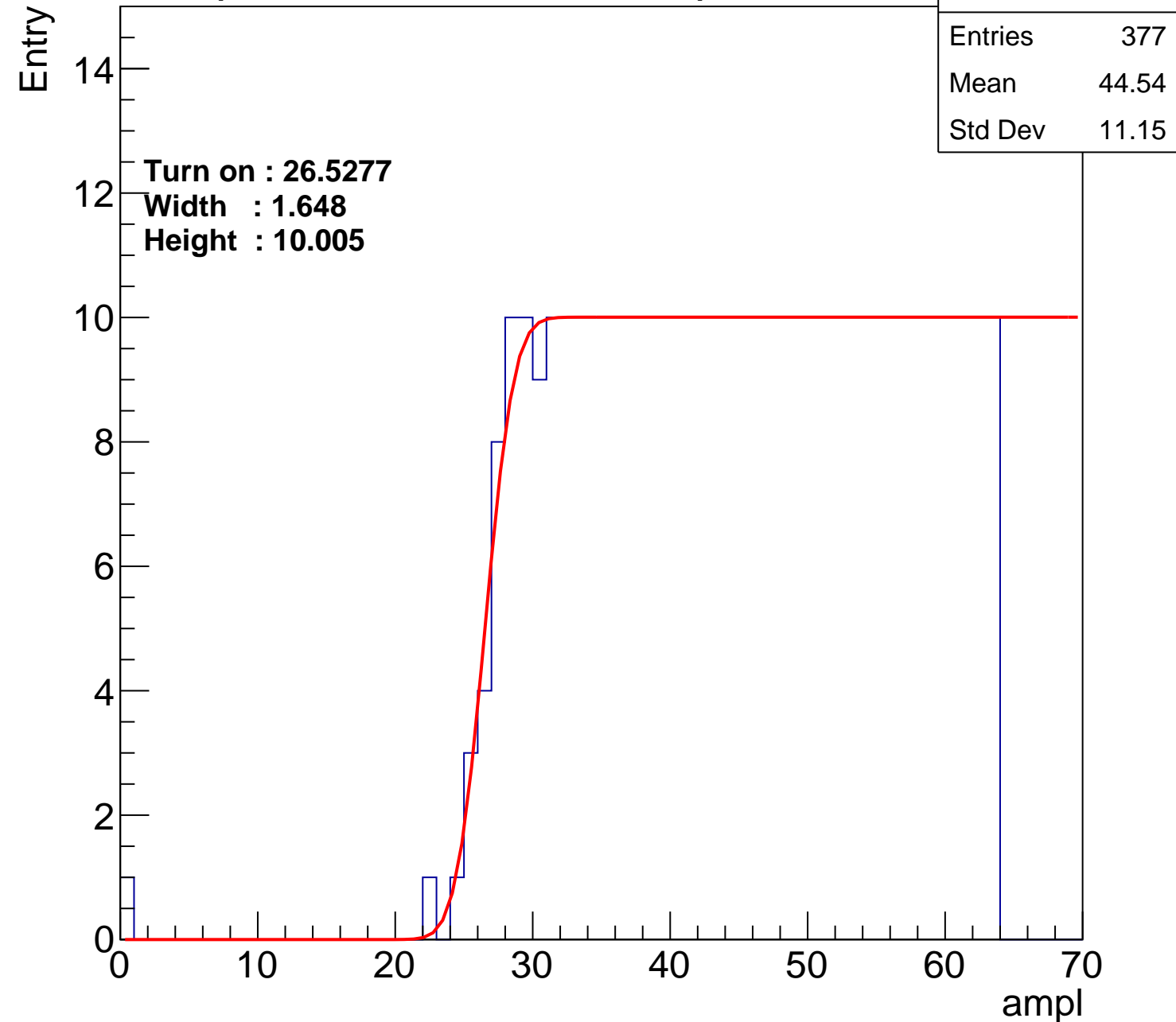
Width : 1.648

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.52
Std Dev	11.23

Turn on : 26.6569

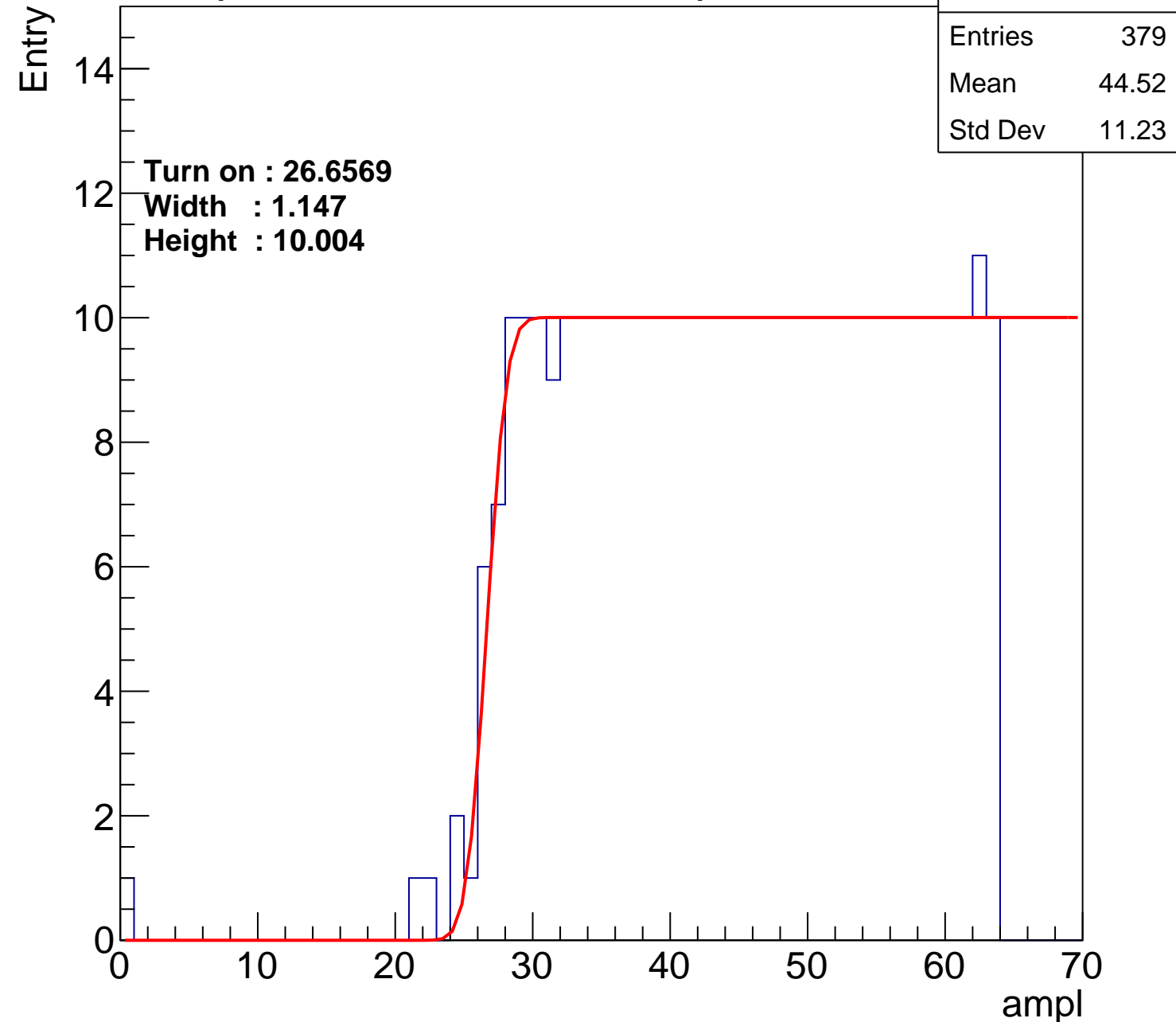
Width : 1.147

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.07
Std Dev	11.73

Turn on : 25.7316

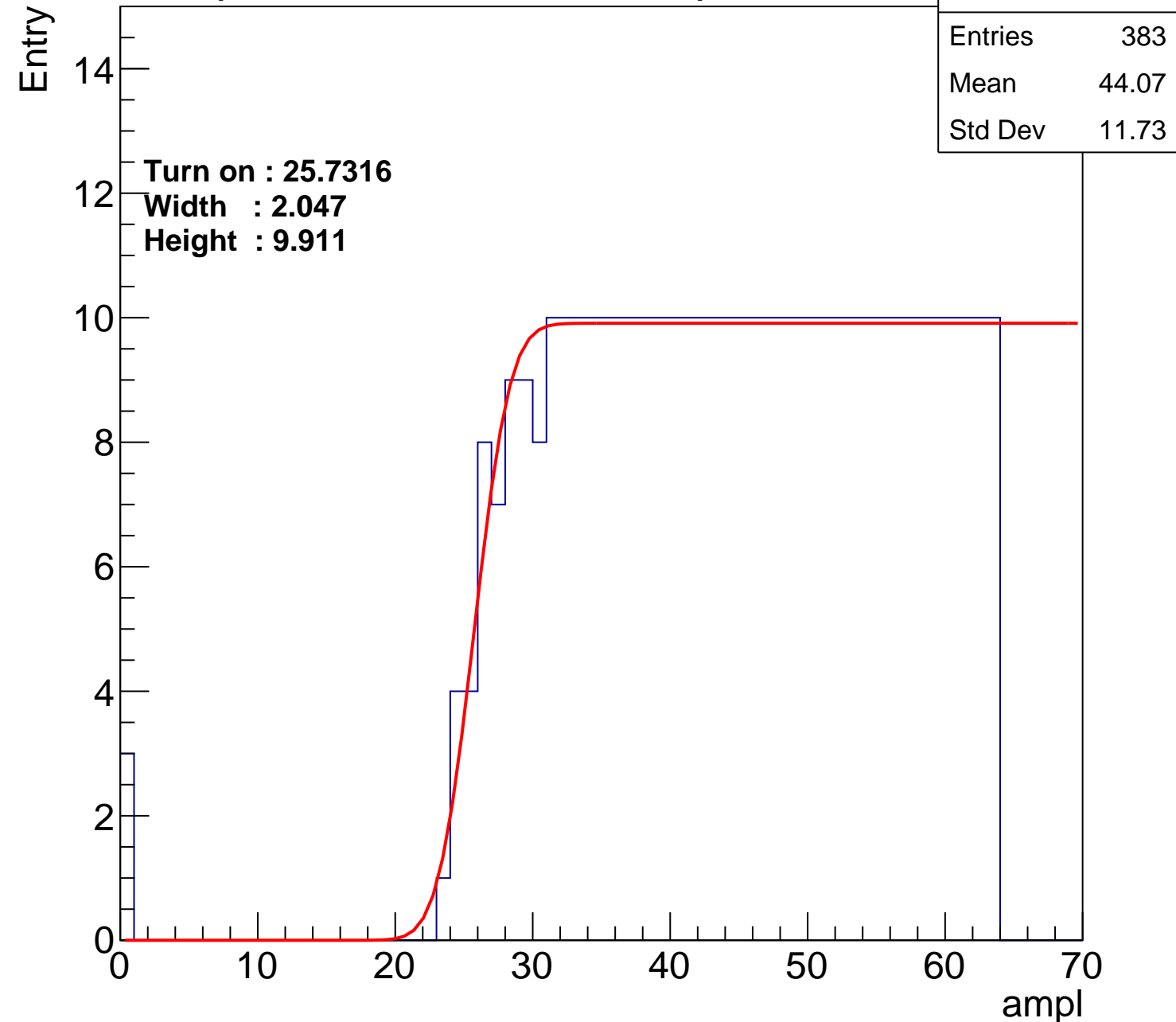
Width : 2.047

Height : 9.911

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch5

calib_packv5_042523_0143.root, FC#3, port B3

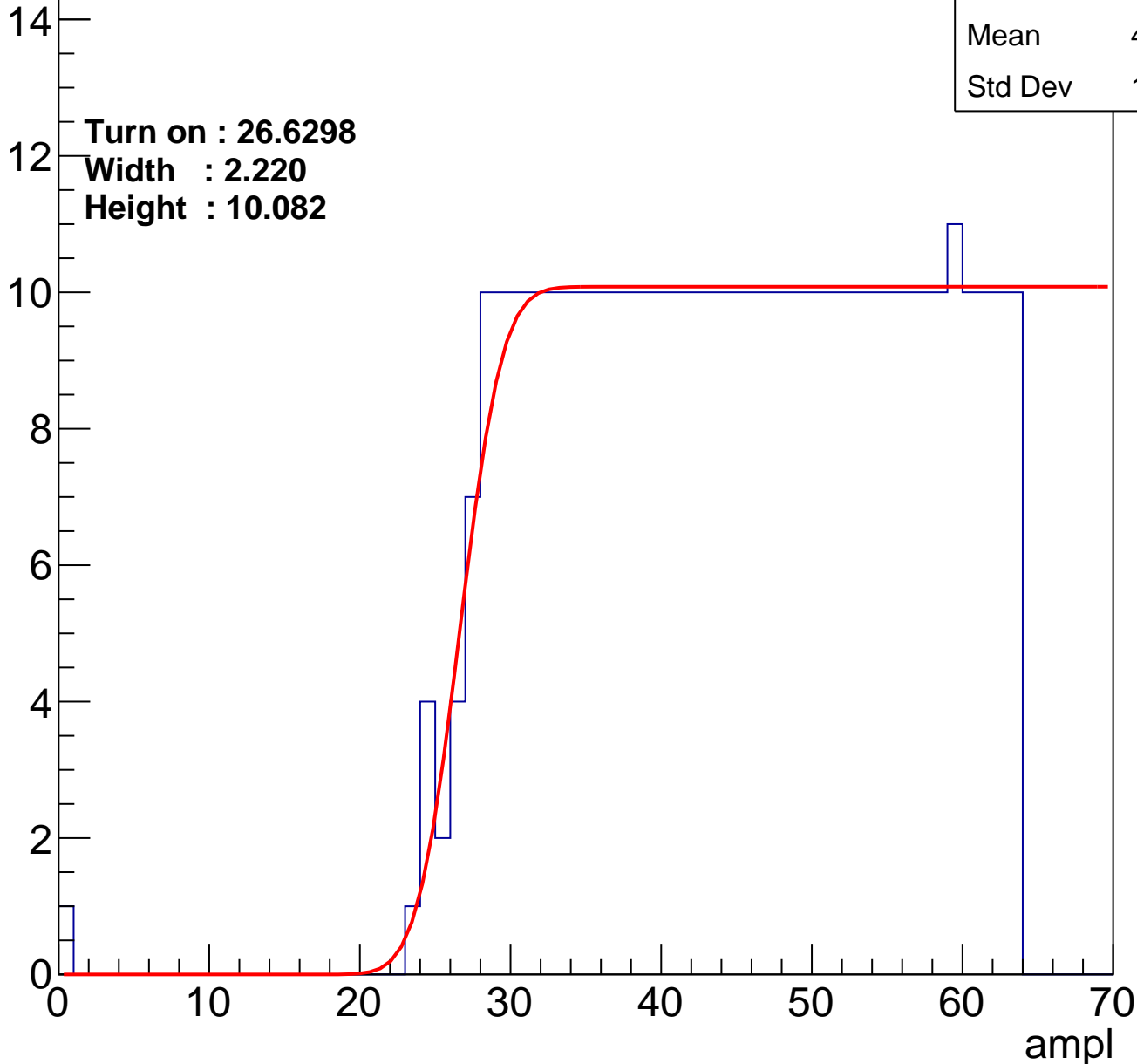
Entry

Entries	380
Mean	44.48
Std Dev	11.22

Turn on : 26.6298

Width : 2.220

Height : 10.082



B1L000S, U22-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.12
Std Dev	12.45

Turn on : 24.8806

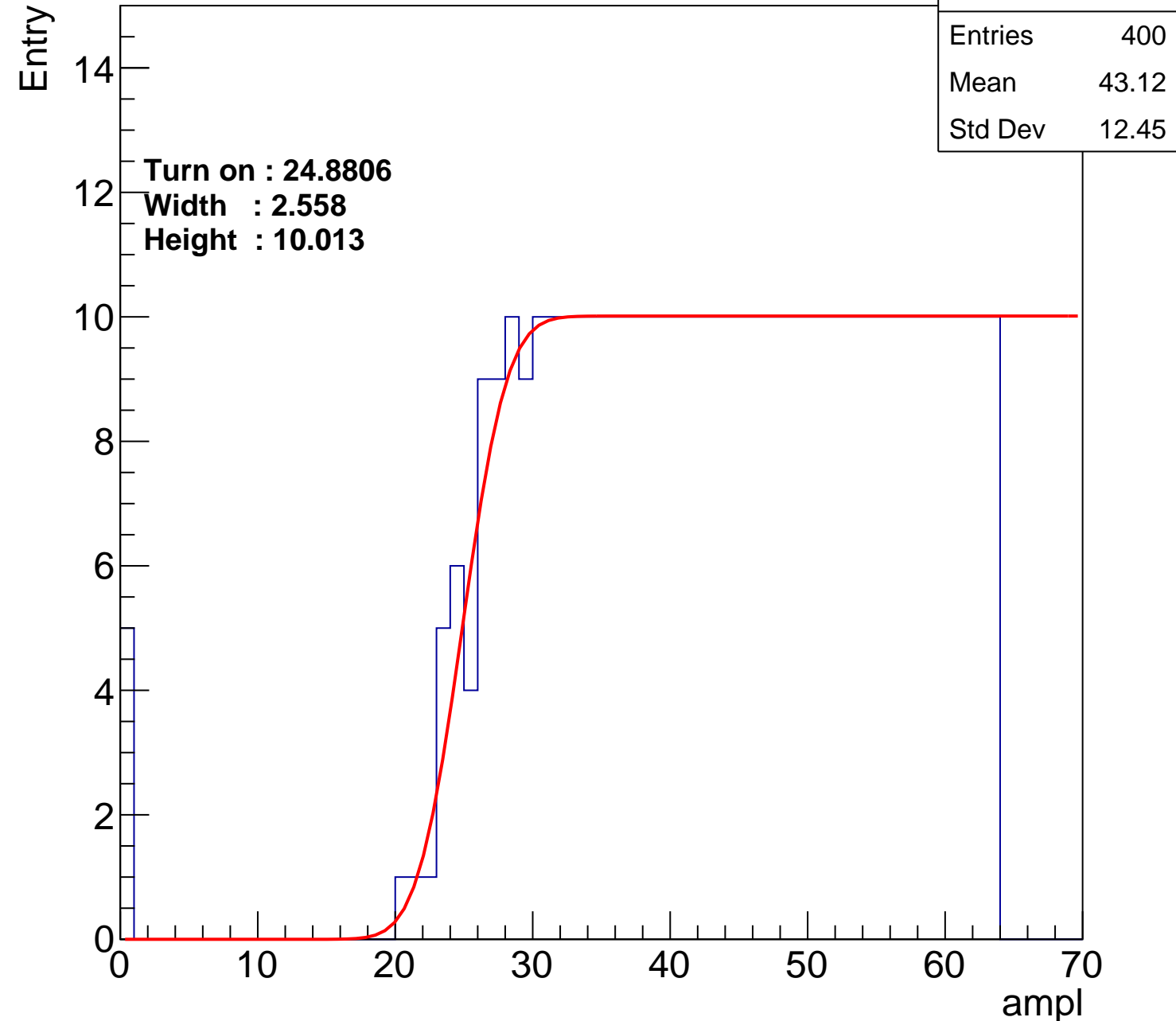
Width : 2.558

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44.02
Std Dev	11.47

Turn on : 25.5775

Width : 2.730

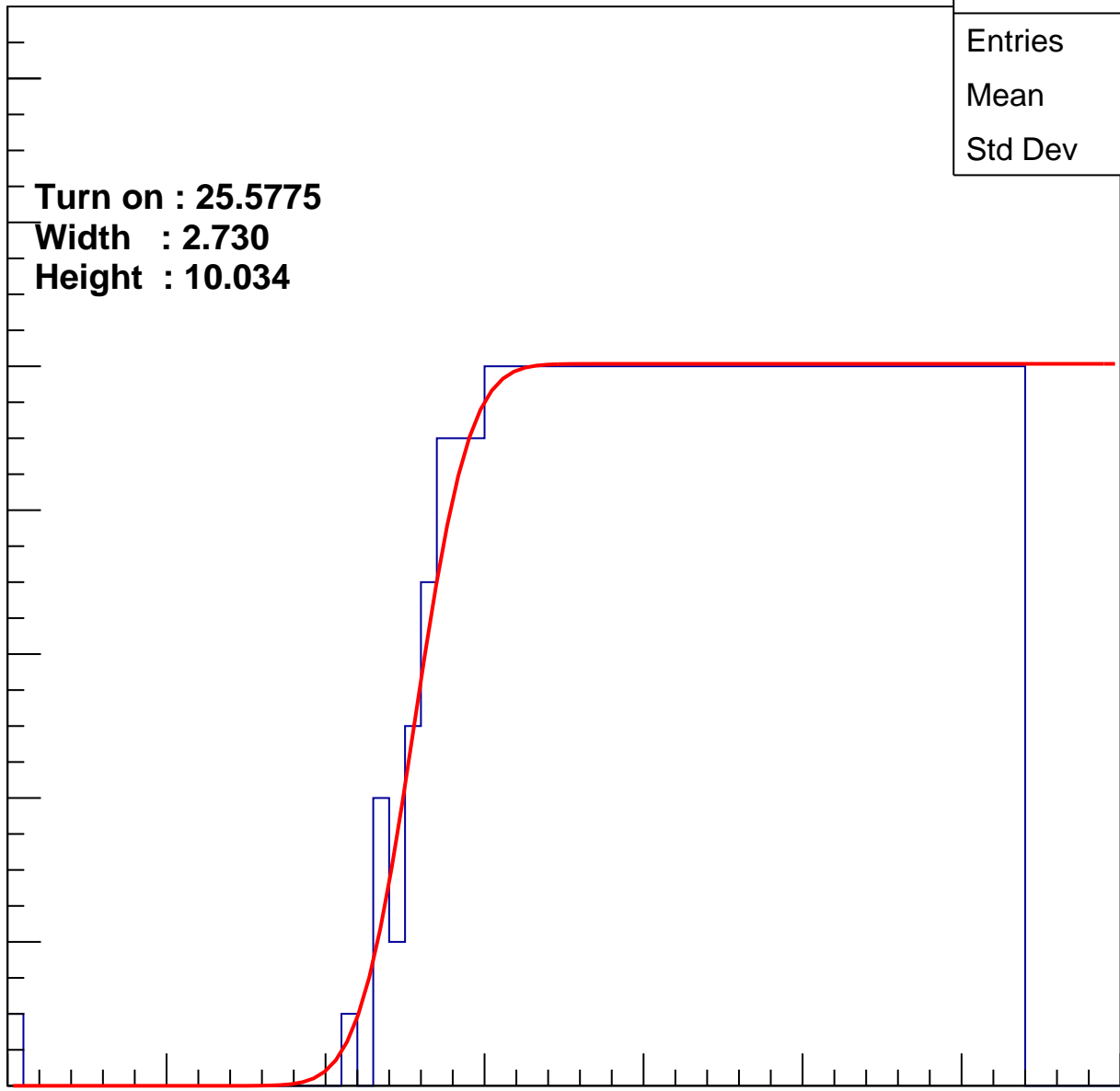
Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U22-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	353
Mean	45.61
Std Dev	10.81

Turn on : 29.2679

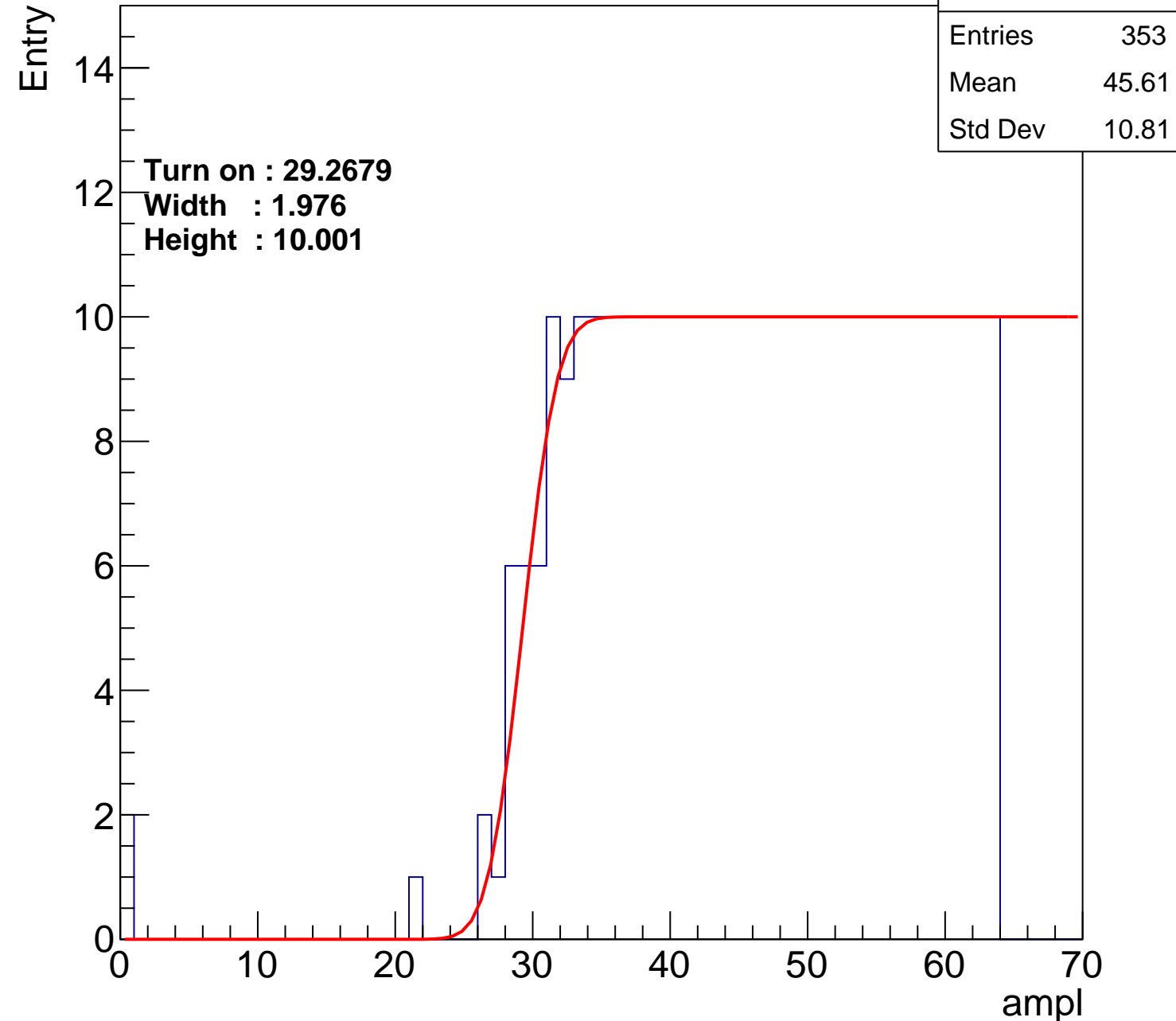
Width : 1.976

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.15
Std Dev	11.54

Turn on : 26.3086

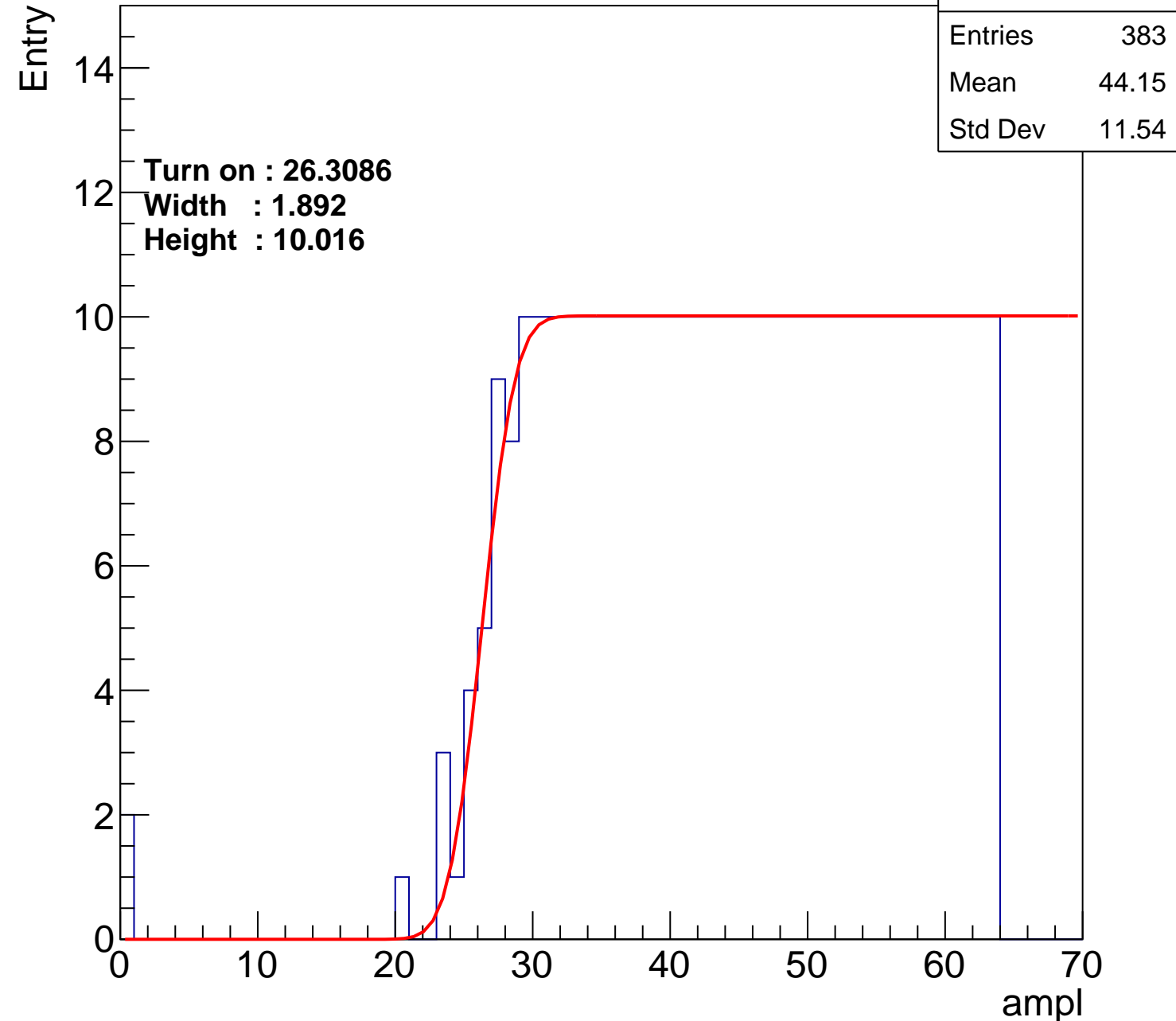
Width : 1.892

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	350
Mean	45.85
Std Dev	10.47

Turn on : 29.2134

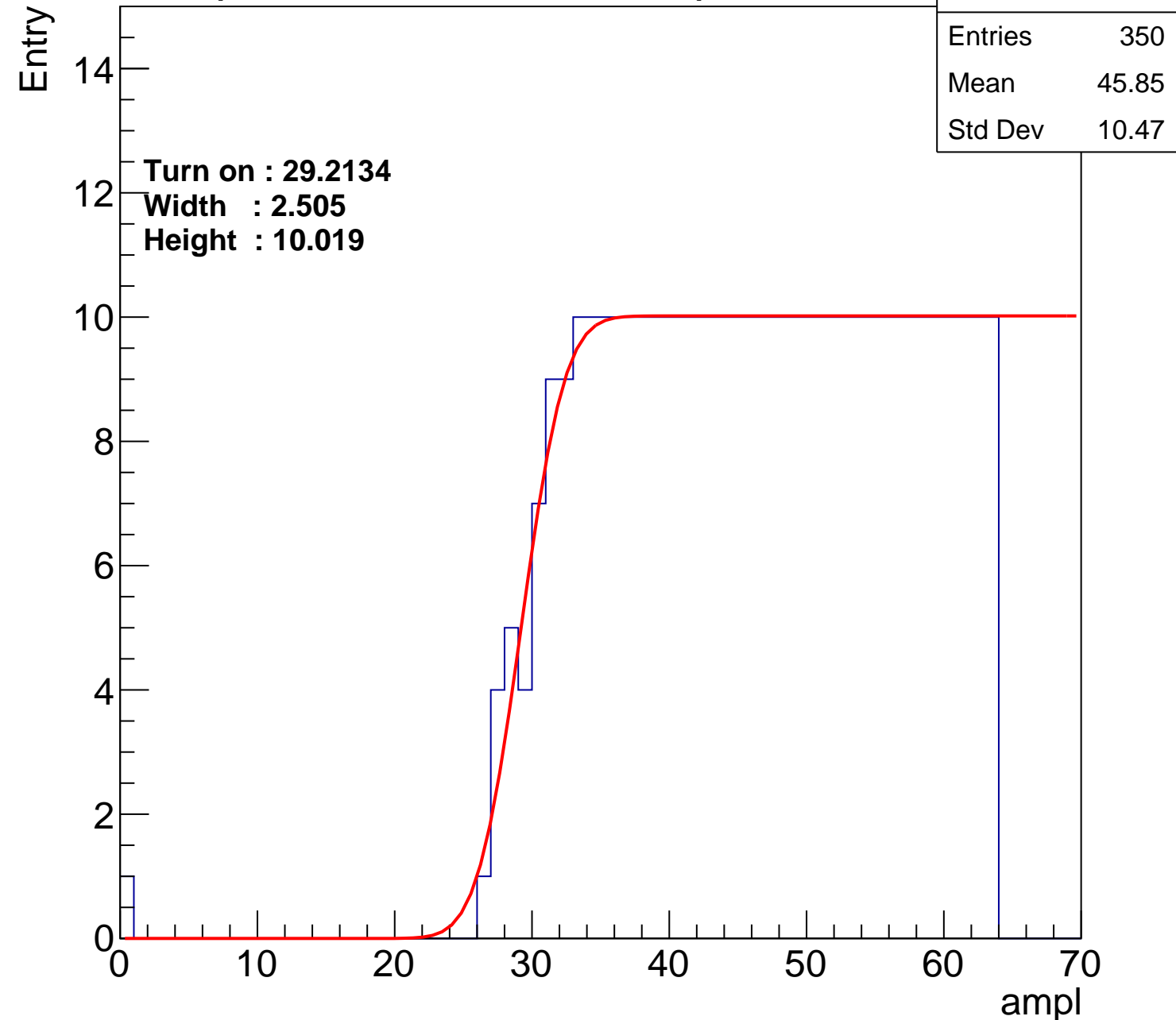
Width : 2.505

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.57
Std Dev	11.48

Turn on : 27.4424

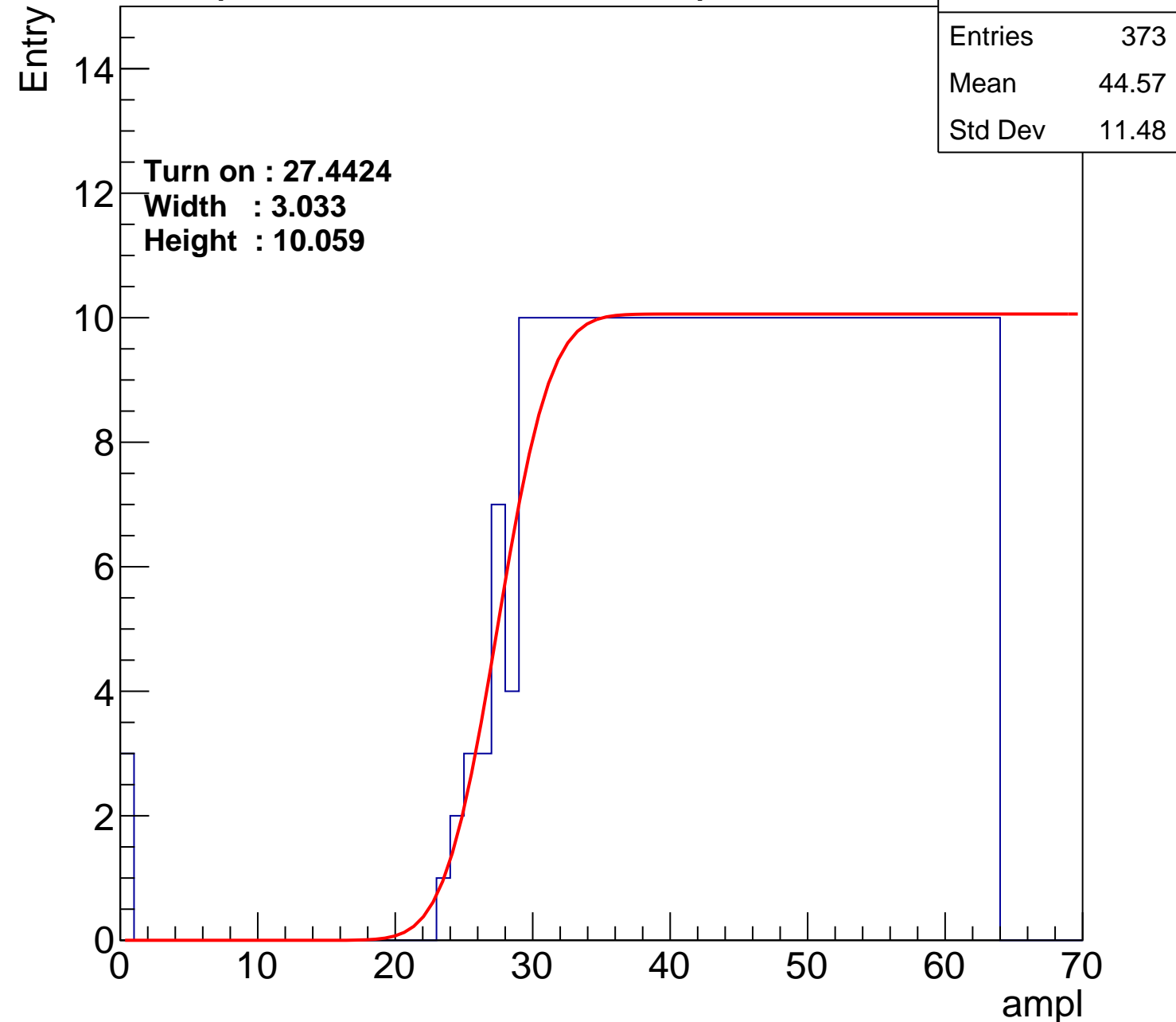
Width : 3.033

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.4520

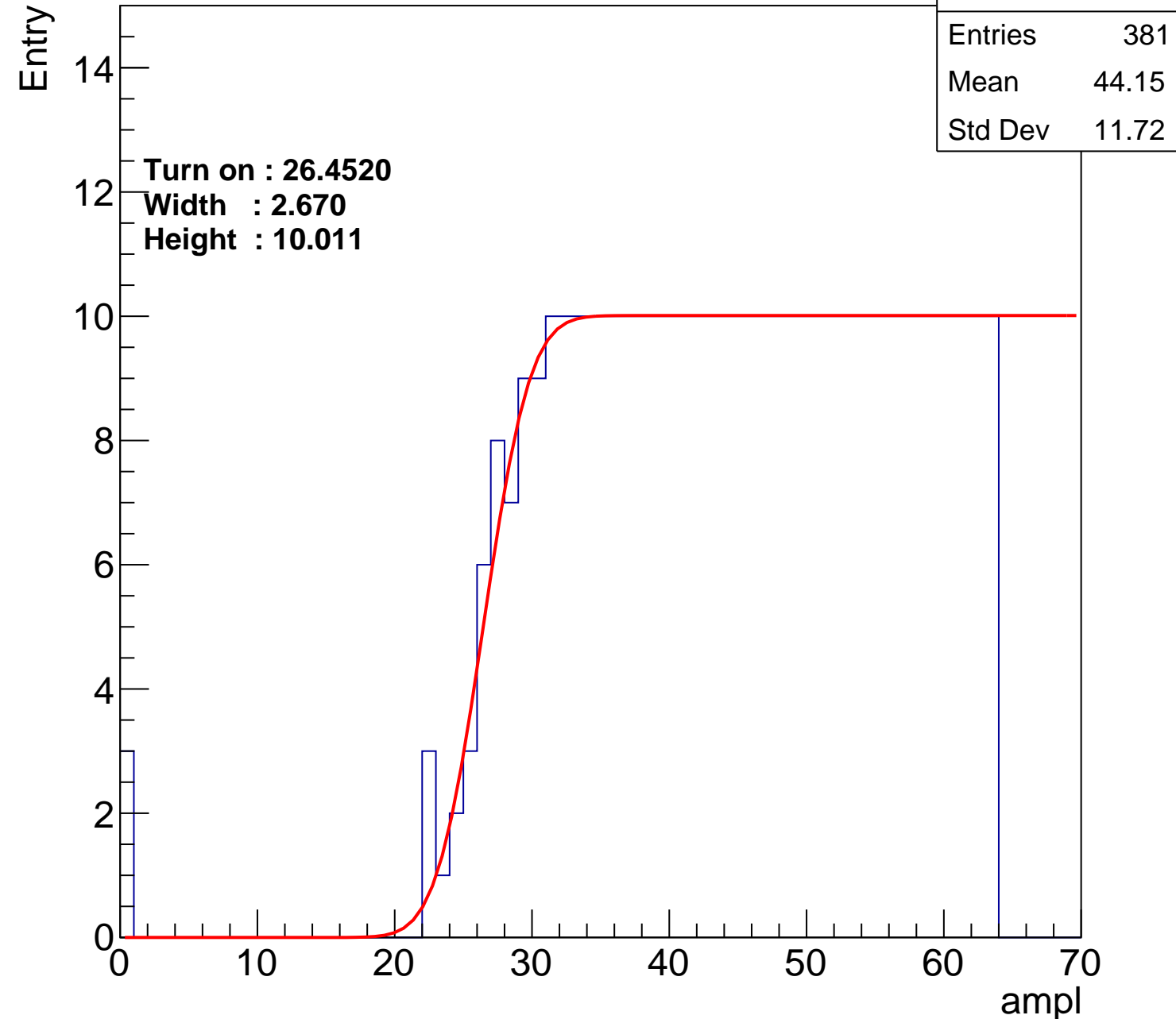
Width : 2.670

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch13

calib_packv5_042523_0143.root, FC#3, port B3

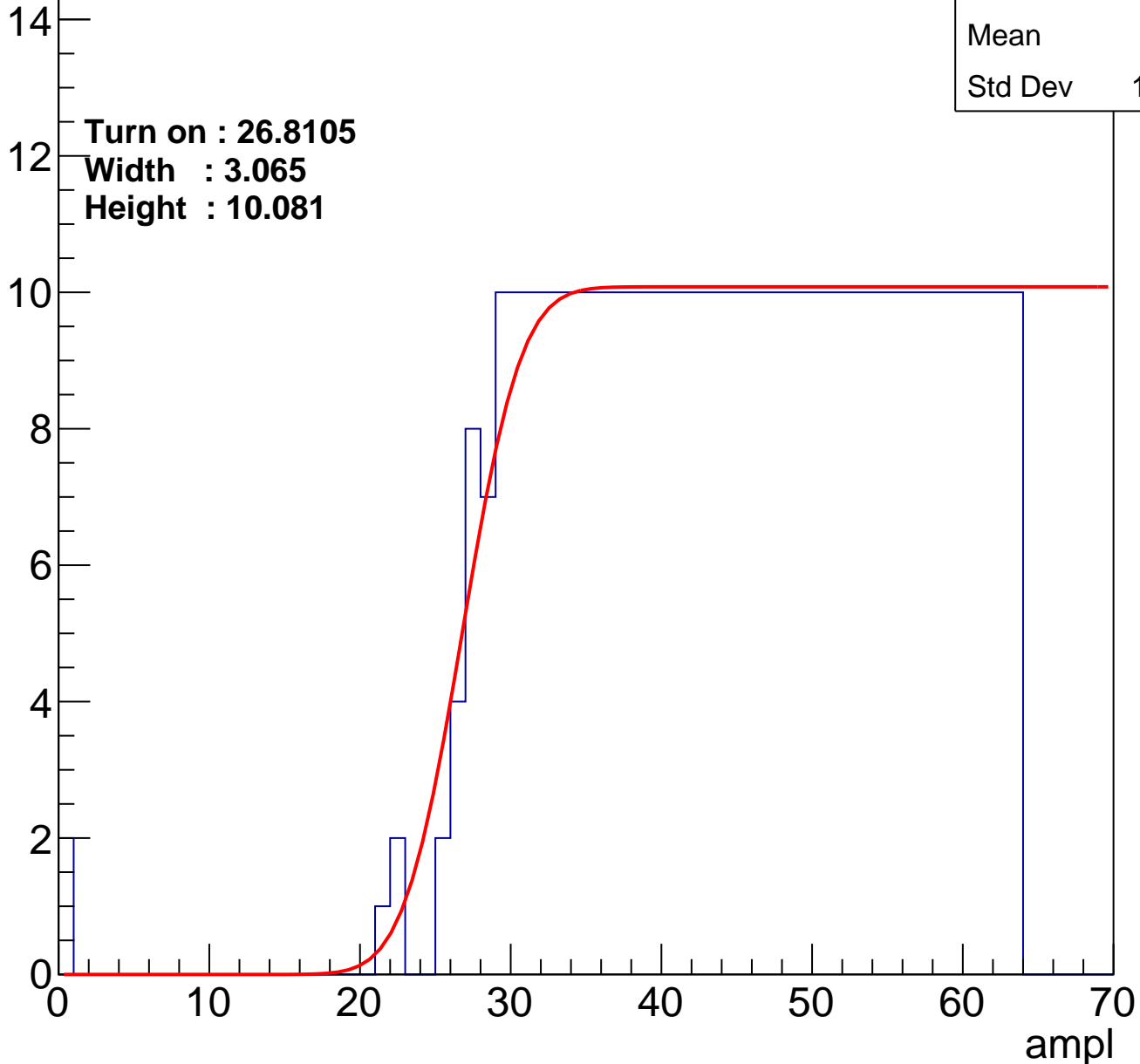
Entries	376
Mean	44.5
Std Dev	11.36

Turn on : 26.8105

Width : 3.065

Height : 10.081

Entry



B1L000S, U22-ch14

calib_packv5_042523_0143.root, FC#3, port B3

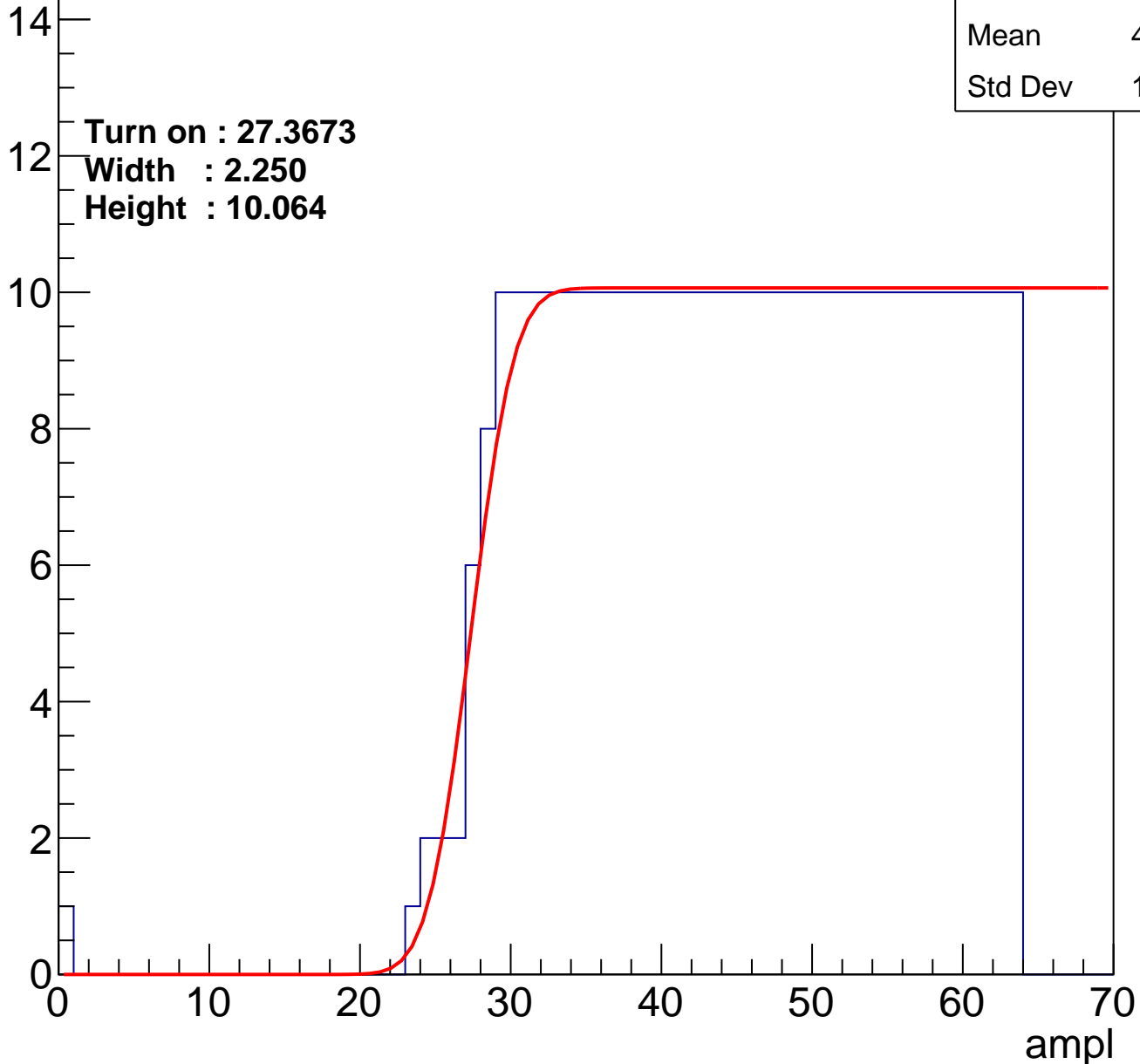
Entries	372
Mean	44.78
Std Dev	11.02

Turn on : 27.3673

Width : 2.250

Height : 10.064

Entry



B1L000S, U22-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.31
Std Dev	11.3

Turn on : 26.2819

Width : 2.119

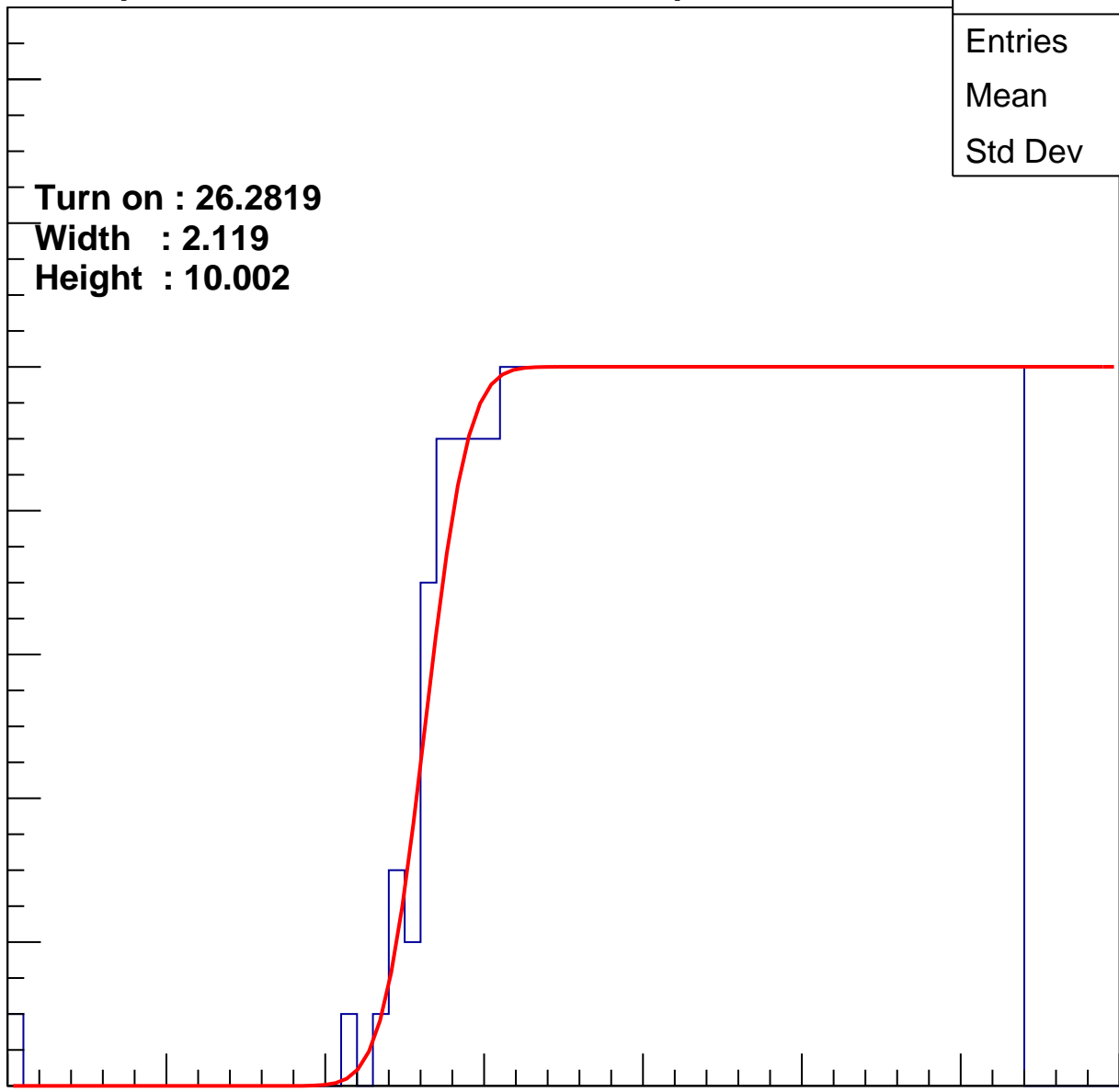
Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U22-ch16

calib_packv5_042523_0143.root, FC#3, port B3

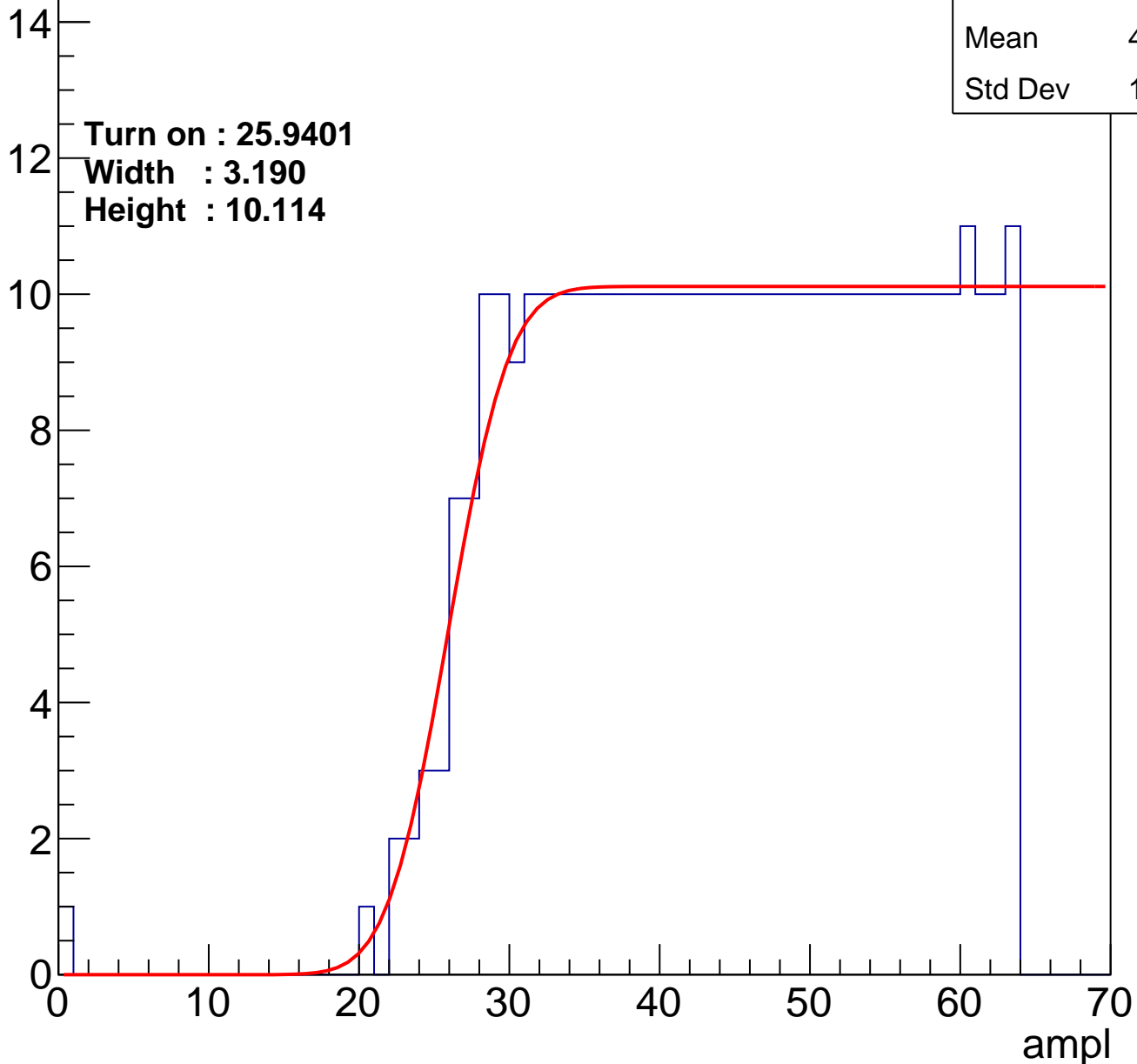
Entries	387
Mean	44.19
Std Dev	11.48

Turn on : 25.9401

Width : 3.190

Height : 10.114

Entry



B1L000S, U22-ch17

calib_packv5_042523_0143.root, FC#3, port B3

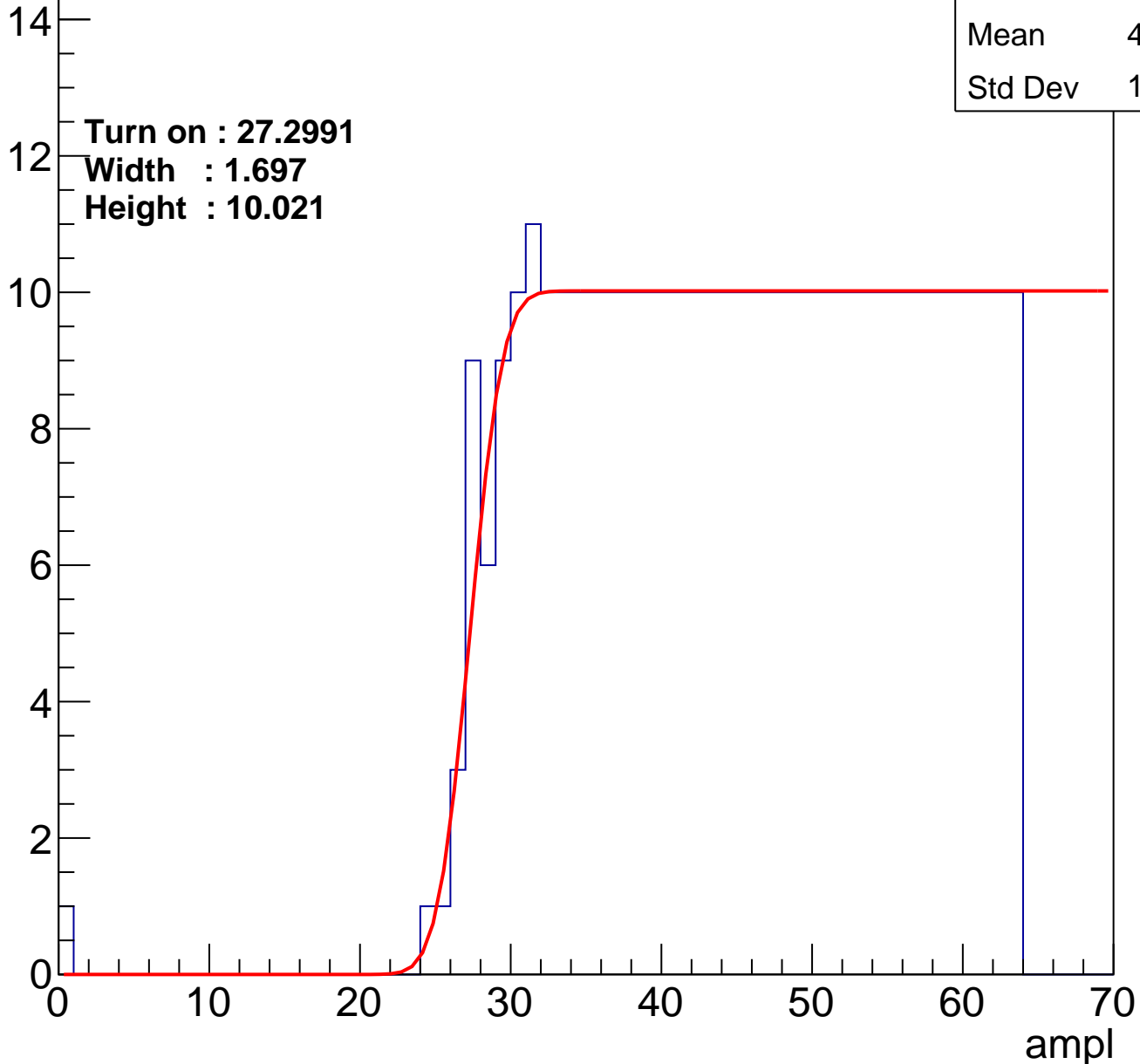
Entries	371
Mean	44.85
Std Dev	10.96

Turn on : 27.2991

Width : 1.697

Height : 10.021

Entry



B1L000S, U22-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 26.2135

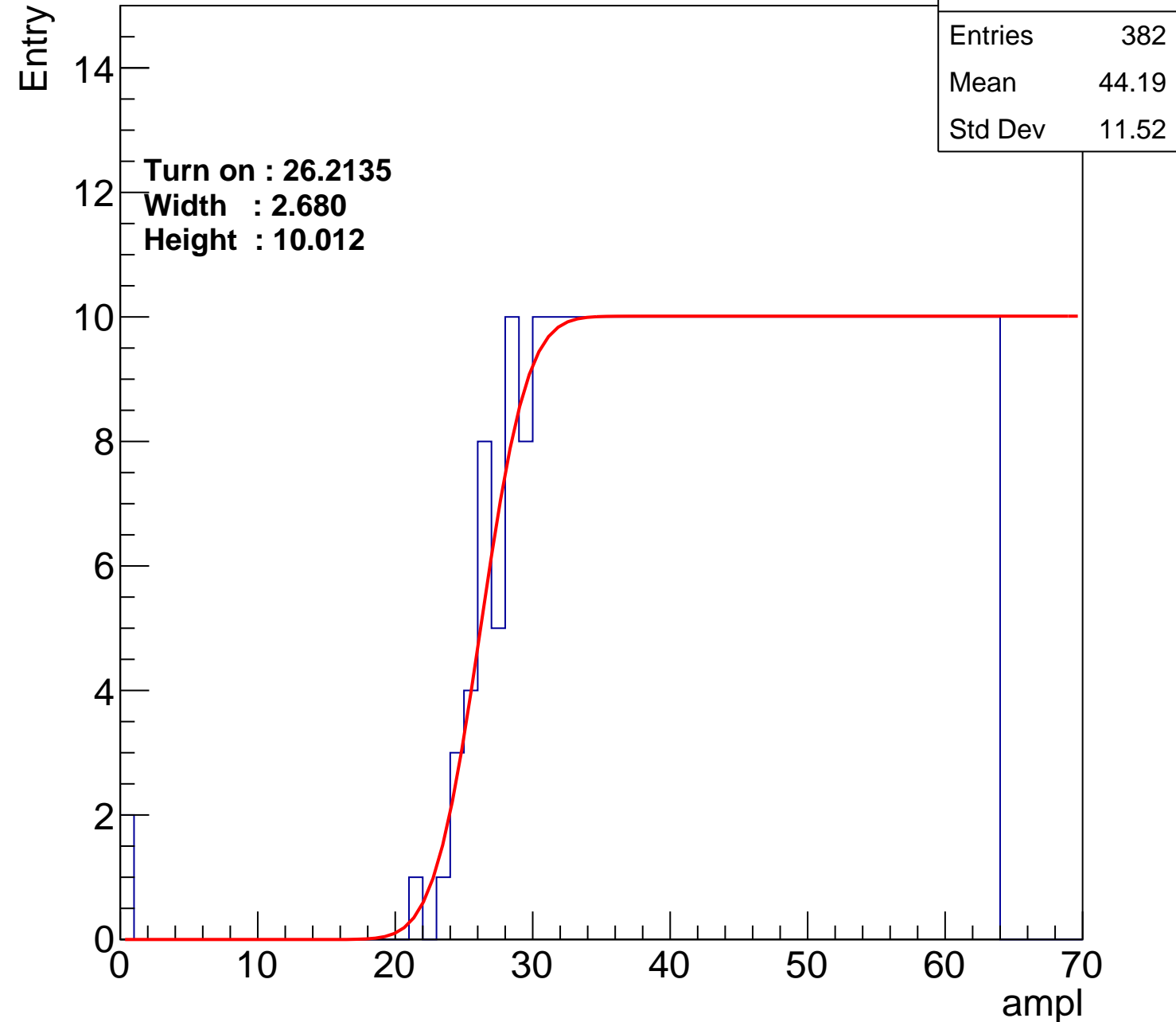
Width : 2.680

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch19

calib_packv5_042523_0143.root, FC#3, port B3

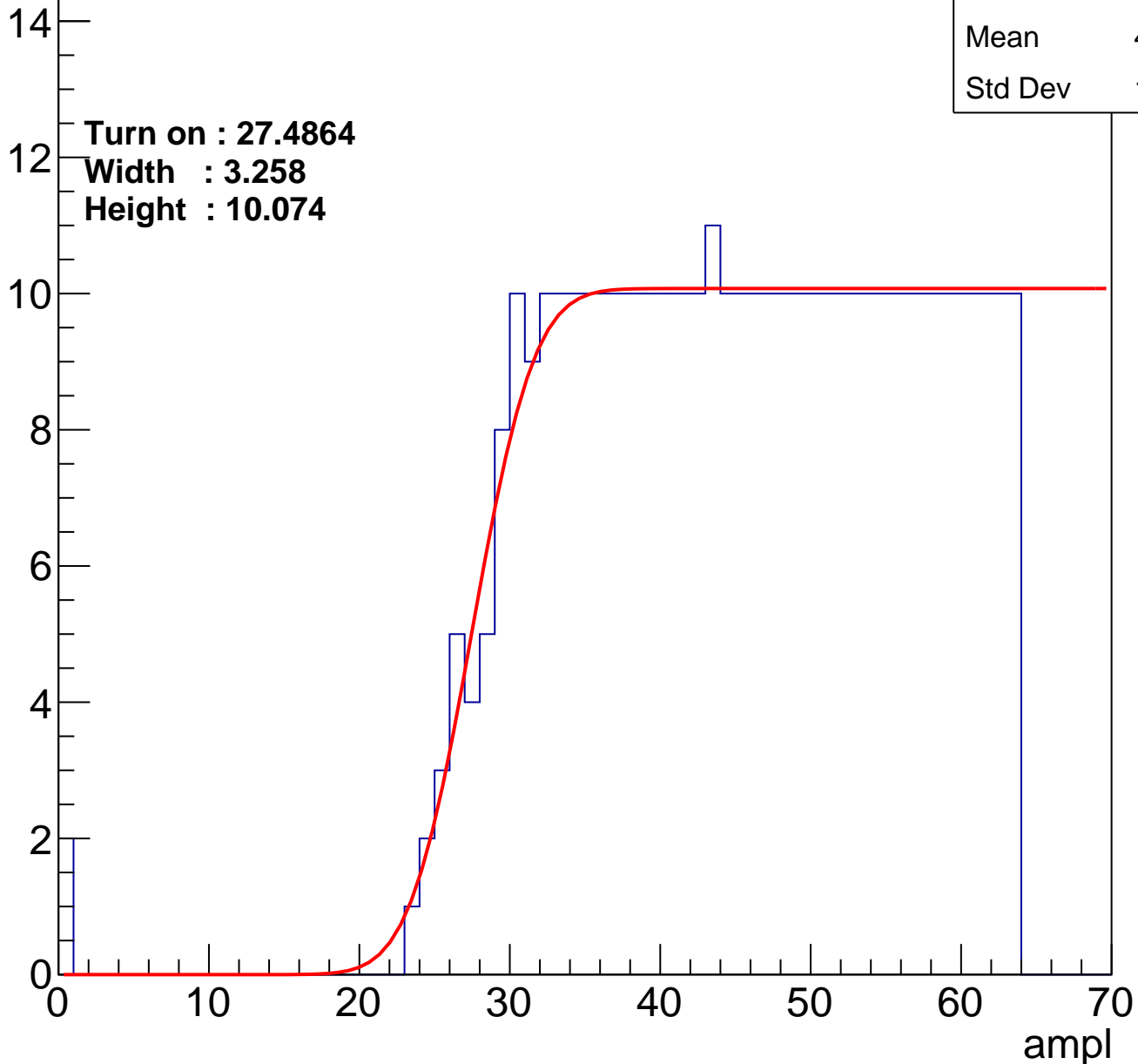
Entries	370
Mean	44.81
Std Dev	11.21

Turn on : 27.4864

Width : 3.258

Height : 10.074

Entry



B1L000S, U22-ch20

calib_packv5_042523_0143.root, FC#3, port B3

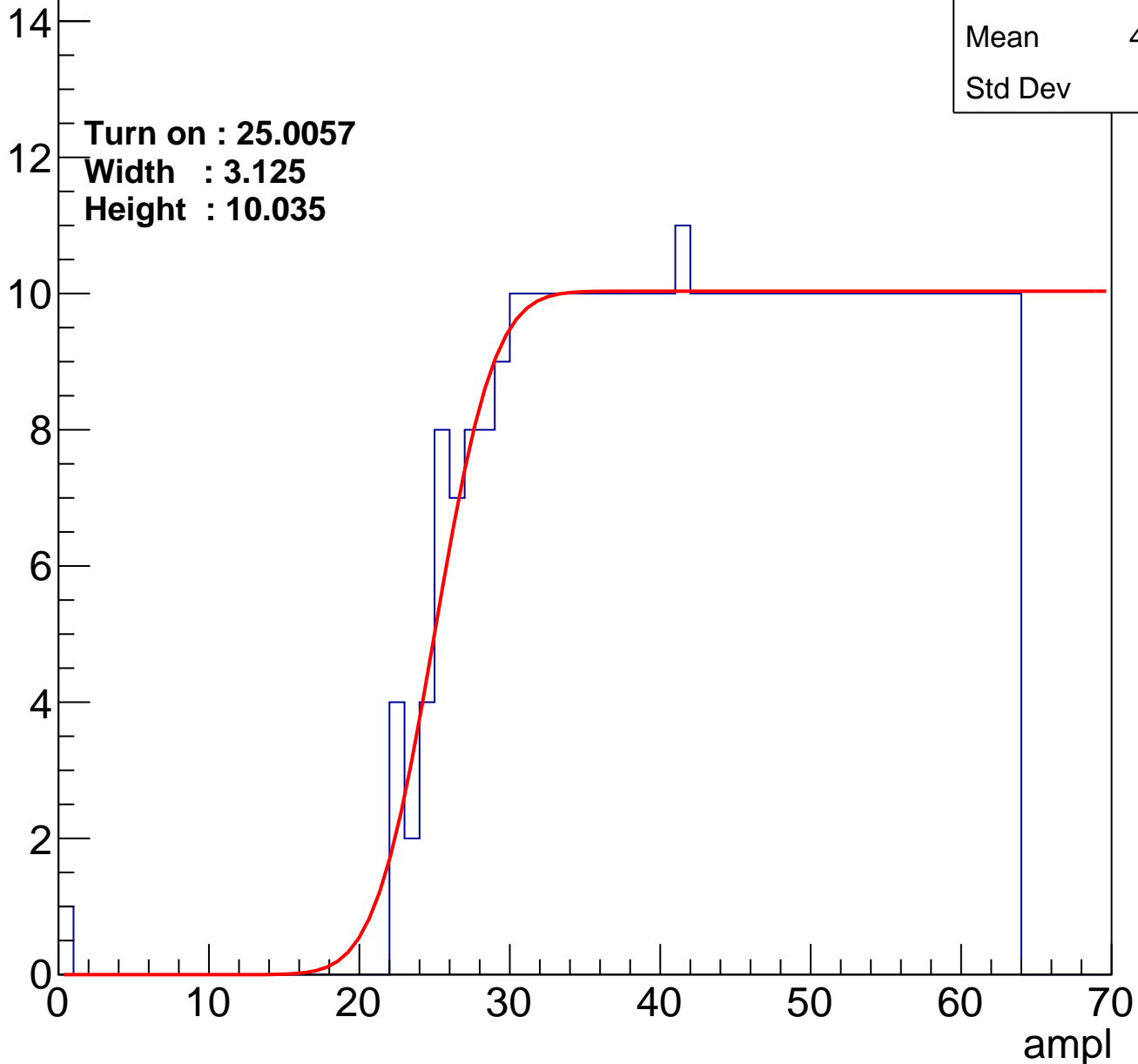
Entries	392
Mean	43.79
Std Dev	11.6

Turn on : 25.0057

Width : 3.125

Height : 10.035

Entry



B1L000S, U22-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.58
Std Dev	11.32

Turn on : 27.2511

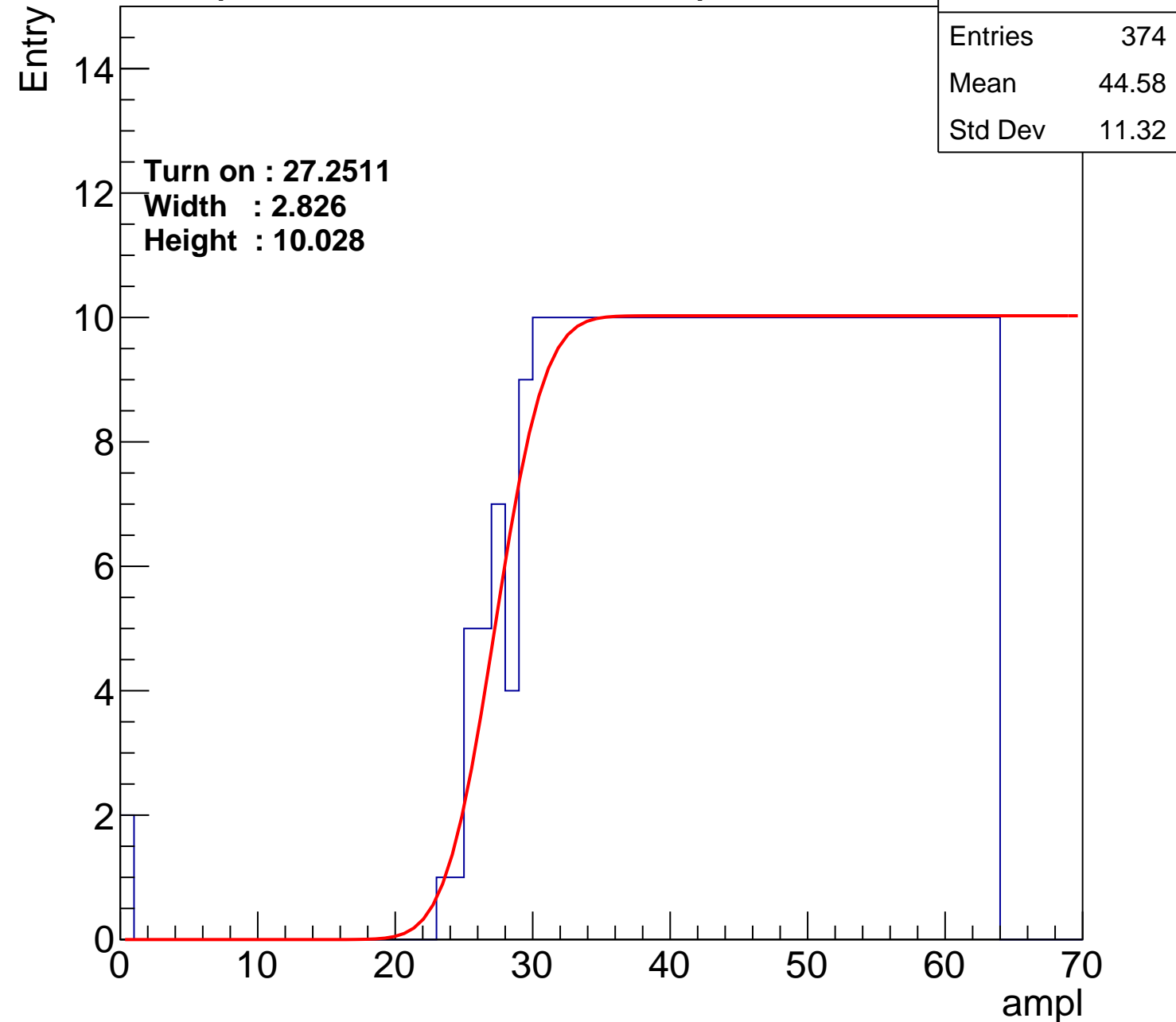
Width : 2.826

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.05
Std Dev	11.64

Turn on : 26.4963

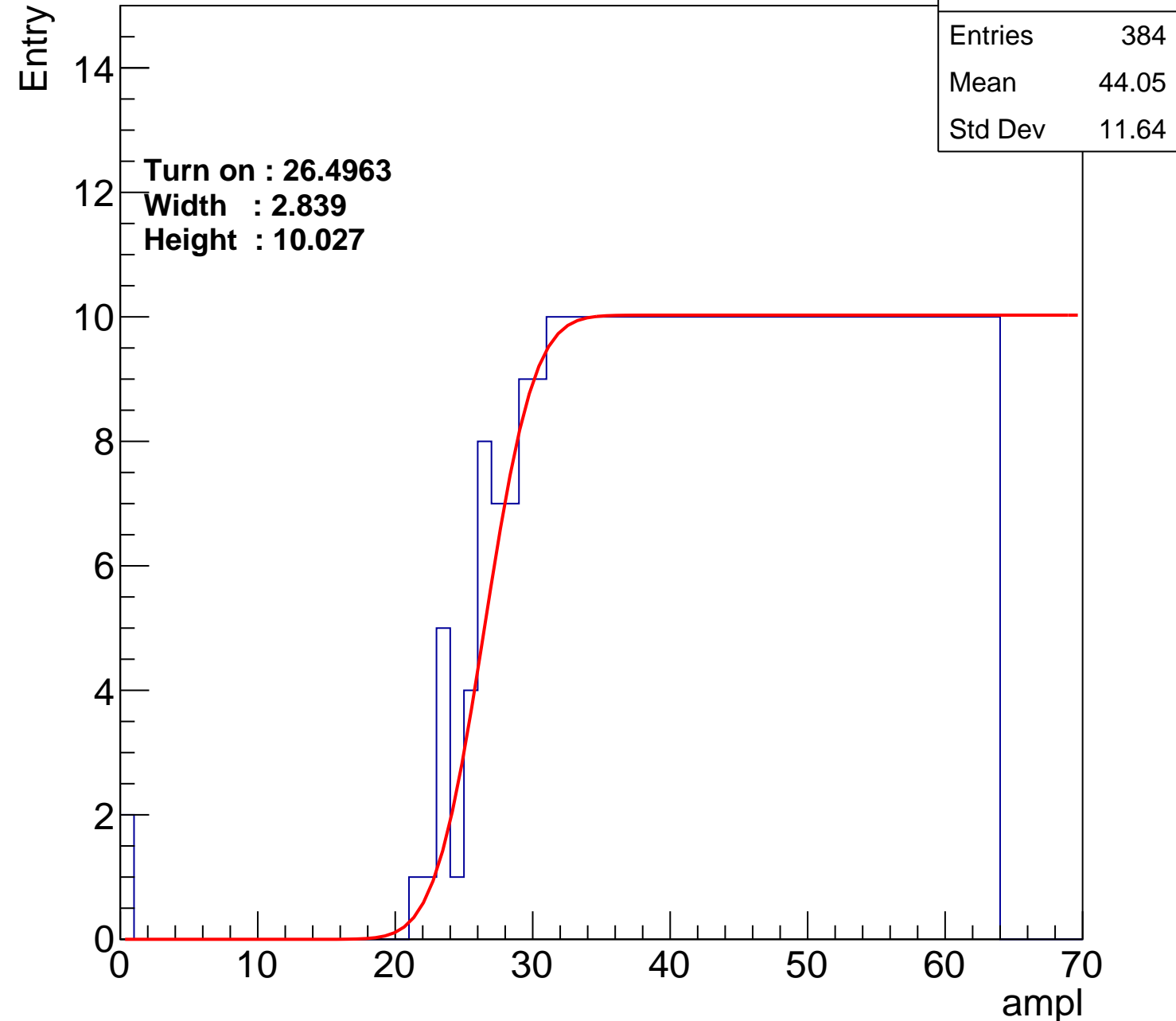
Width : 2.839

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.8
Std Dev	11.07

Turn on : 26.9967

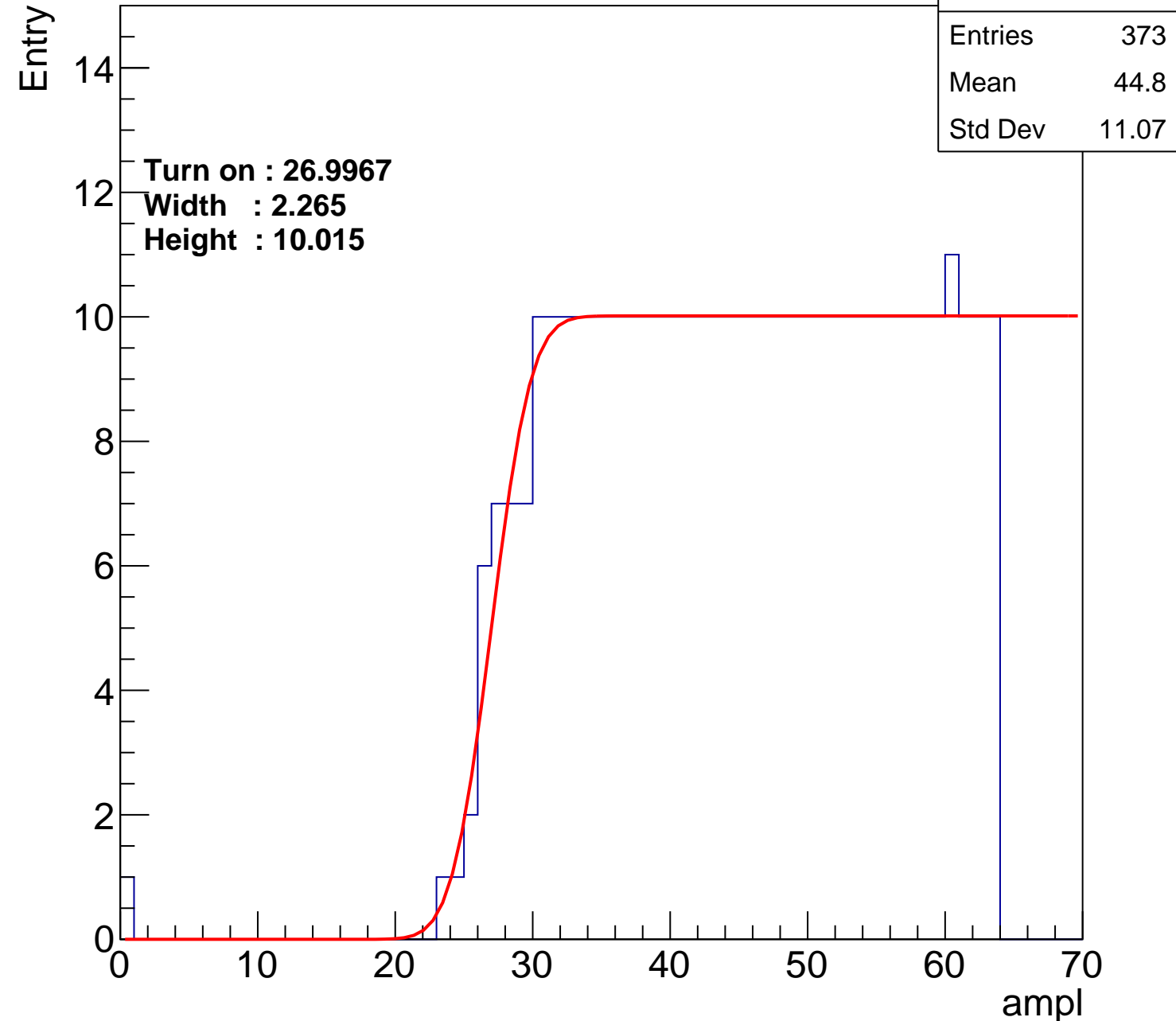
Width : 2.265

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch24

calib_packv5_042523_0143.root, FC#3, port B3

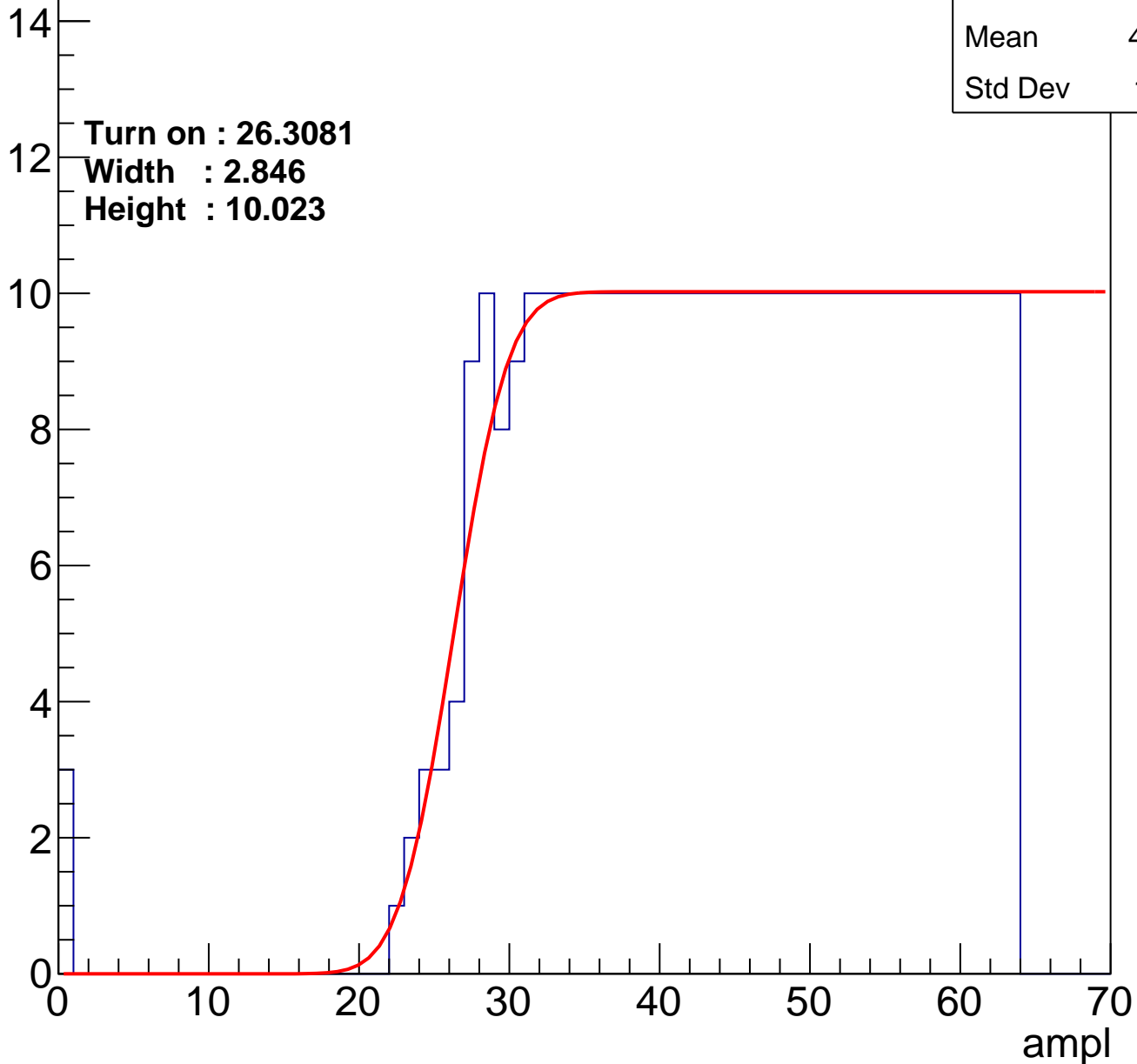
Entries	382
Mean	44.12
Std Dev	11.71

Turn on : 26.3081

Width : 2.846

Height : 10.023

Entry



B1L000S, U22-ch25

calib_packv5_042523_0143.root, FC#3, port B3

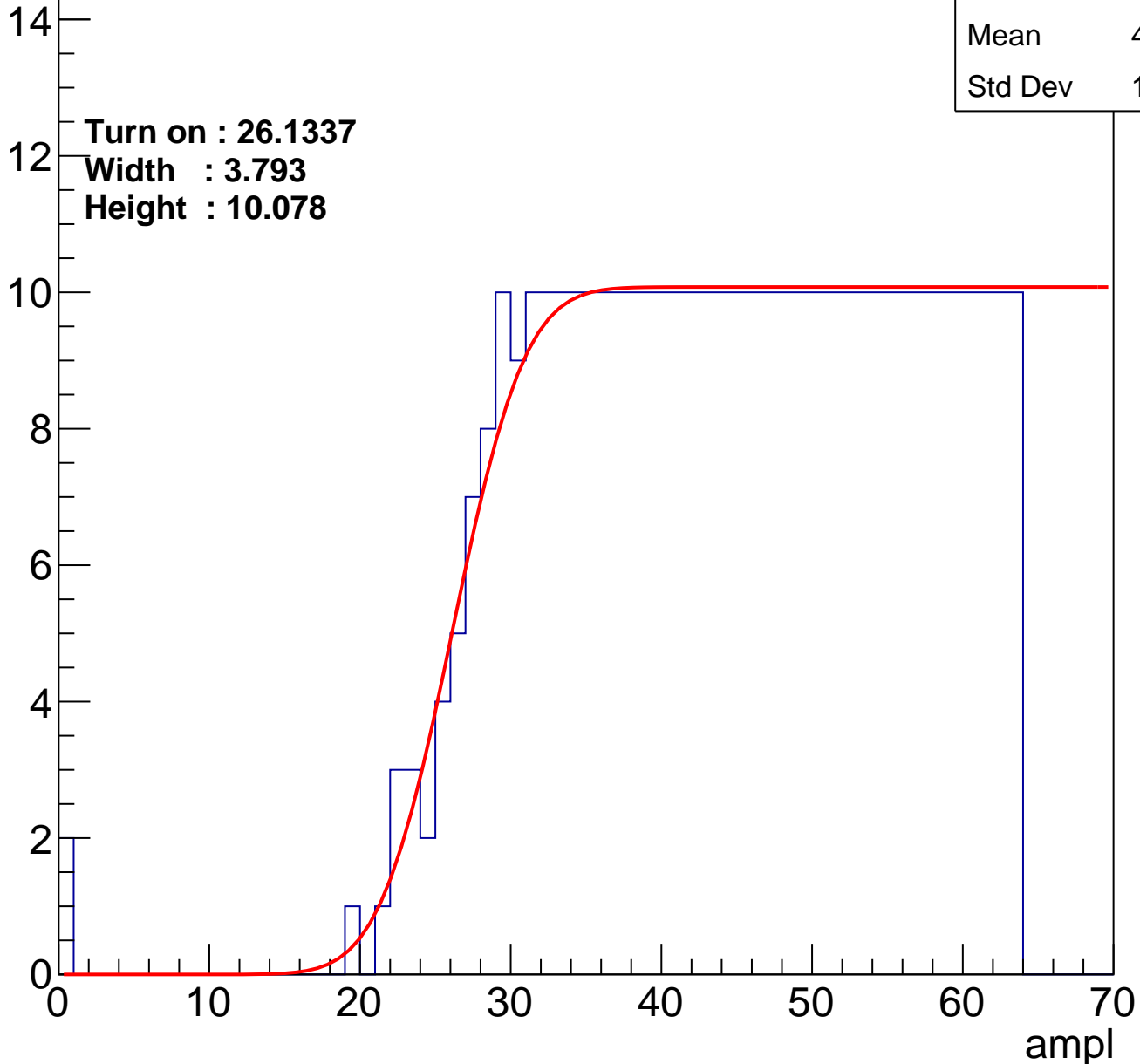
Entries	385
Mean	43.99
Std Dev	11.69

Turn on : 26.1337

Width : 3.793

Height : 10.078

Entry



B1L000S, U22-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.69
Std Dev	11.08

Turn on : 26.6131

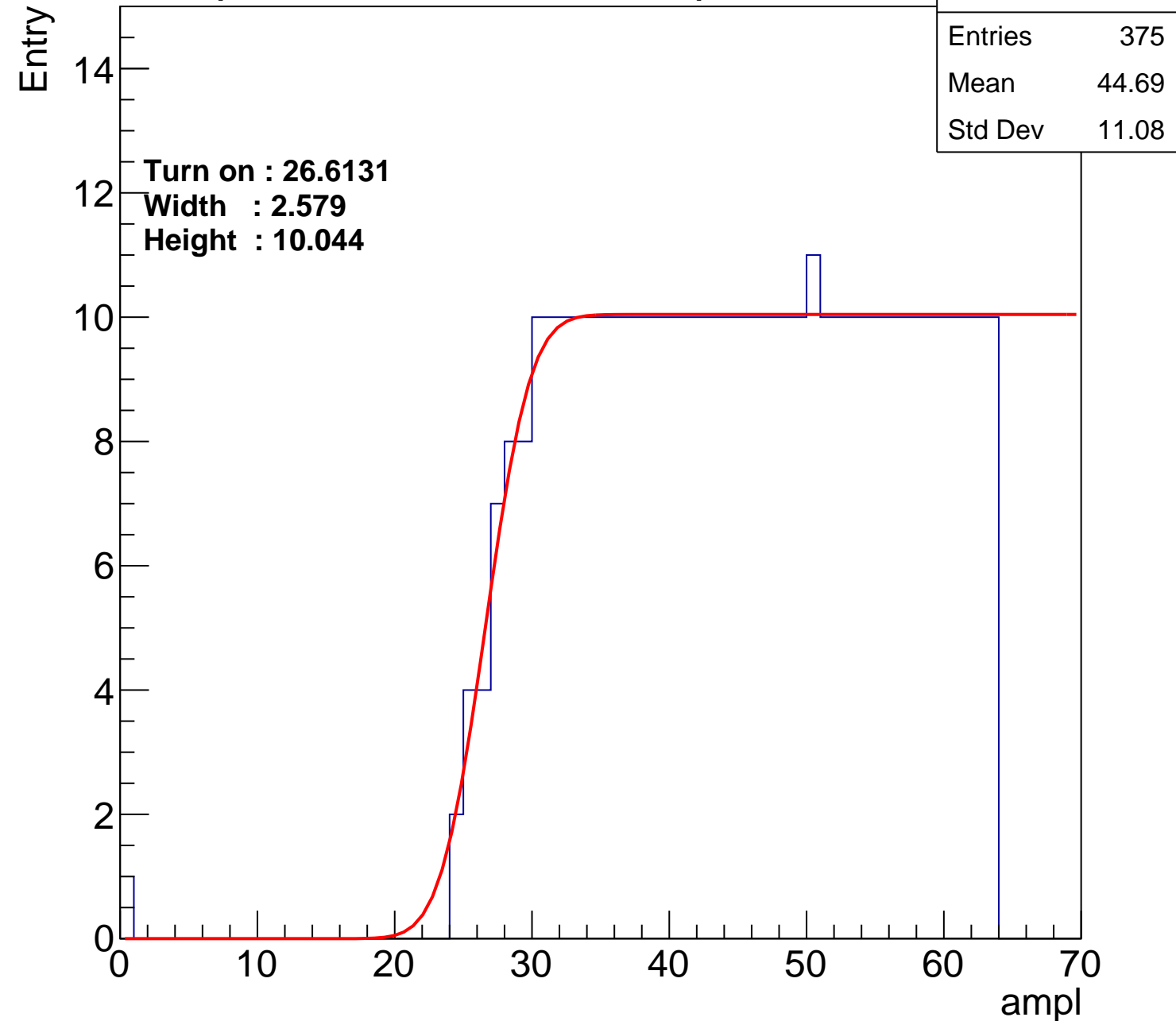
Width : 2.579

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.92
Std Dev	11.49

Turn on : 25.0807

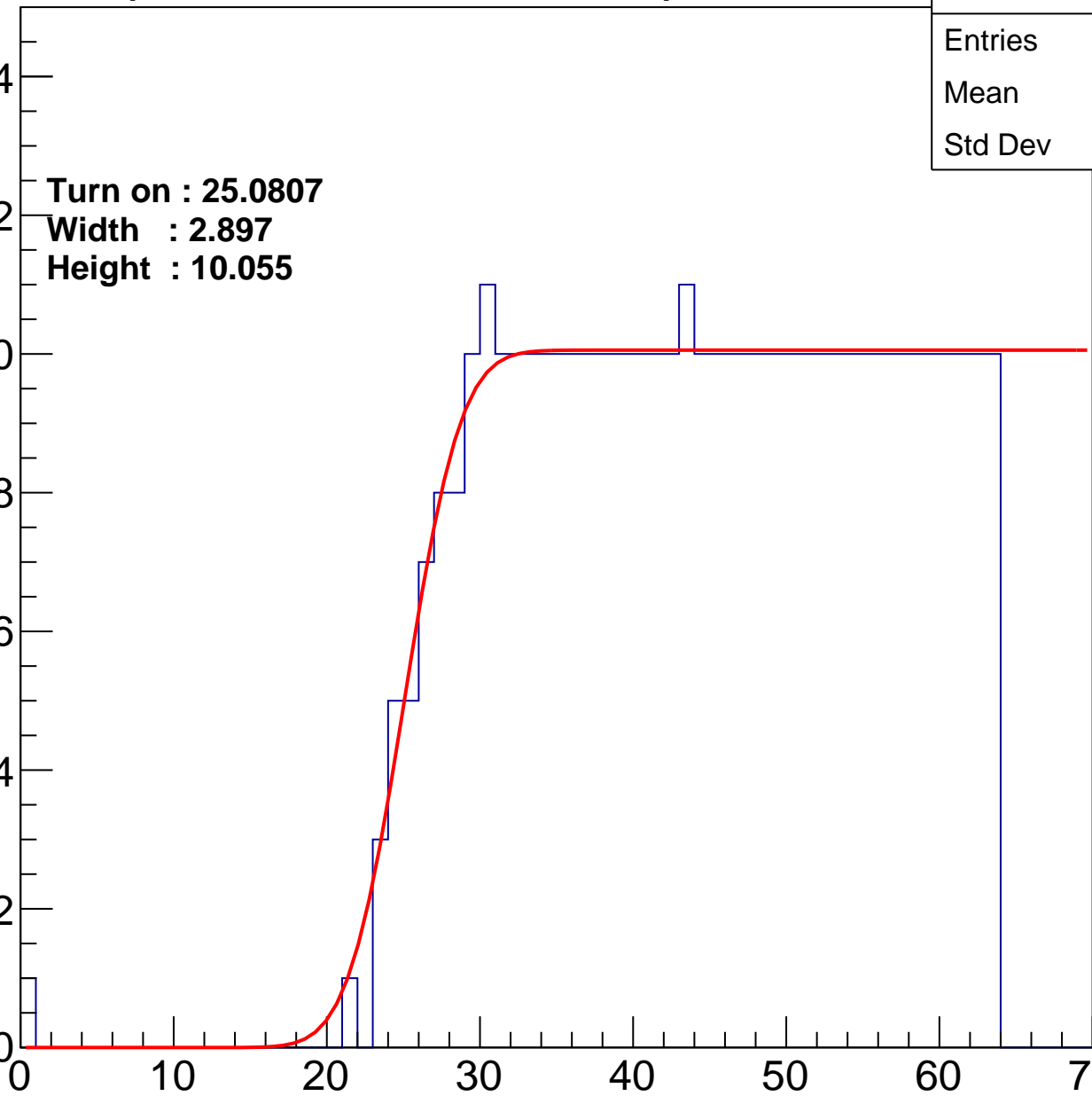
Width : 2.897

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.58
Std Dev	11.33

Turn on : 26.4359

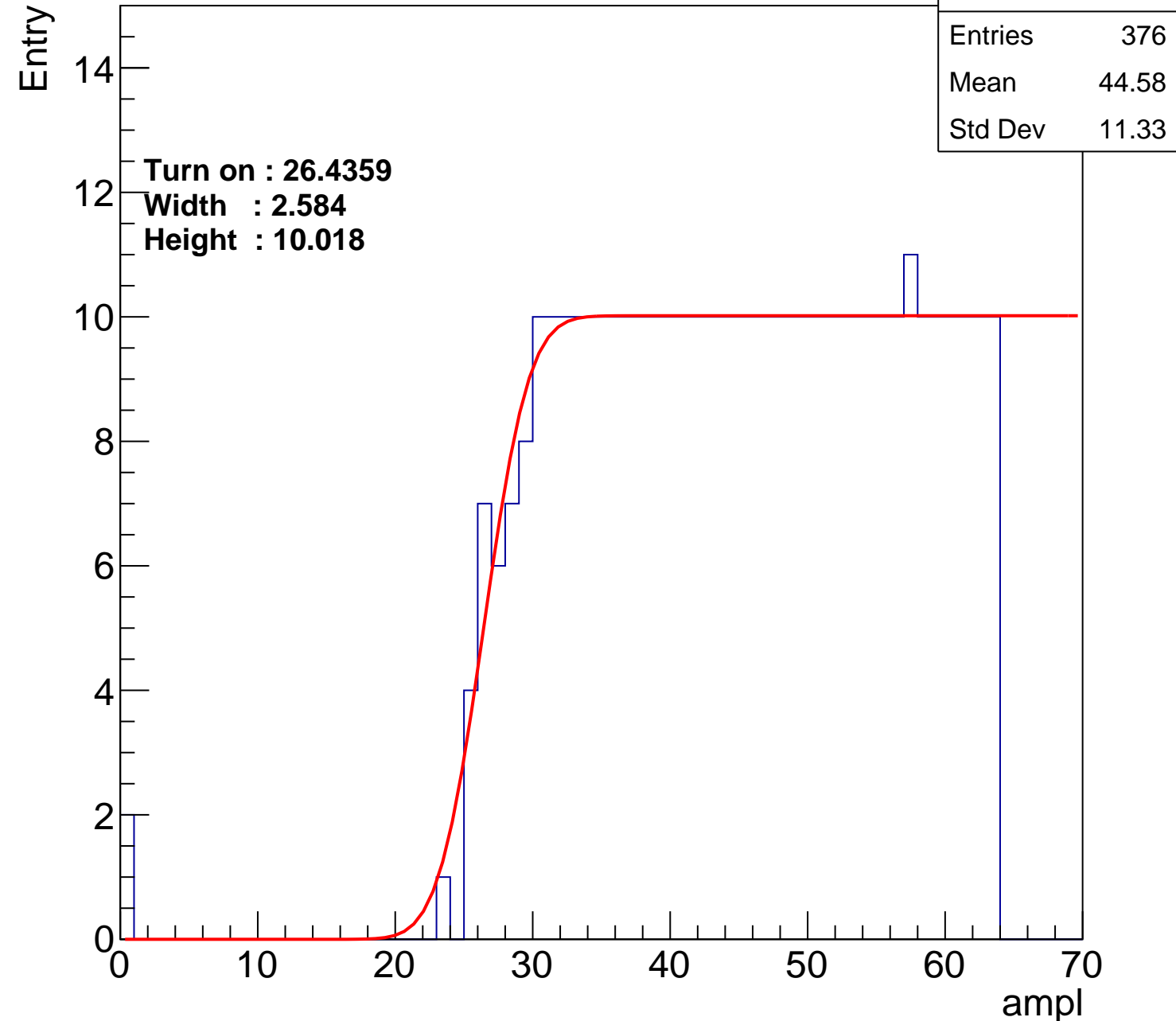
Width : 2.584

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.03
Std Dev	11.29

Turn on : 28.6177

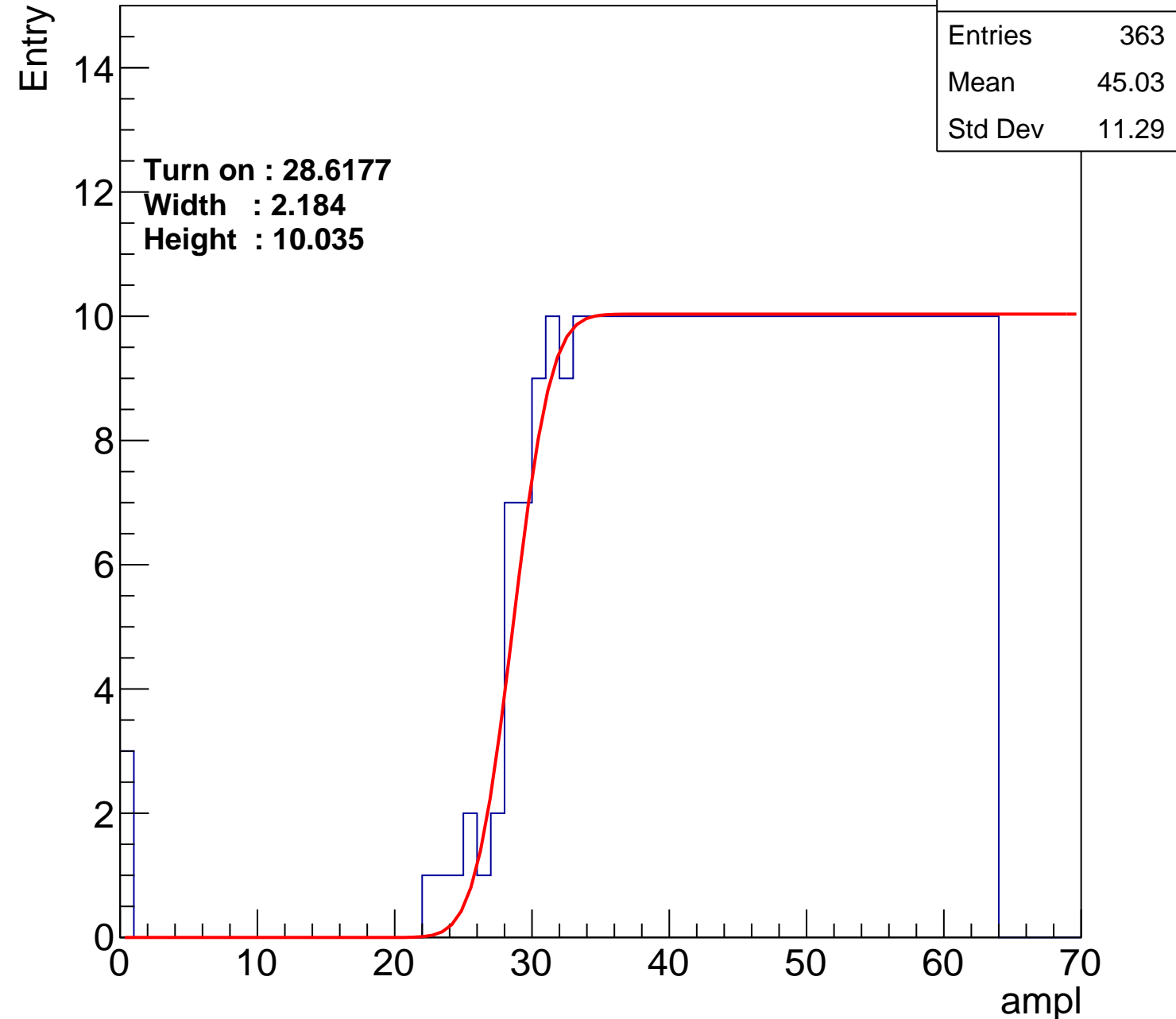
Width : 2.184

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch30

calib_packv5_042523_0143.root, FC#3, port B3

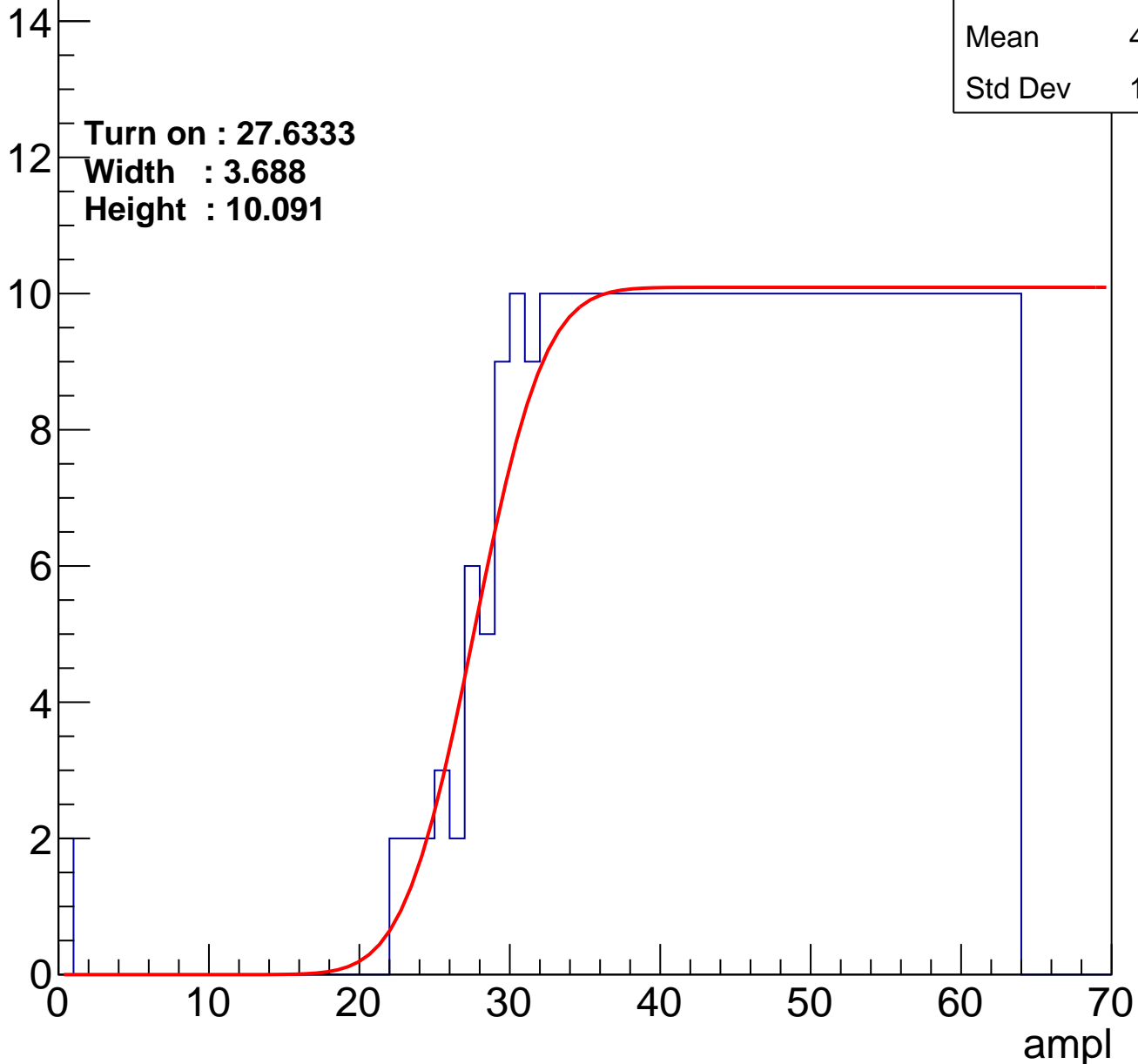
Entries	372
Mean	44.64
Std Dev	11.34

Turn on : 27.6333

Width : 3.688

Height : 10.091

Entry



B1L000S, U22-ch31

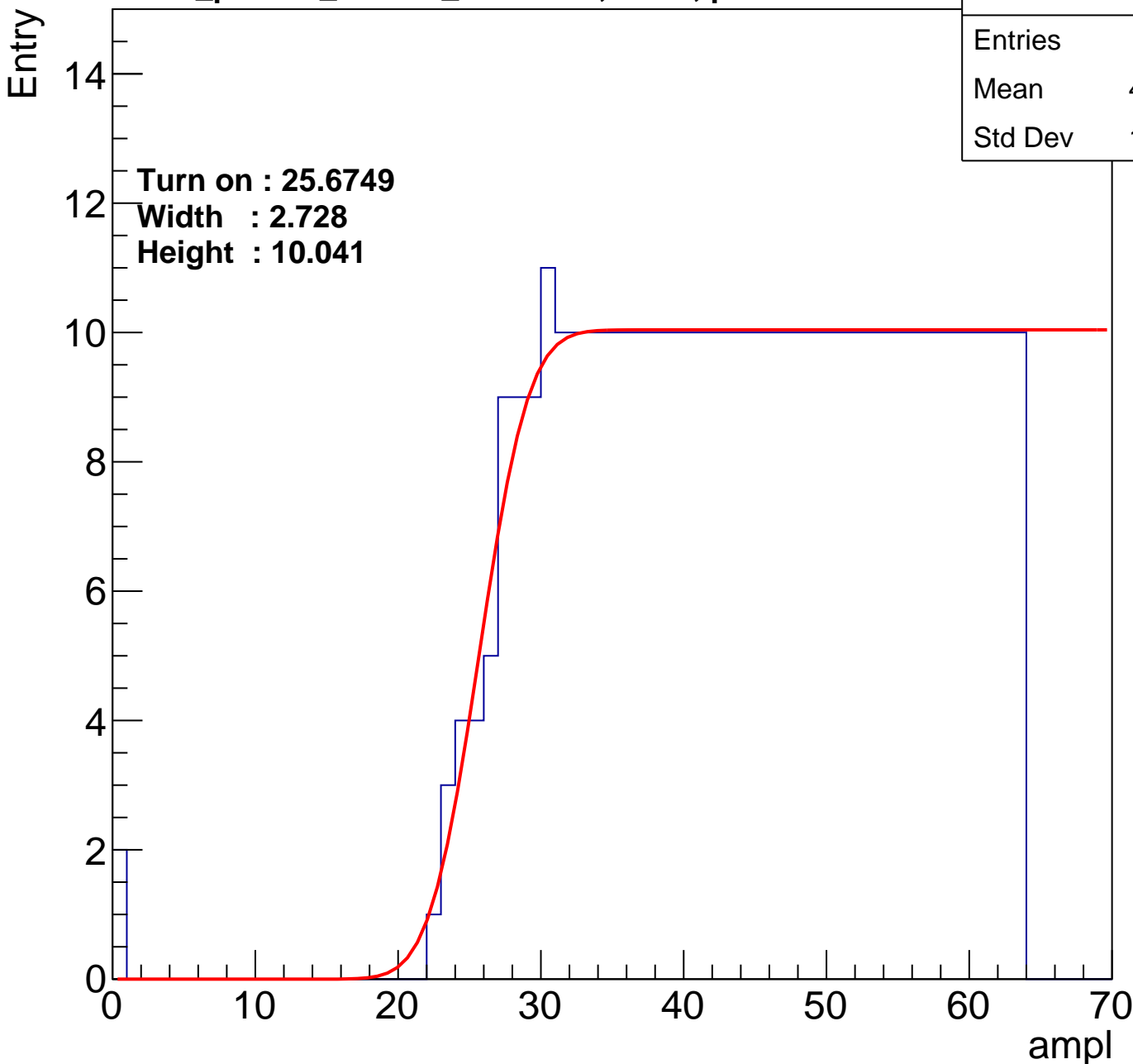
calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.6749

Width : 2.728

Height : 10.041



B1L000S, U22-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.42
Std Dev	11.42

Turn on : 26.5141

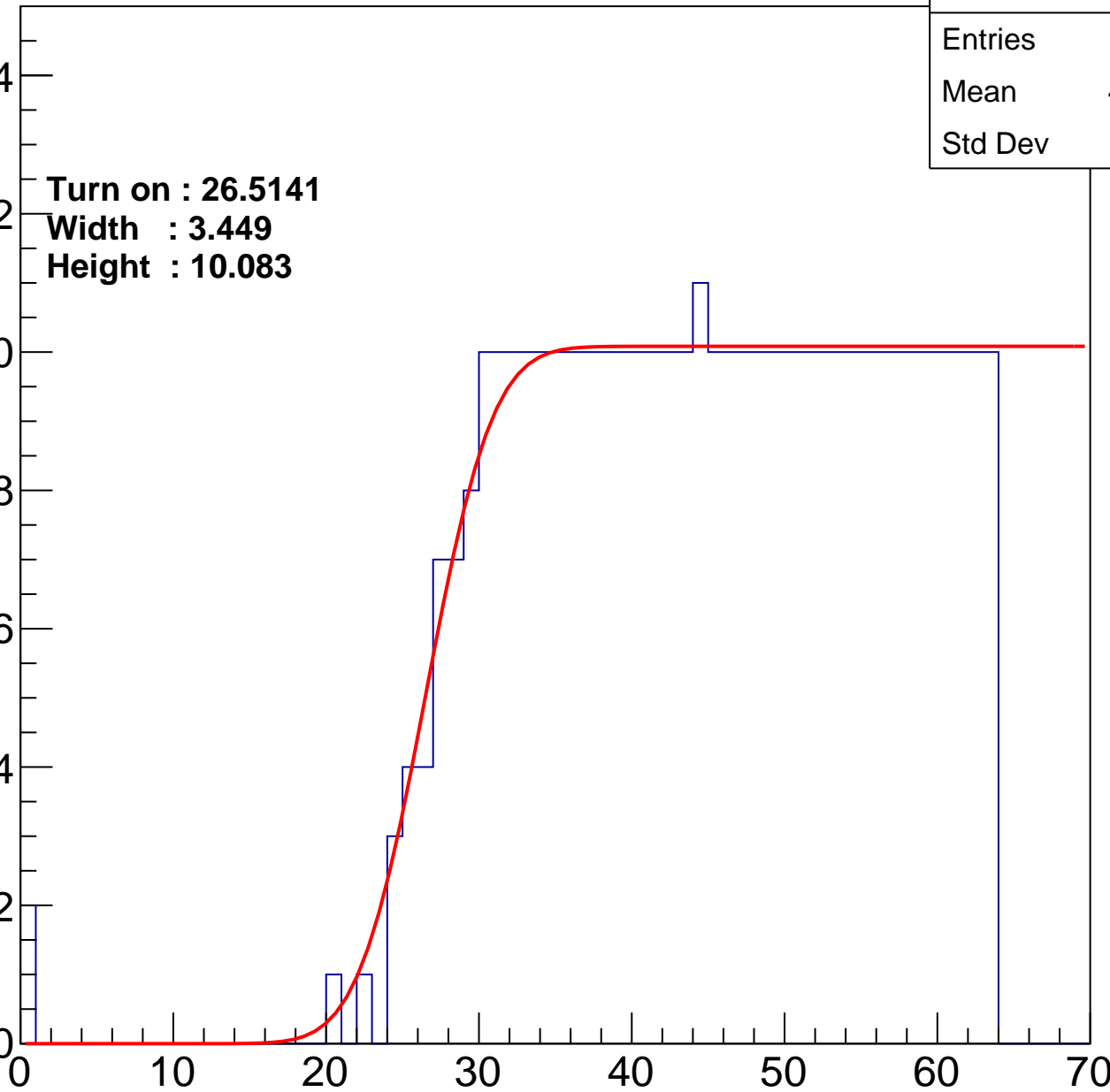
Width : 3.449

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.79
Std Dev	11.98

Turn on : 26.0813

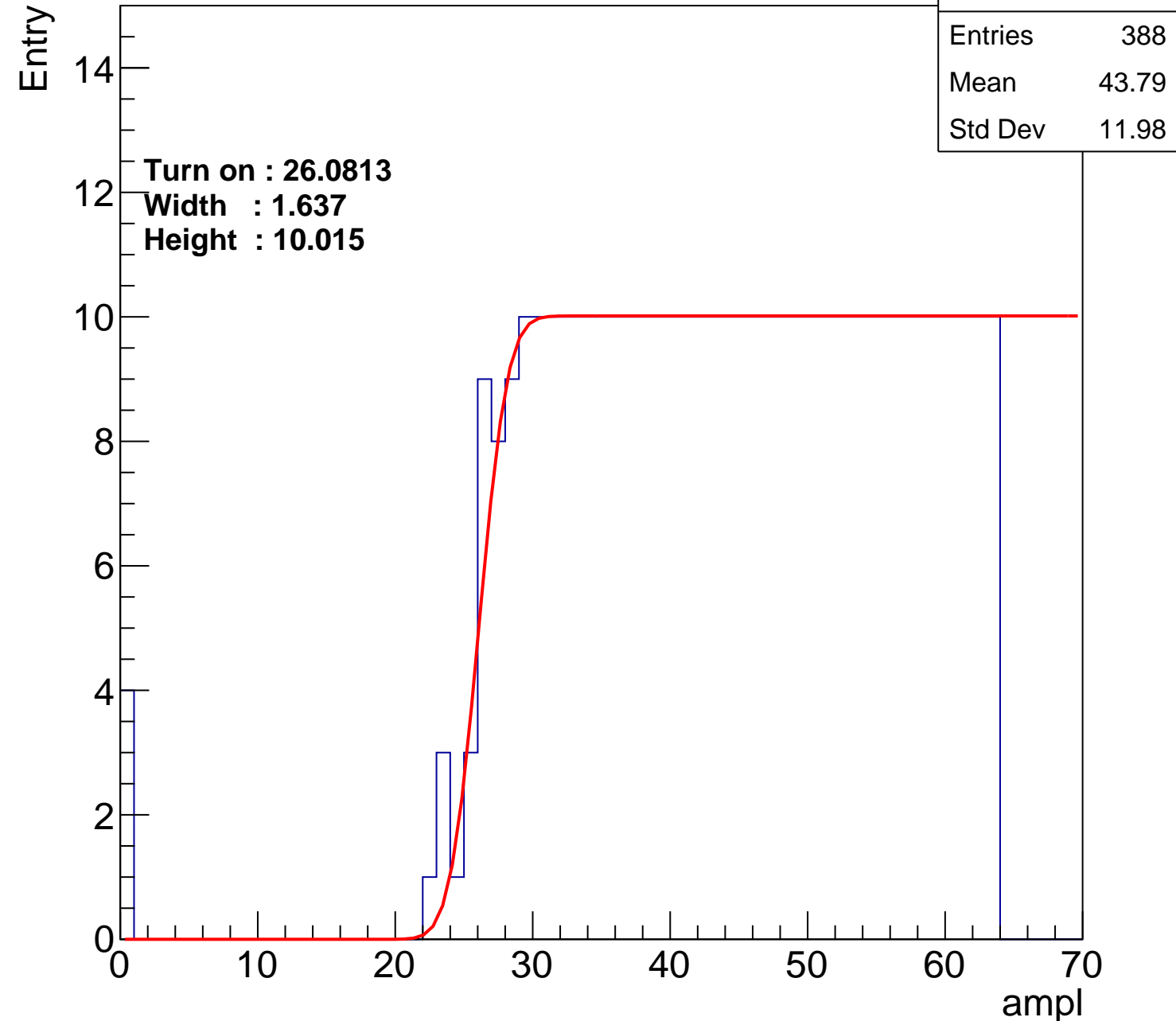
Width : 1.637

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.37
Std Dev	11.26

Turn on : 26.2083

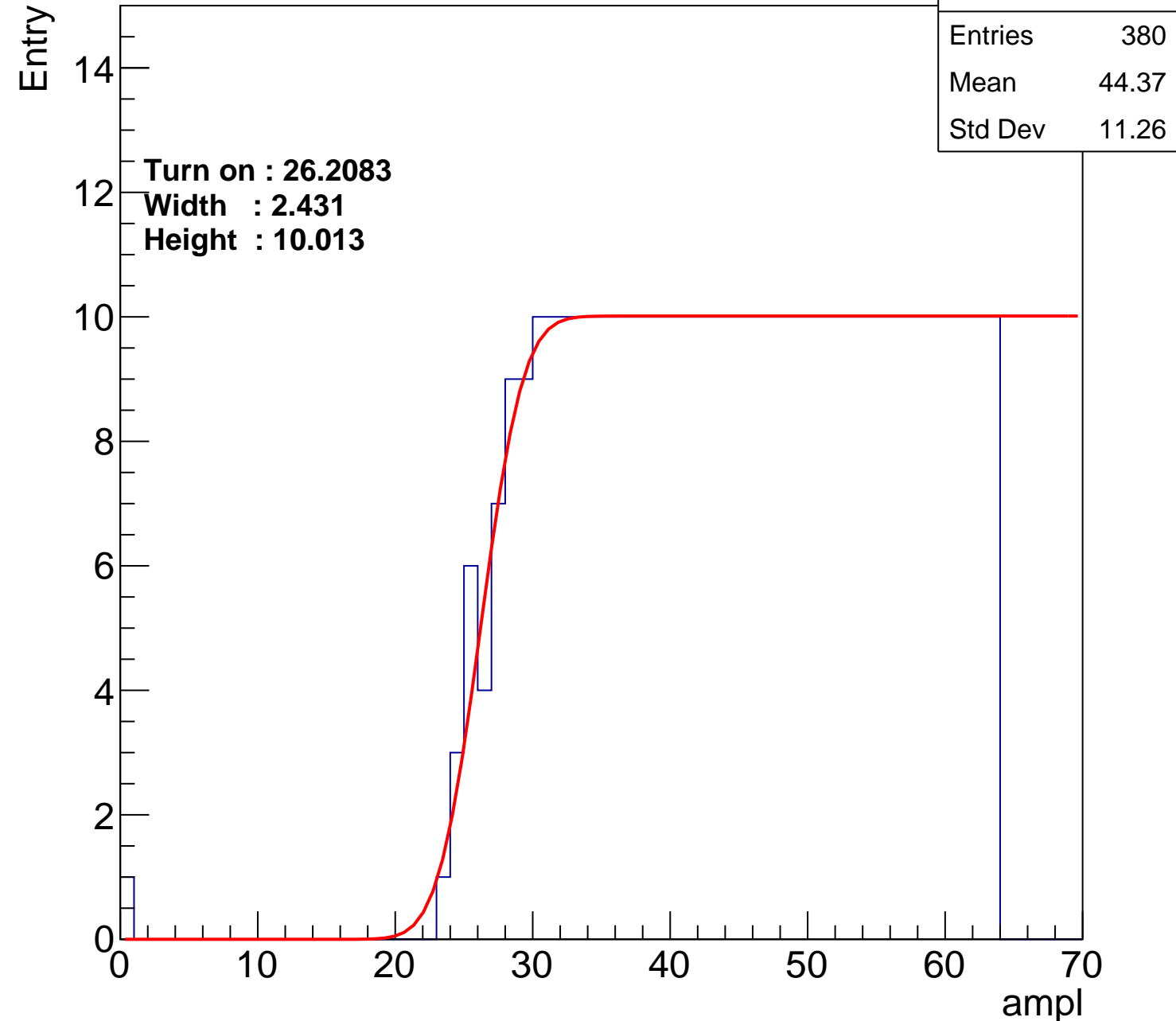
Width : 2.431

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.62
Std Dev	11.46

Turn on : 27.3089

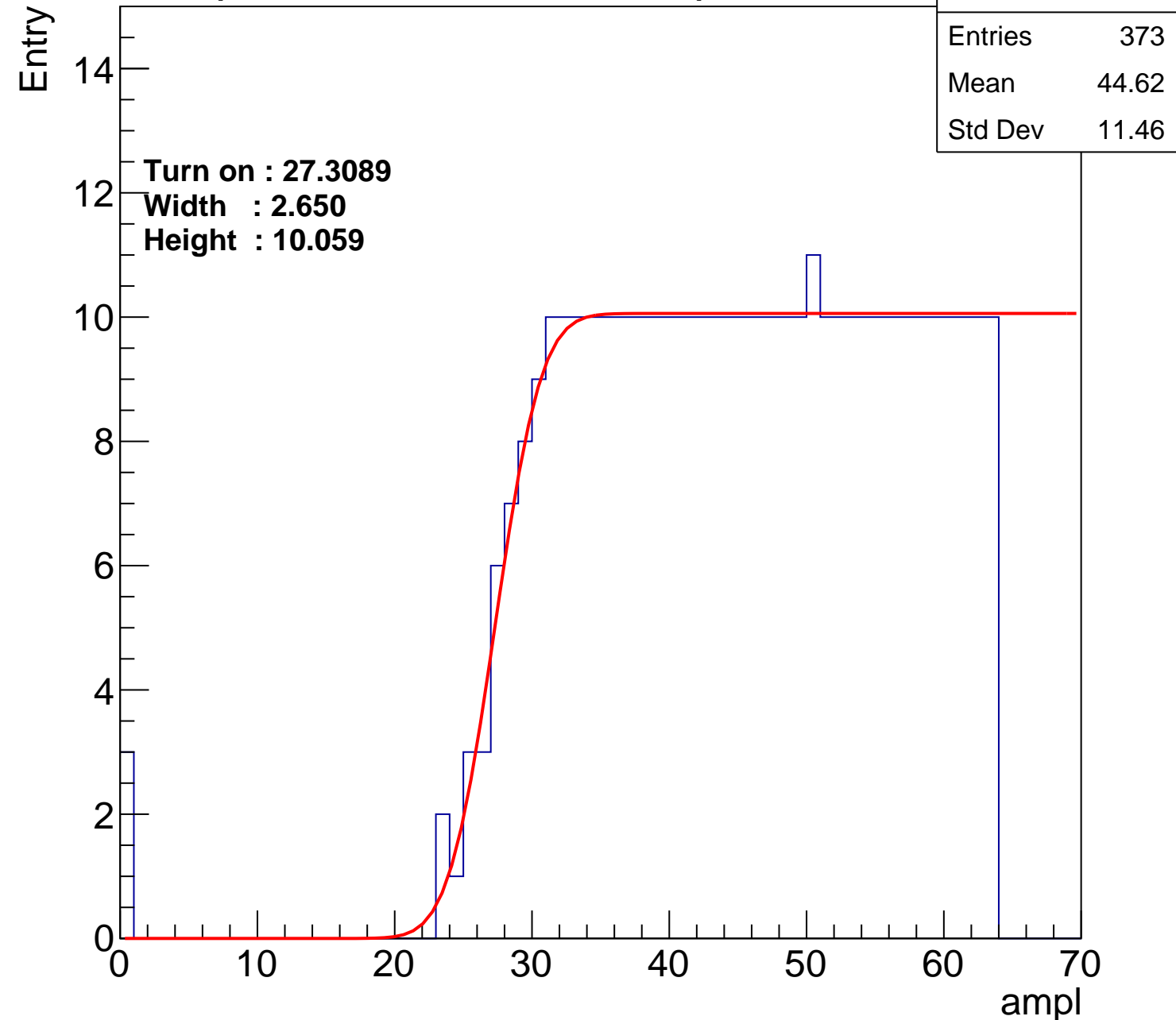
Width : 2.650

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.59
Std Dev	11.18

Turn on : 27.2011

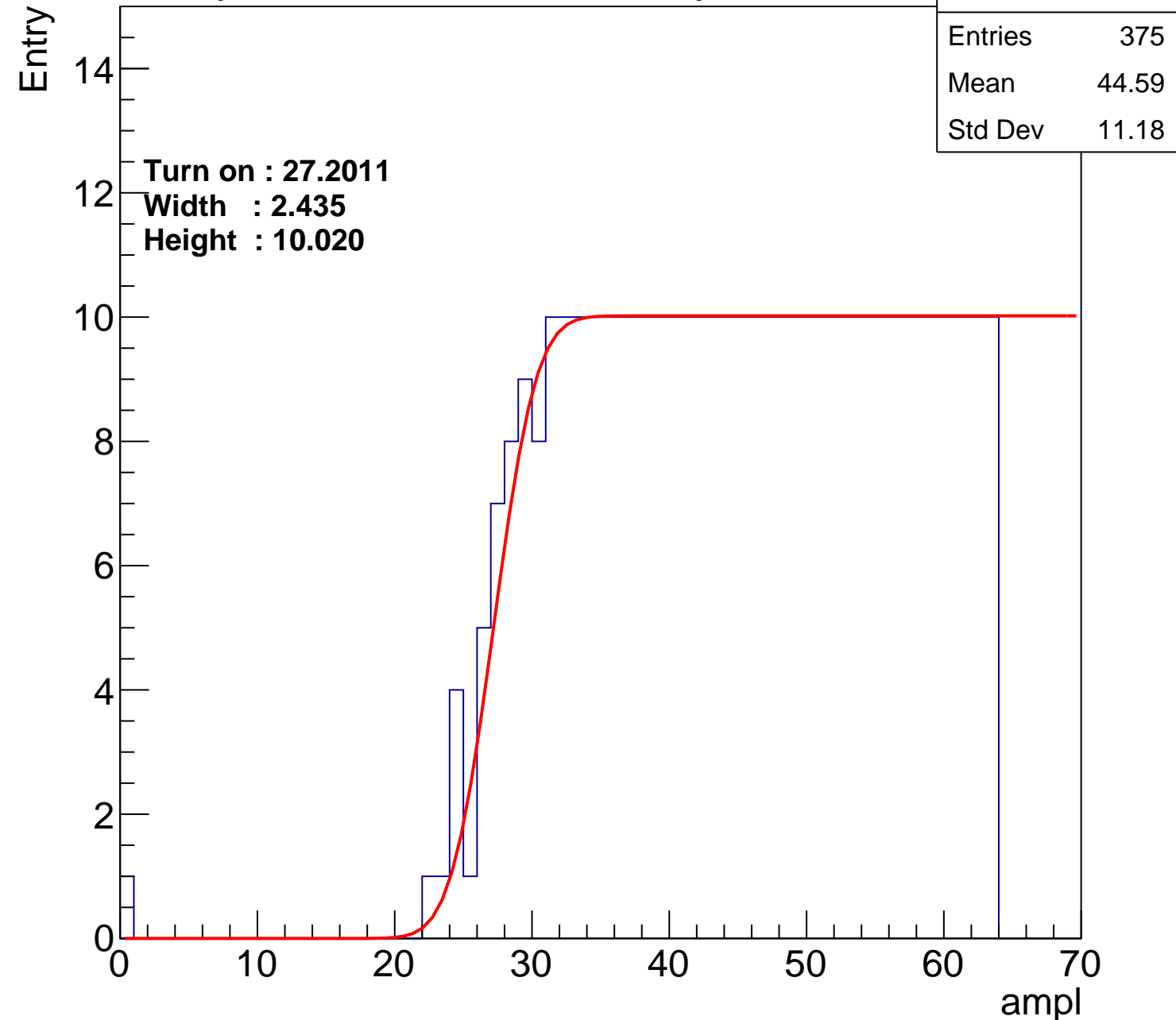
Width : 2.435

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch37

calib_packv5_042523_0143.root, FC#3, port B3

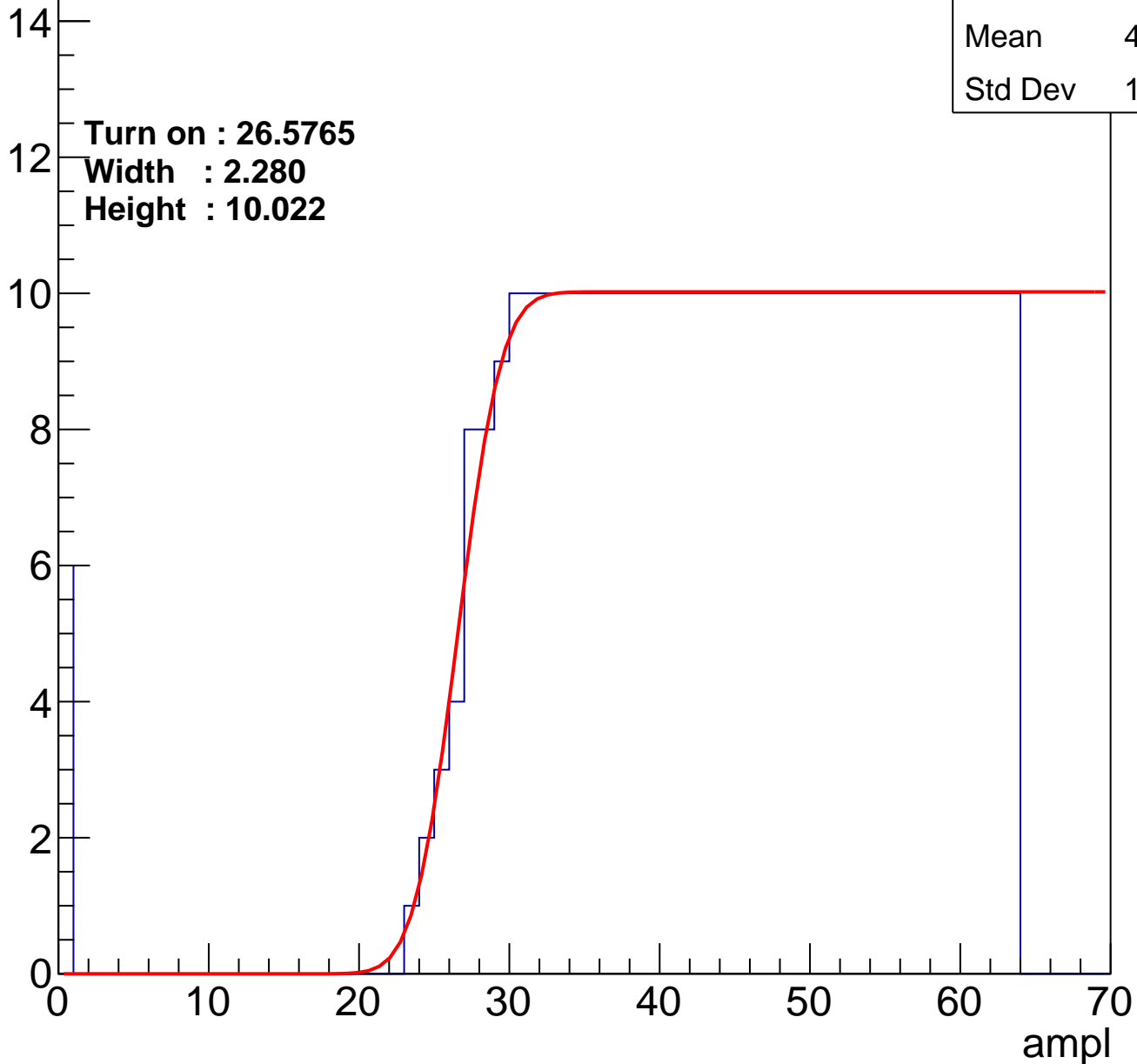
Entries	381
Mean	43.99
Std Dev	12.17

Turn on : 26.5765

Width : 2.280

Height : 10.022

Entry



B1L000S, U22-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.53
Std Dev	12.13

Turn on : 24.7503

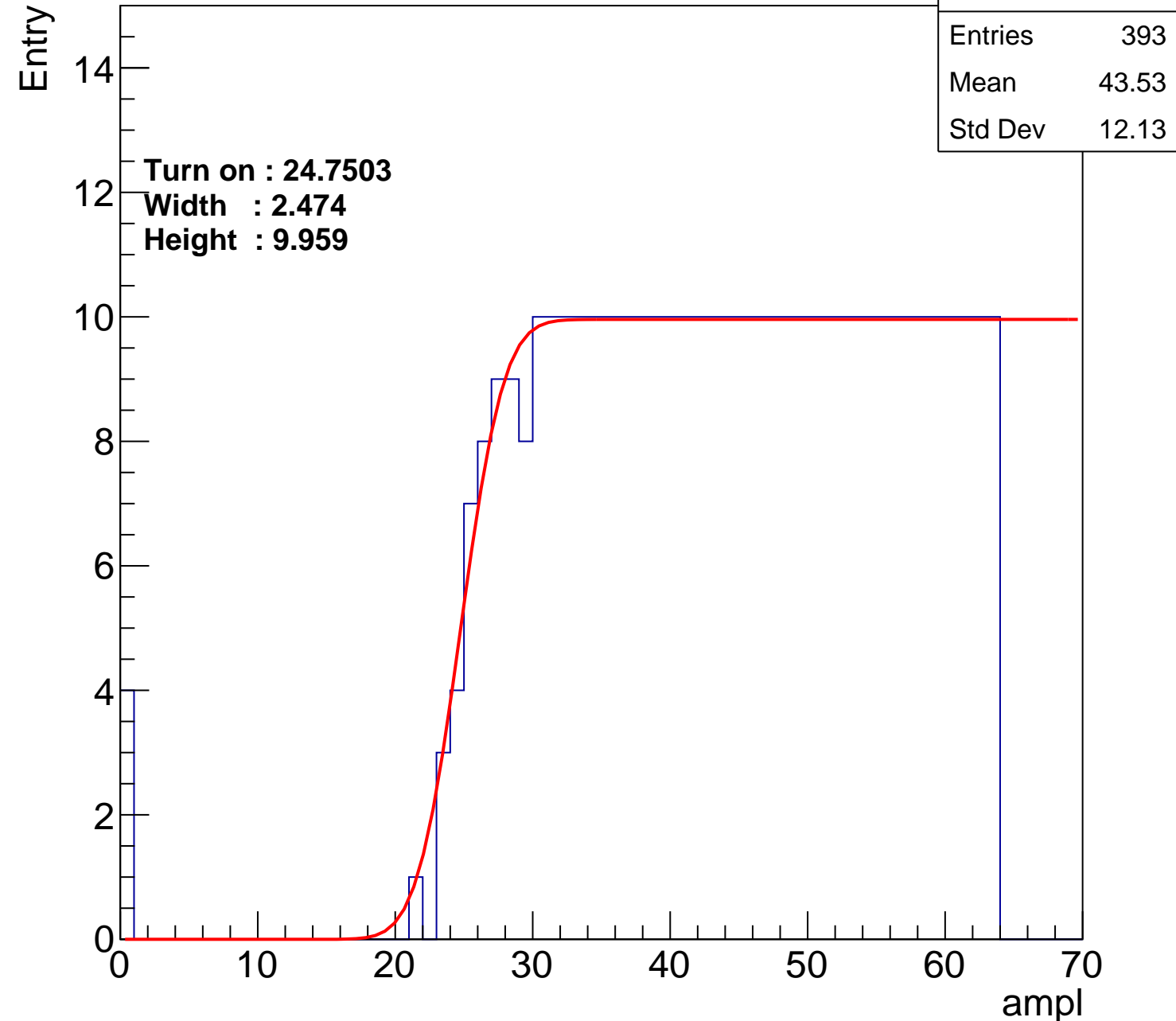
Width : 2.474

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 26.0134

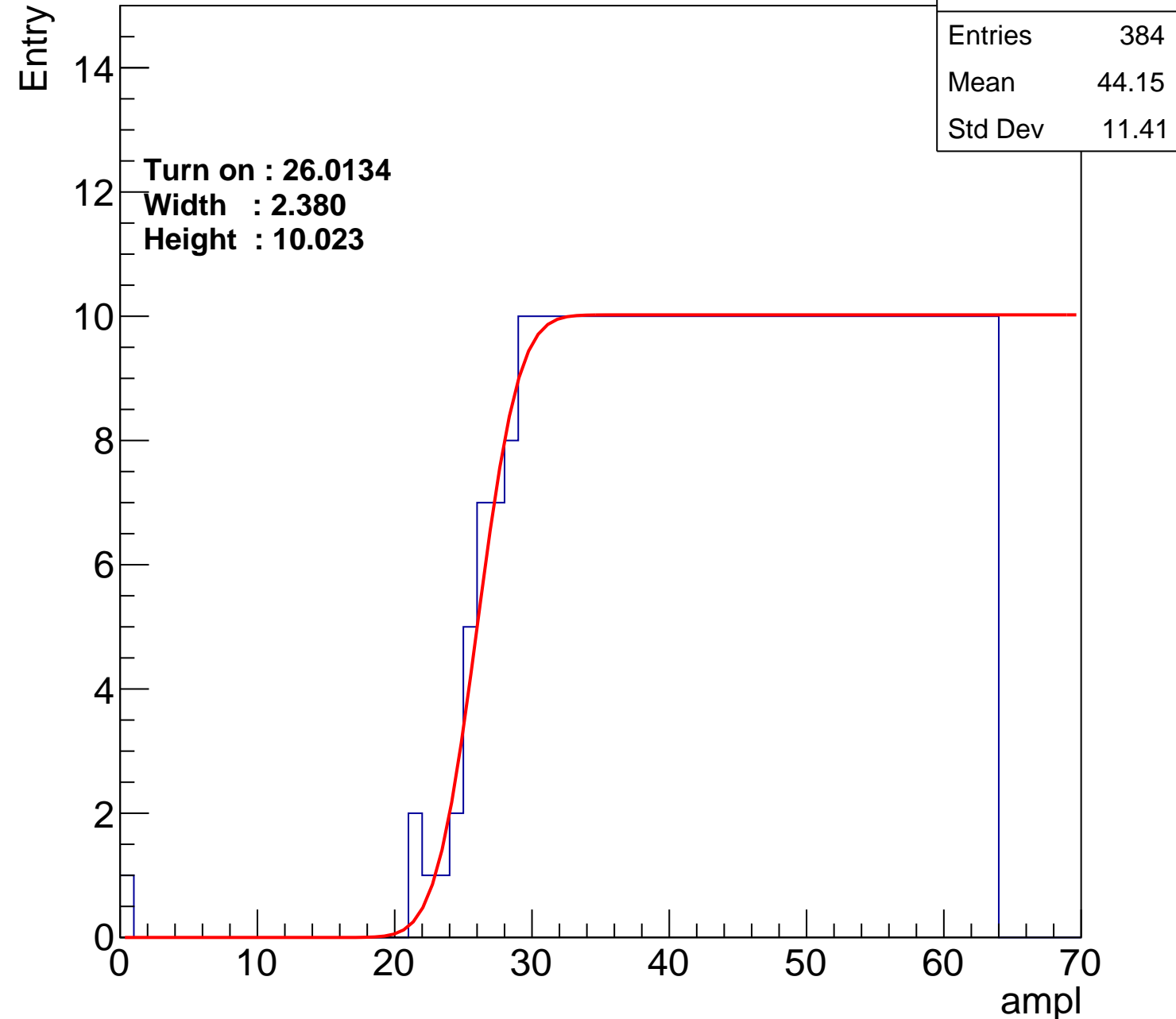
Width : 2.380

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.58
Std Dev	11.31

Turn on : 27.0164

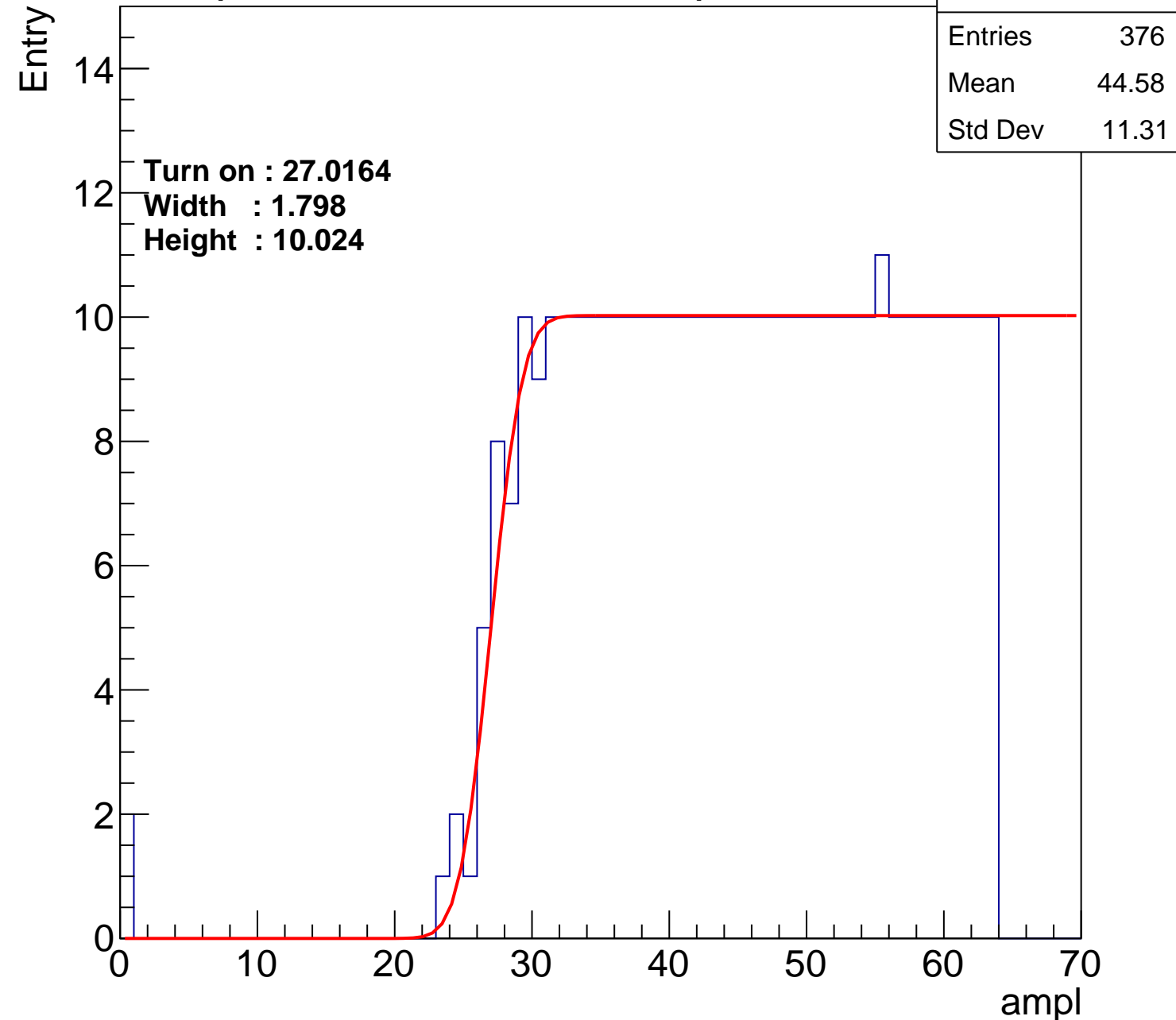
Width : 1.798

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.85
Std Dev	12

Turn on : 25.1878

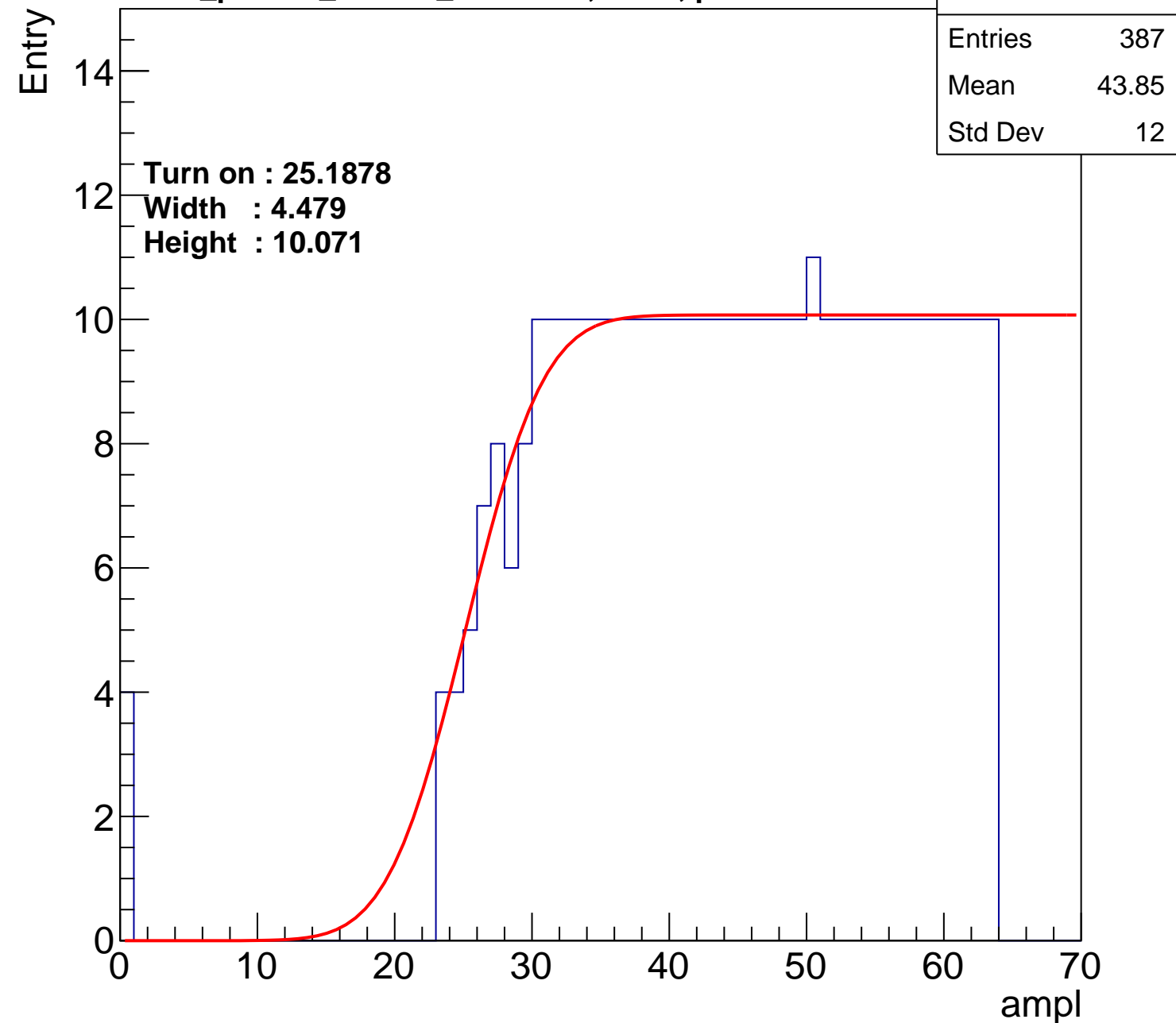
Width : 4.479

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch42

calib_packv5_042523_0143.root, FC#3, port B3

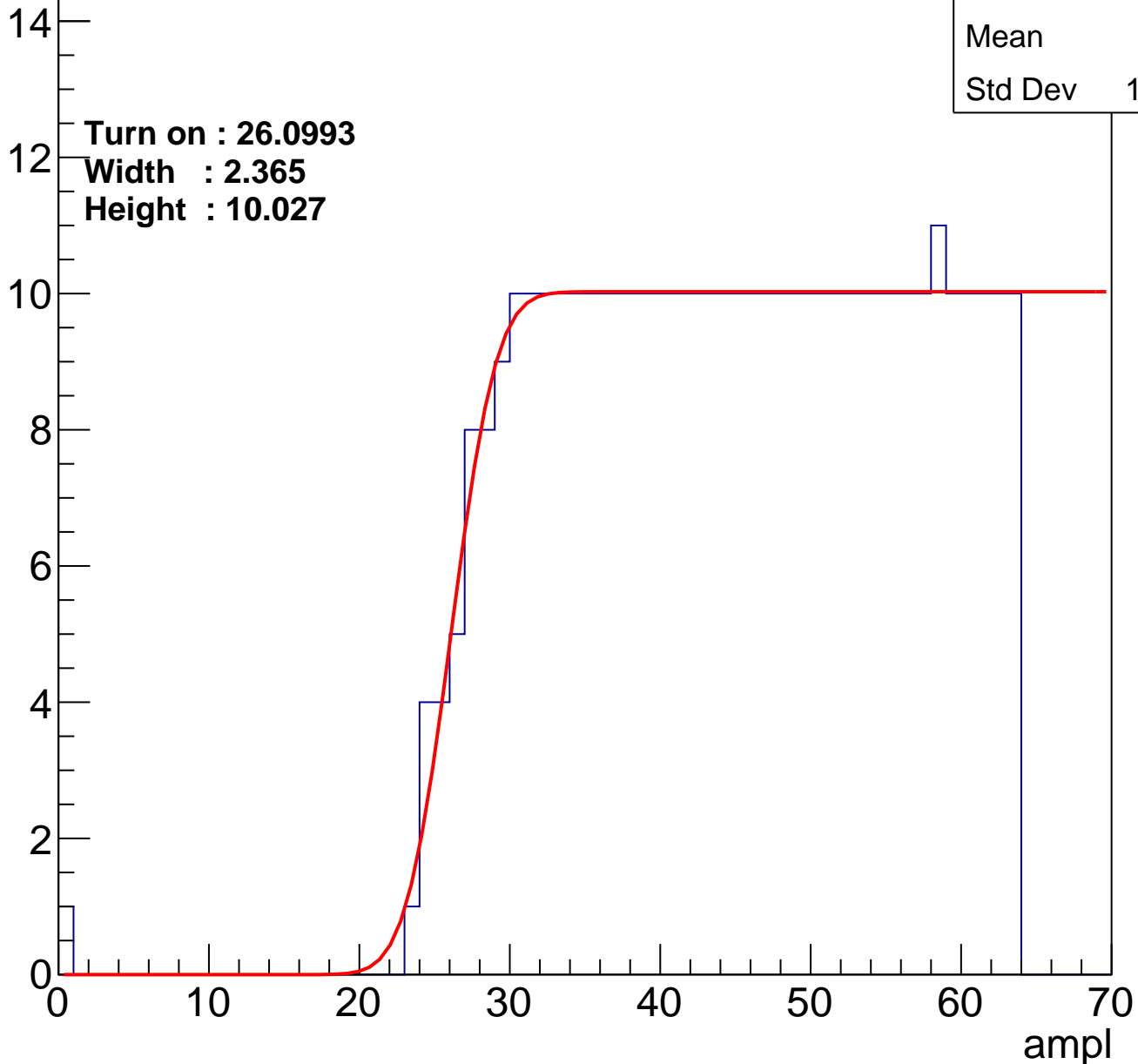
Entries	381
Mean	44.4
Std Dev	11.27

Turn on : 26.0993

Width : 2.365

Height : 10.027

Entry



B1L000S, U22-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 27.0487

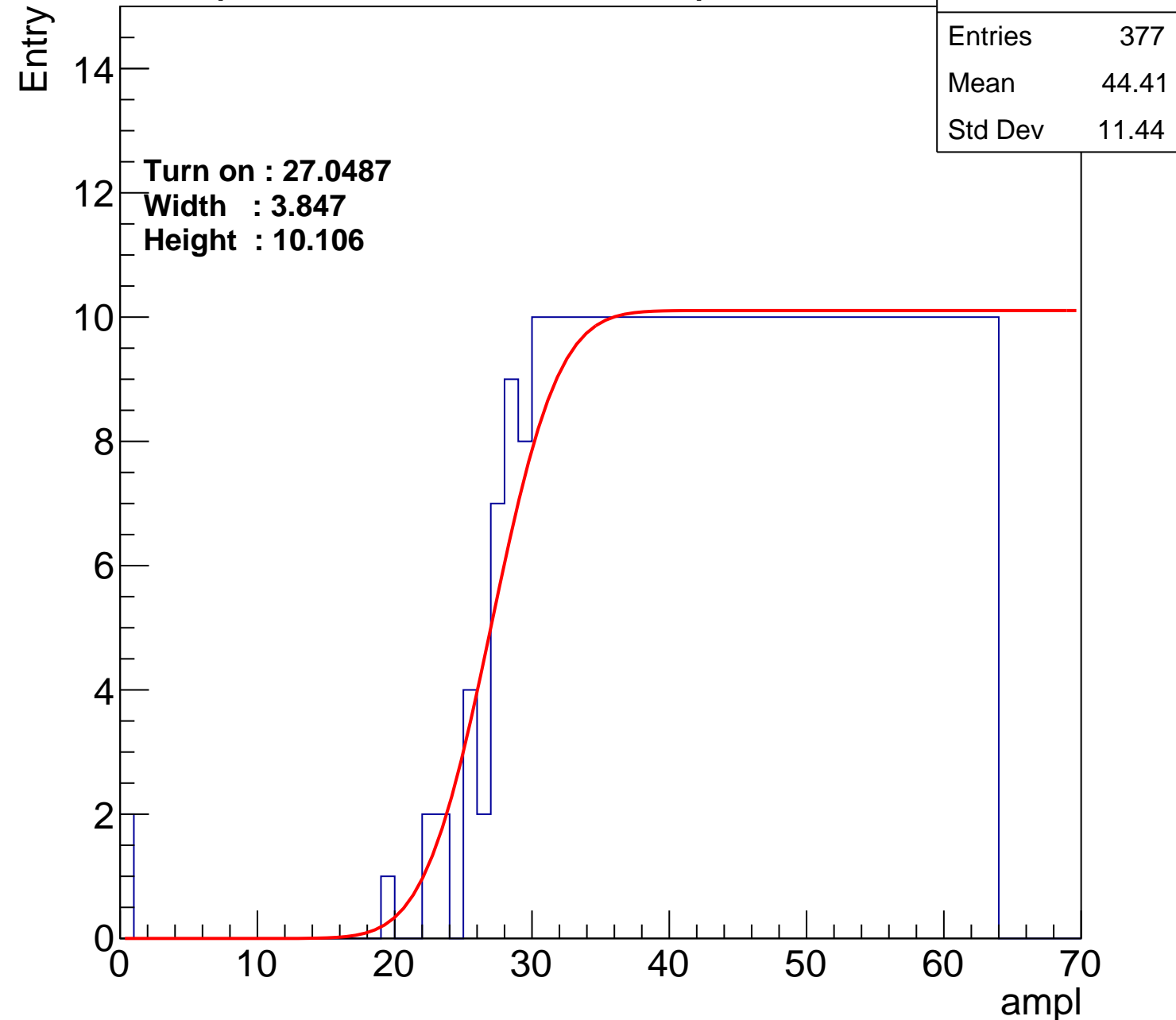
Width : 3.847

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.89
Std Dev	11.72

Turn on : 25.7661

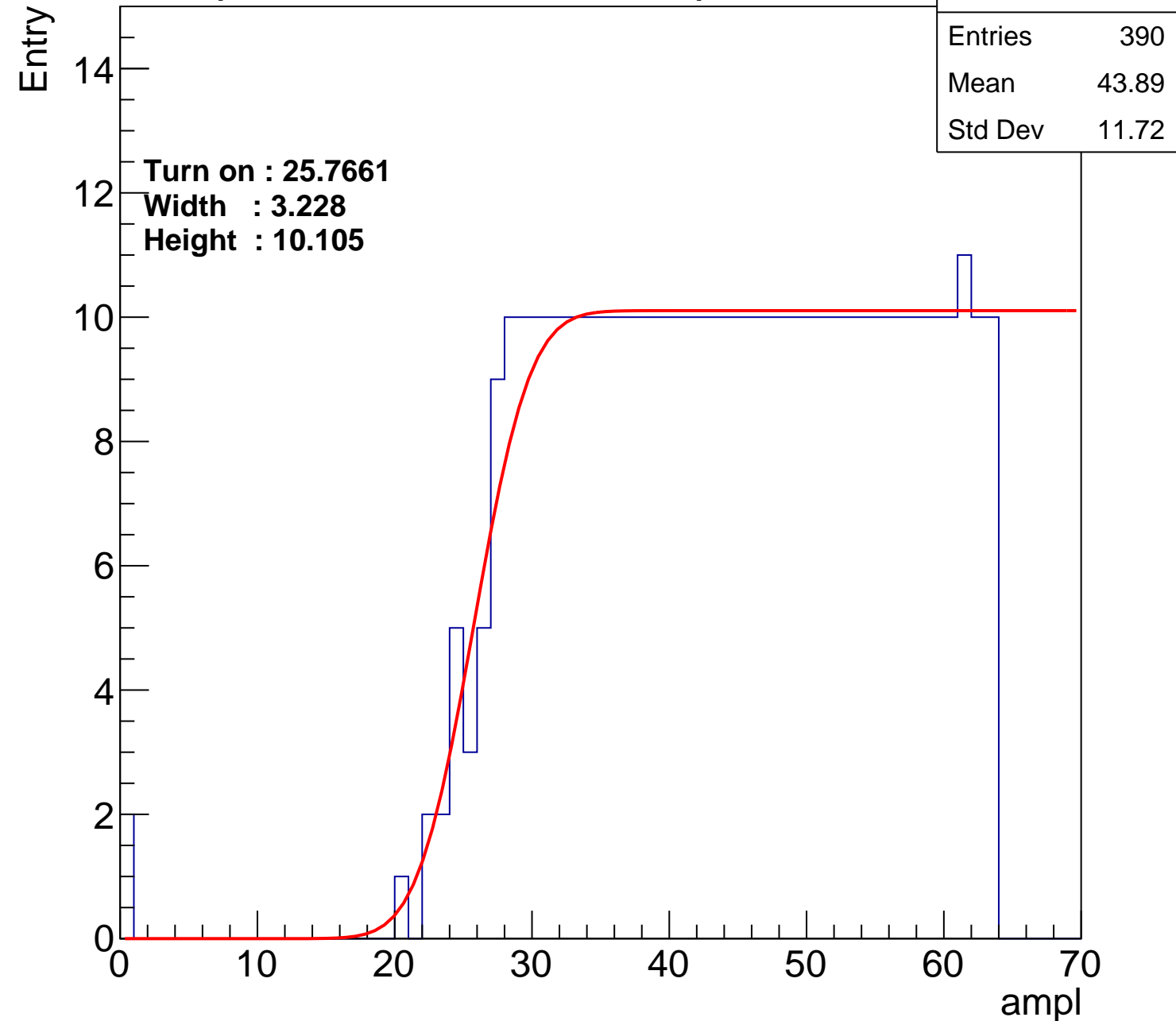
Width : 3.228

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.89
Std Dev	11.77

Turn on : 26.1162

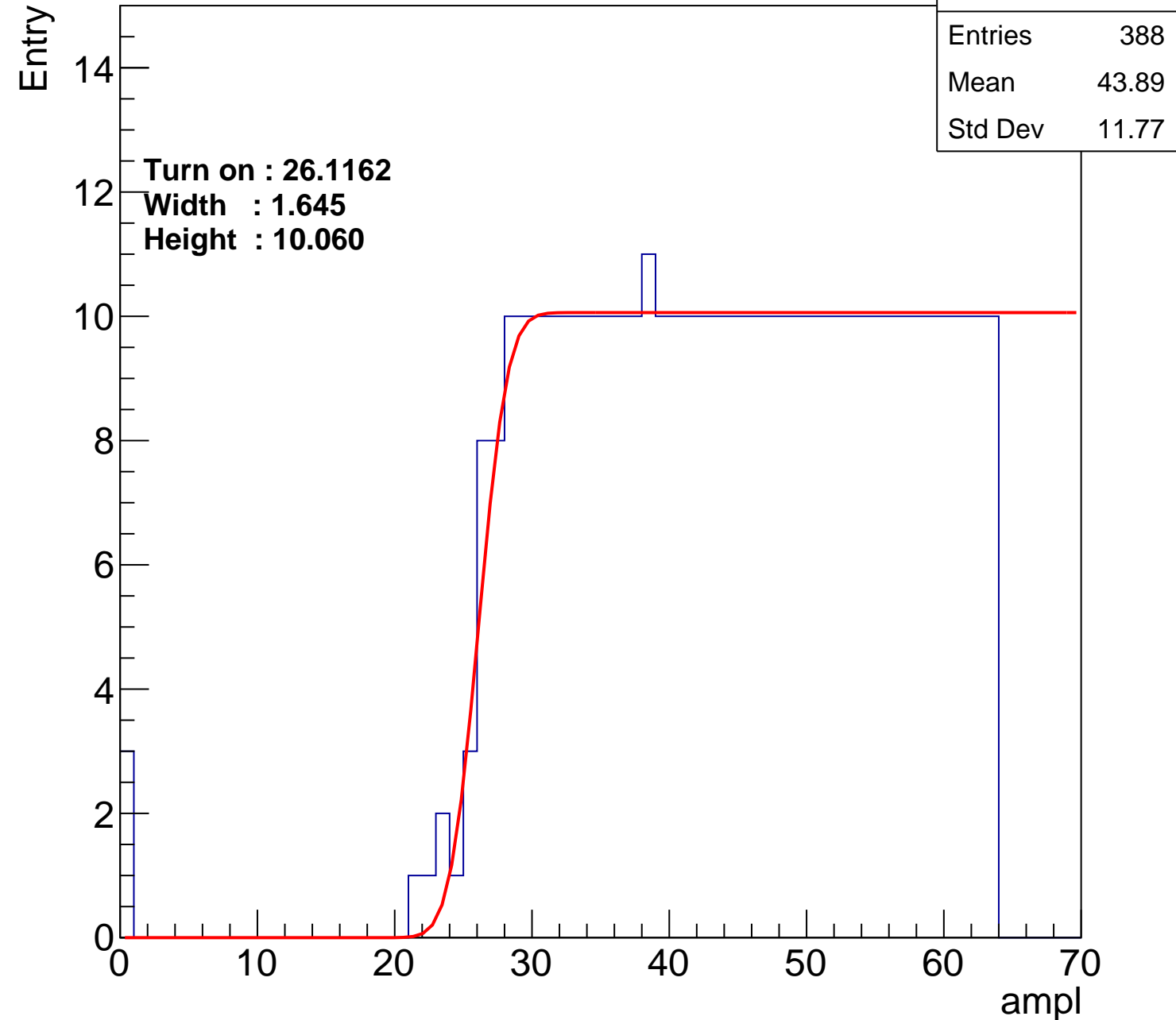
Width : 1.645

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.85
Std Dev	11.87

Turn on : 25.3712

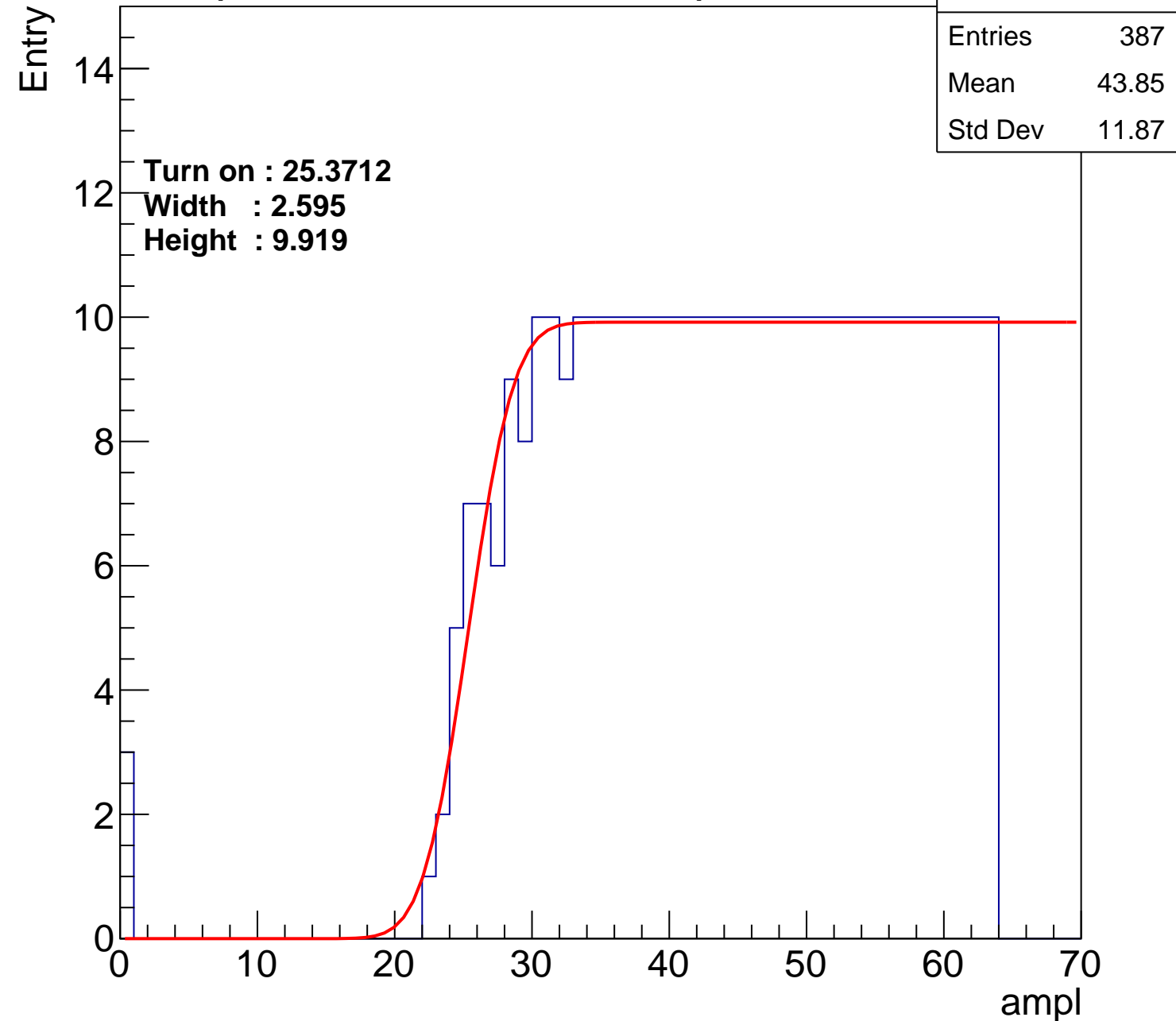
Width : 2.595

Height : 9.919

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.56
Std Dev	11.67

Turn on : 27.3927

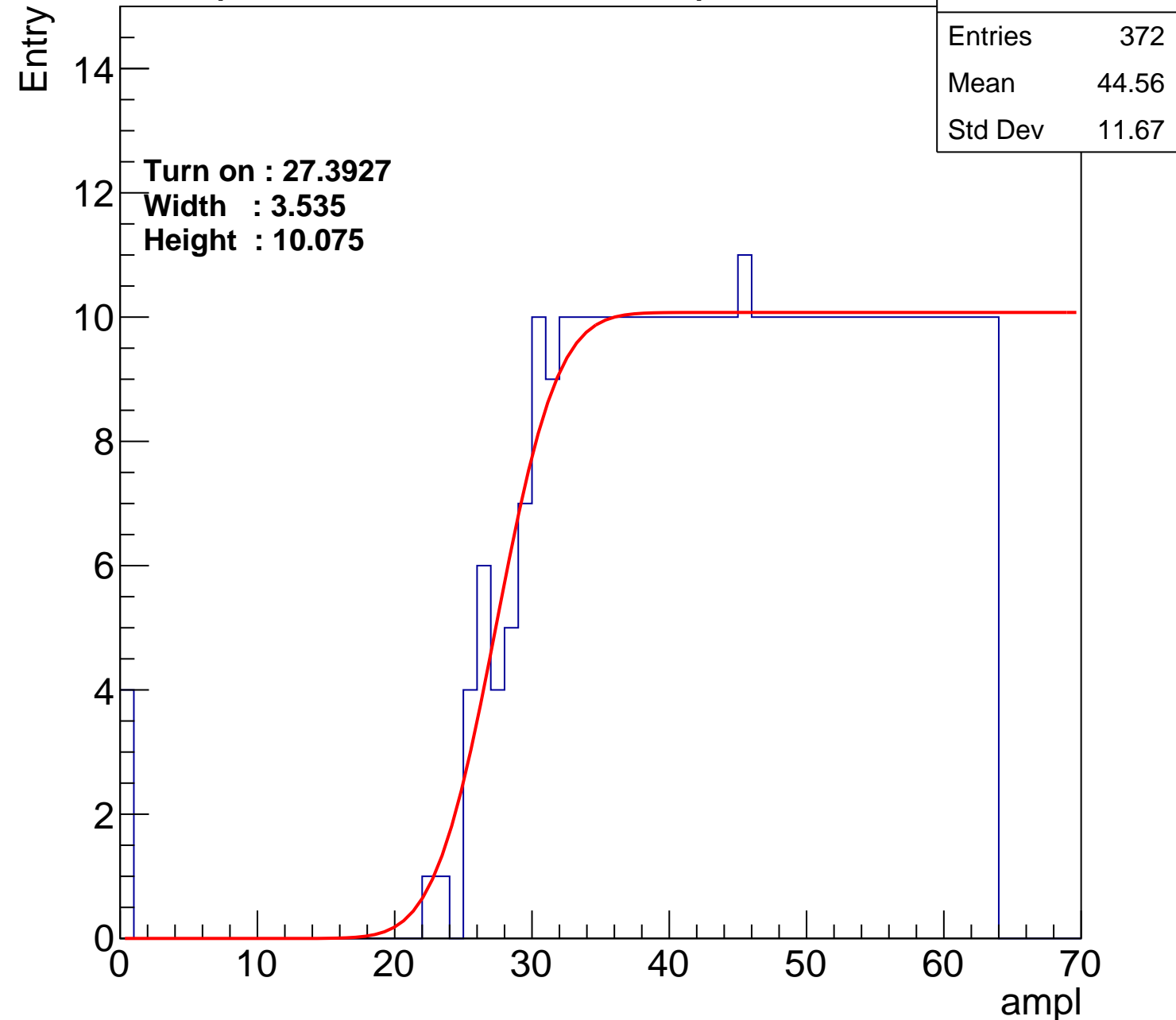
Width : 3.535

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.01
Std Dev	11.61

Turn on : 25.7673

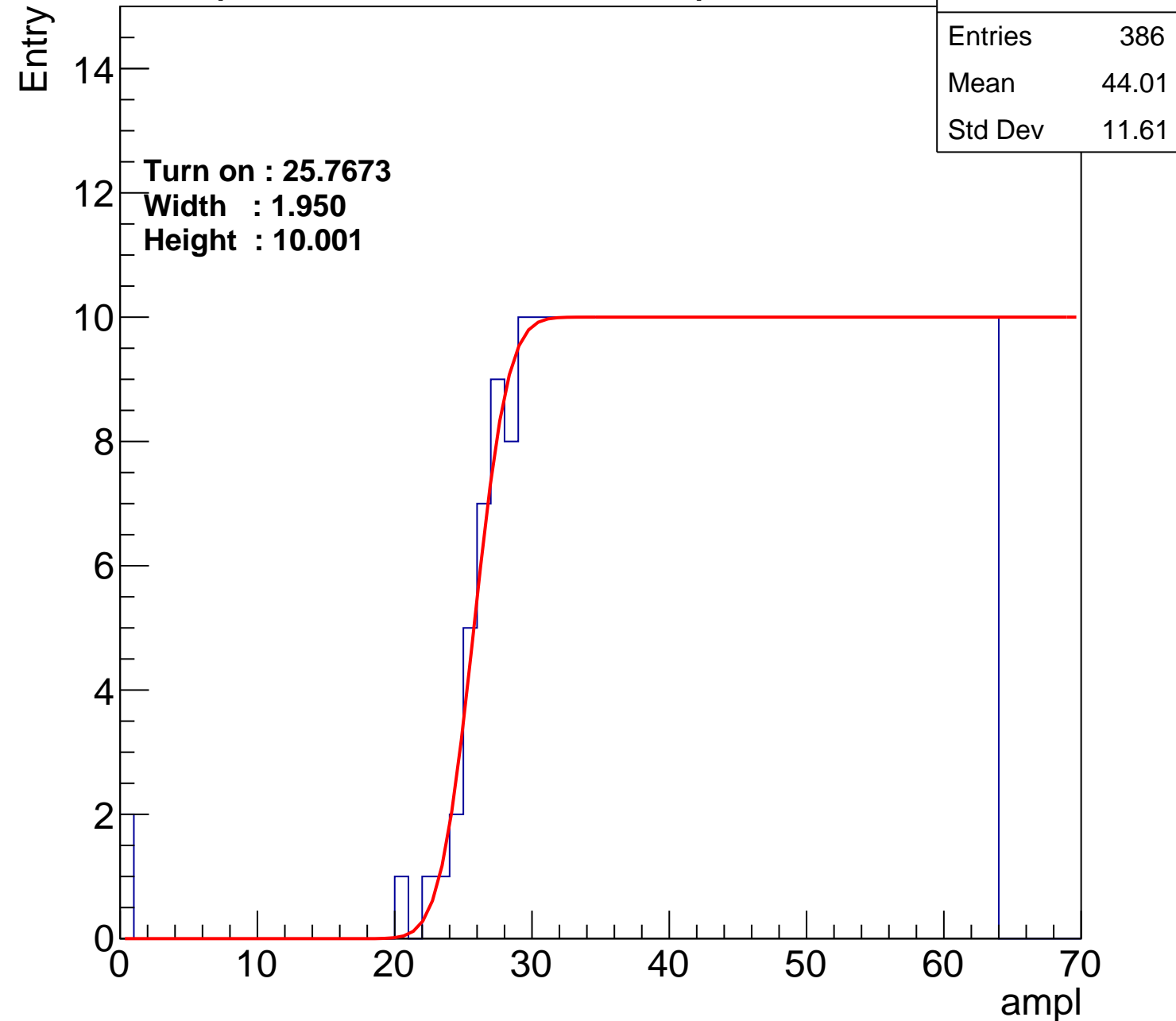
Width : 1.950

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch49

calib_packv5_042523_0143.root, FC#3, port B3

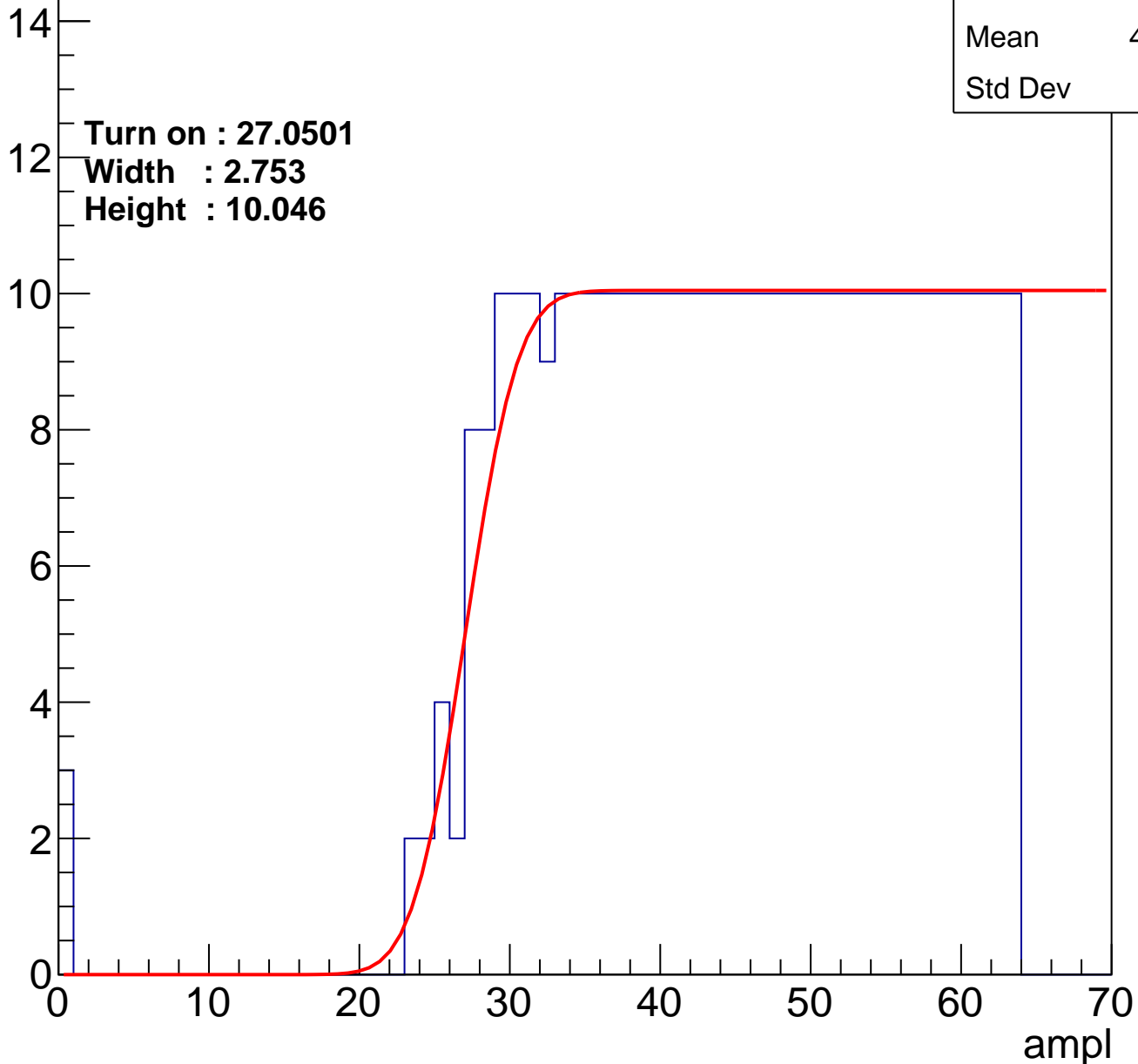
Entries	378
Mean	44.32
Std Dev	11.6

Turn on : 27.0501

Width : 2.753

Height : 10.046

Entry



B1L000S, U22-ch50

calib_packv5_042523_0143.root, FC#3, port B3

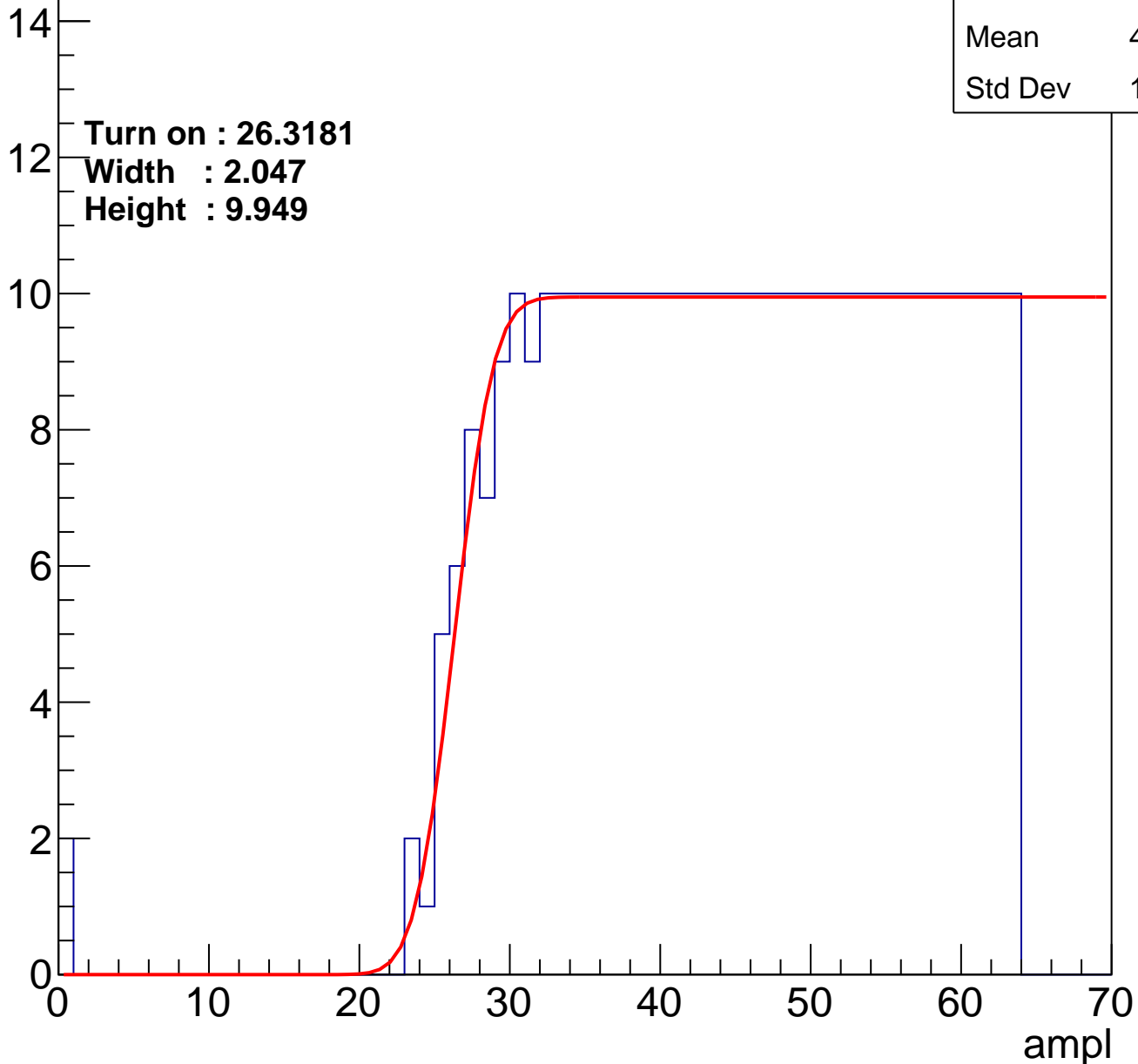
Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 26.3181

Width : 2.047

Height : 9.949

Entry



B1L000S, U22-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.64
Std Dev	11.14

Turn on : 26.4862

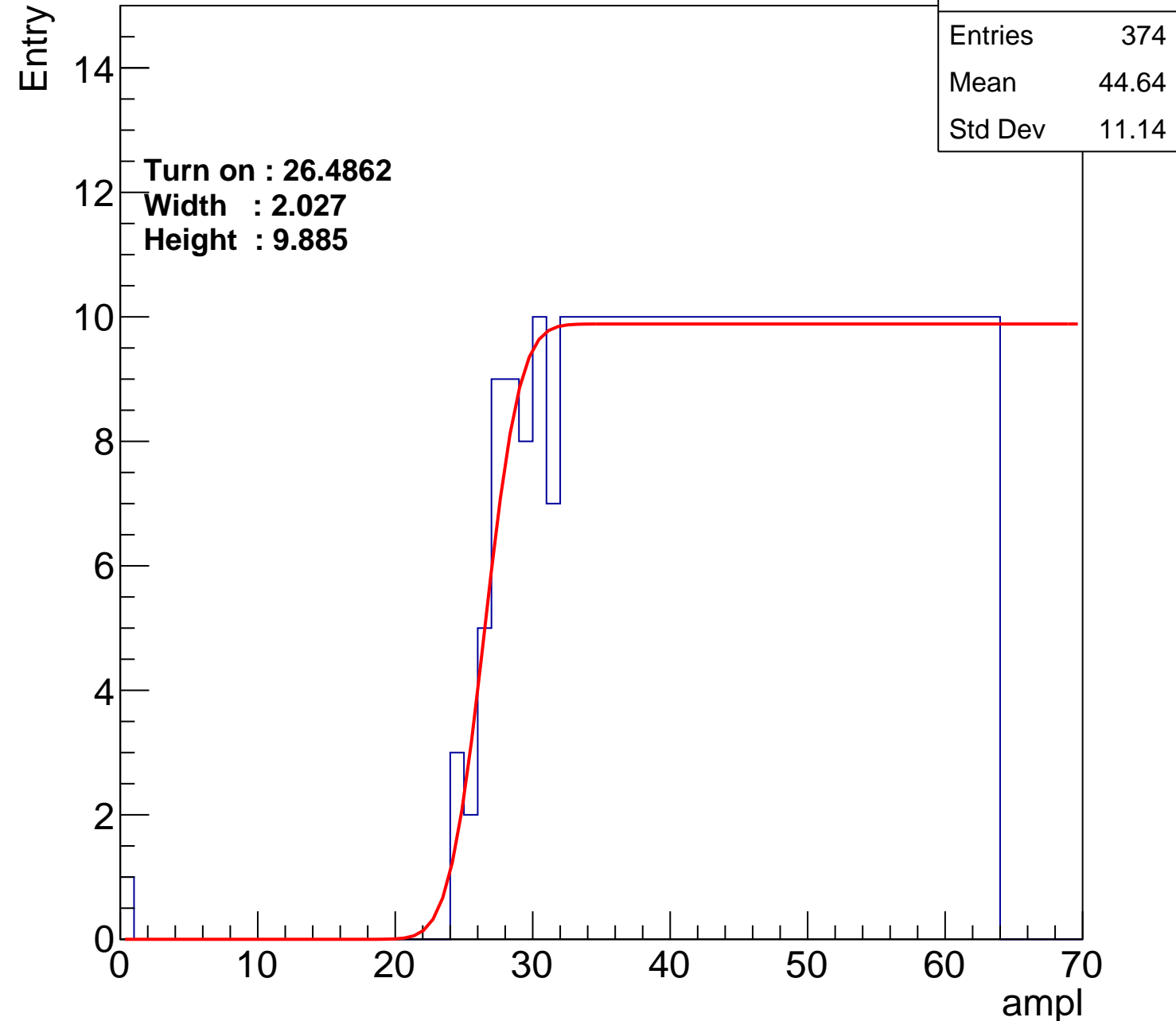
Width : 2.027

Height : 9.885

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.11
Std Dev	11.45

Turn on : 26.0416

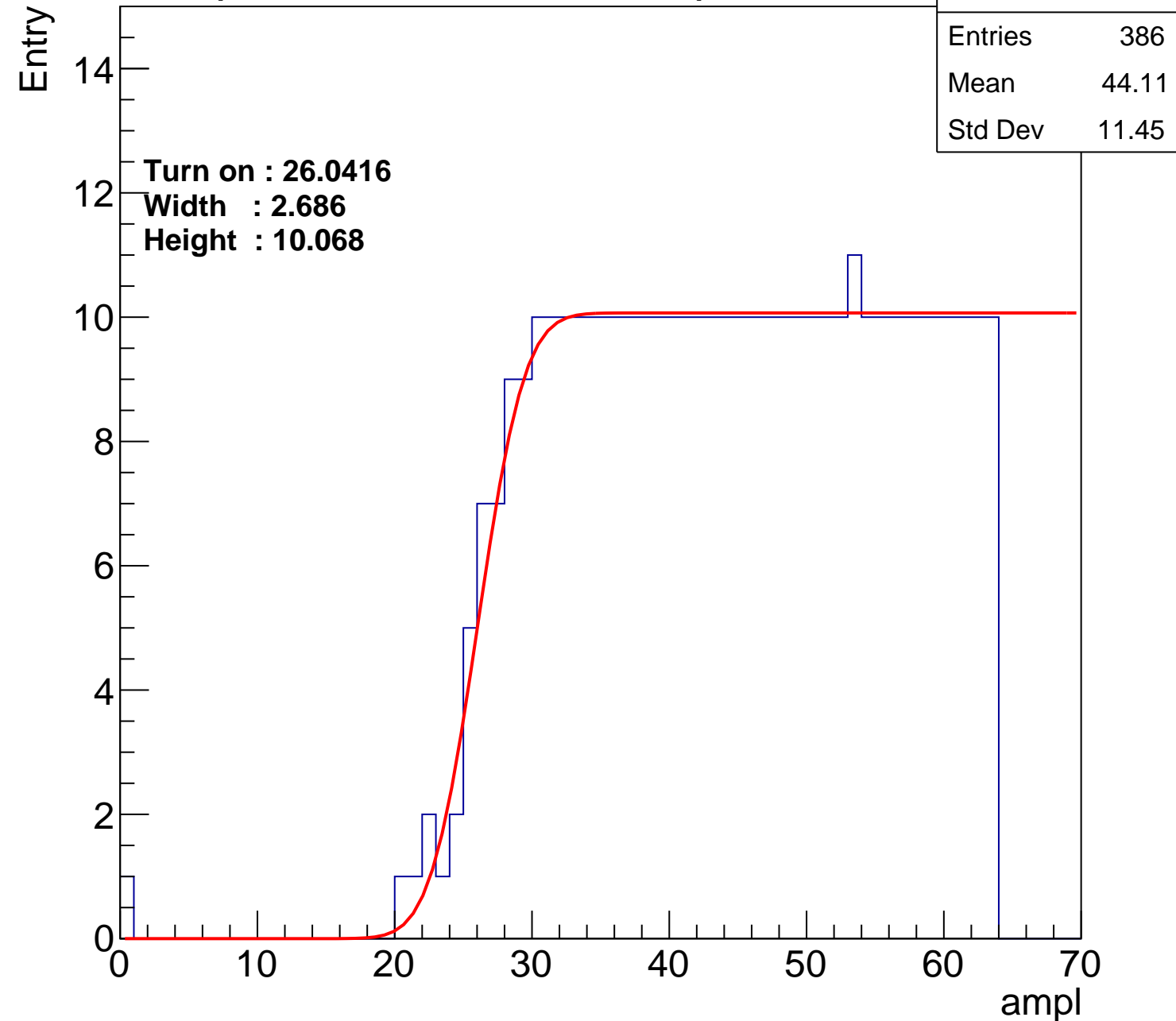
Width : 2.686

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch53

calib_packv5_042523_0143.root, FC#3, port B3

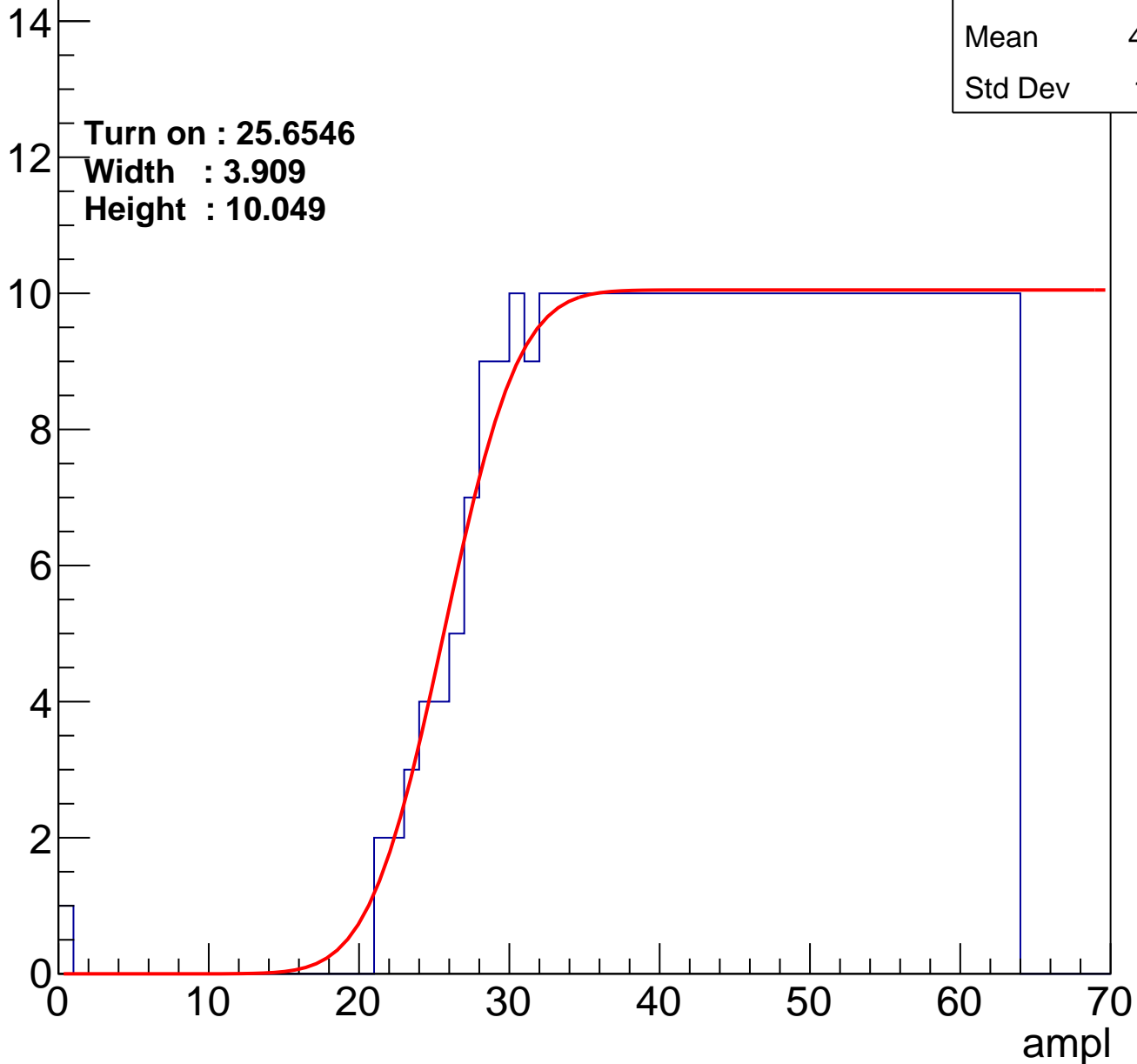
Entries	385
Mean	44.06
Std Dev	11.51

Turn on : 25.6546

Width : 3.909

Height : 10.049

Entry



B1L000S, U22-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.6350

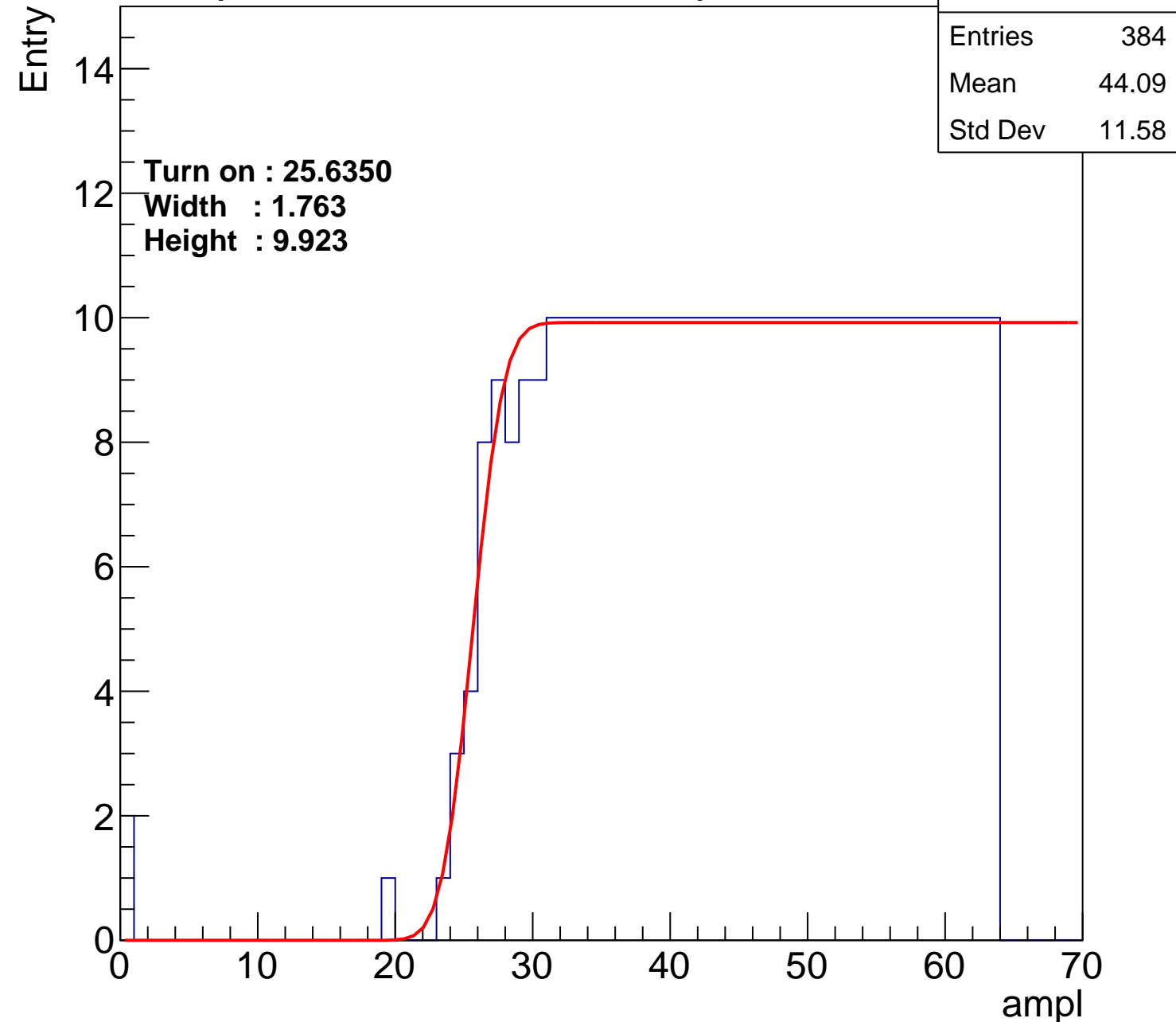
Width : 1.763

Height : 9.923

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.11
Std Dev	11.65

Turn on : 26.3248

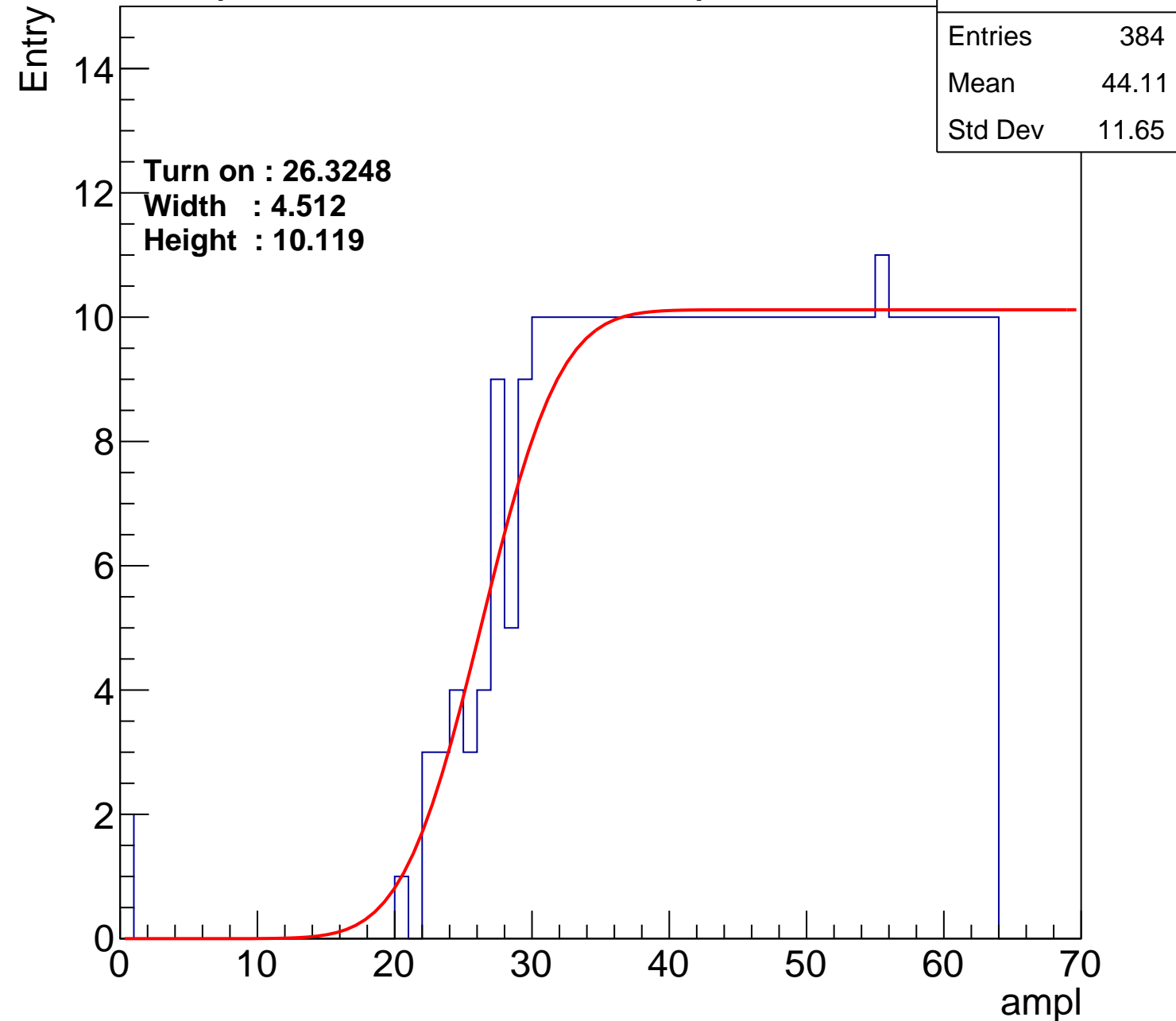
Width : 4.512

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch56

calib_packv5_042523_0143.root, FC#3, port B3

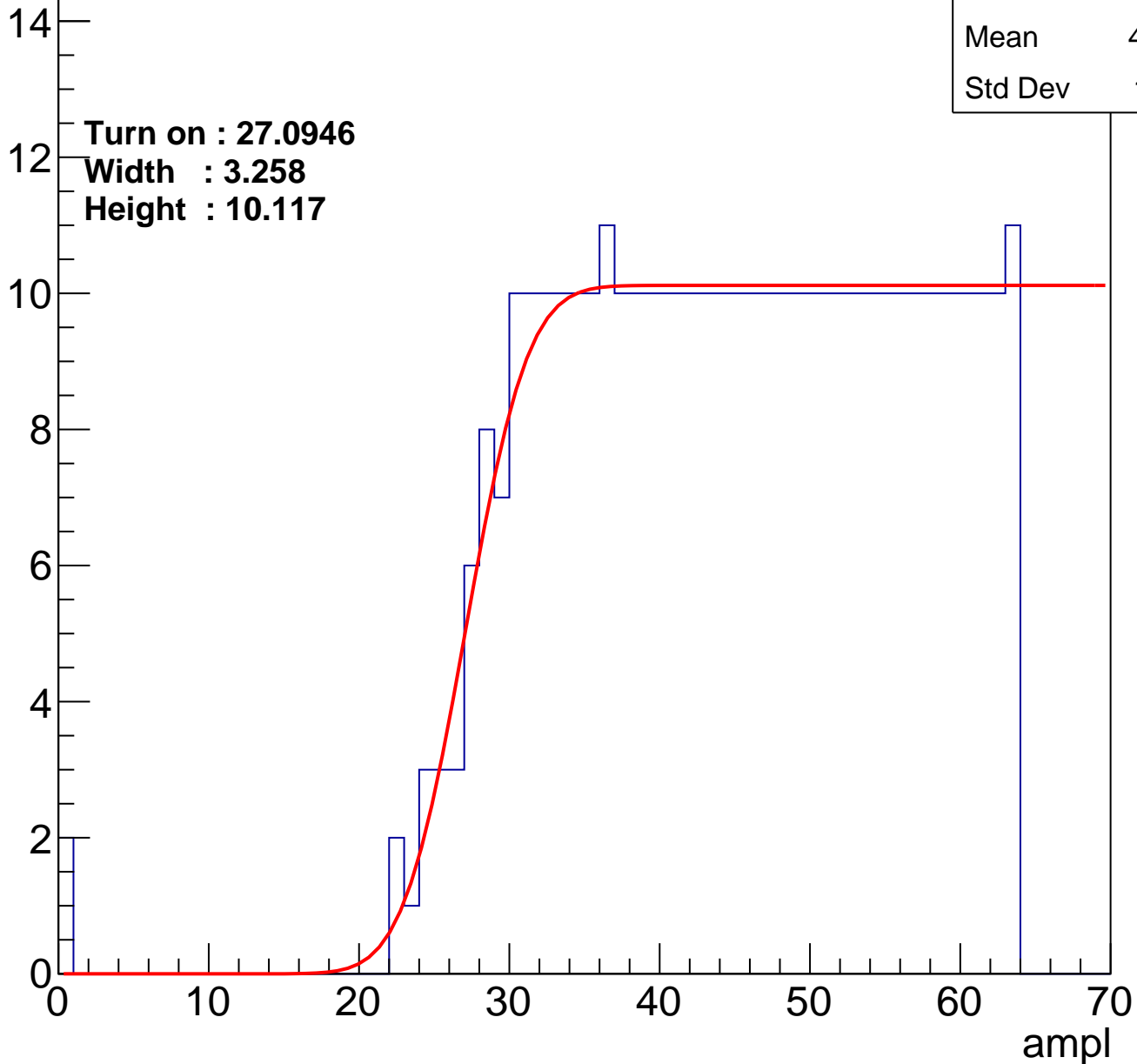
Entries	377
Mean	44.54
Std Dev	11.41

Turn on : 27.0946

Width : 3.258

Height : 10.117

Entry



B1L000S, U22-ch57

calib_packv5_042523_0143.root, FC#3, port B3

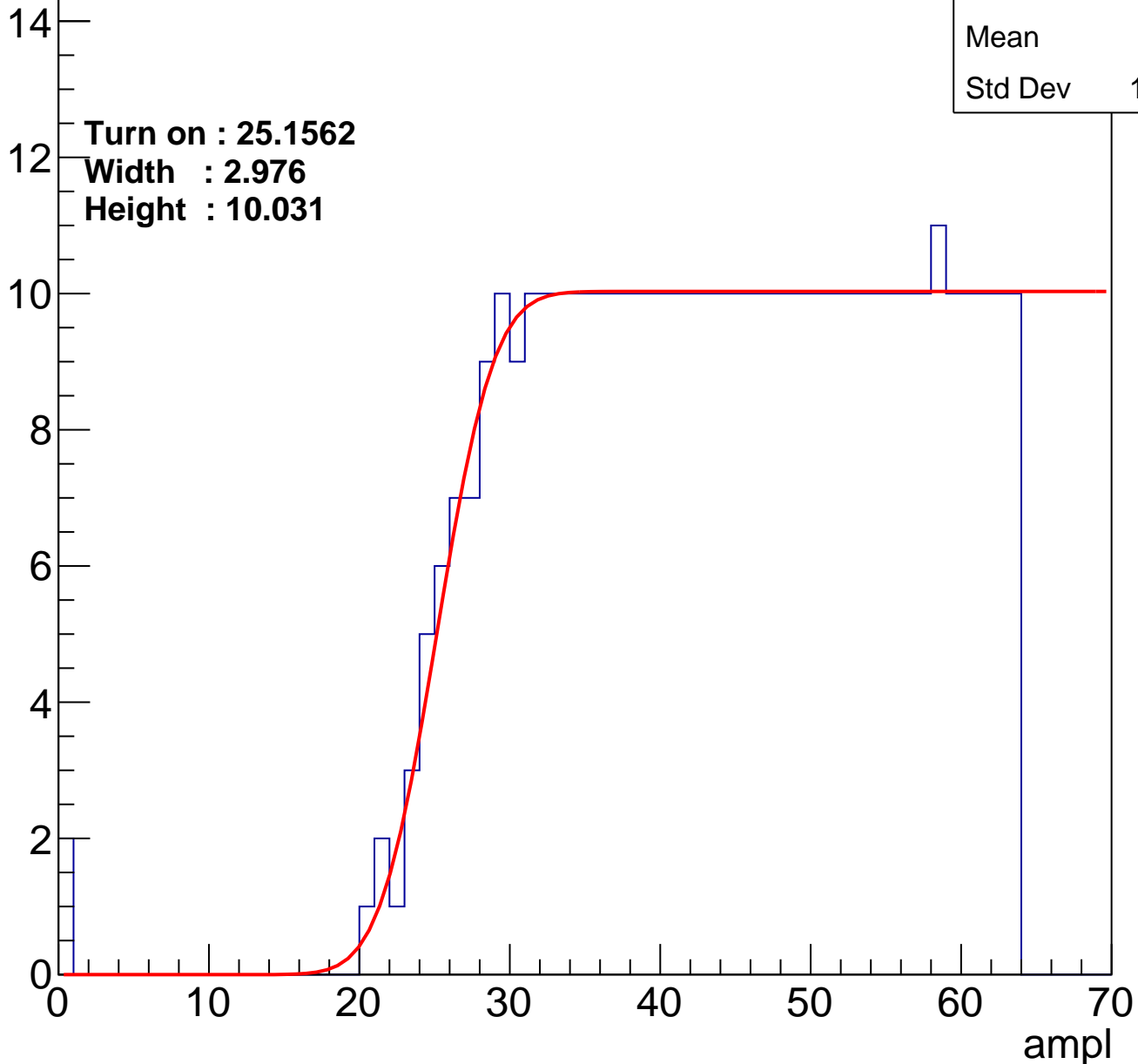
Entries	393
Mean	43.7
Std Dev	11.84

Turn on : 25.1562

Width : 2.976

Height : 10.031

Entry



B1L000S, U22-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	45.02
Std Dev	10.9

Turn on : 27.5433

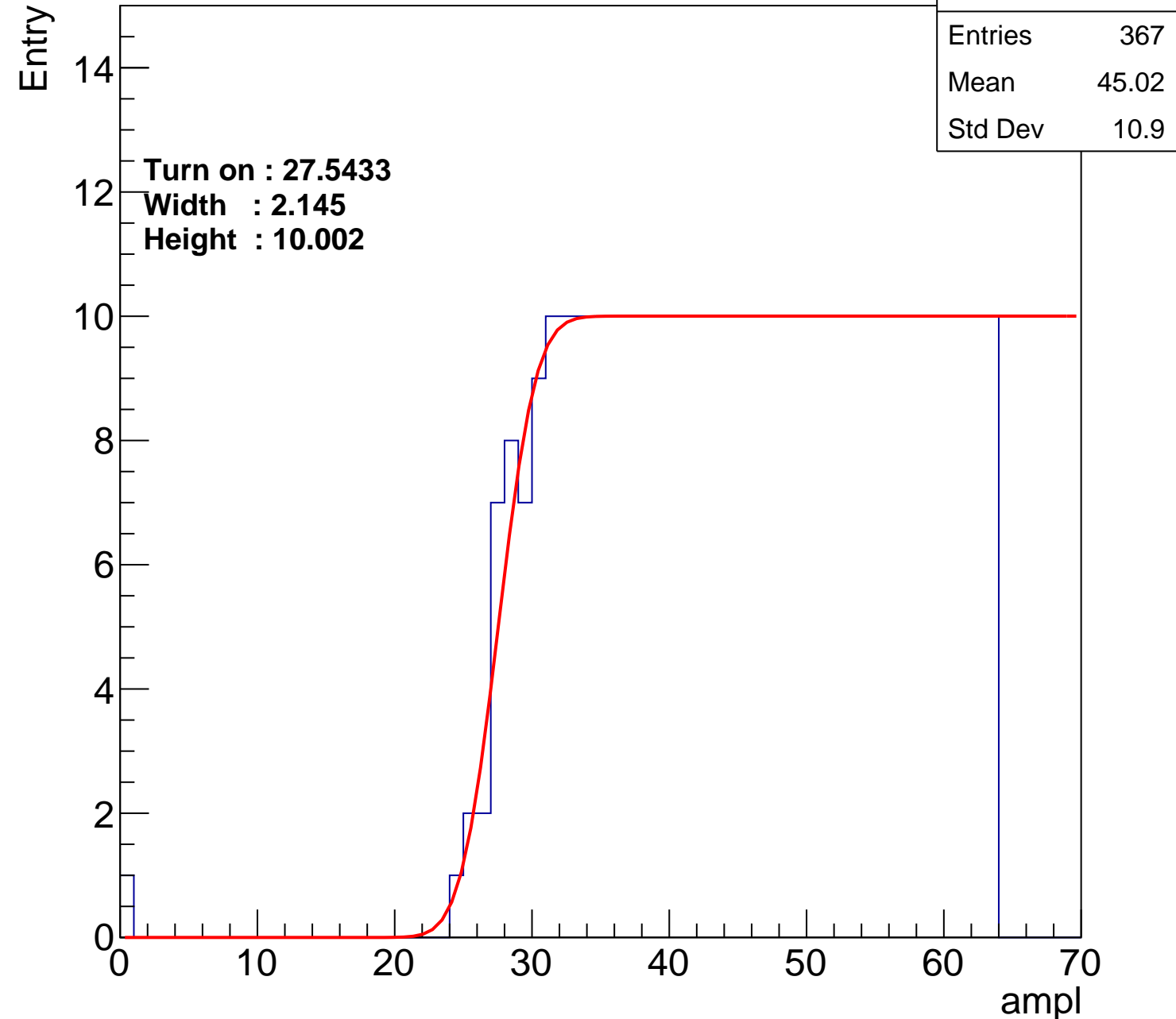
Width : 2.145

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch59

calib_packv5_042523_0143.root, FC#3, port B3

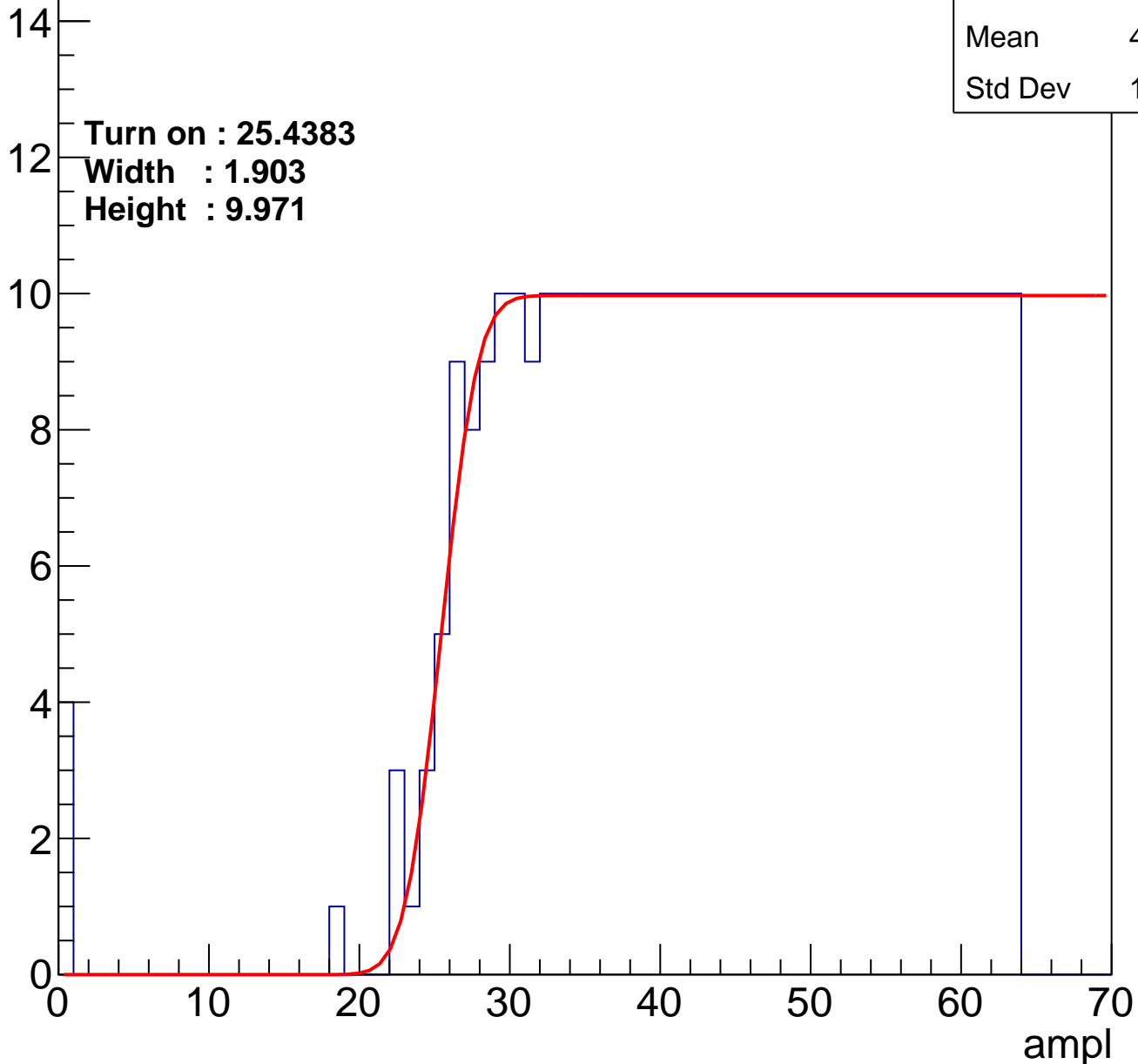
Entries	392
Mean	43.56
Std Dev	12.13

Turn on : 25.4383

Width : 1.903

Height : 9.971

Entry



B1L000S, U22-ch60

calib_packv5_042523_0143.root, FC#3, port B3

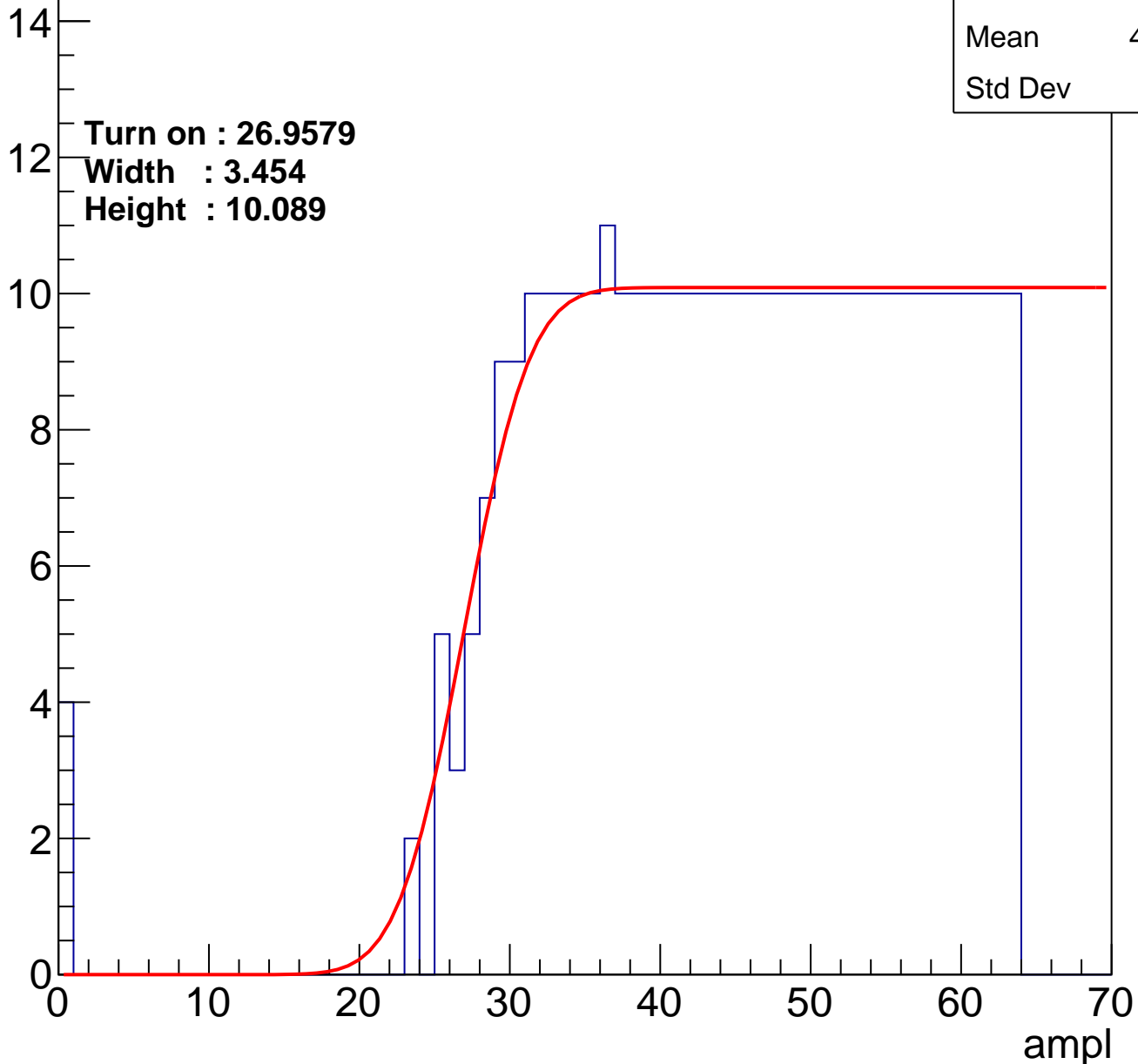
Entries	375
Mean	44.42
Std Dev	11.7

Turn on : 26.9579

Width : 3.454

Height : 10.089

Entry



B1L000S, U22-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.33
Std Dev	10.72

Turn on : 28.0457

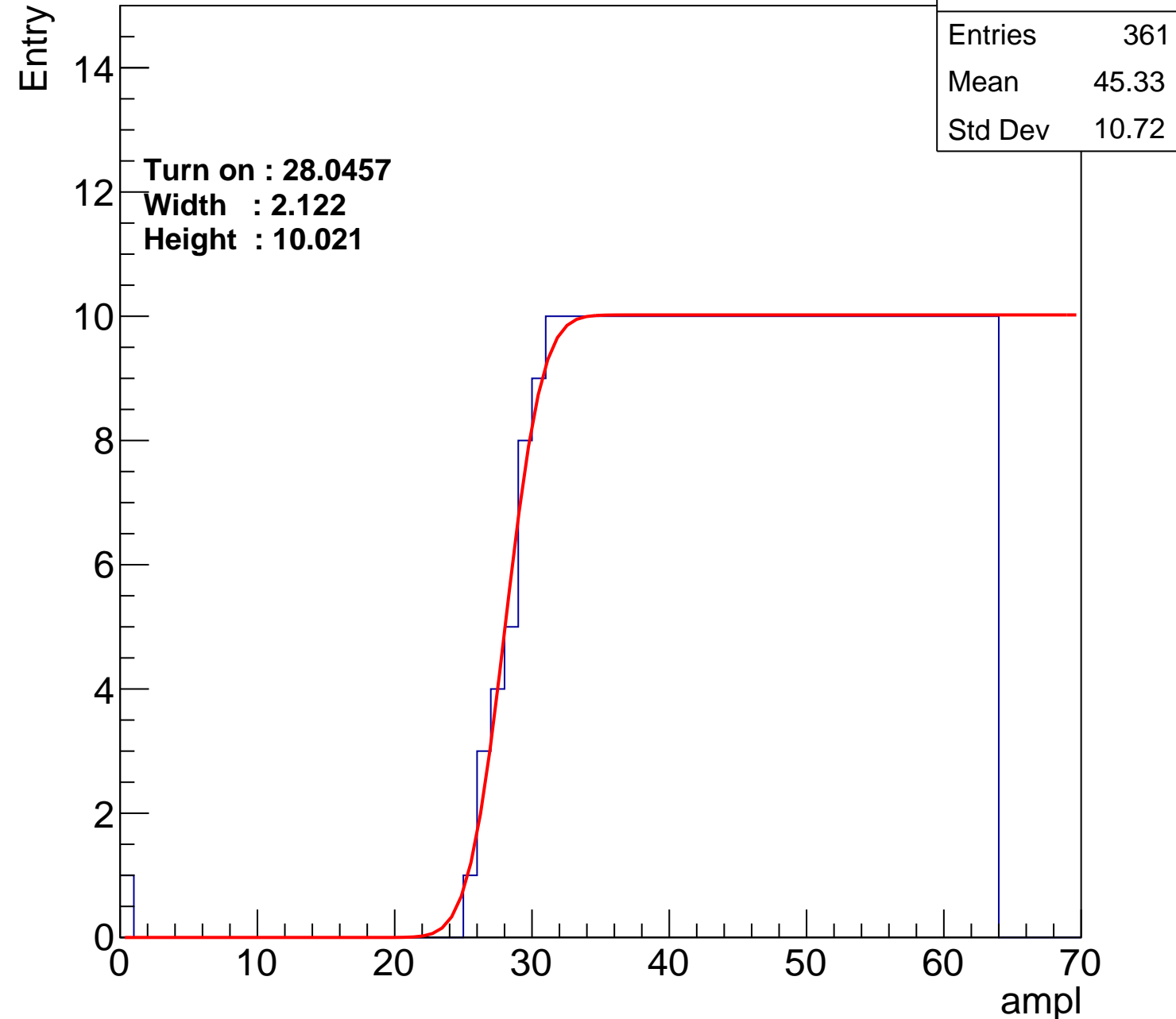
Width : 2.122

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 27.2757

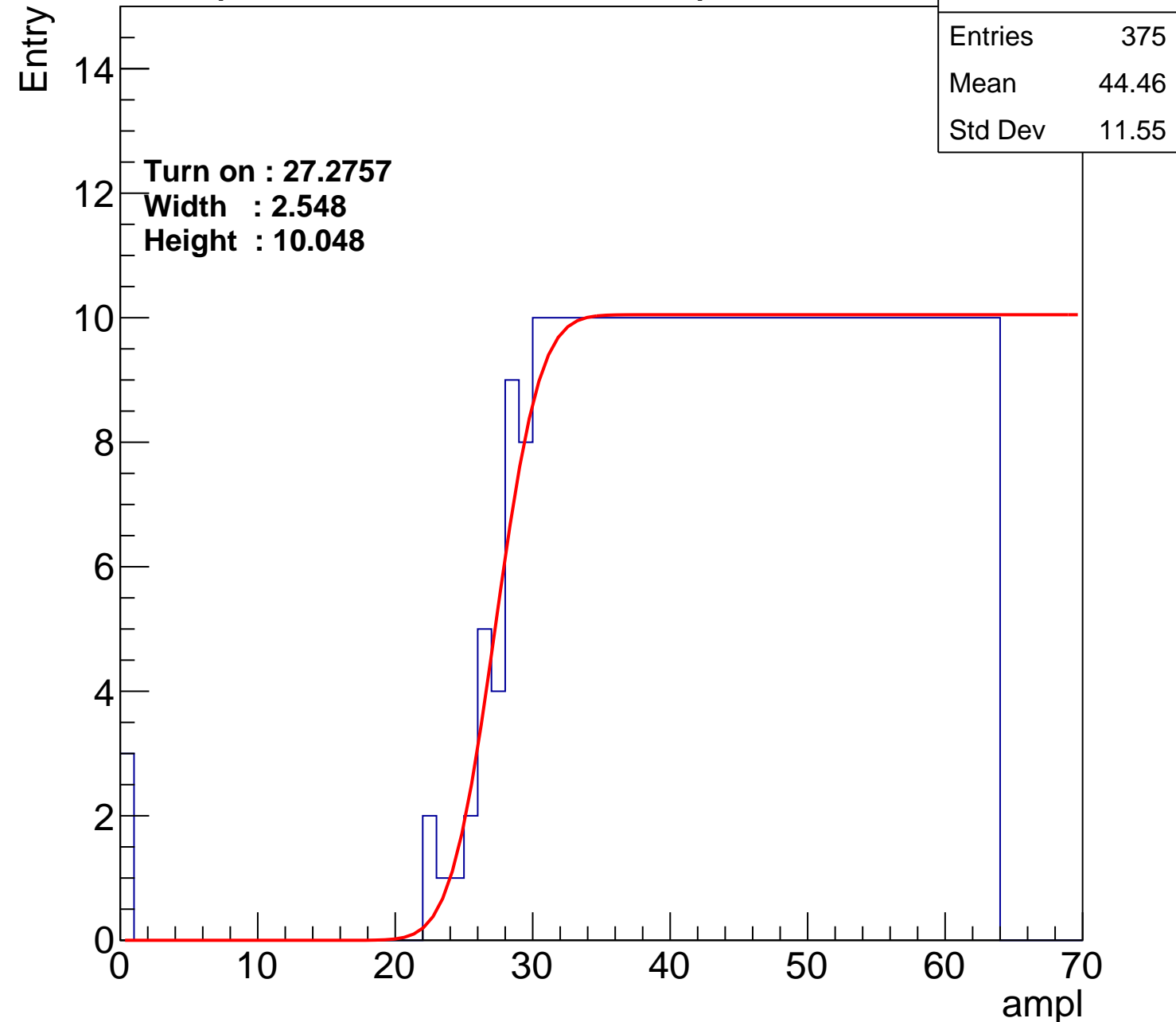
Width : 2.548

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	359
Mean	45.28
Std Dev	11

Turn on : 28.3849

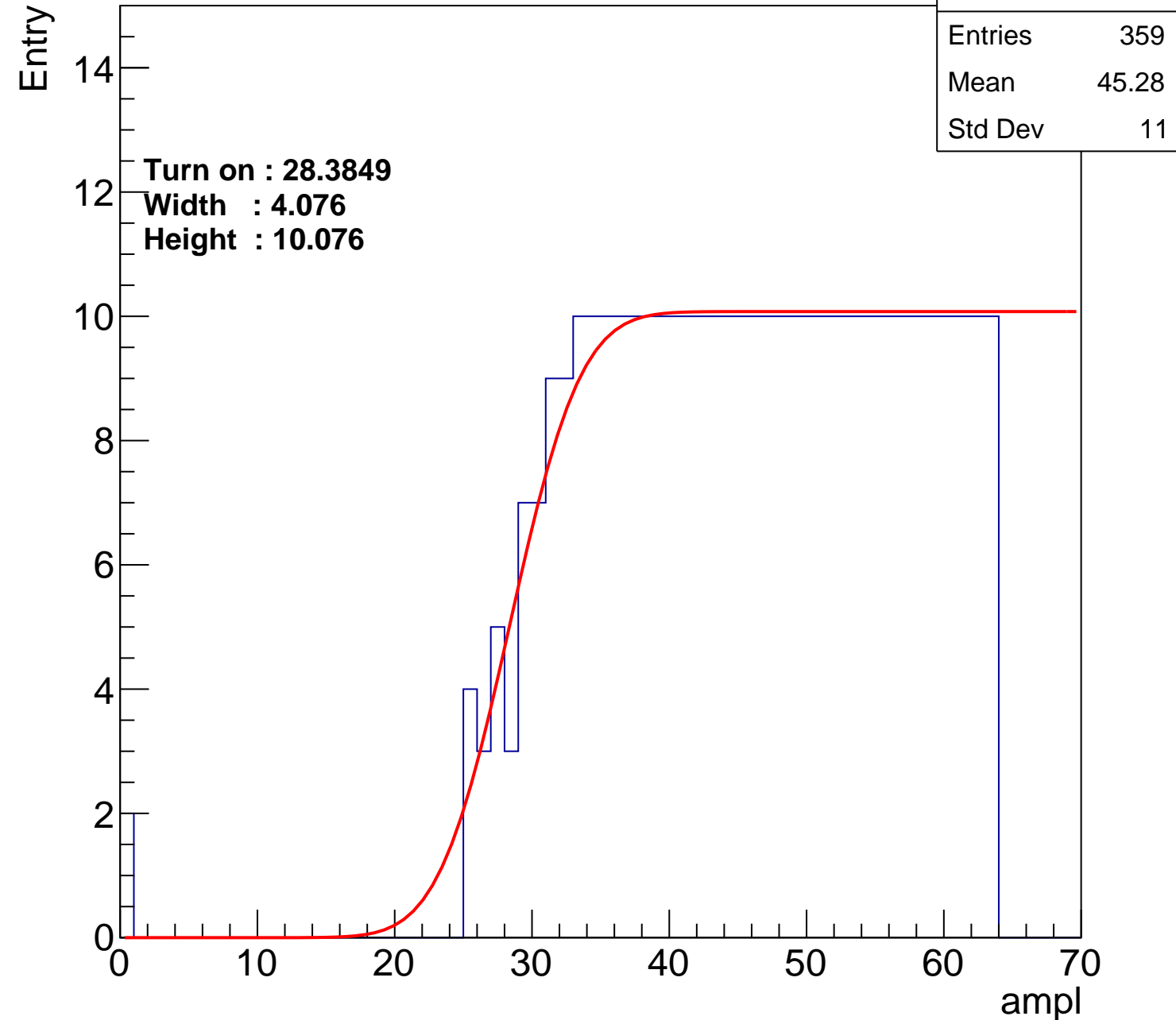
Width : 4.076

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch64

calib_packv5_042523_0143.root, FC#3, port B3

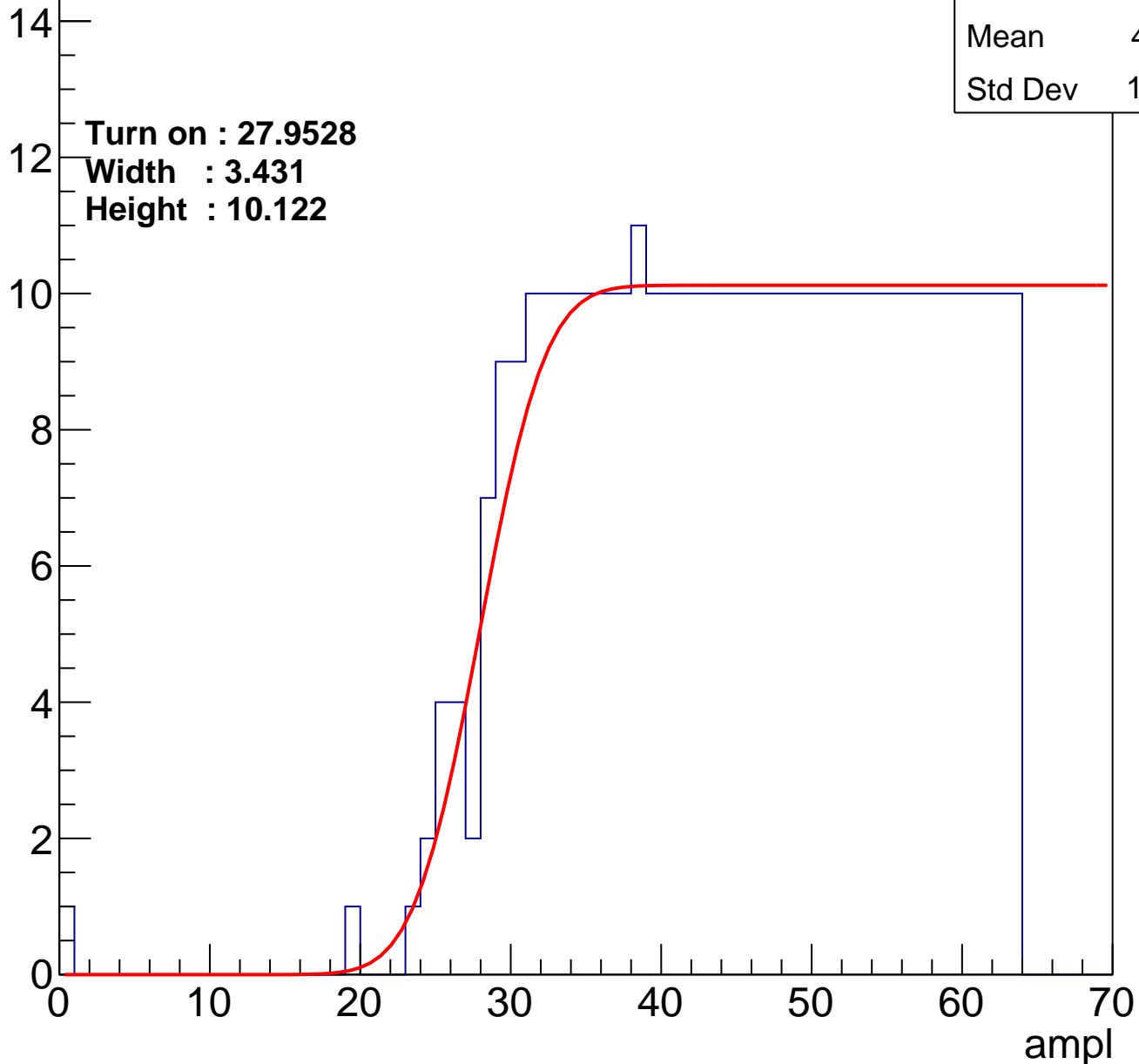
Entries	371
Mean	44.81
Std Dev	11.06

Turn on : 27.9528

Width : 3.431

Height : 10.122

Entry



B1L000S, U22-ch65

calib_packv5_042523_0143.root, FC#3, port B3

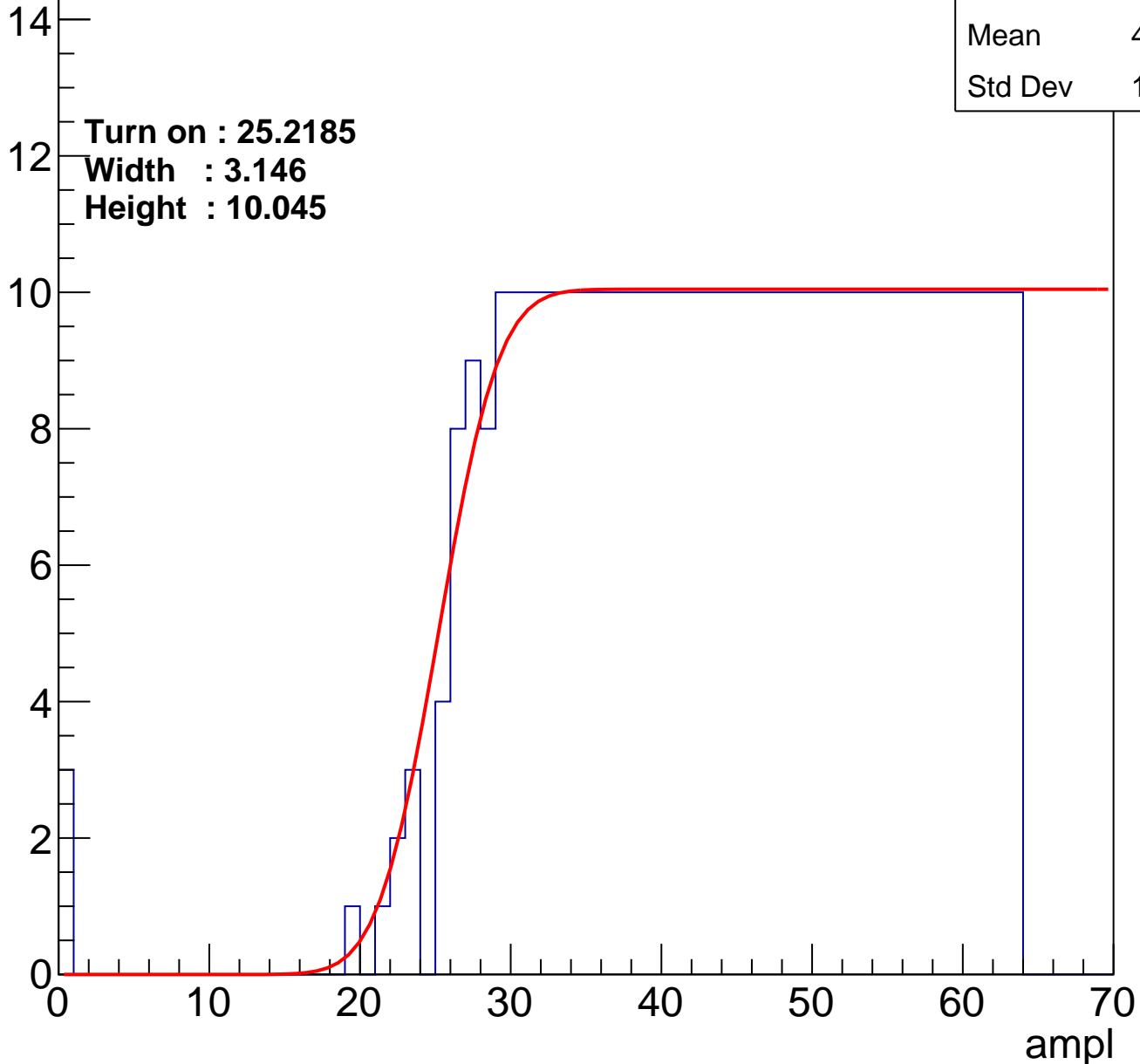
Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.2185

Width : 3.146

Height : 10.045

Entry



B1L000S, U22-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.37
Std Dev	11.4

Turn on : 26.2528

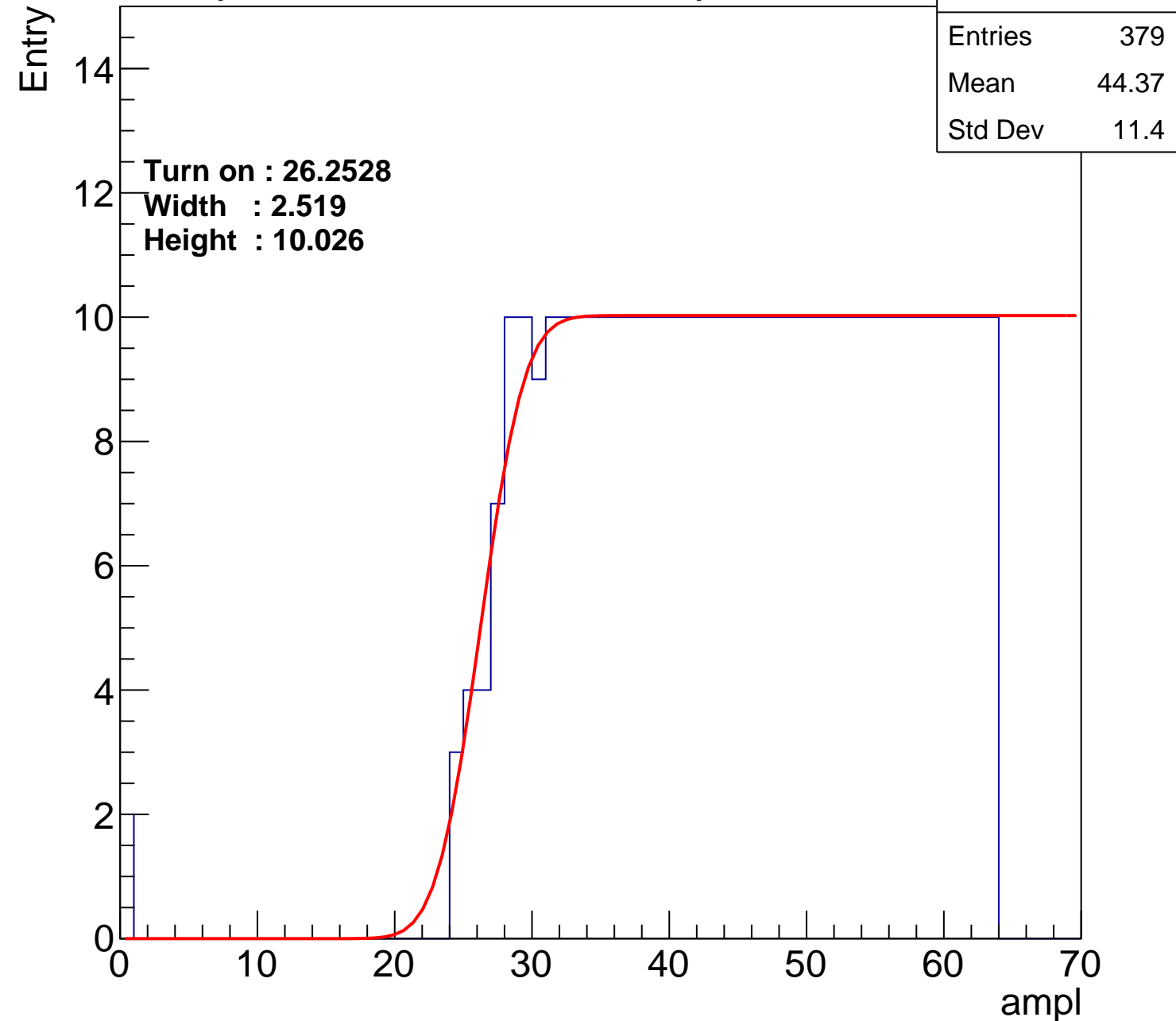
Width : 2.519

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.74
Std Dev	12.17

Turn on : 26.3087

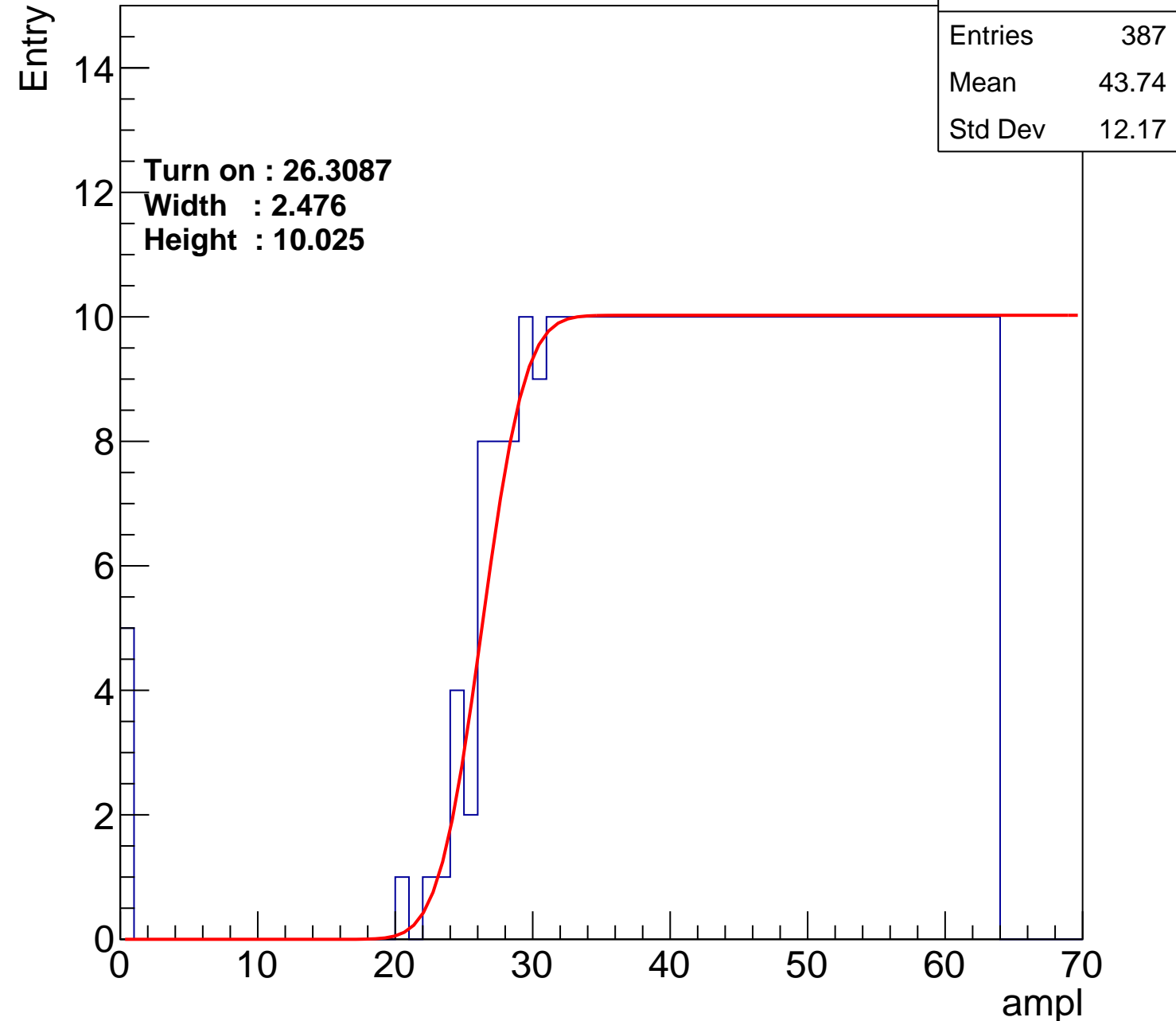
Width : 2.476

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch68

calib_packv5_042523_0143.root, FC#3, port B3

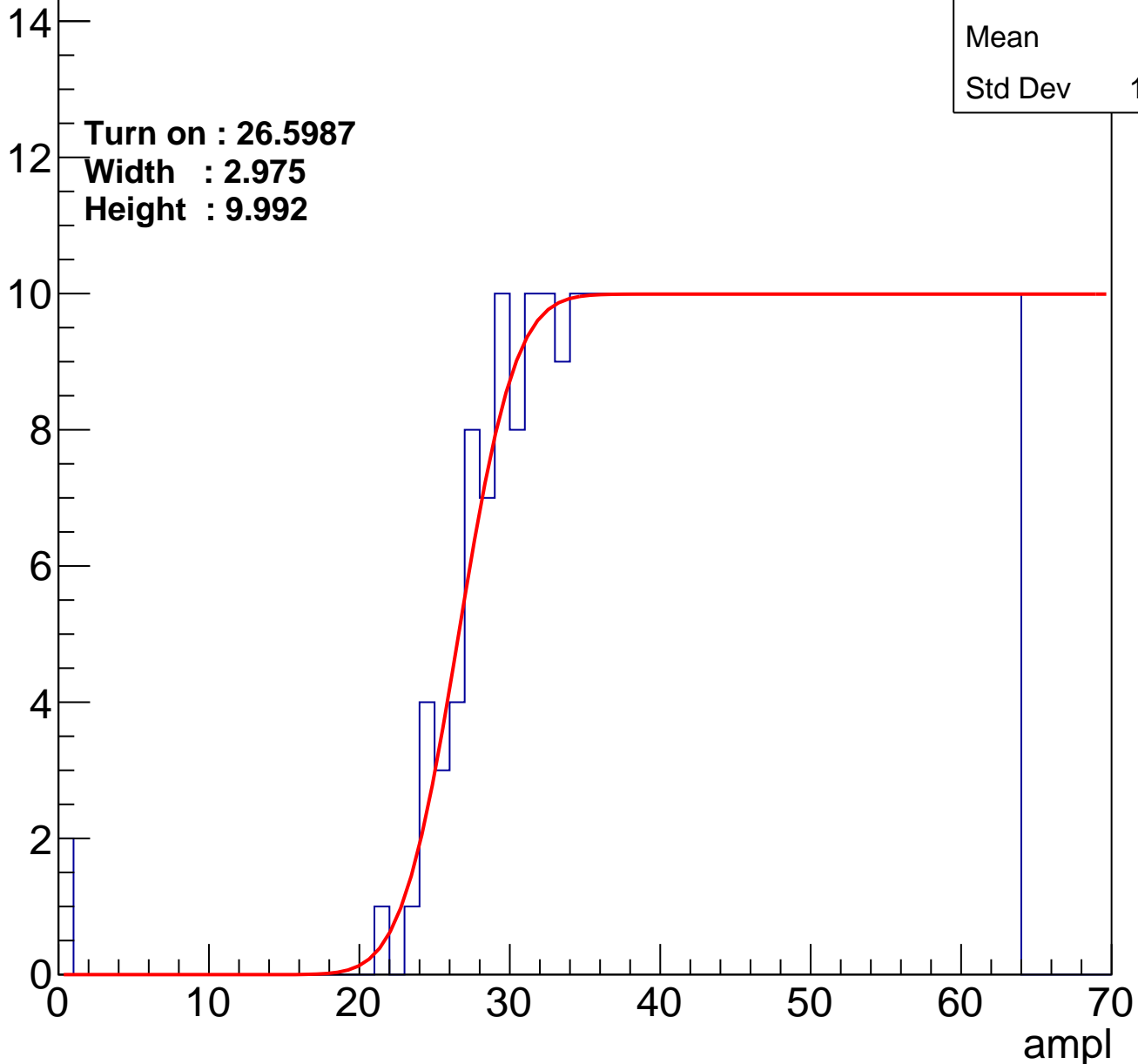
Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.5987

Width : 2.975

Height : 9.992

Entry



B1L000S, U22-ch69

calib_packv5_042523_0143.root, FC#3, port B3

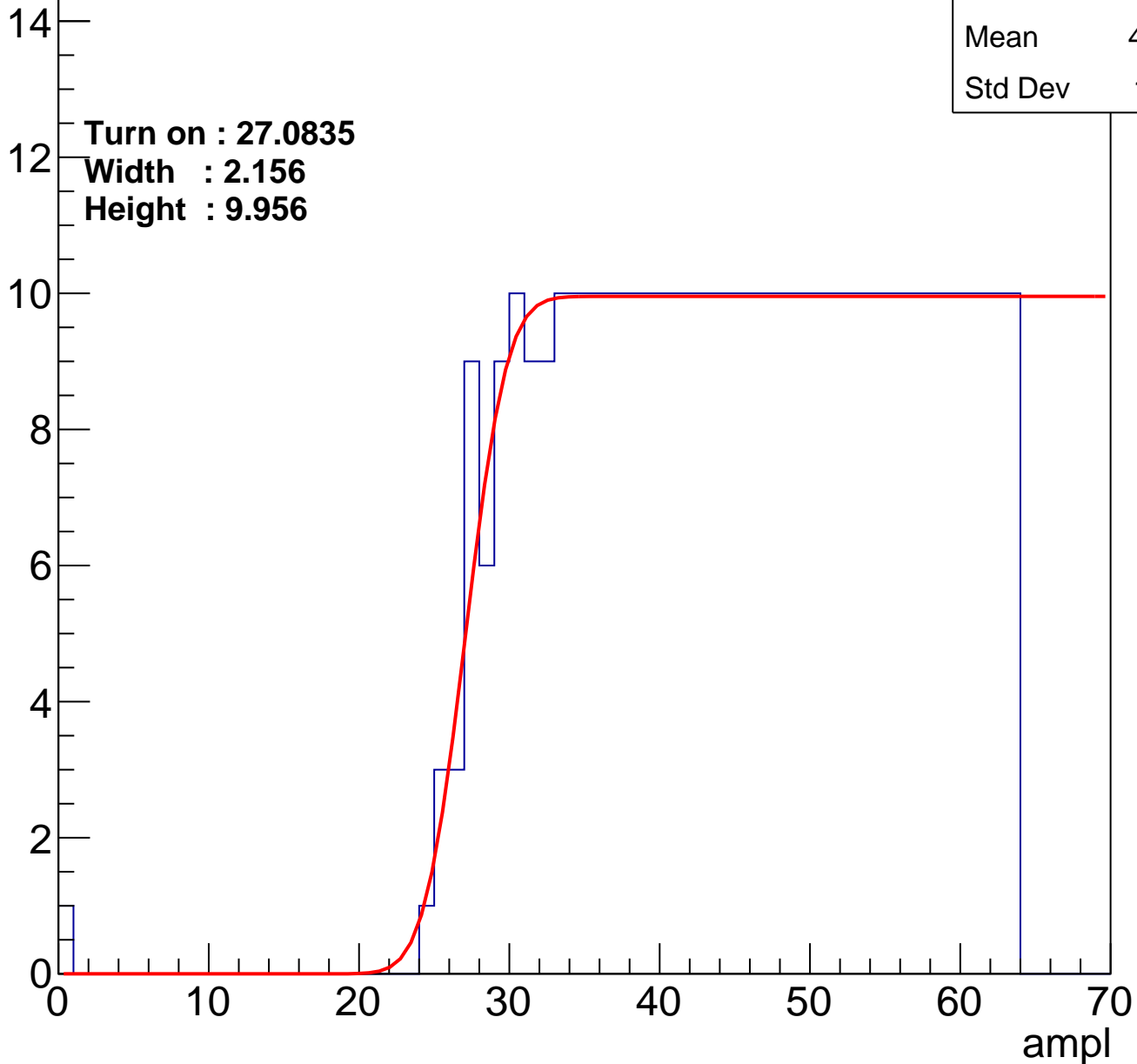
Entries	370
Mean	44.85
Std Dev	11.01

Turn on : 27.0835

Width : 2.156

Height : 9.956

Entry



B1L000S, U22-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.45
Std Dev	12.02

Turn on : 24.8087

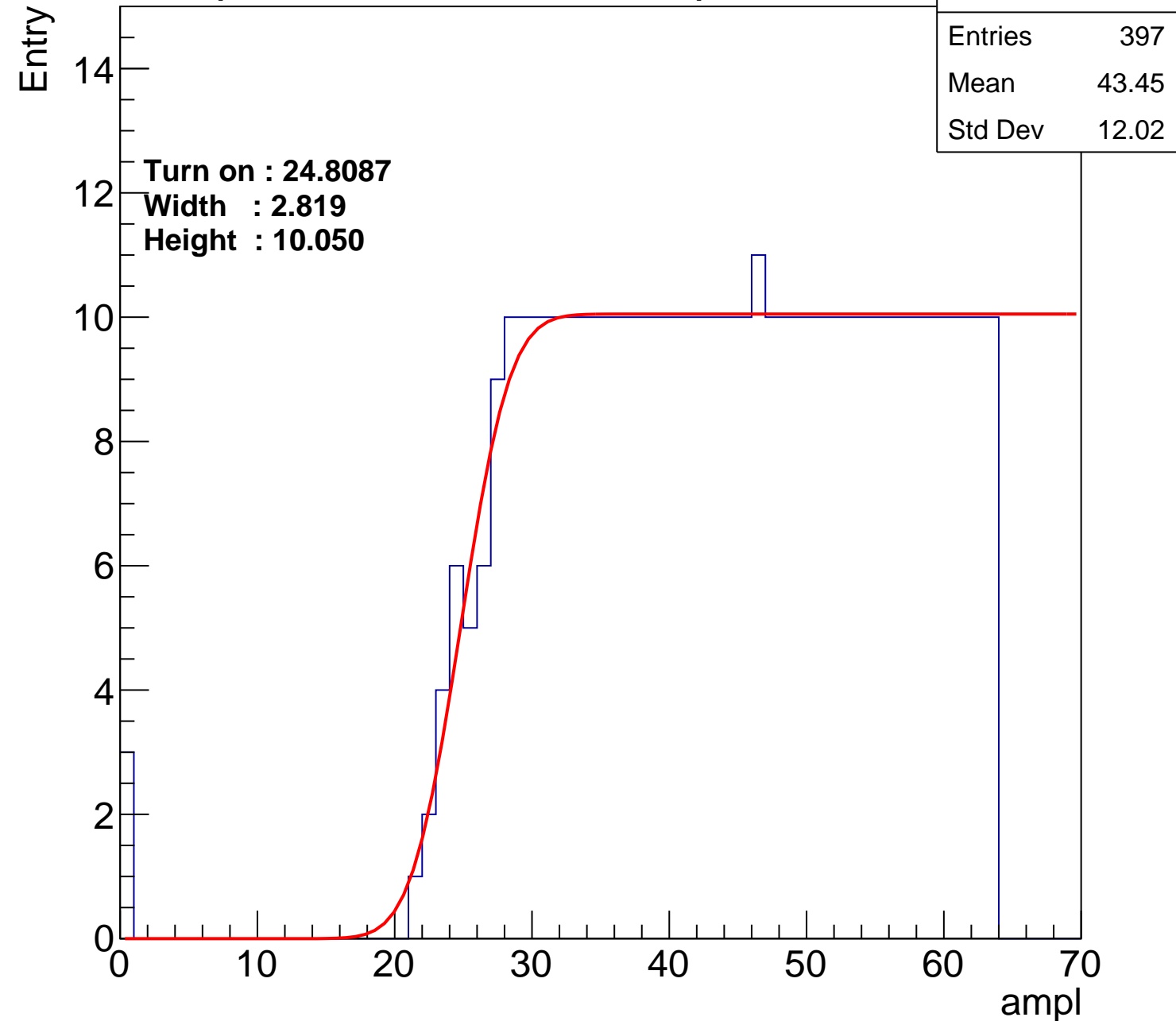
Width : 2.819

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44.04
Std Dev	11.45

Turn on : 25.9293

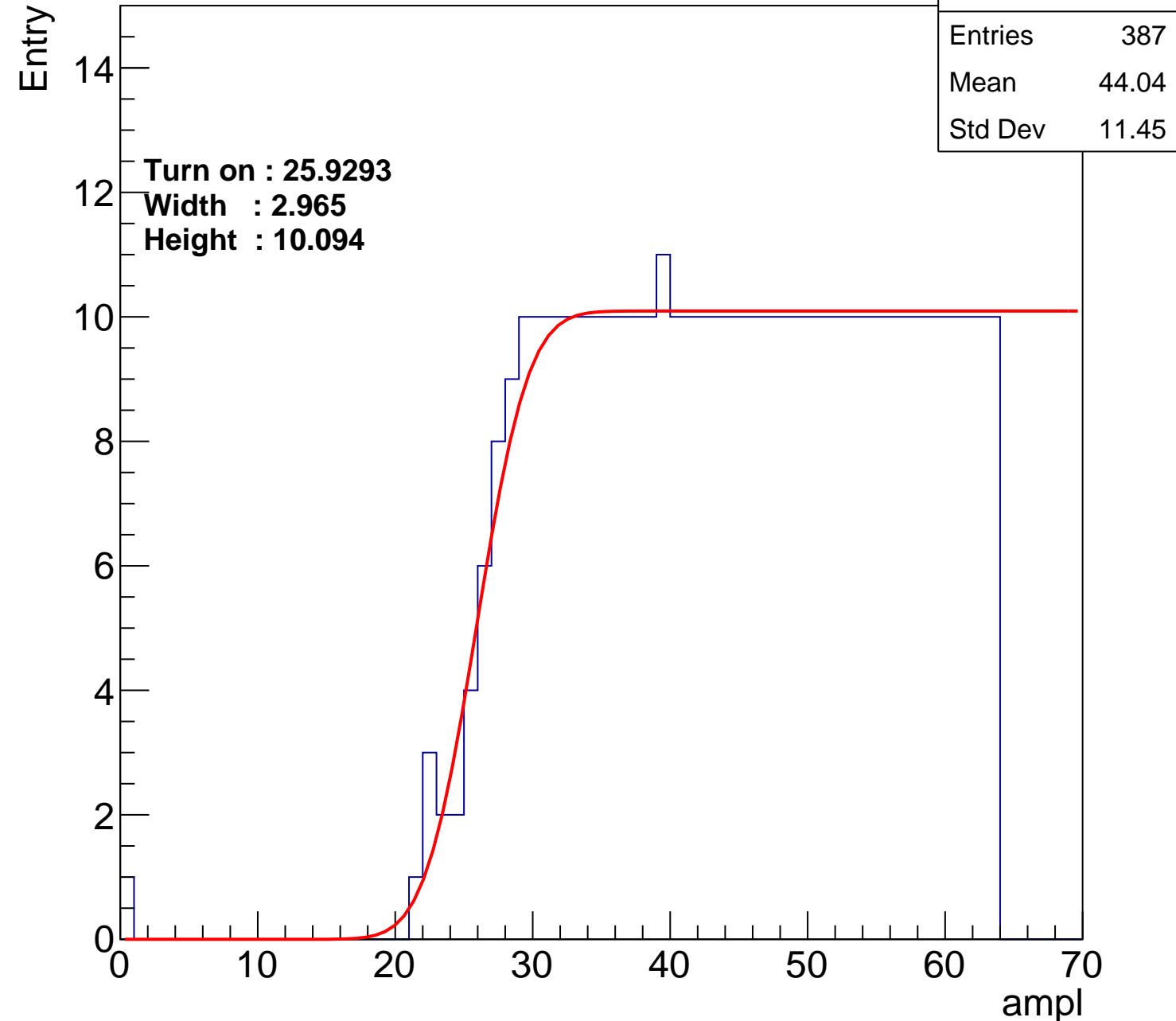
Width : 2.965

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch72

calib_packv5_042523_0143.root, FC#3, port B3

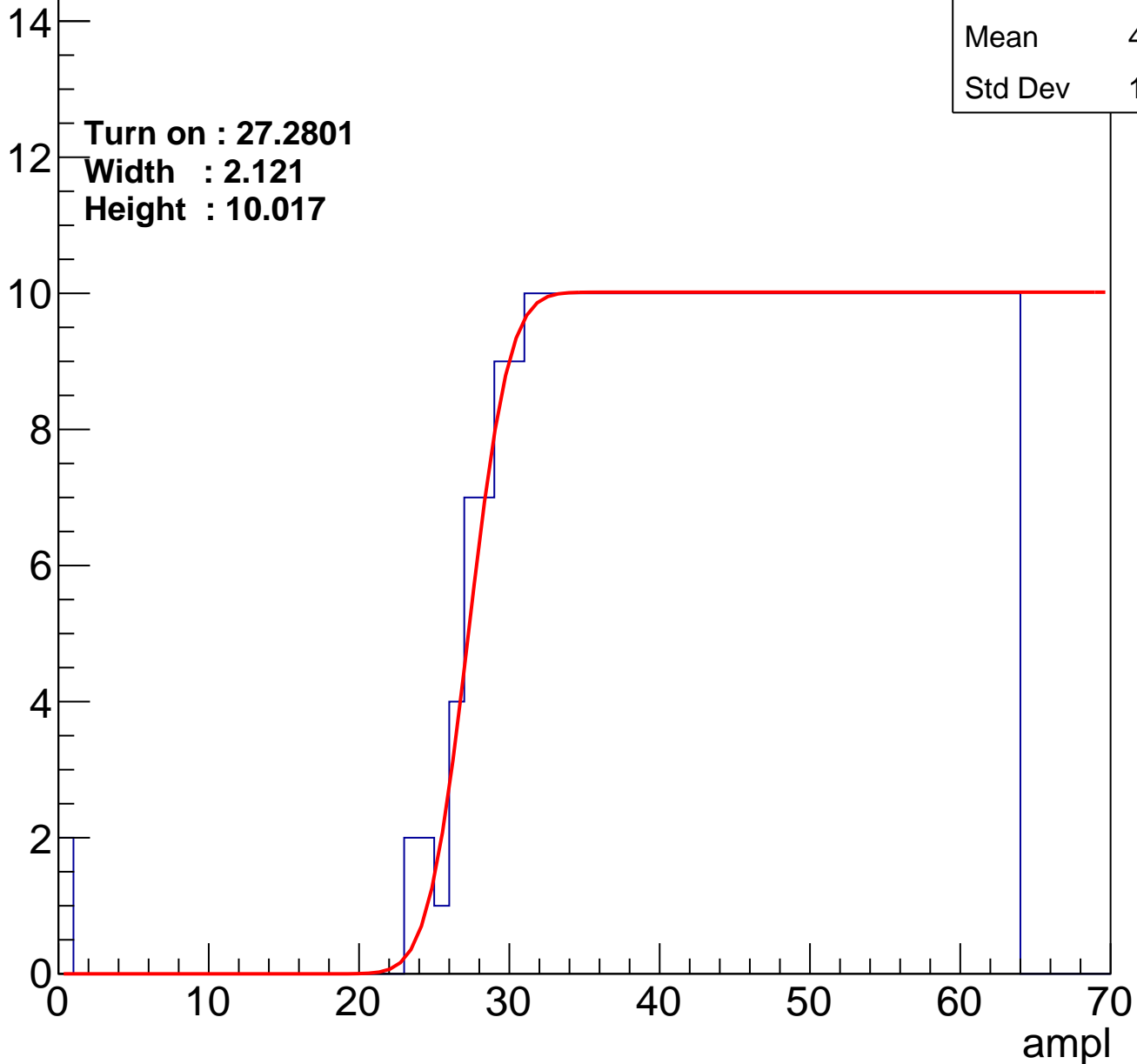
Entries	373
Mean	44.64
Std Dev	11.29

Turn on : 27.2801

Width : 2.121

Height : 10.017

Entry



B1L000S, U22-ch73

calib_packv5_042523_0143.root, FC#3, port B3

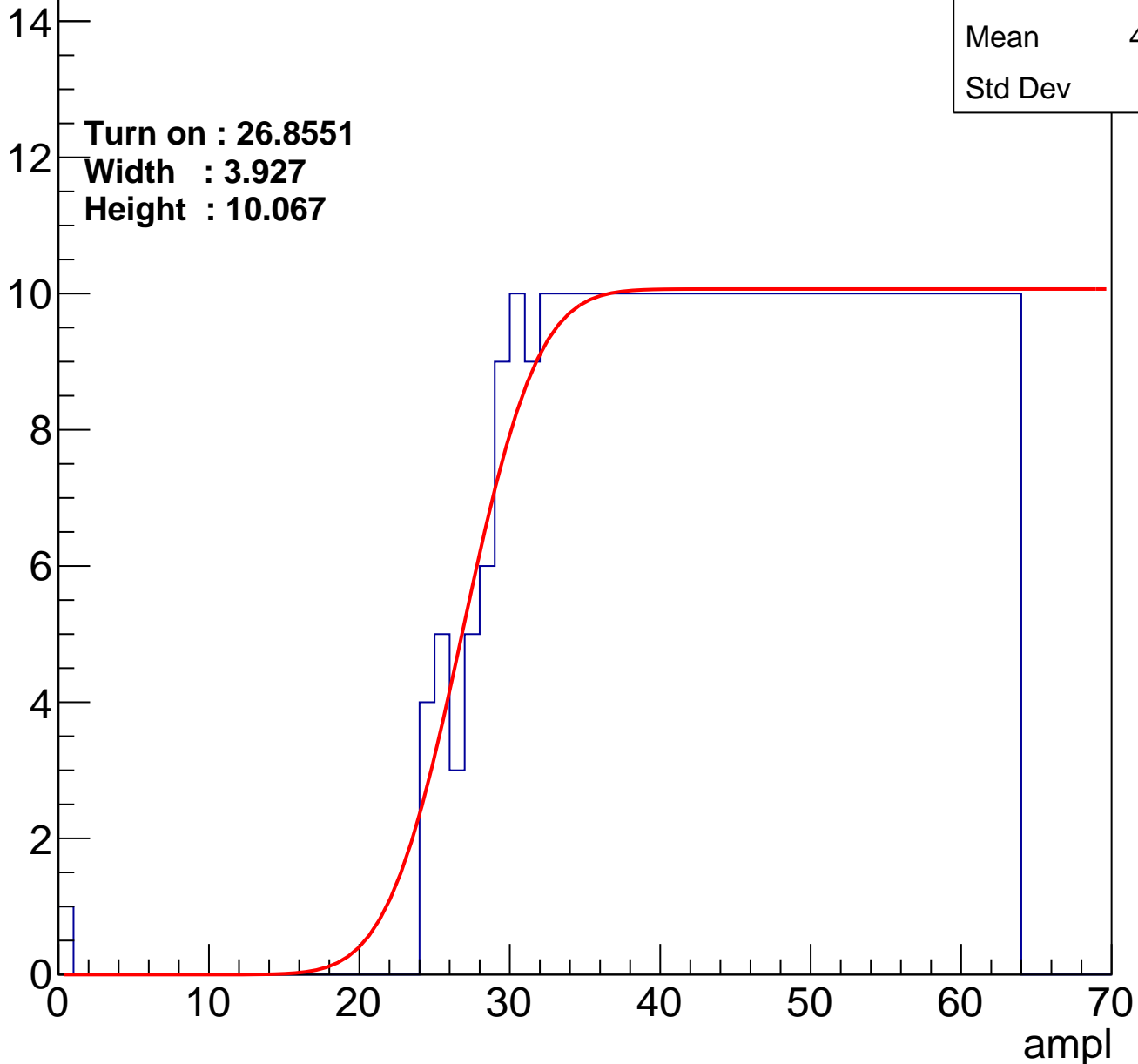
Entries	372
Mean	44.74
Std Dev	11.1

Turn on : 26.8551

Width : 3.927

Height : 10.067

Entry



B1L000S, U22-ch74

calib_packv5_042523_0143.root, FC#3, port B3

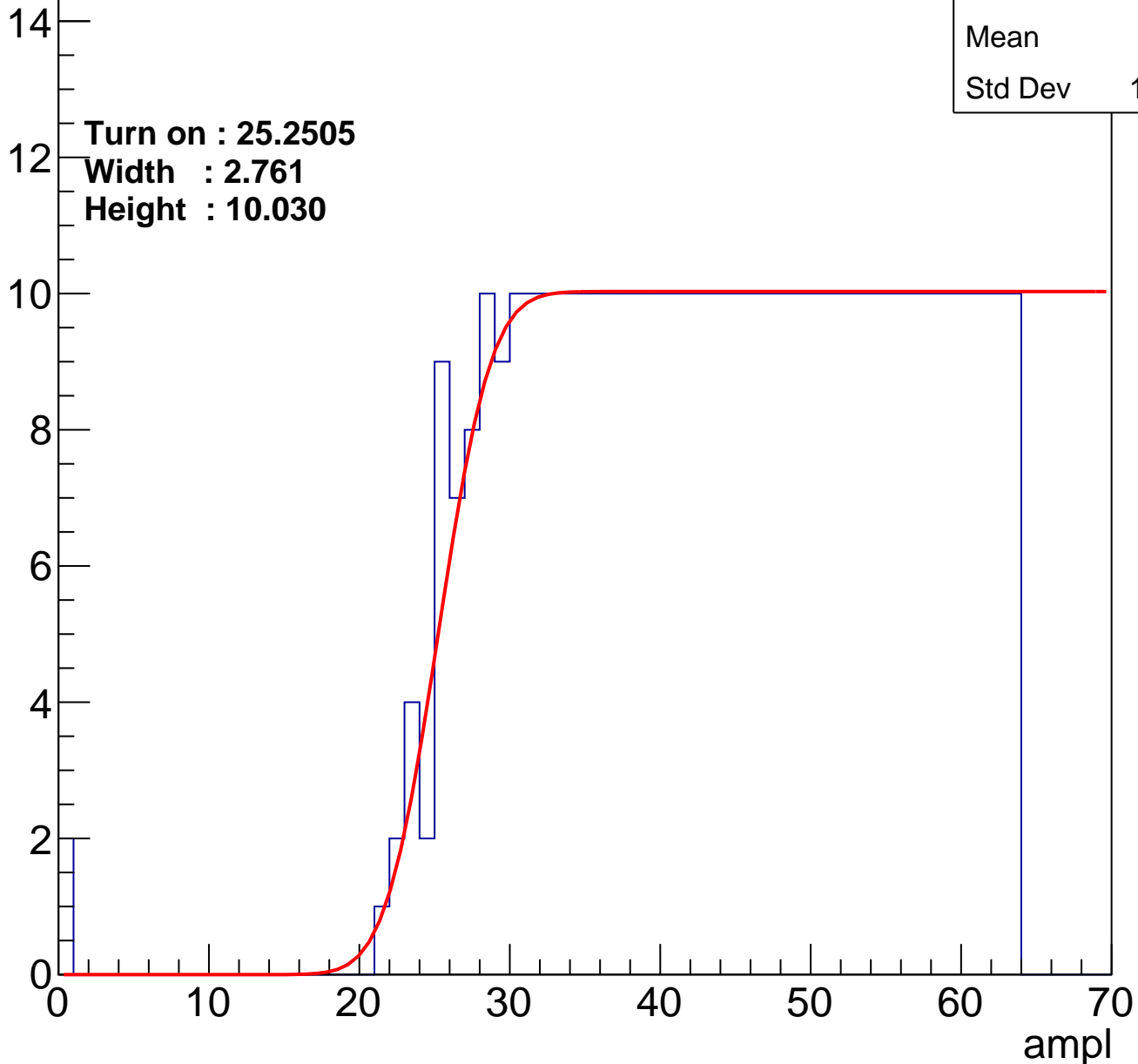
Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 25.2505

Width : 2.761

Height : 10.030

Entry



B1L000S, U22-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.12
Std Dev	10.87

Turn on : 28.0436

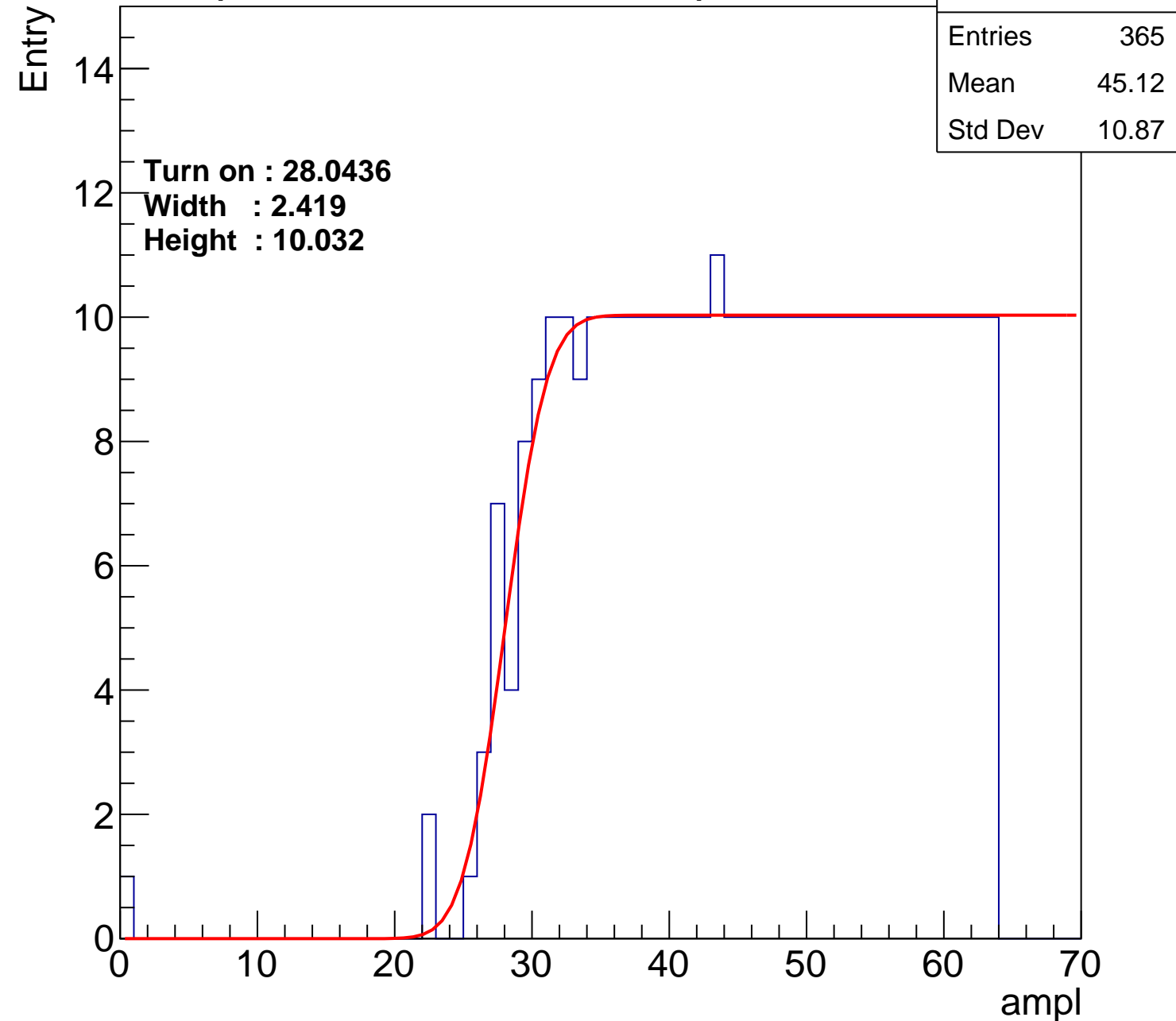
Width : 2.419

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch76

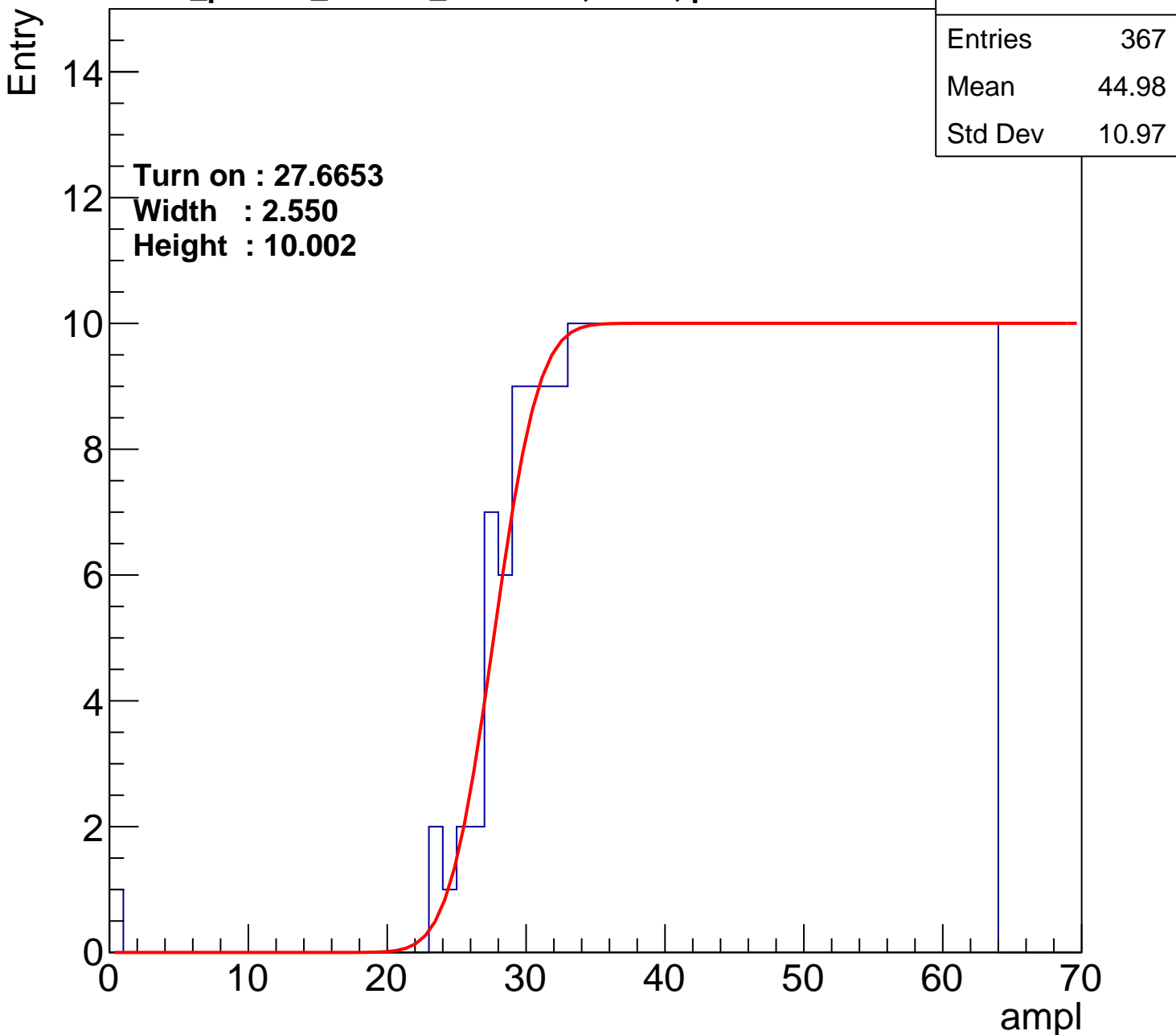
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 27.6653

Width : 2.550

Height : 10.002

Entries	367
Mean	44.98
Std Dev	10.97



B1L000S, U22-ch77

calib_packv5_042523_0143.root, FC#3, port B3

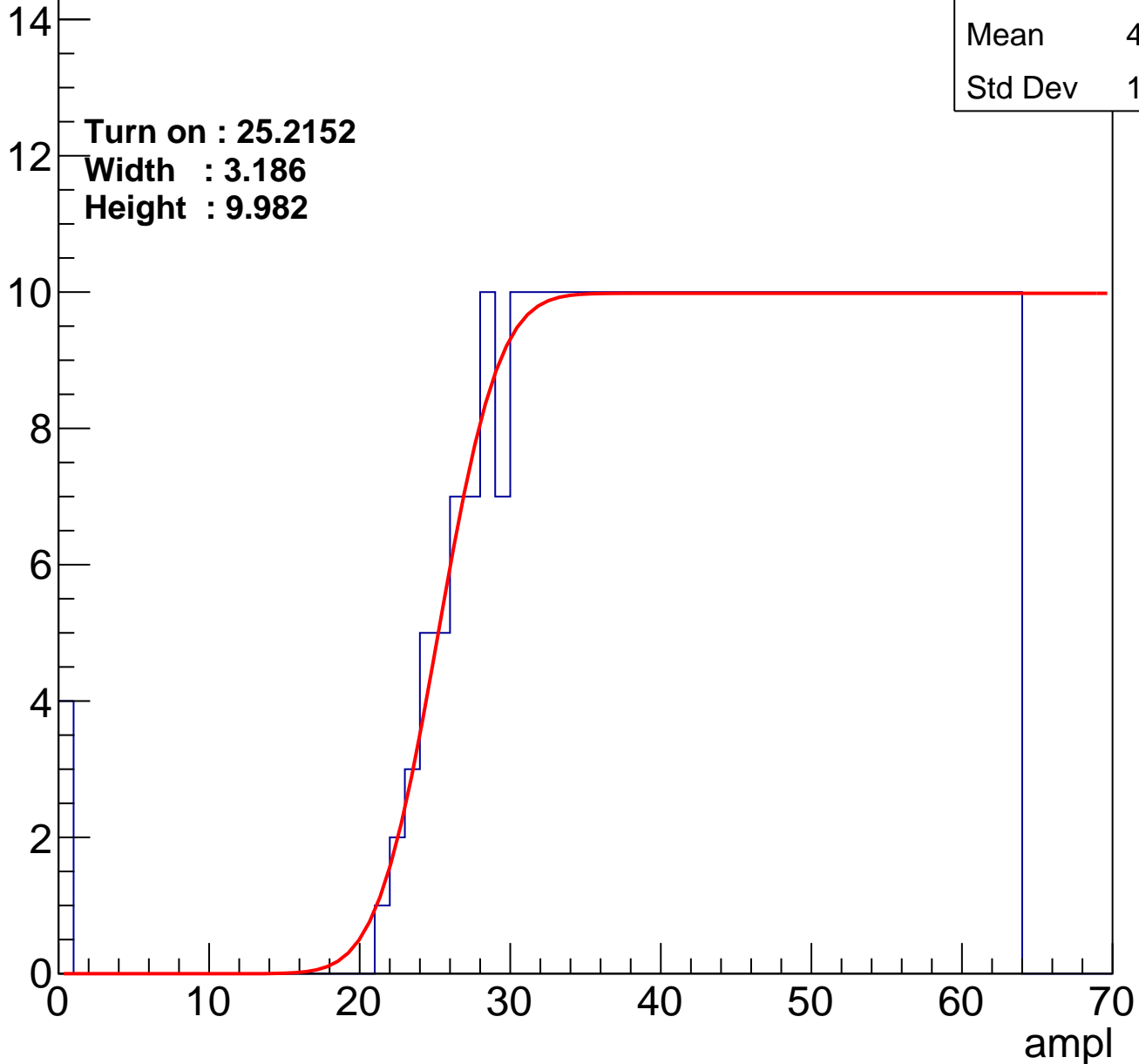
Entries	391
Mean	43.59
Std Dev	12.14

Turn on : 25.2152

Width : 3.186

Height : 9.982

Entry



B1L000S, U22-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.09
Std Dev	11.4

Turn on : 25.4813

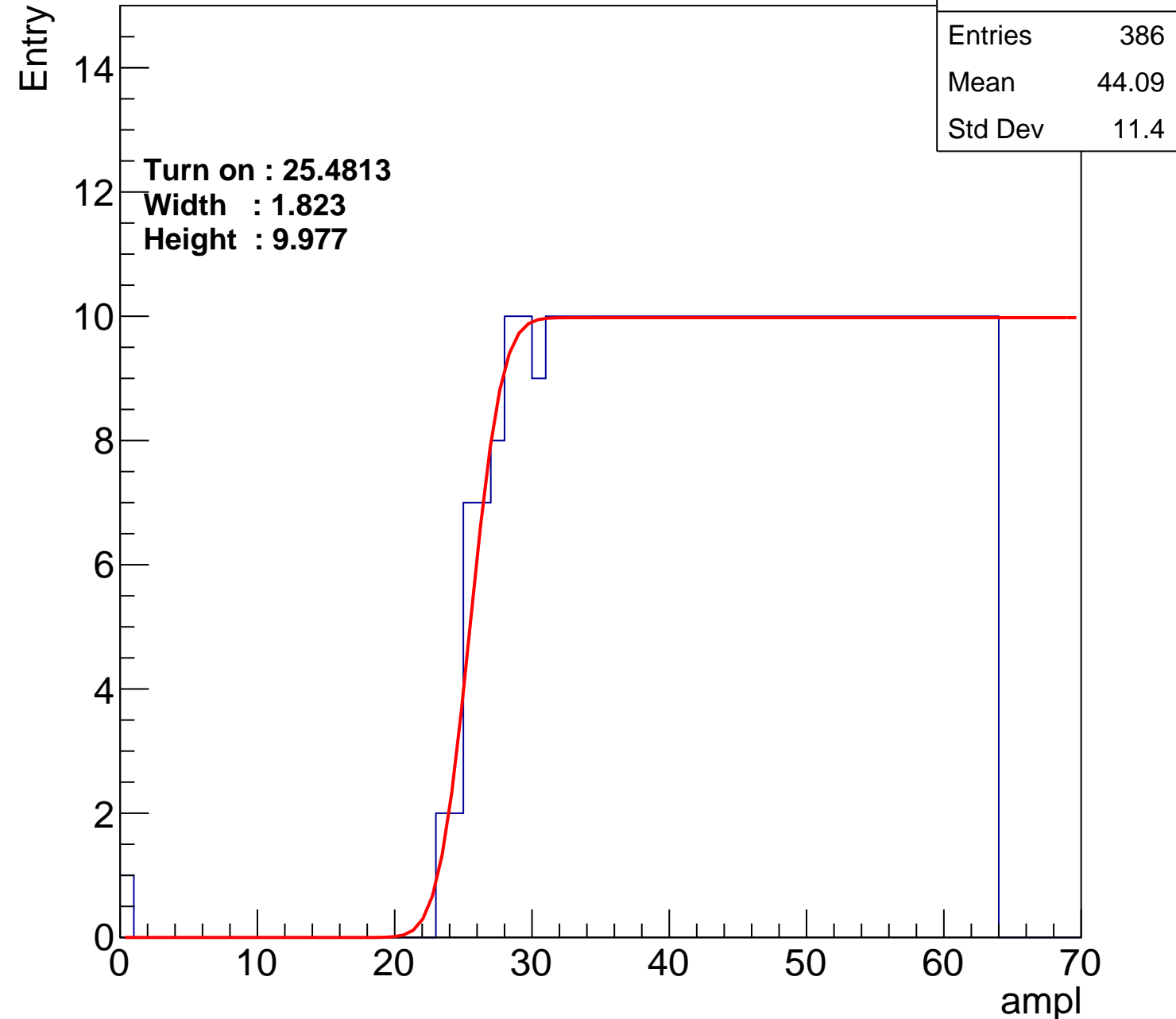
Width : 1.823

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 26.3738
Width : 2.913
Height : 10.122



B1L000S, U22-ch80

calib_packv5_042523_0143.root, FC#3, port B3

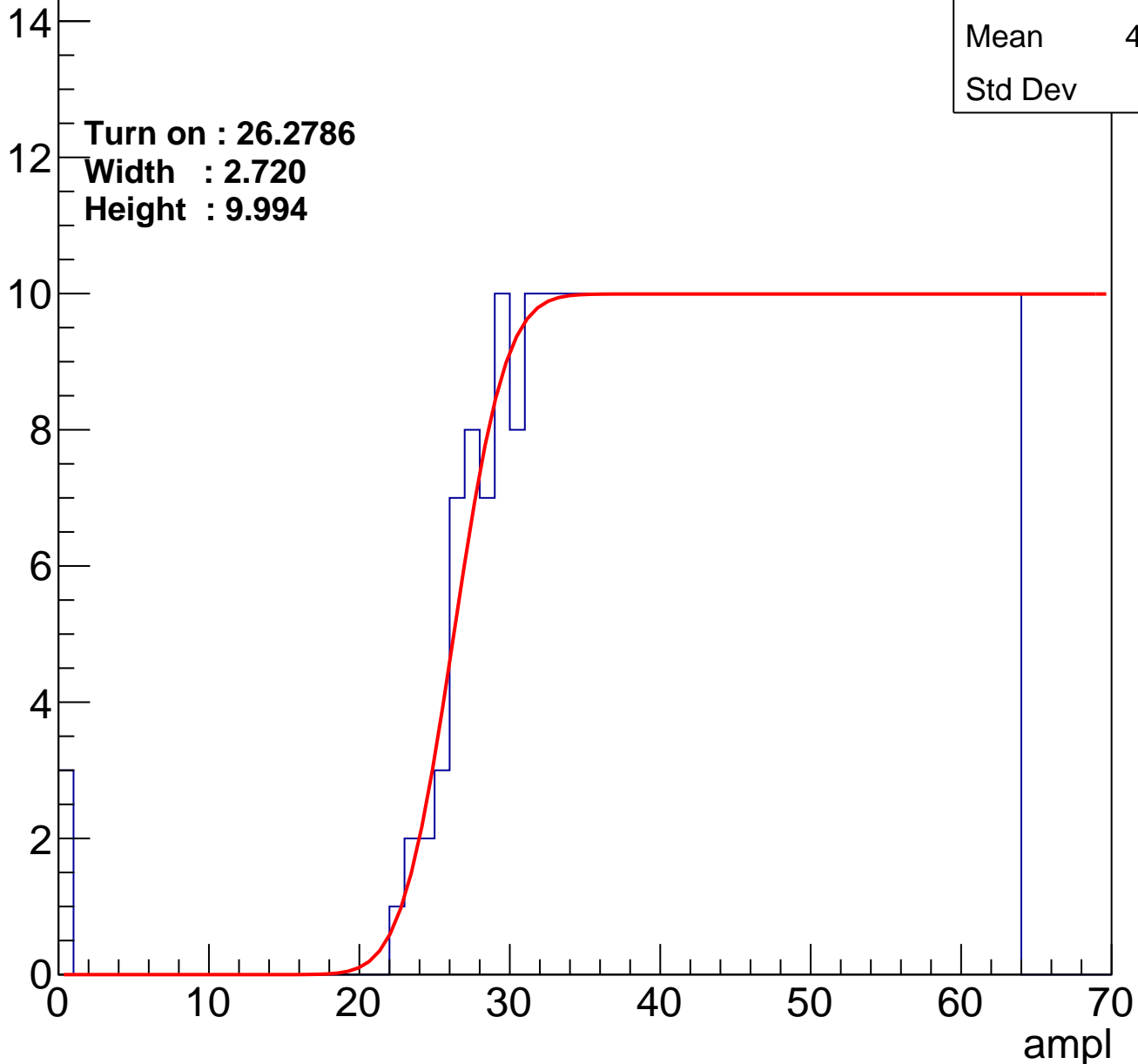
Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 26.2786

Width : 2.720

Height : 9.994

Entry



B1L000S, U22-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.9882

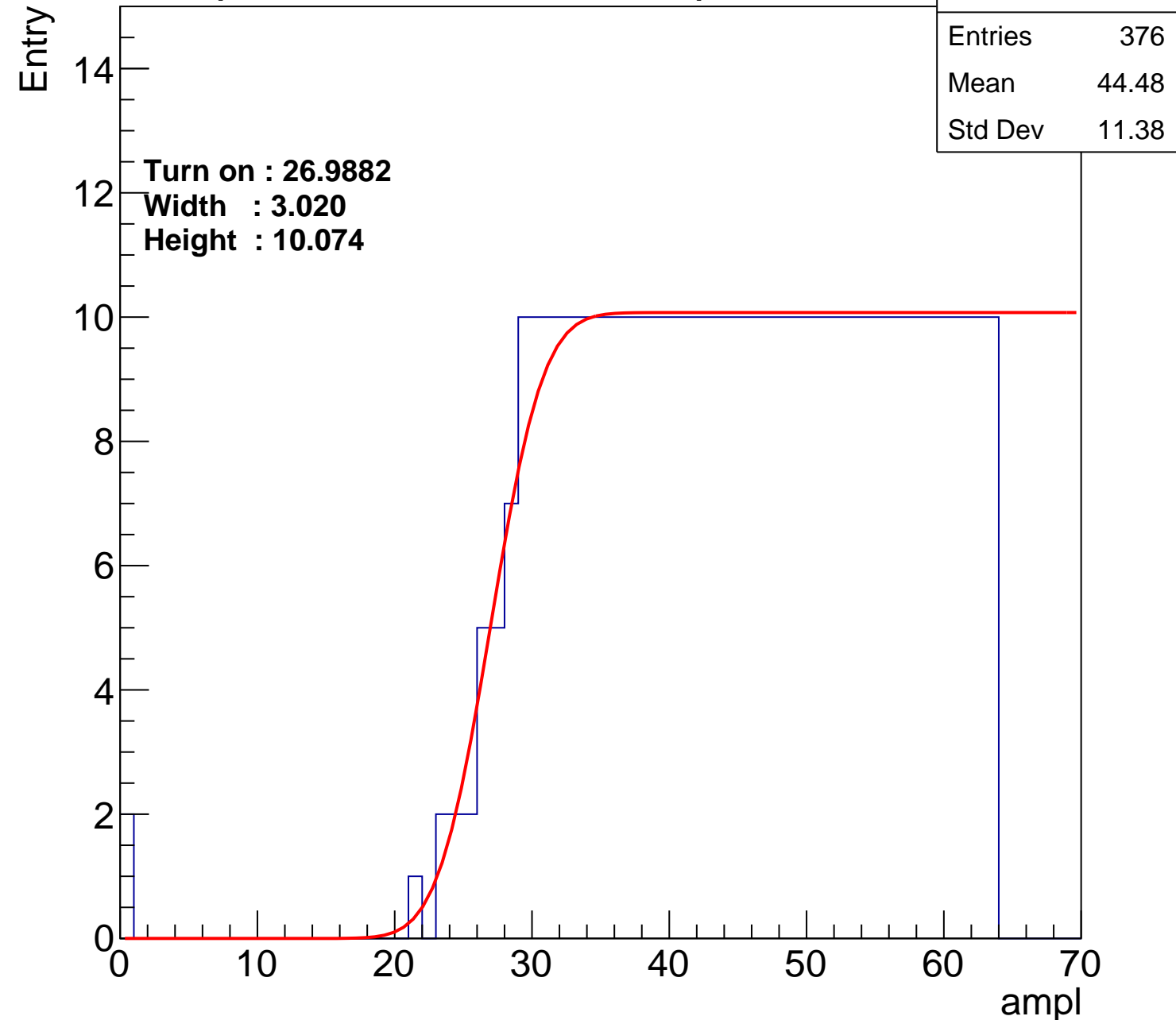
Width : 3.020

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.78
Std Dev	11.88

Turn on : 25.5174

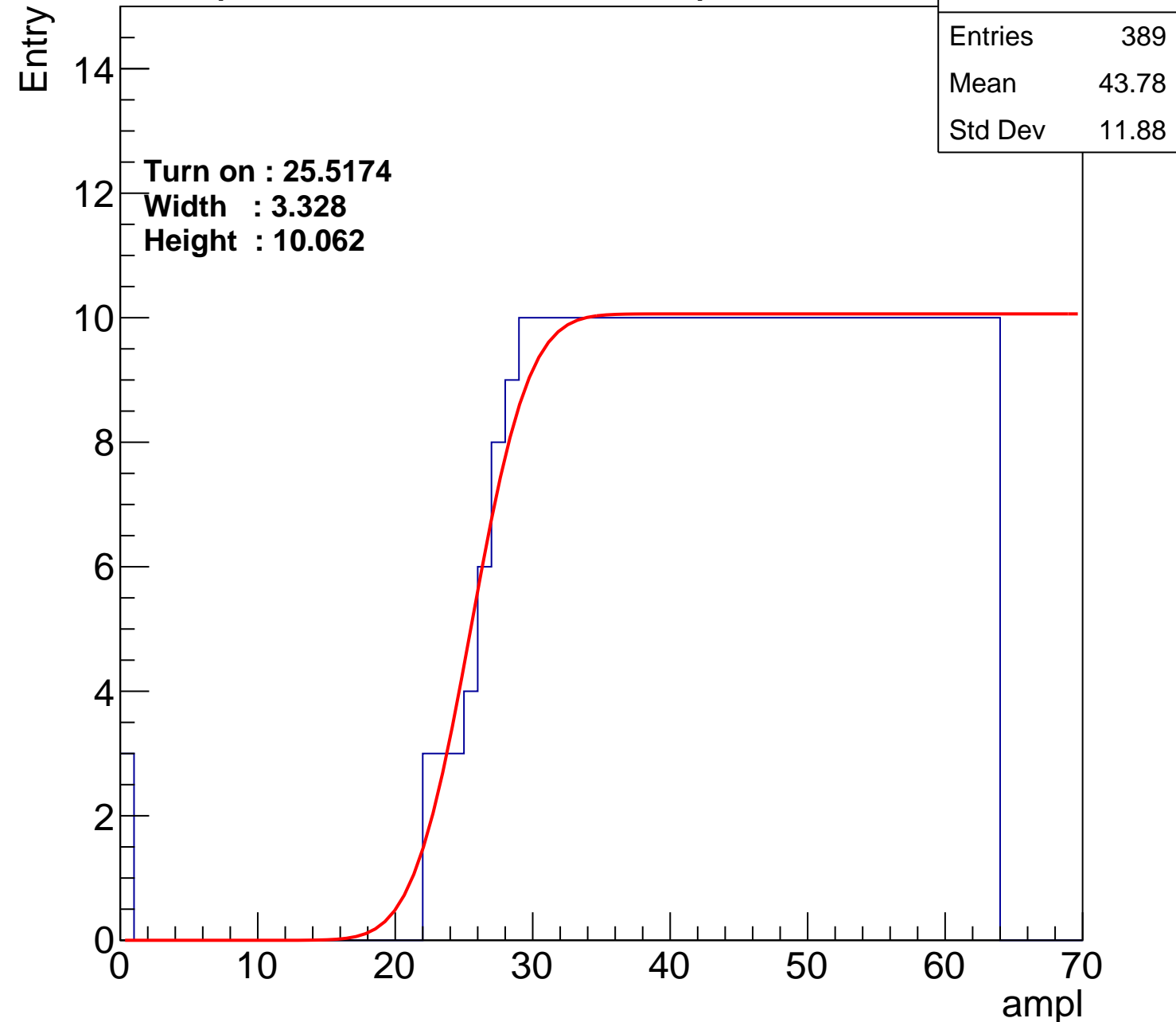
Width : 3.328

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch83

calib_packv5_042523_0143.root, FC#3, port B3

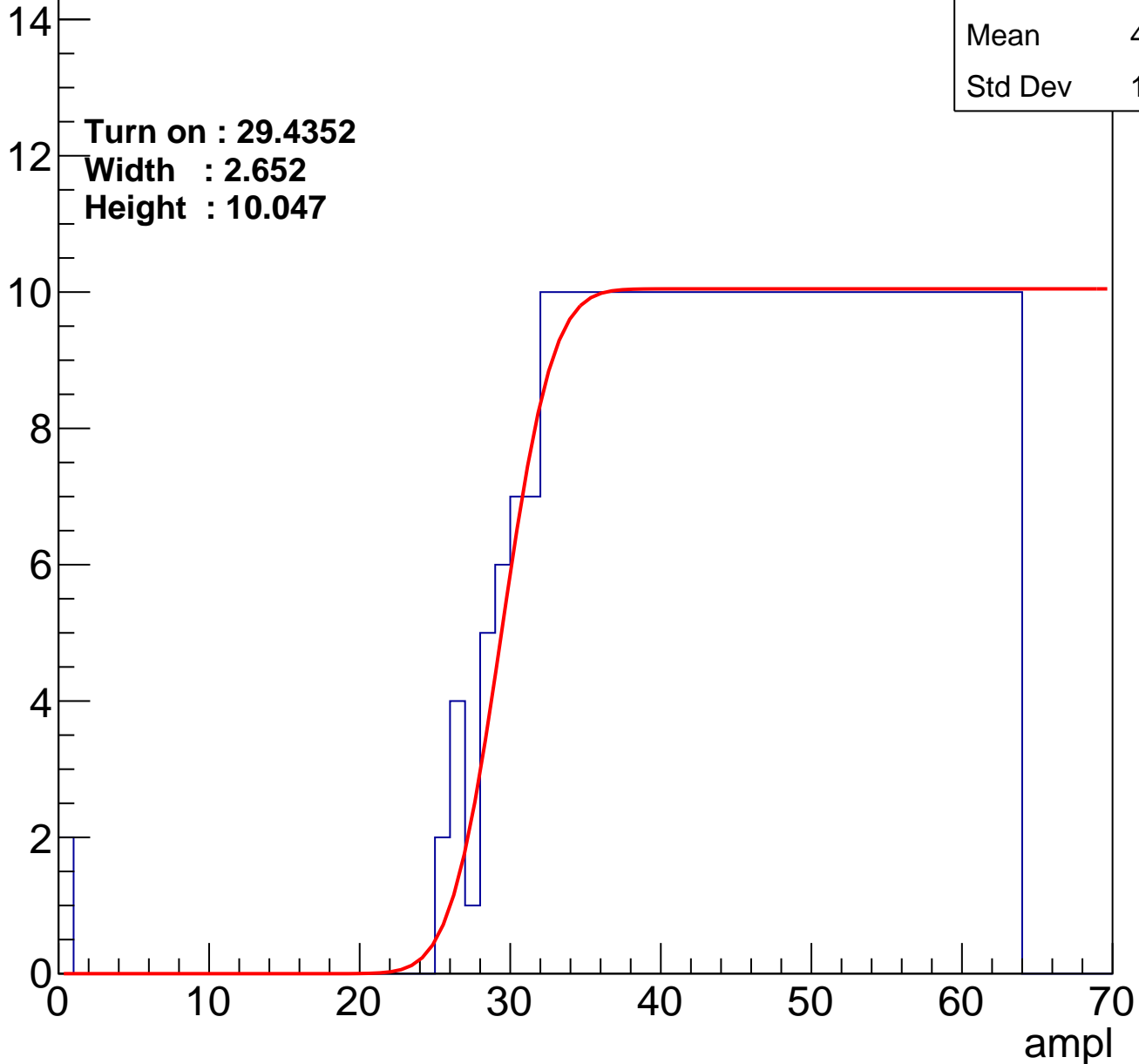
Entries	354
Mean	45.54
Std Dev	10.86

Turn on : 29.4352

Width : 2.652

Height : 10.047

Entry



B1L000S, U22-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.82
Std Dev	11.4

Turn on : 27.3804

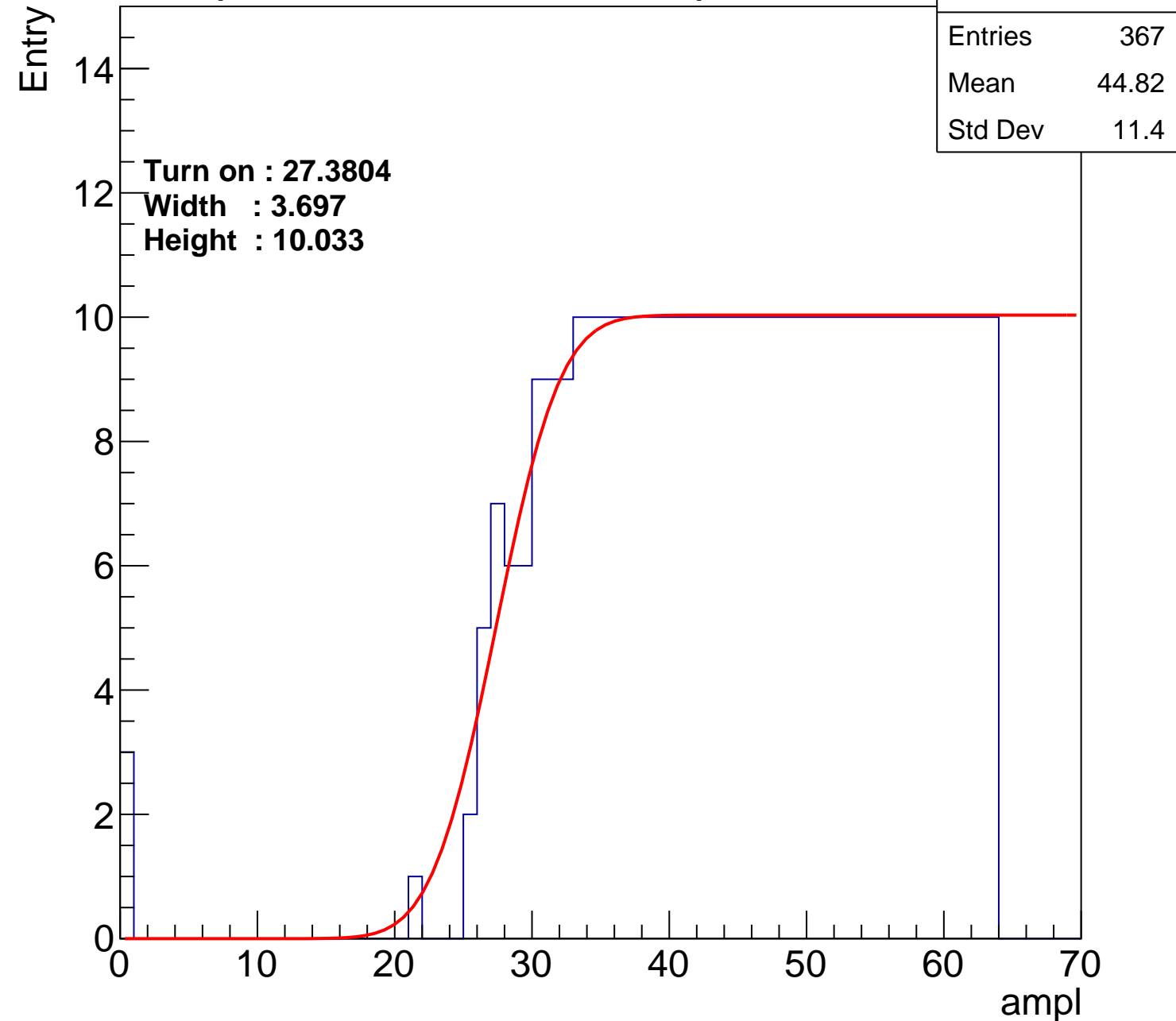
Width : 3.697

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.43
Std Dev	10.72

Turn on : 28.1096

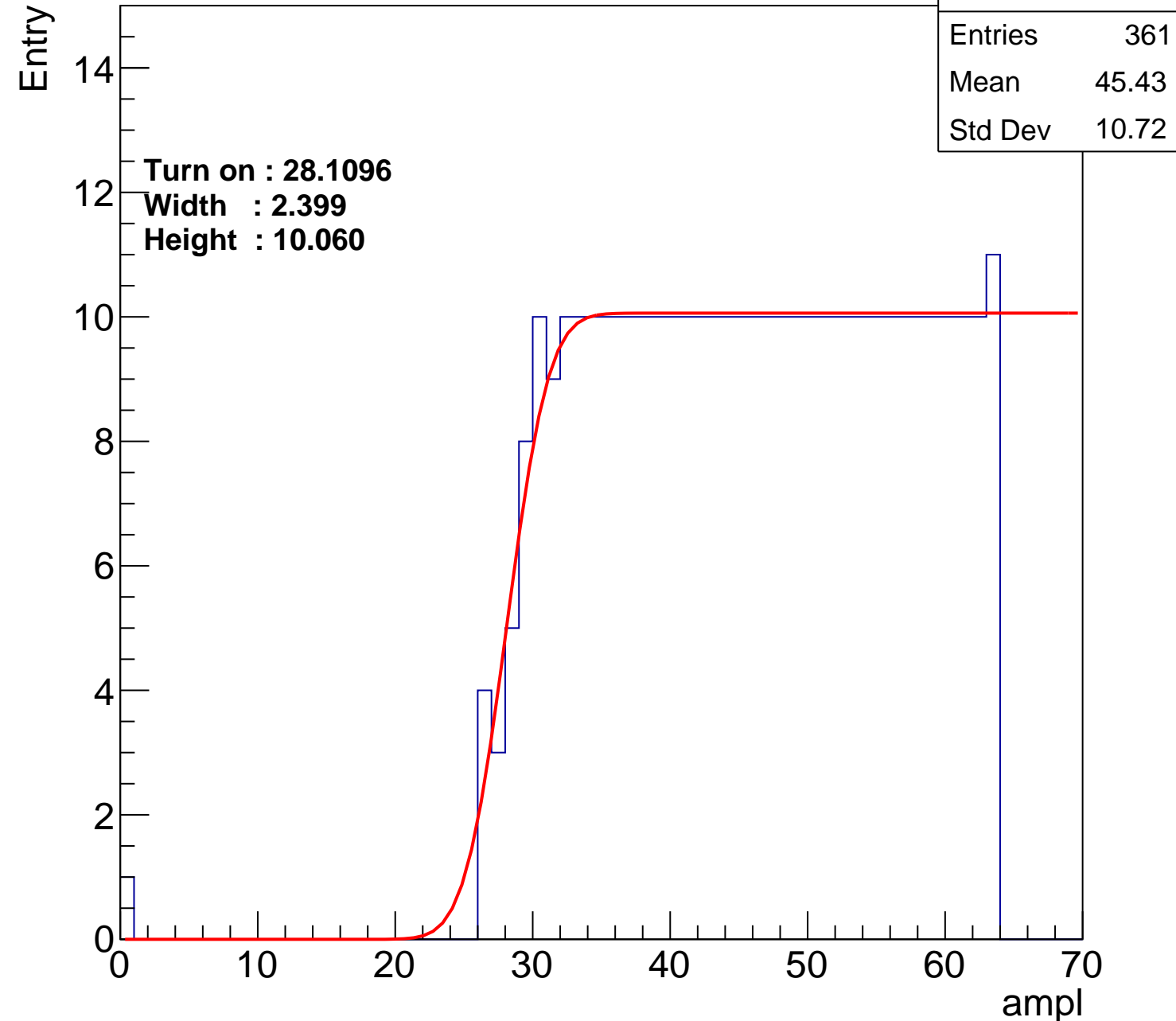
Width : 2.399

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.25
Std Dev	11.36

Turn on : 26.0542

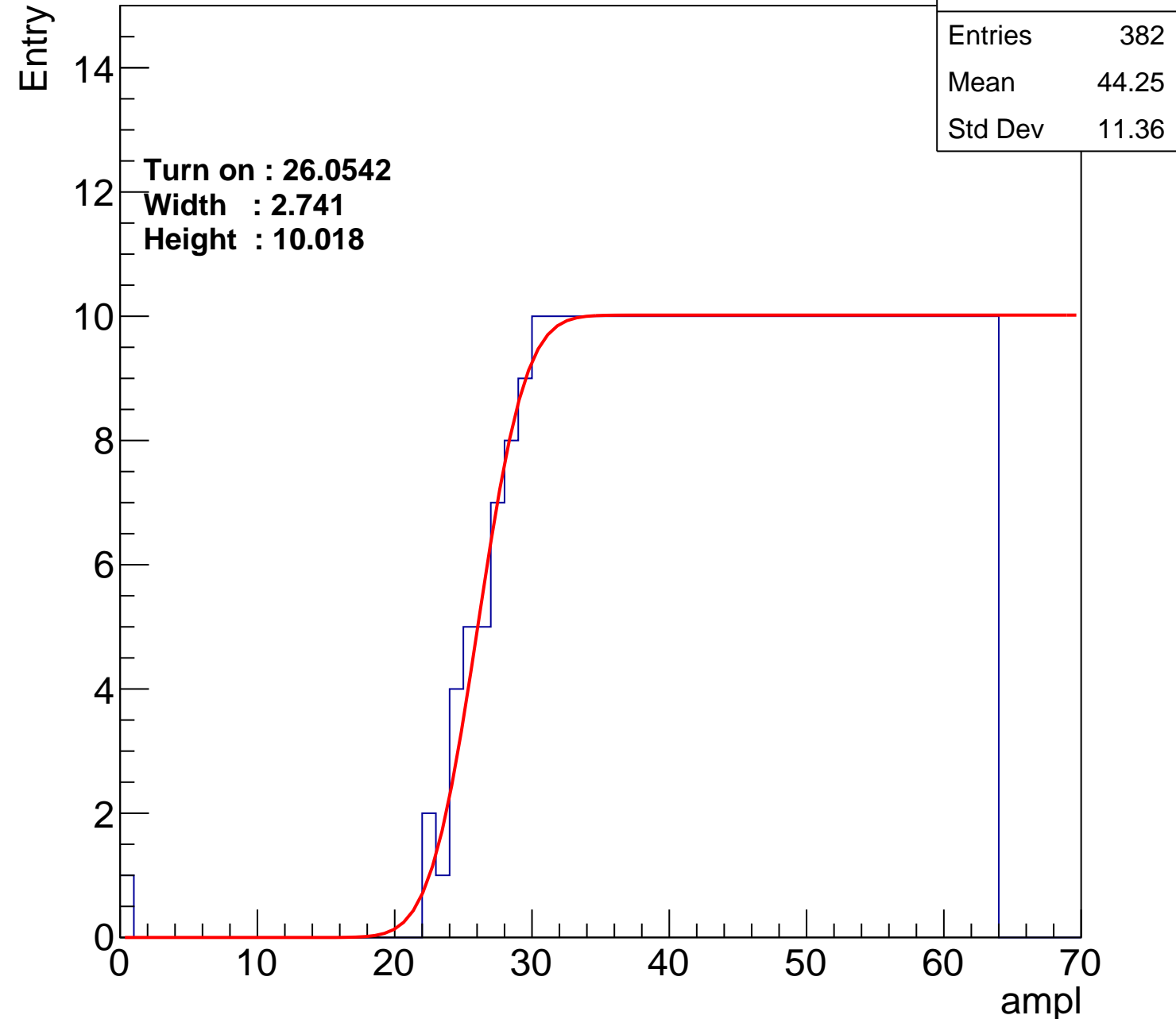
Width : 2.741

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch87

calib_packv5_042523_0143.root, FC#3, port B3

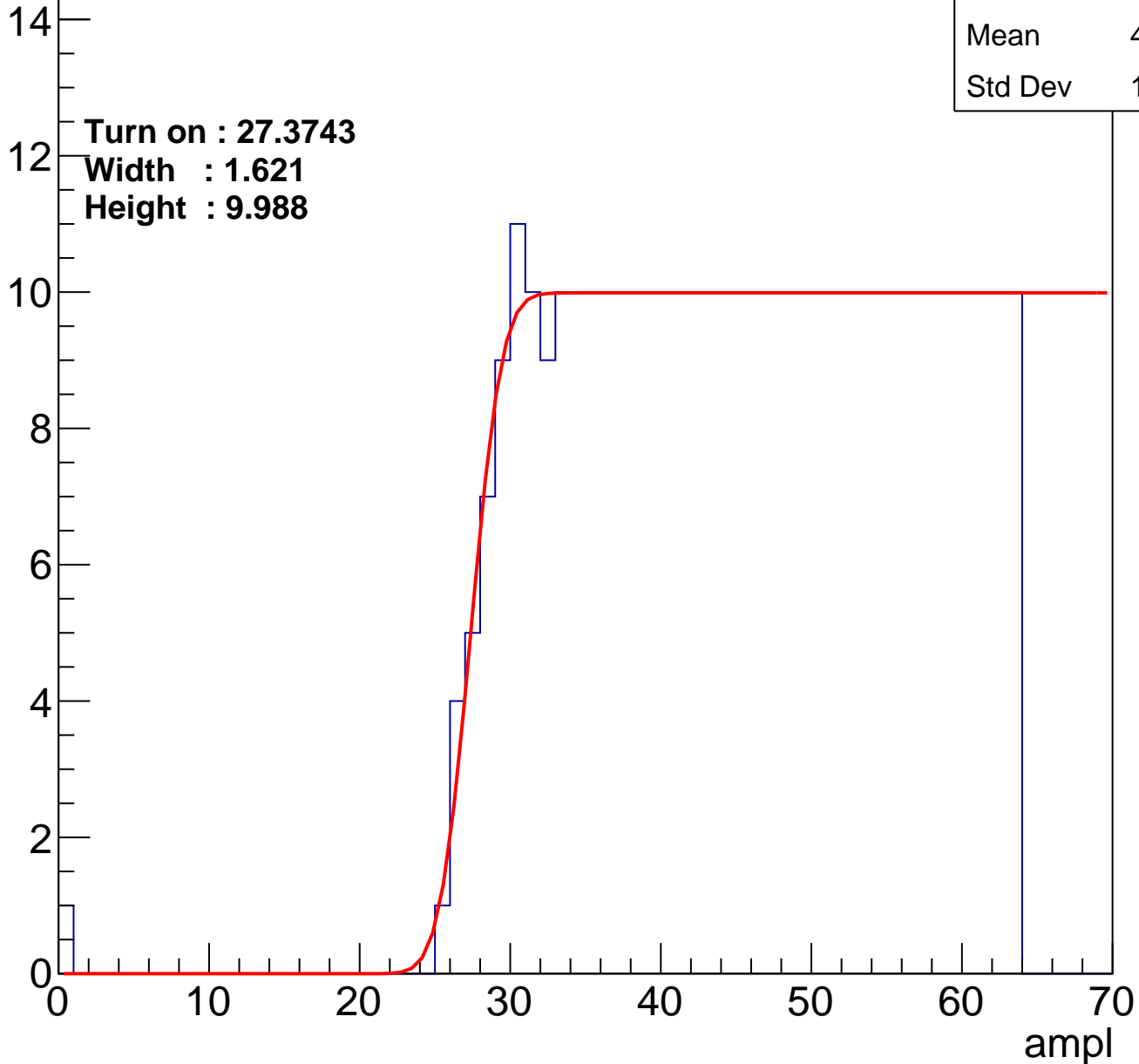
Entries	367
Mean	45.04
Std Dev	10.87

Turn on : 27.3743

Width : 1.621

Height : 9.988

Entry



B1L000S, U22-ch88

calib_packv5_042523_0143.root, FC#3, port B3

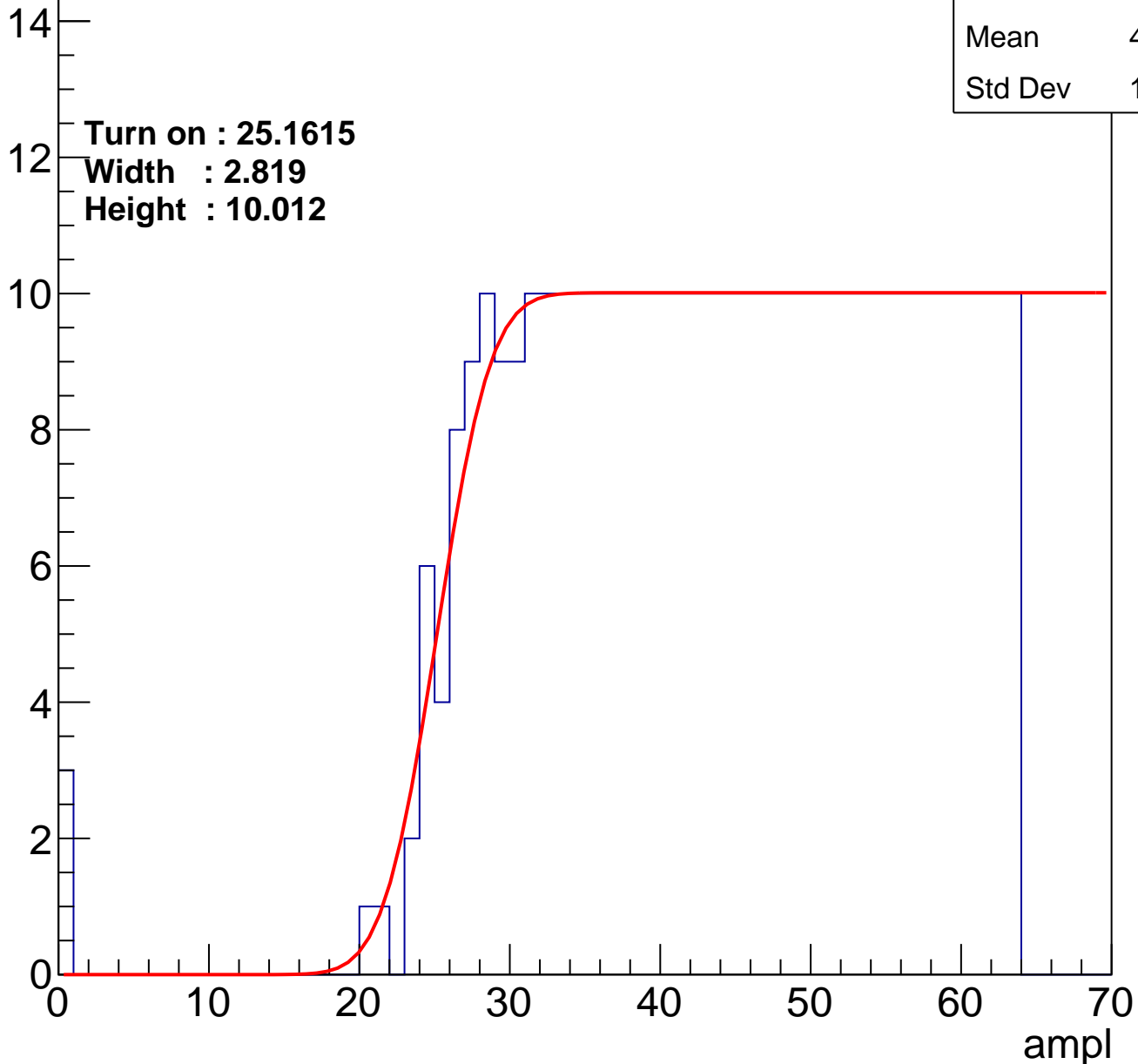
Entries	392
Mean	43.63
Std Dev	11.95

Turn on : 25.1615

Width : 2.819

Height : 10.012

Entry



B1L000S, U22-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 26.0879

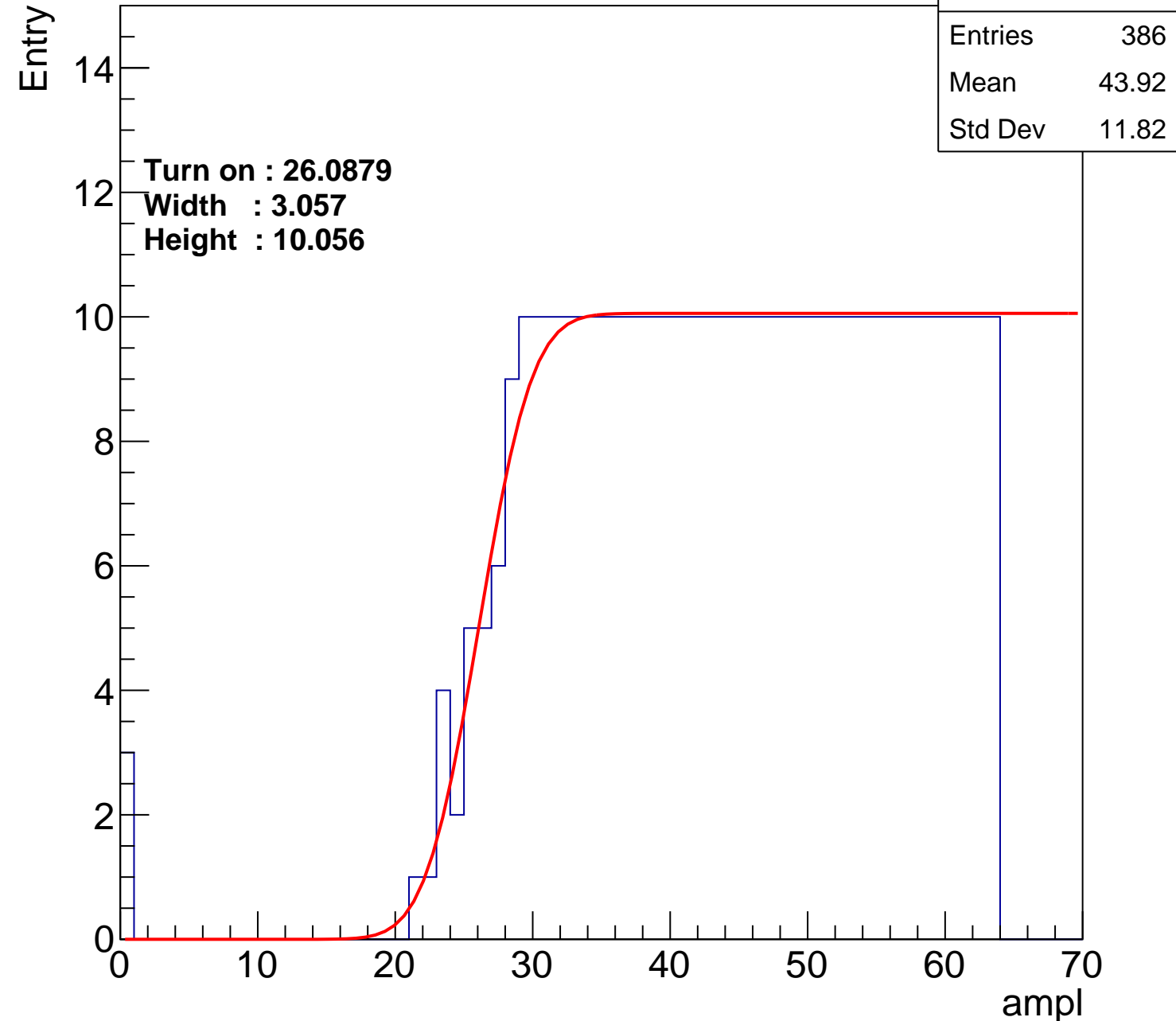
Width : 3.057

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch90

calib_packv5_042523_0143.root, FC#3, port B3

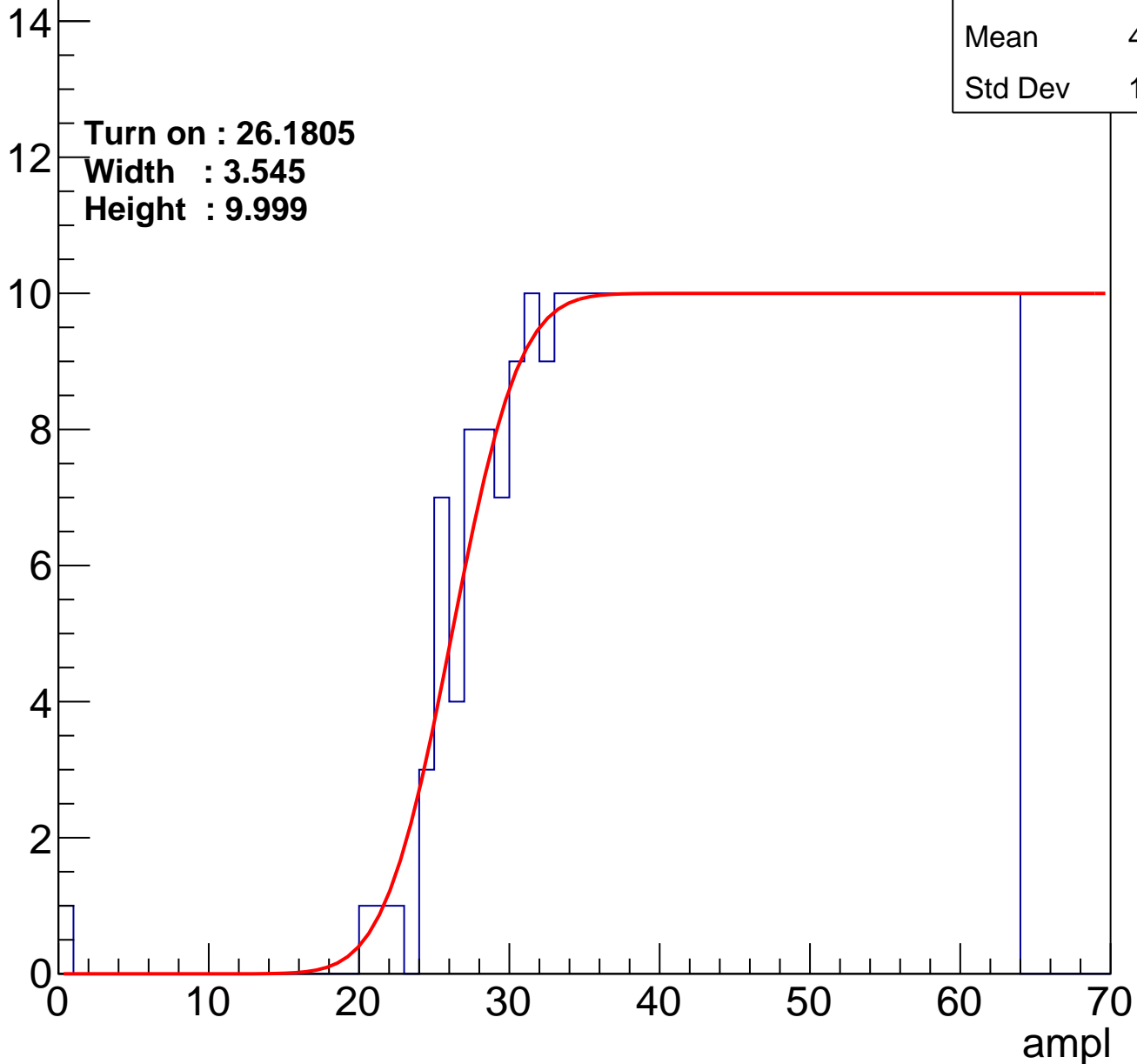
Entries	379
Mean	44.34
Std Dev	11.36

Turn on : 26.1805

Width : 3.545

Height : 9.999

Entry



B1L000S, U22-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.02
Std Dev	11.77

Turn on : 27.1128

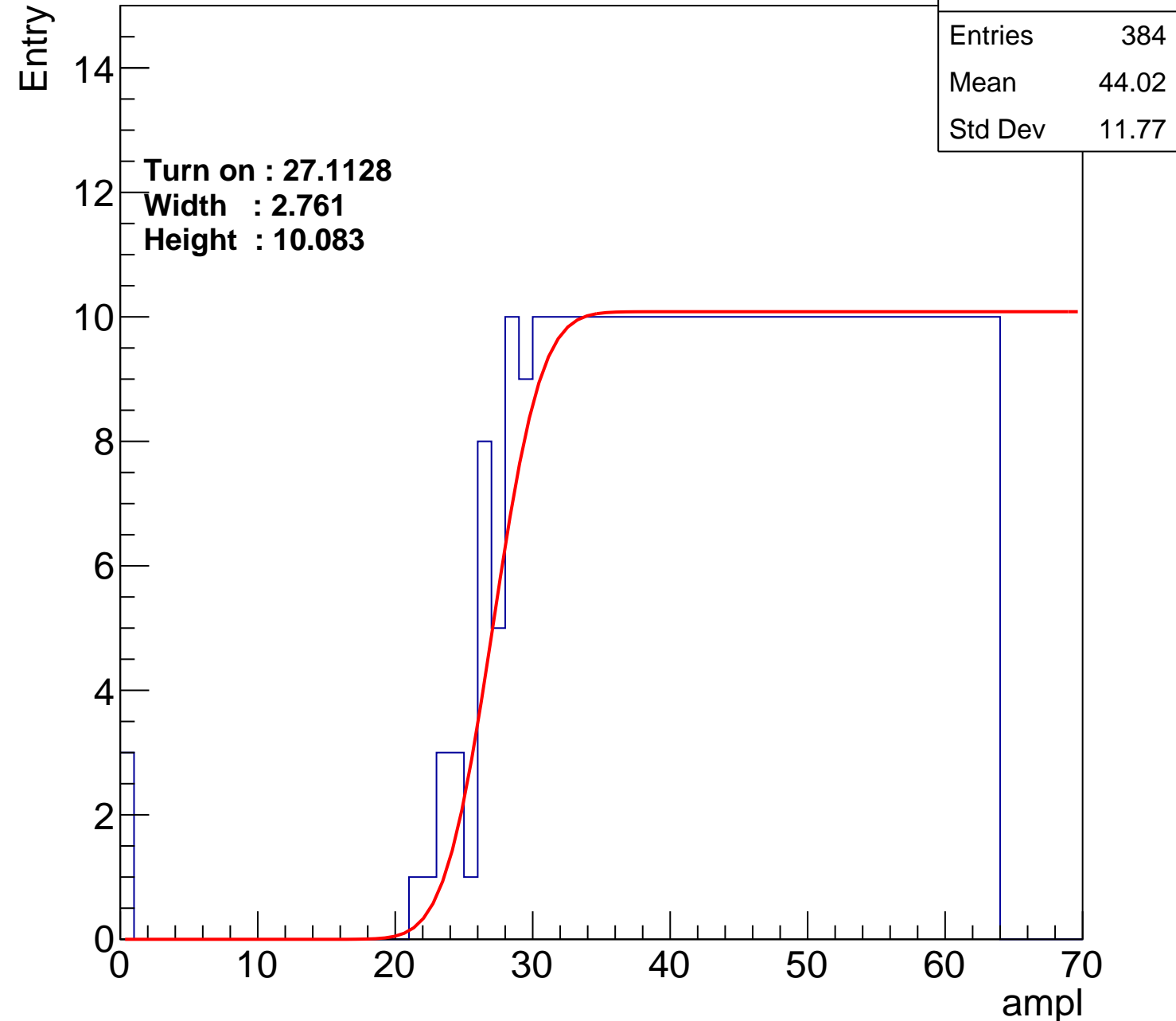
Width : 2.761

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.08
Std Dev	11.72

Turn on : 25.7123

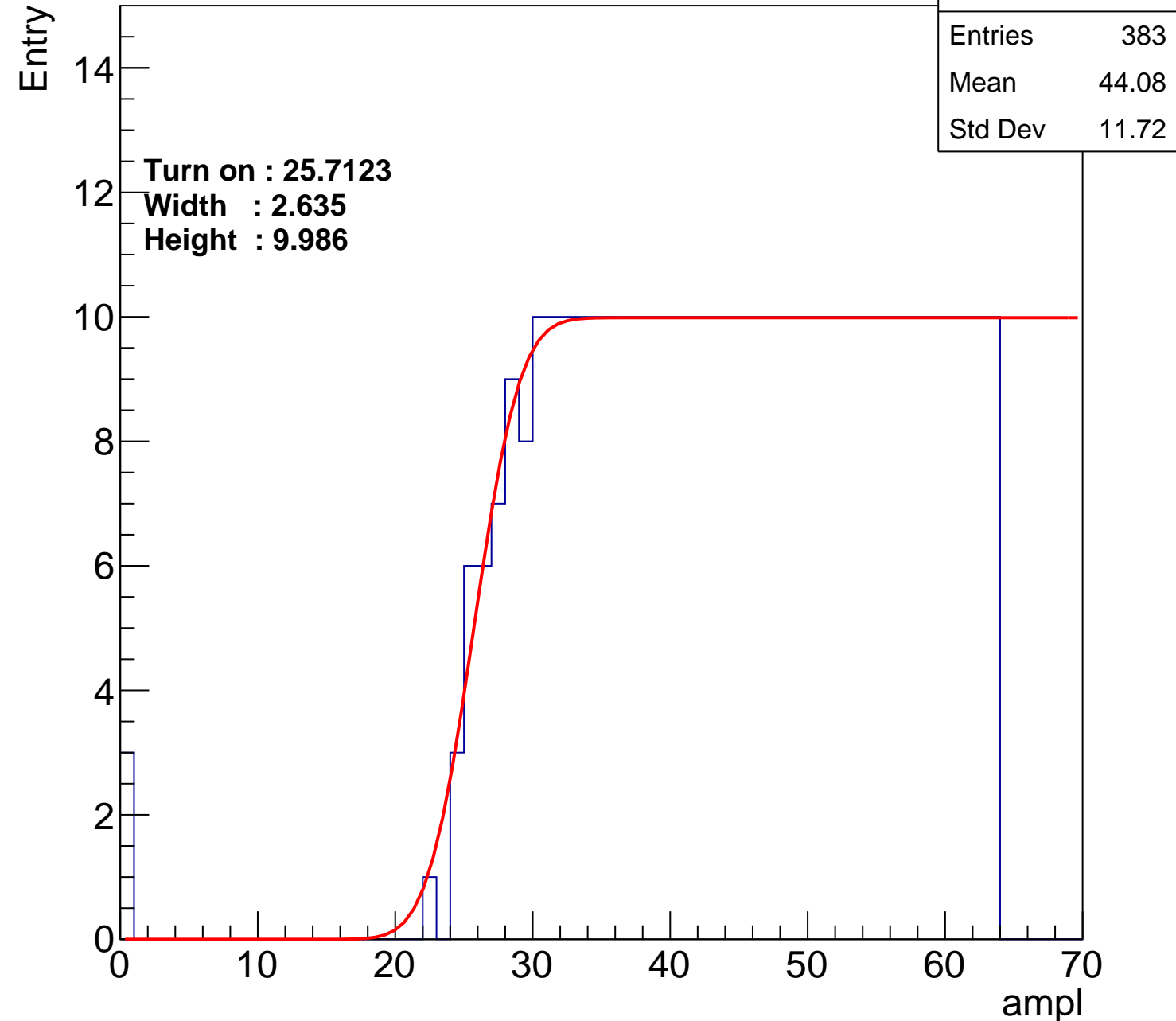
Width : 2.635

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 26.9755

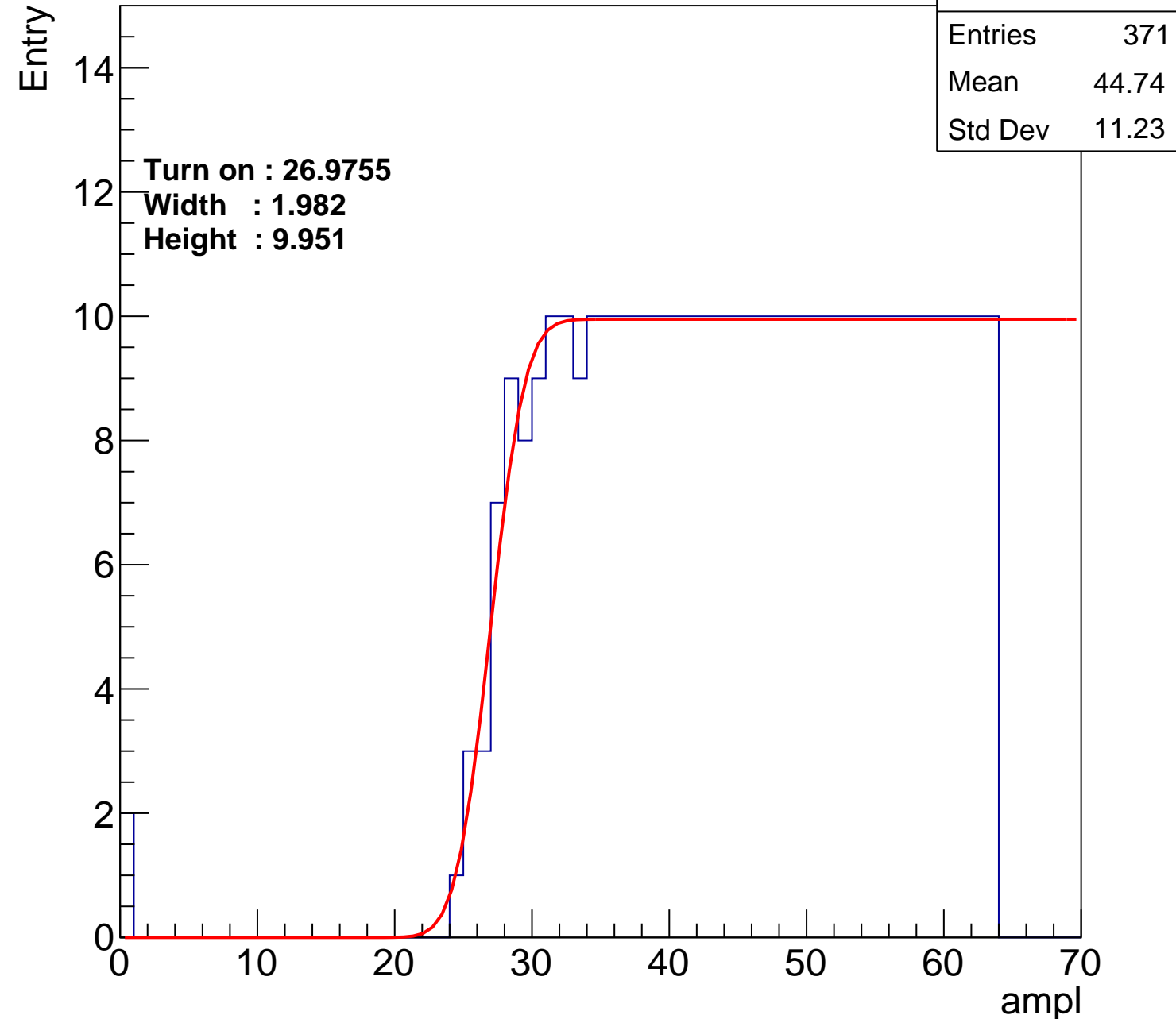
Width : 1.982

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.6225

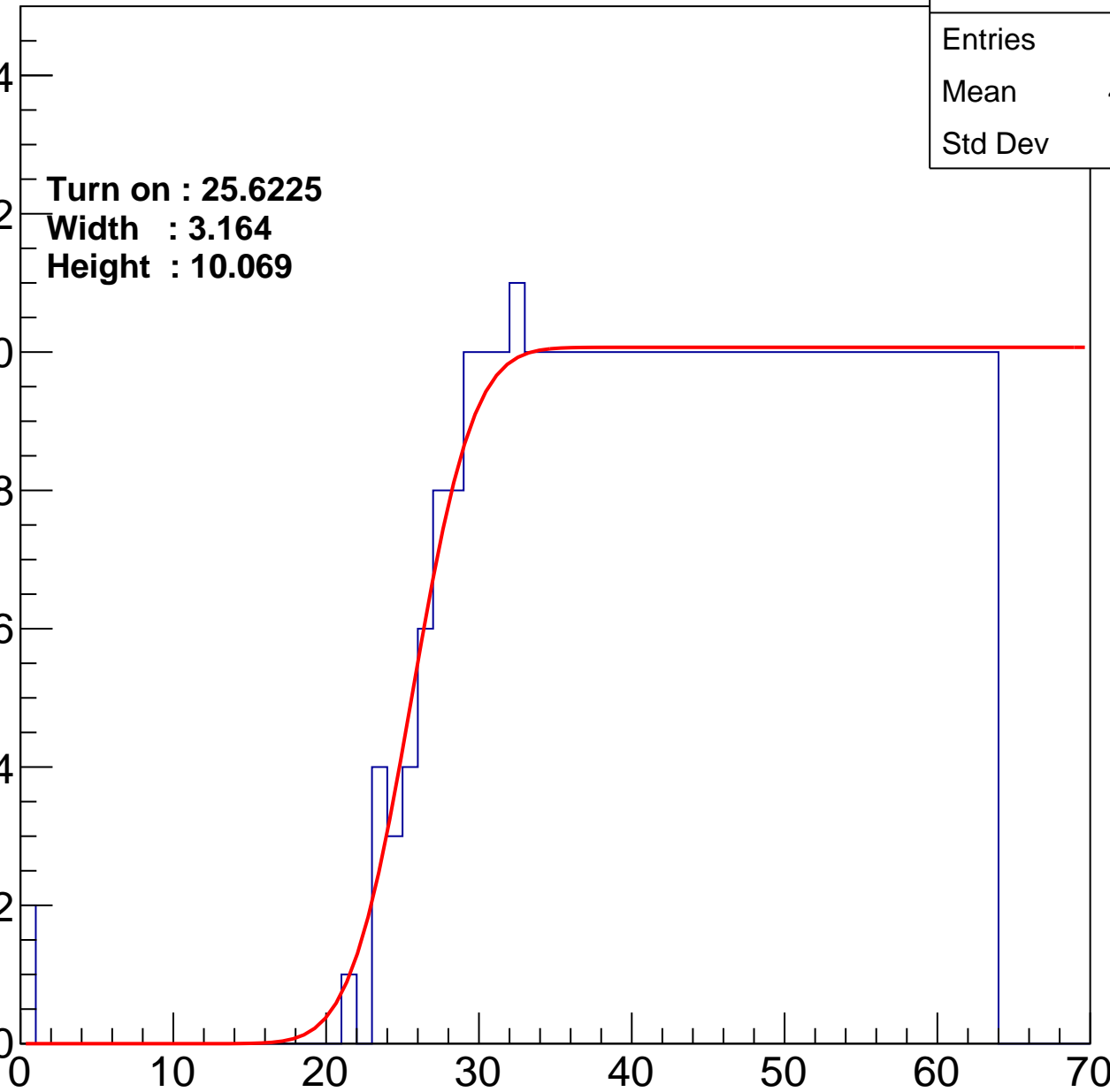
Width : 3.164

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.5572

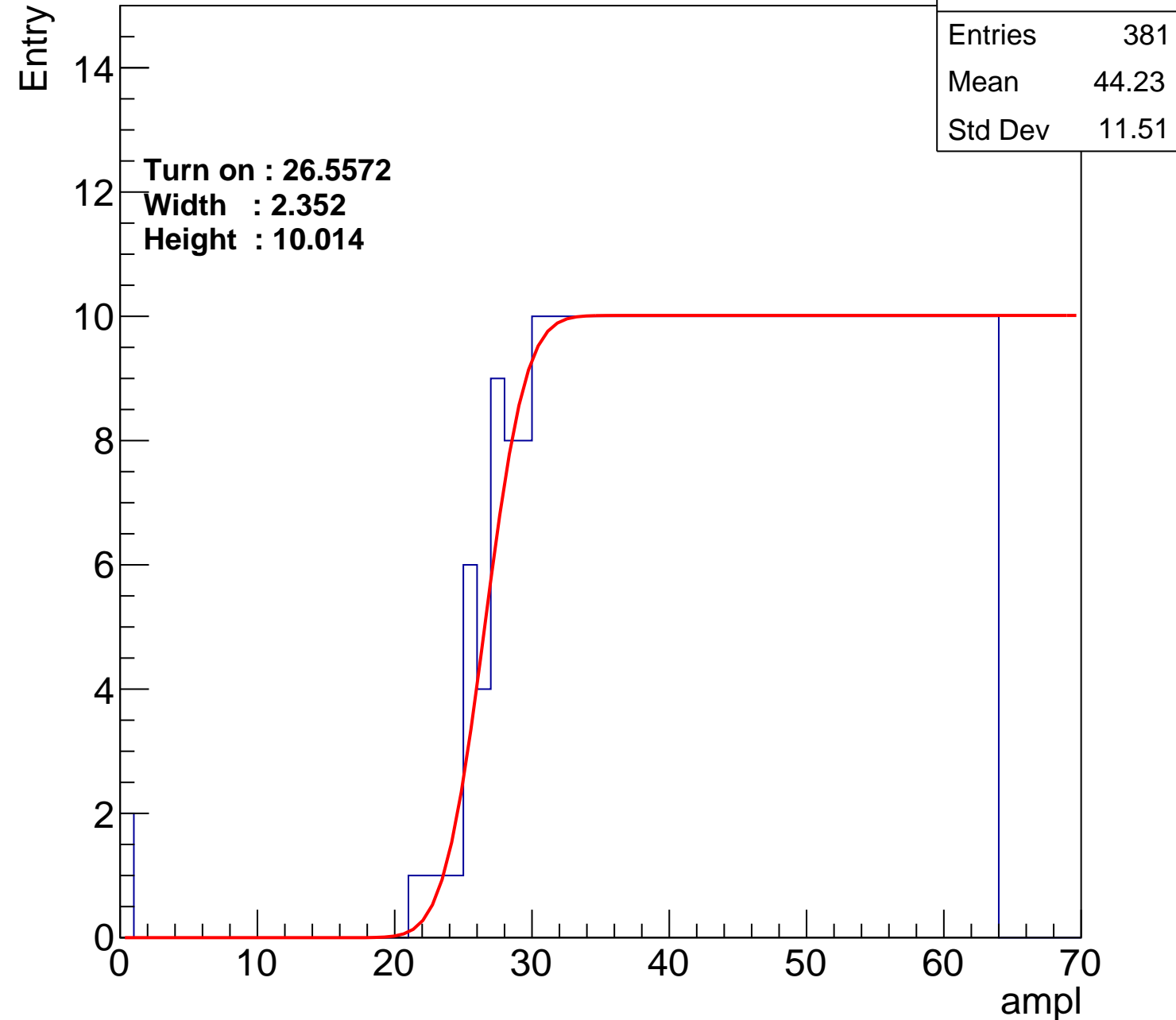
Width : 2.352

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch96

calib_packv5_042523_0143.root, FC#3, port B3

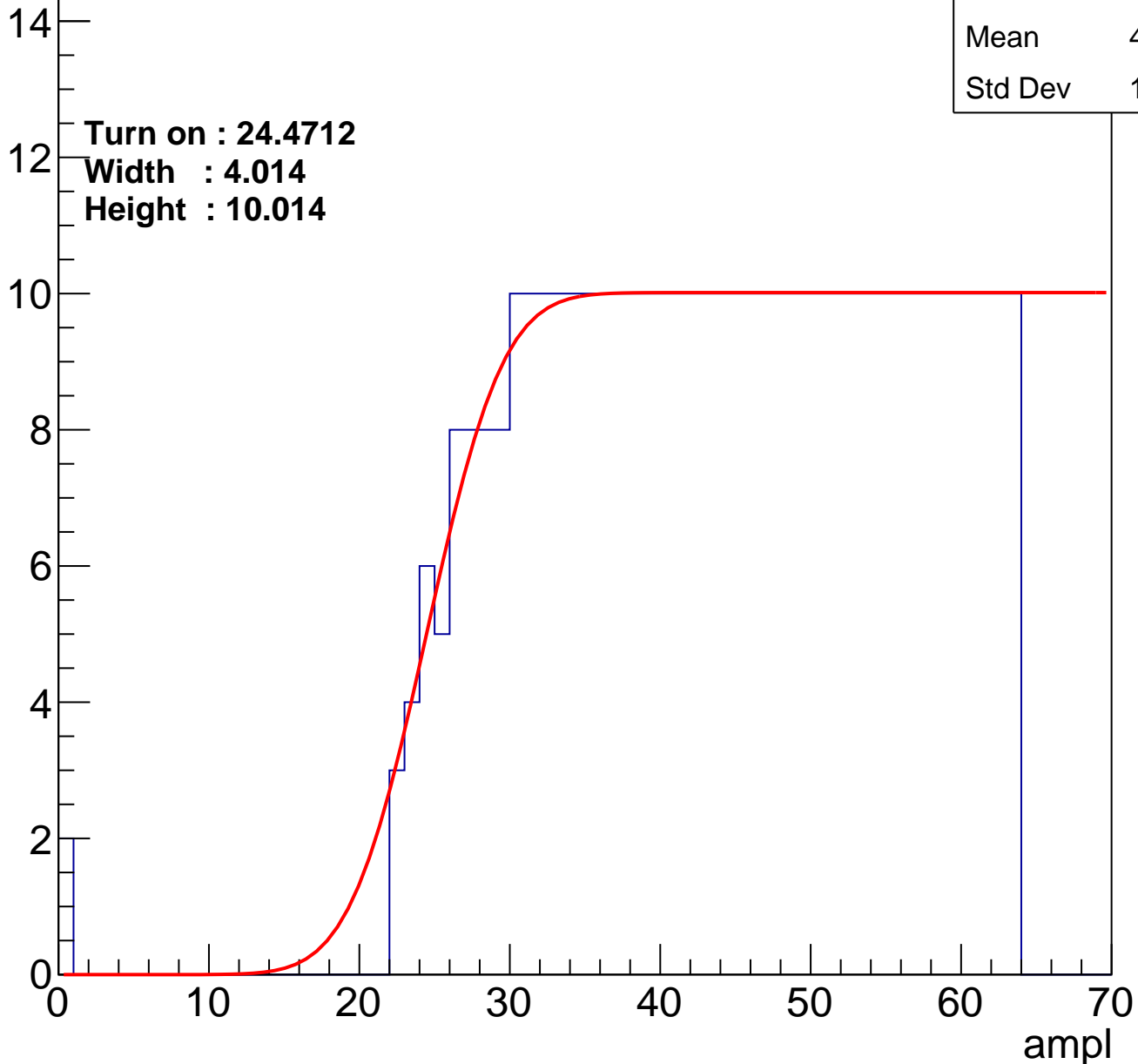
Entries	392
Mean	43.67
Std Dev	11.83

Turn on : 24.4712

Width : 4.014

Height : 10.014

Entry



B1L000S, U22-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.05
Std Dev	11.11

Turn on : 28.4694

Width : 2.597

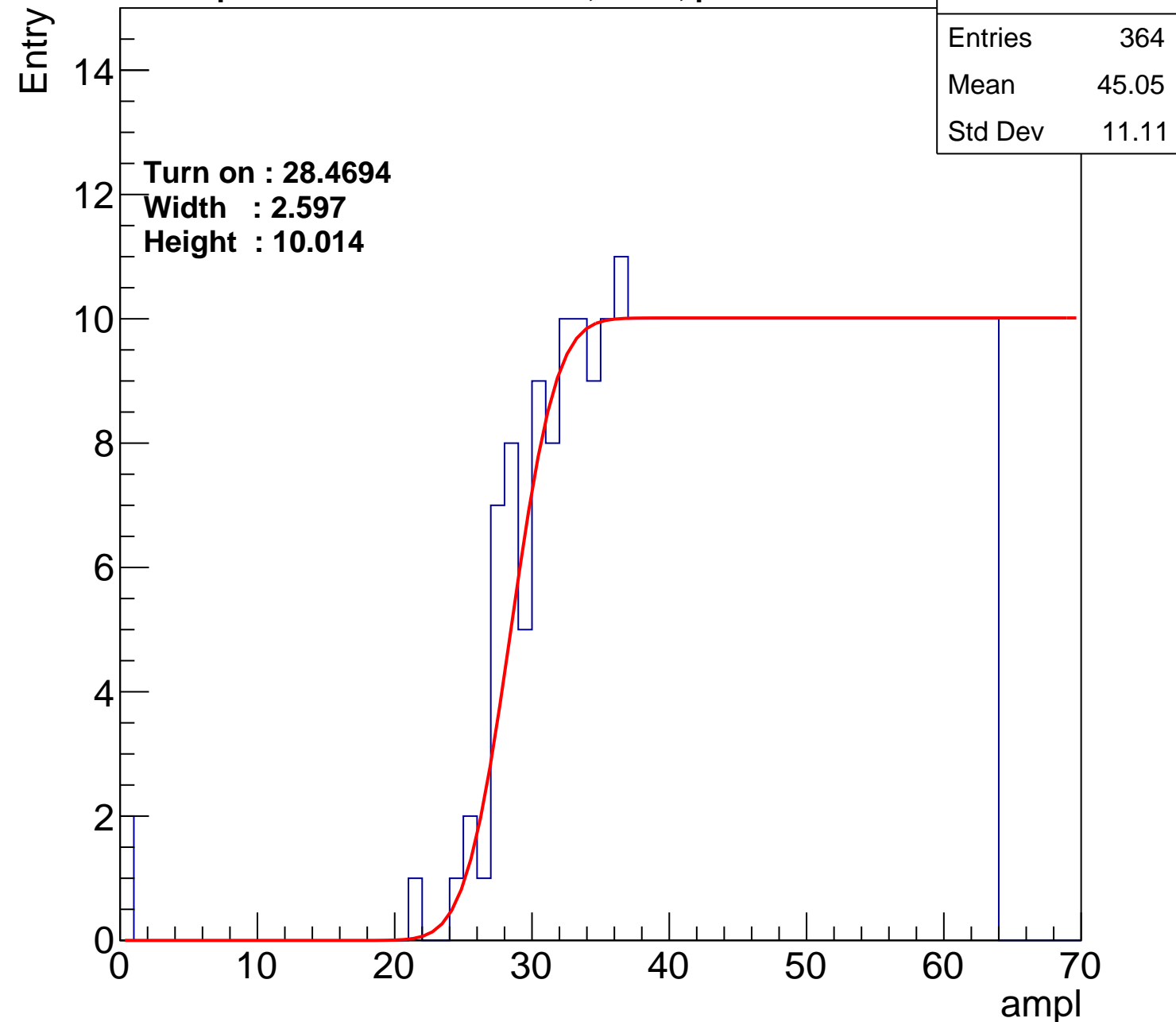
Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U22-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.86
Std Dev	12.02

Turn on : 25.9527

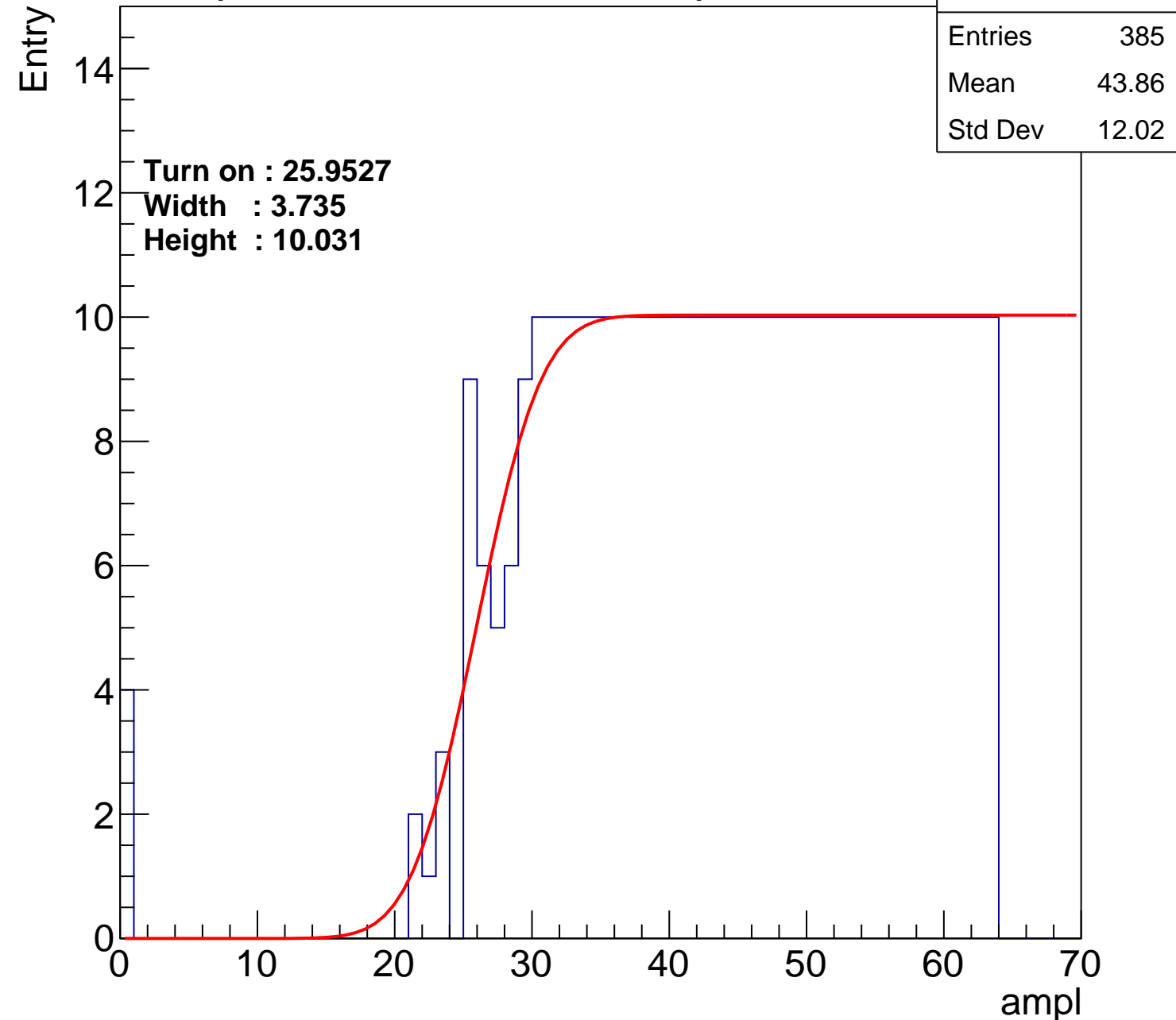
Width : 3.735

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.05
Std Dev	11.64

Turn on : 25.8120

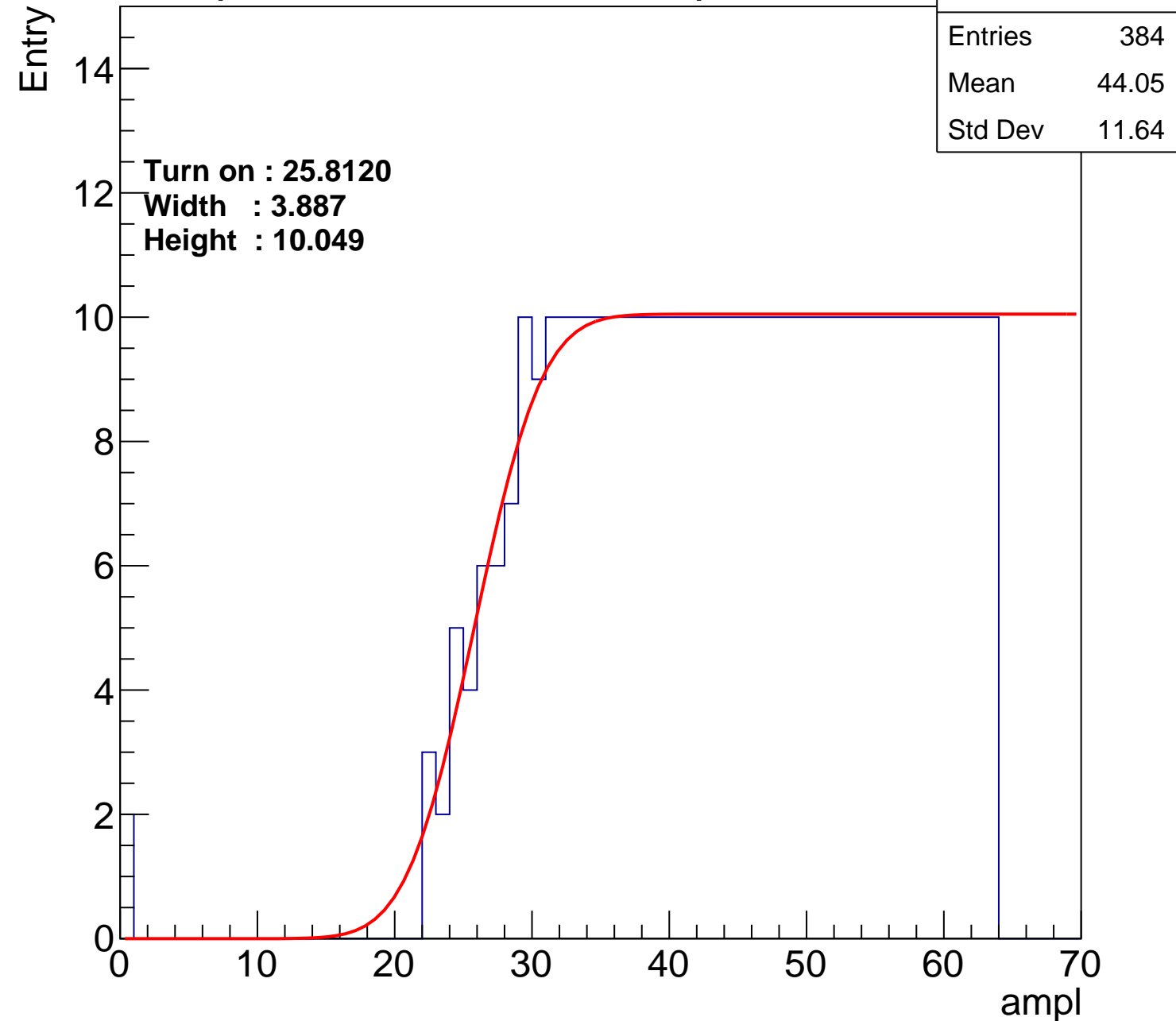
Width : 3.887

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.94
Std Dev	11.83

Turn on : 26.1057

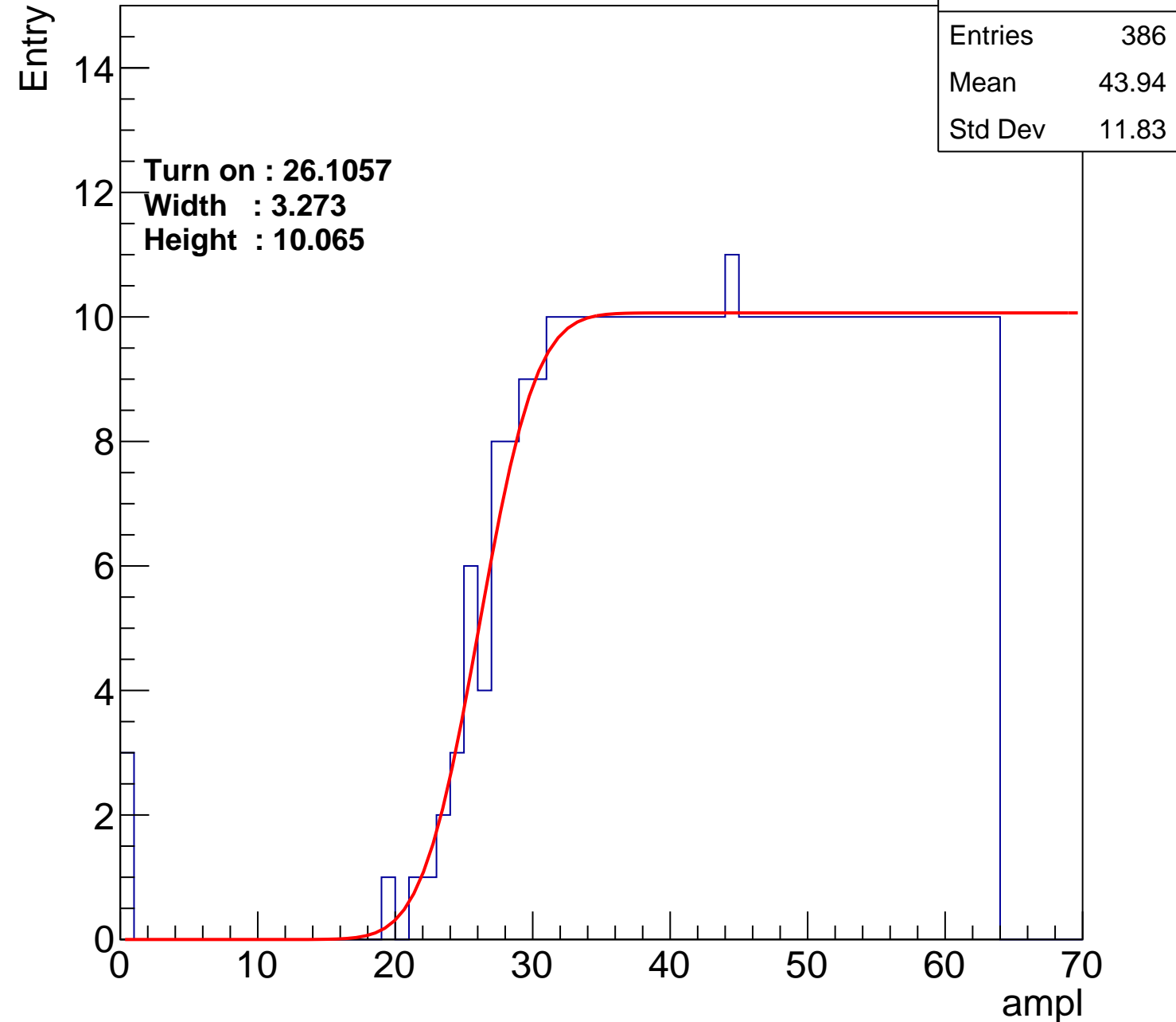
Width : 3.273

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.6018

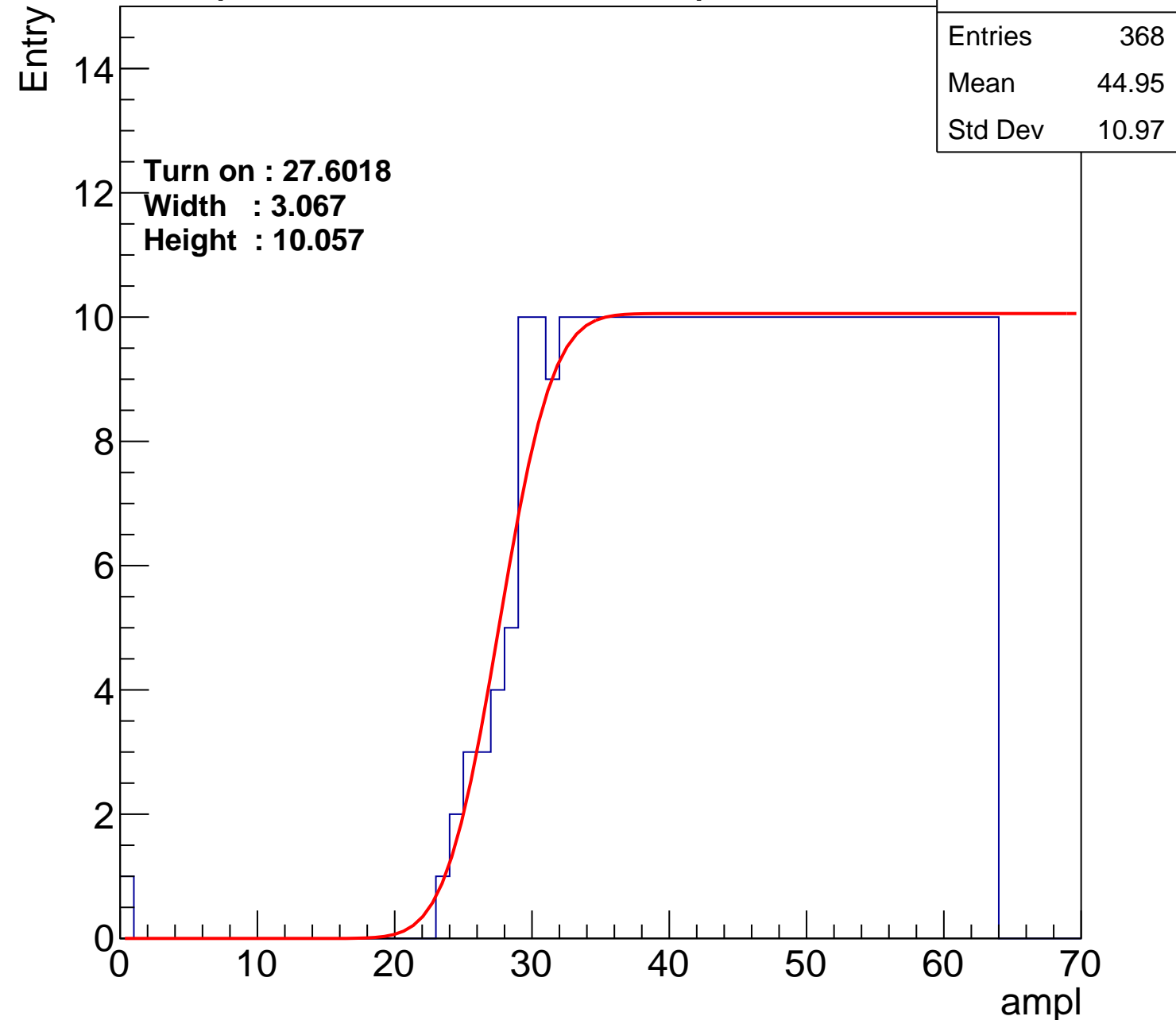
Width : 3.067

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch102

calib_packv5_042523_0143.root, FC#3, port B3

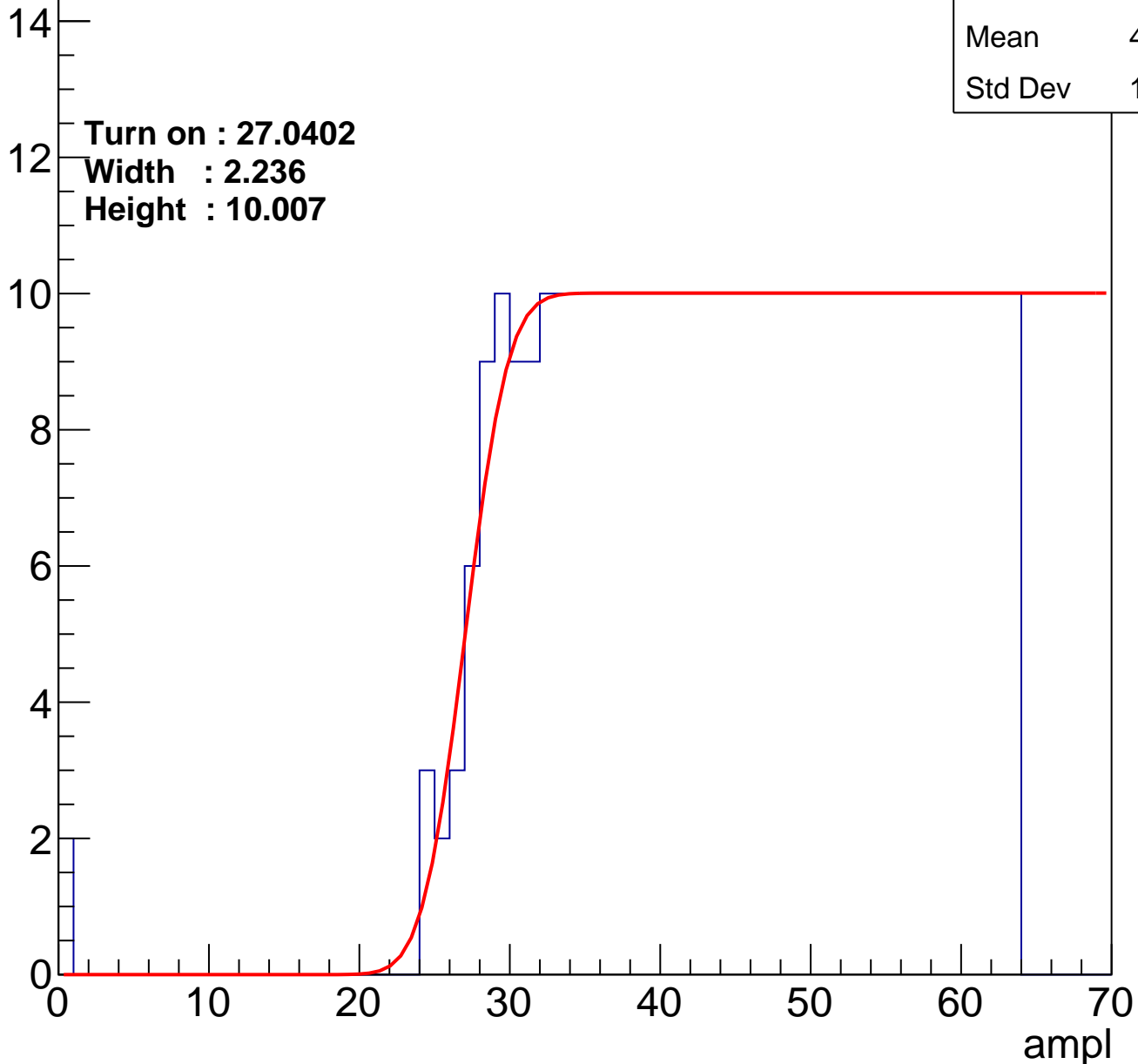
Entries	373
Mean	44.65
Std Dev	11.27

Turn on : 27.0402

Width : 2.236

Height : 10.007

Entry



B1L000S, U22-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.97
Std Dev	11.54

Turn on : 25.6955

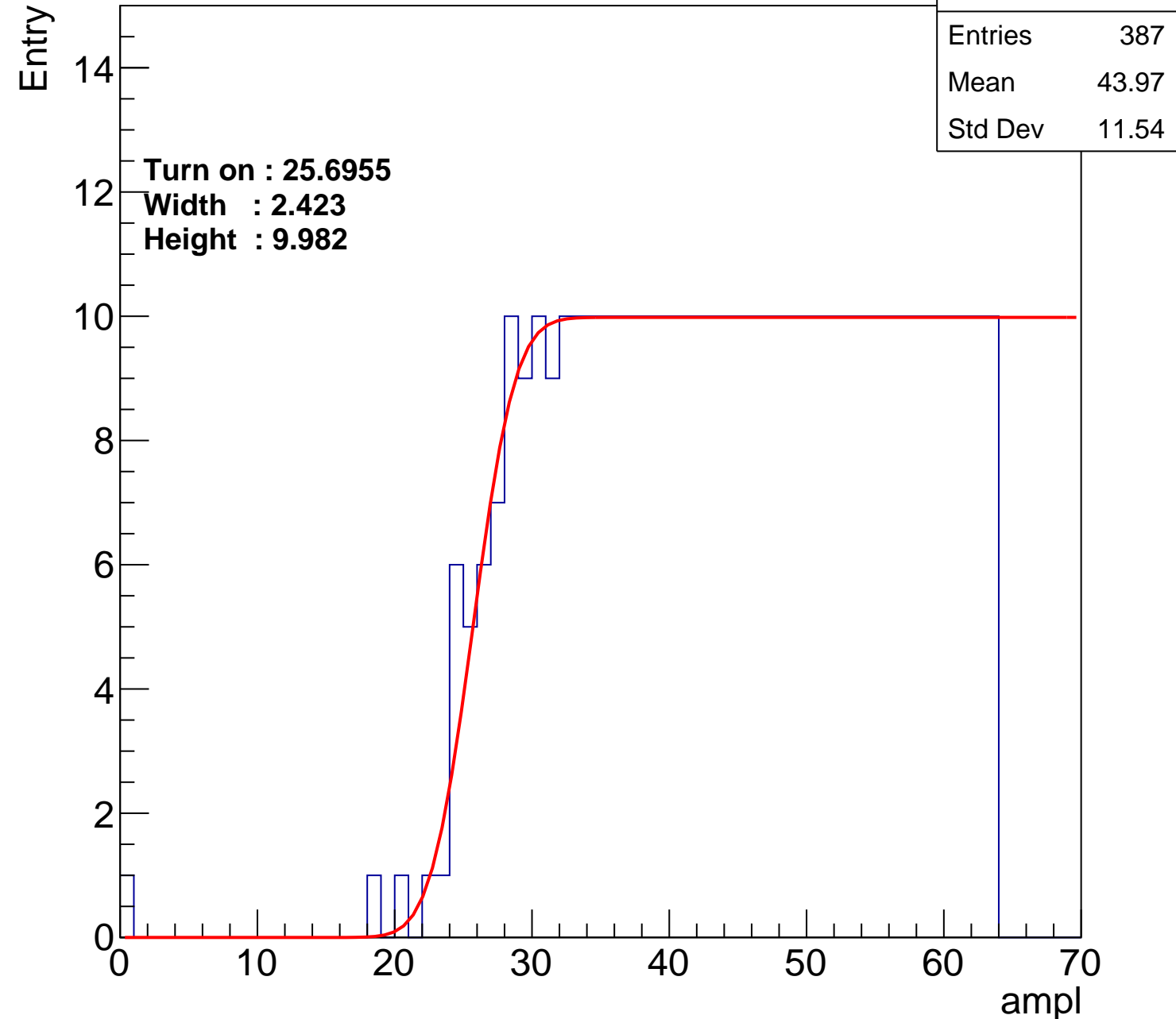
Width : 2.423

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch104

calib_packv5_042523_0143.root, FC#3, port B3

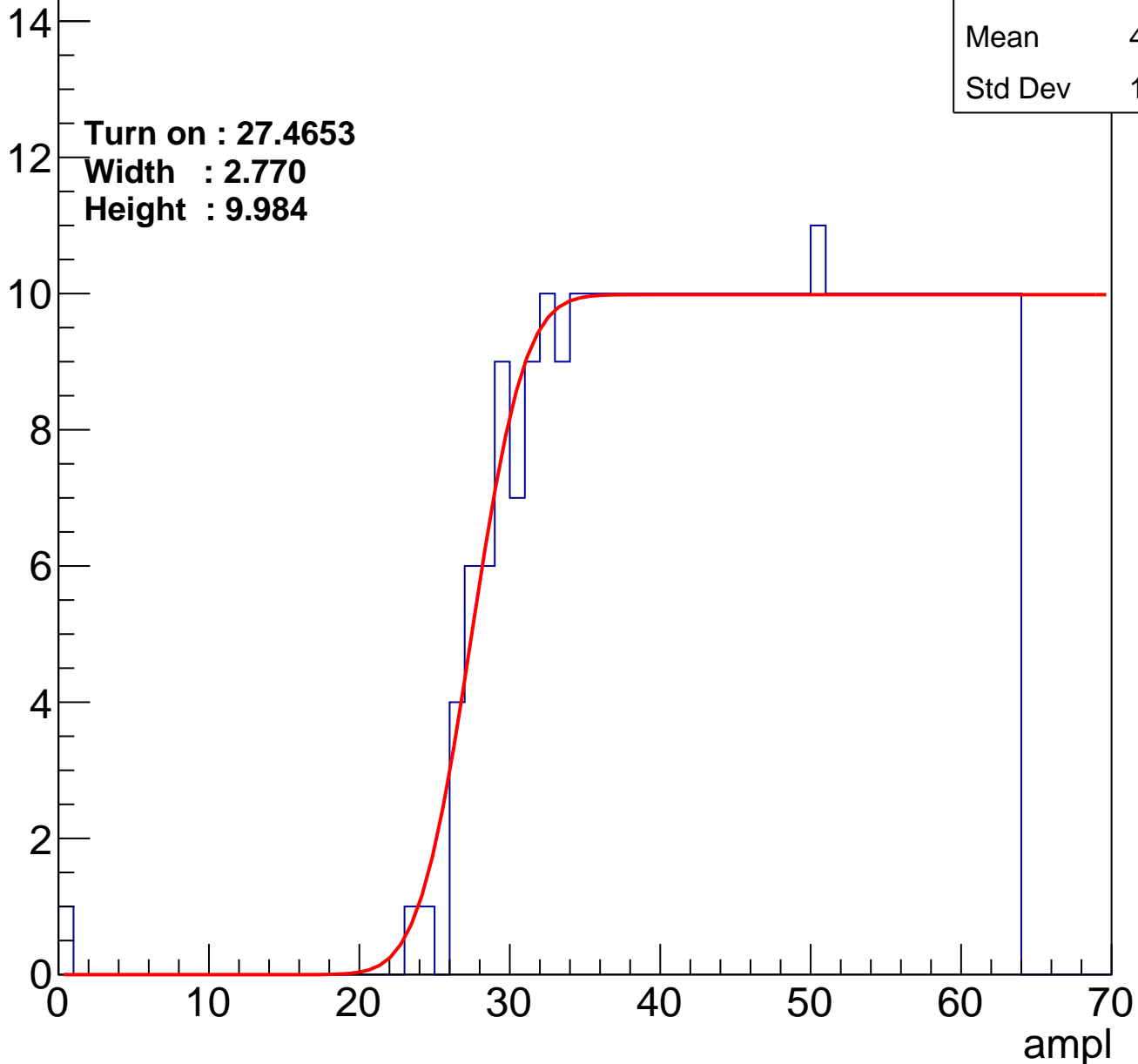
Entries	364
Mean	45.19
Std Dev	10.85

Turn on : 27.4653

Width : 2.770

Height : 9.984

Entry



B1L000S, U22-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.64
Std Dev	11.94

Turn on : 25.2221

Width : 0.584

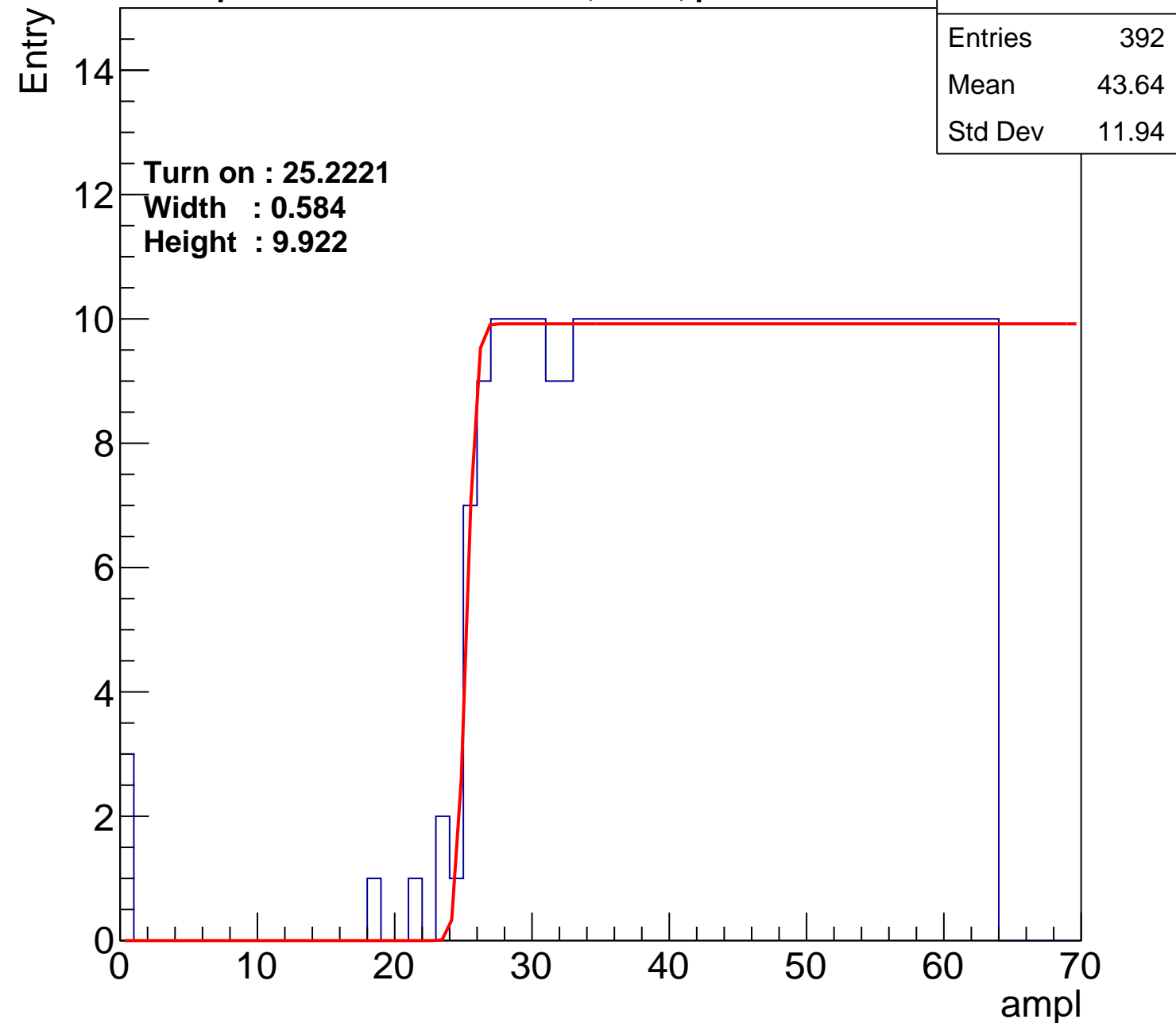
Height : 9.922

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U22-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.7
Std Dev	11.62

Turn on : 27.4698

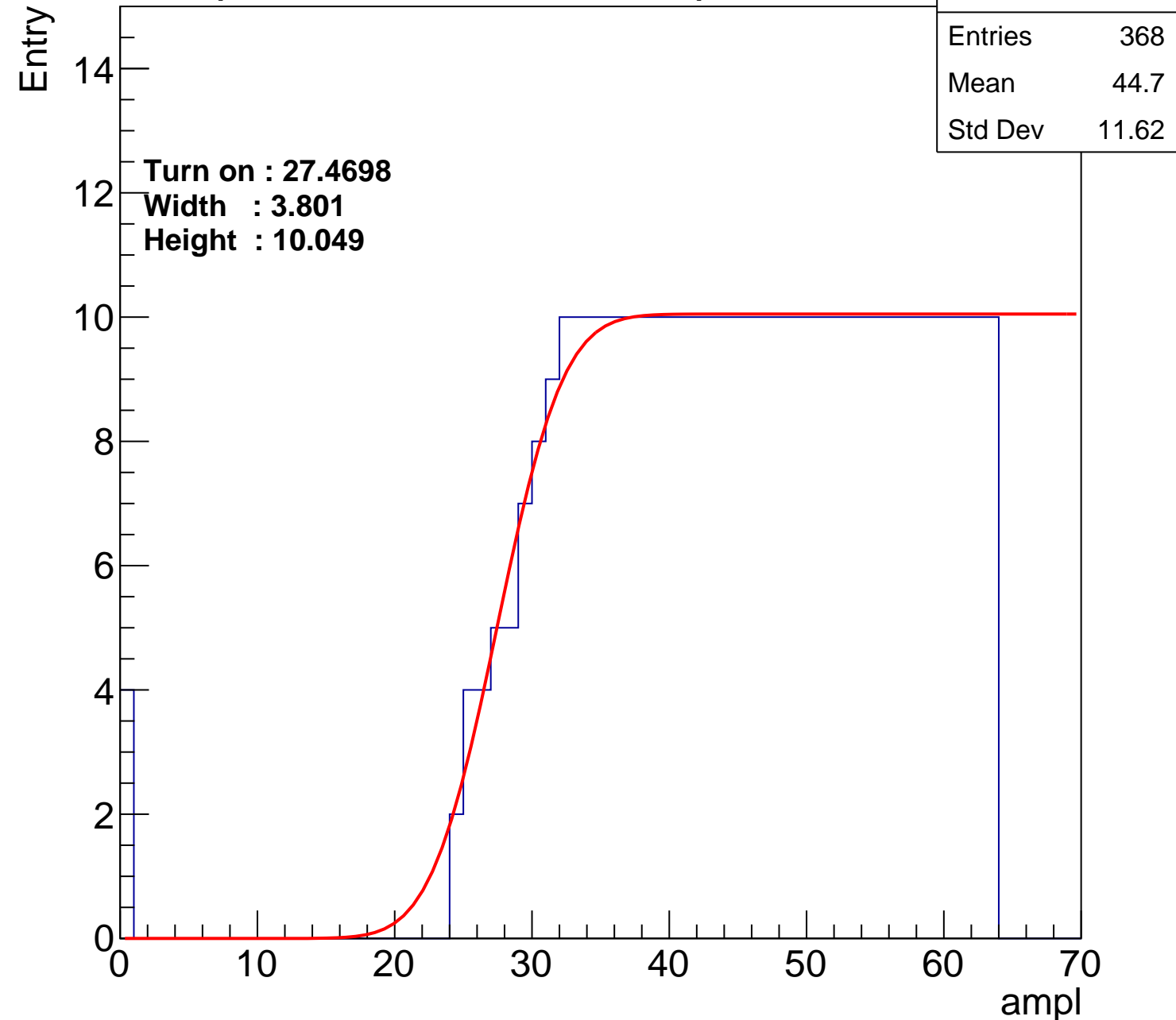
Width : 3.801

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch107

calib_packv5_042523_0143.root, FC#3, port B3

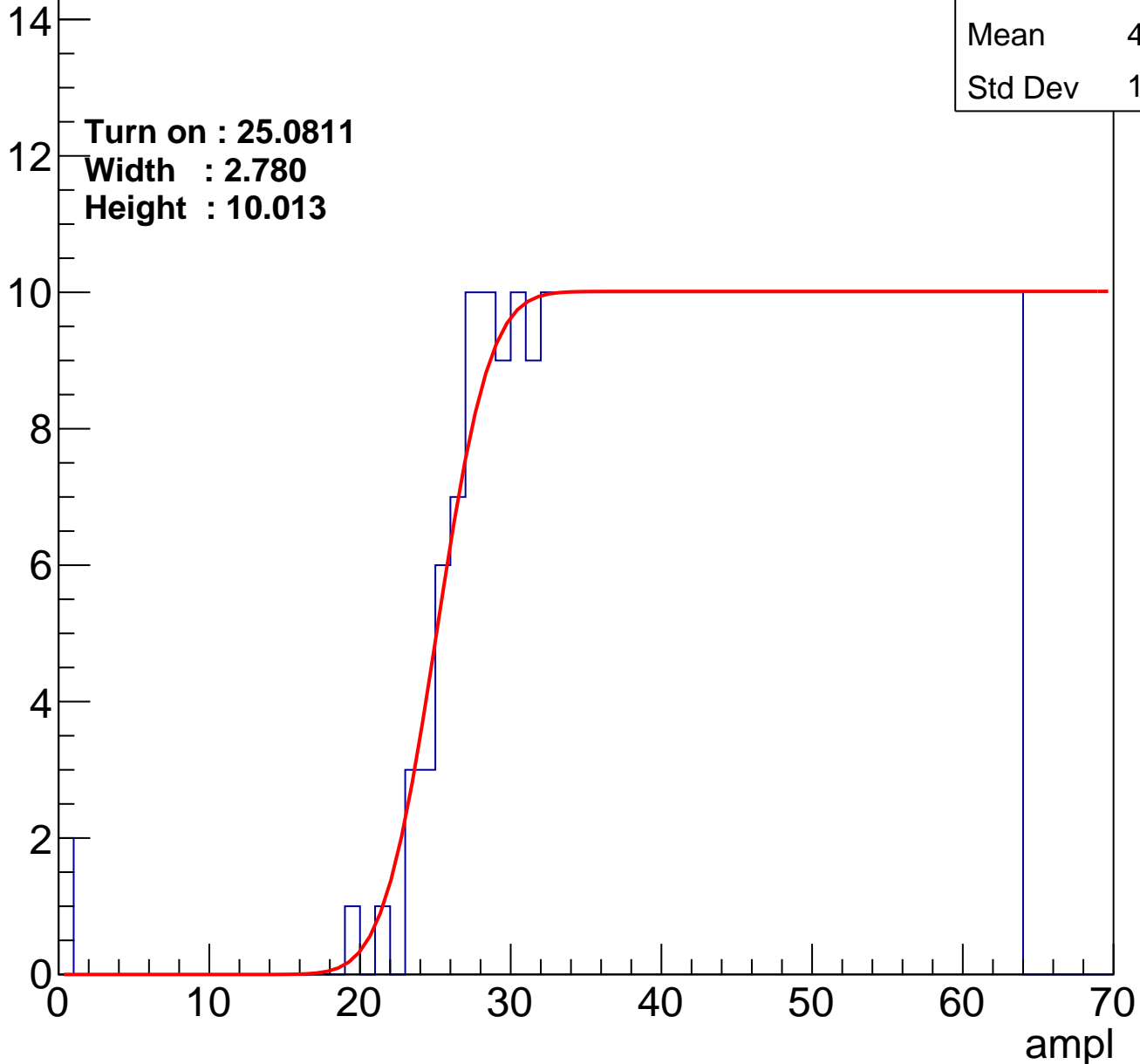
Entries	391
Mean	43.74
Std Dev	11.76

Turn on : 25.0811

Width : 2.780

Height : 10.013

Entry



B1L000S, U22-ch108

calib_packv5_042523_0143.root, FC#3, port B3

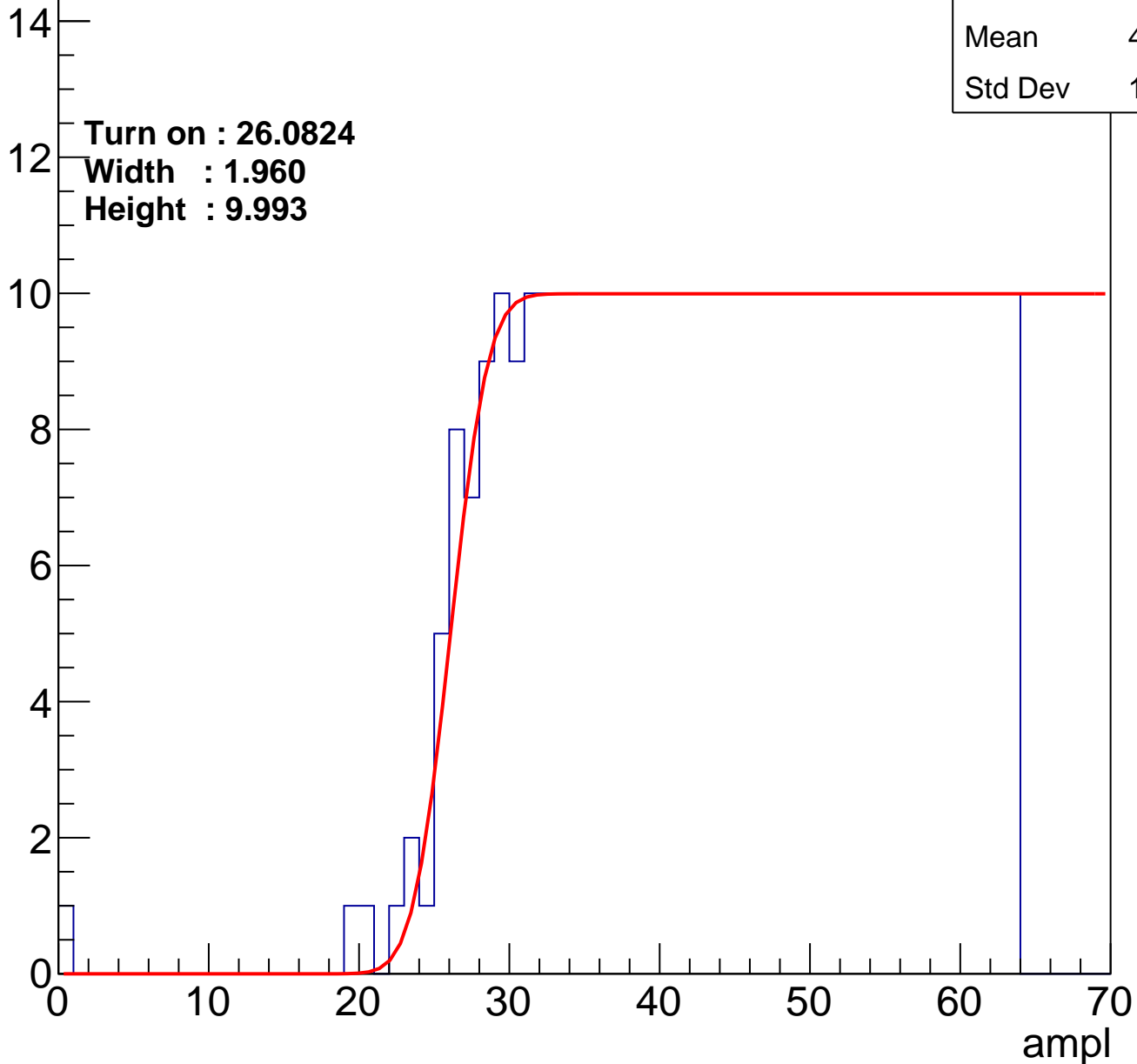
Entries	385
Mean	44.09
Std Dev	11.46

Turn on : 26.0824

Width : 1.960

Height : 9.993

Entry



B1L000S, U22-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.17
Std Dev	11.43

Turn on : 25.7282

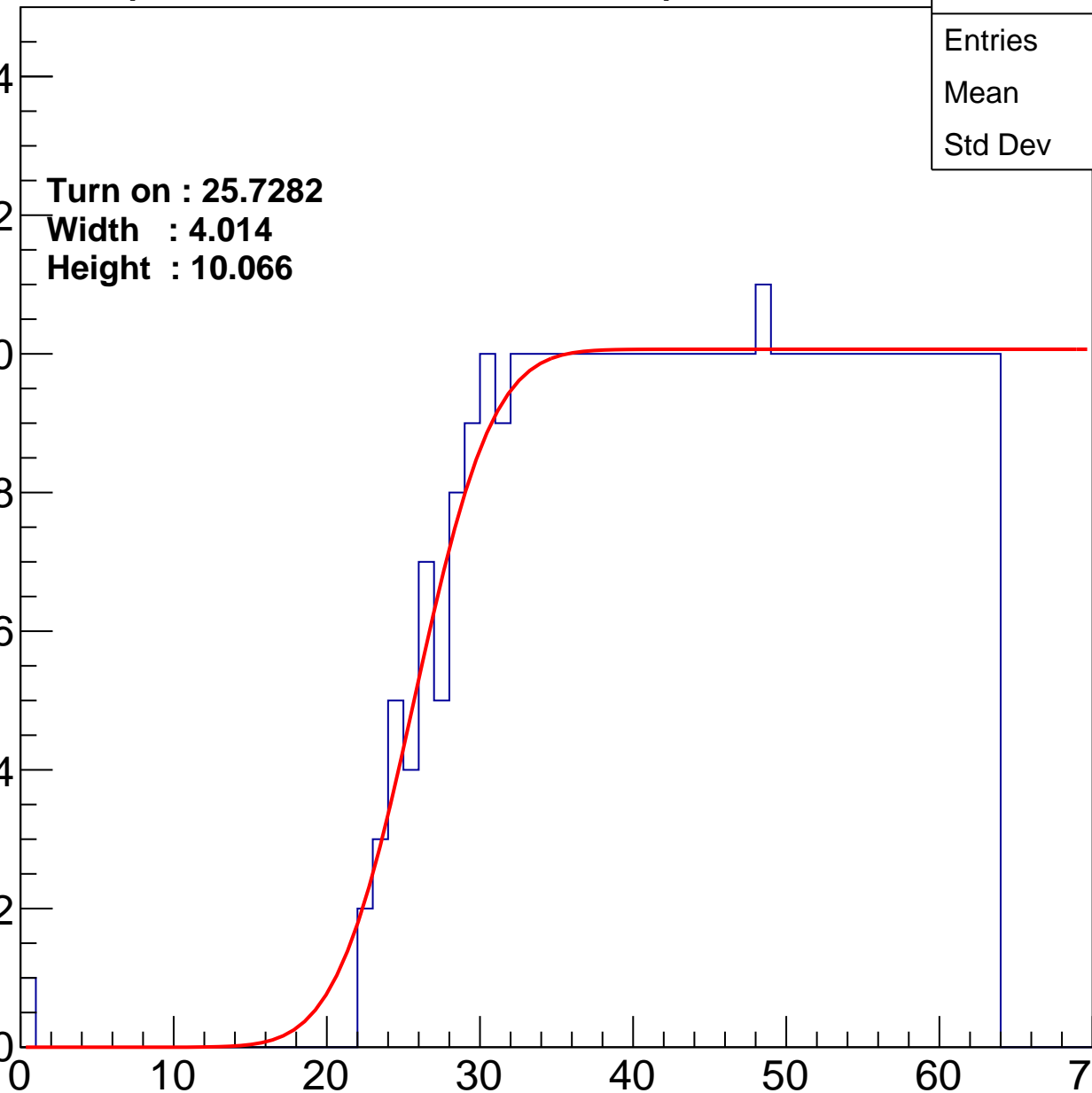
Width : 4.014

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.37
Std Dev	12.05

Turn on : 24.0925

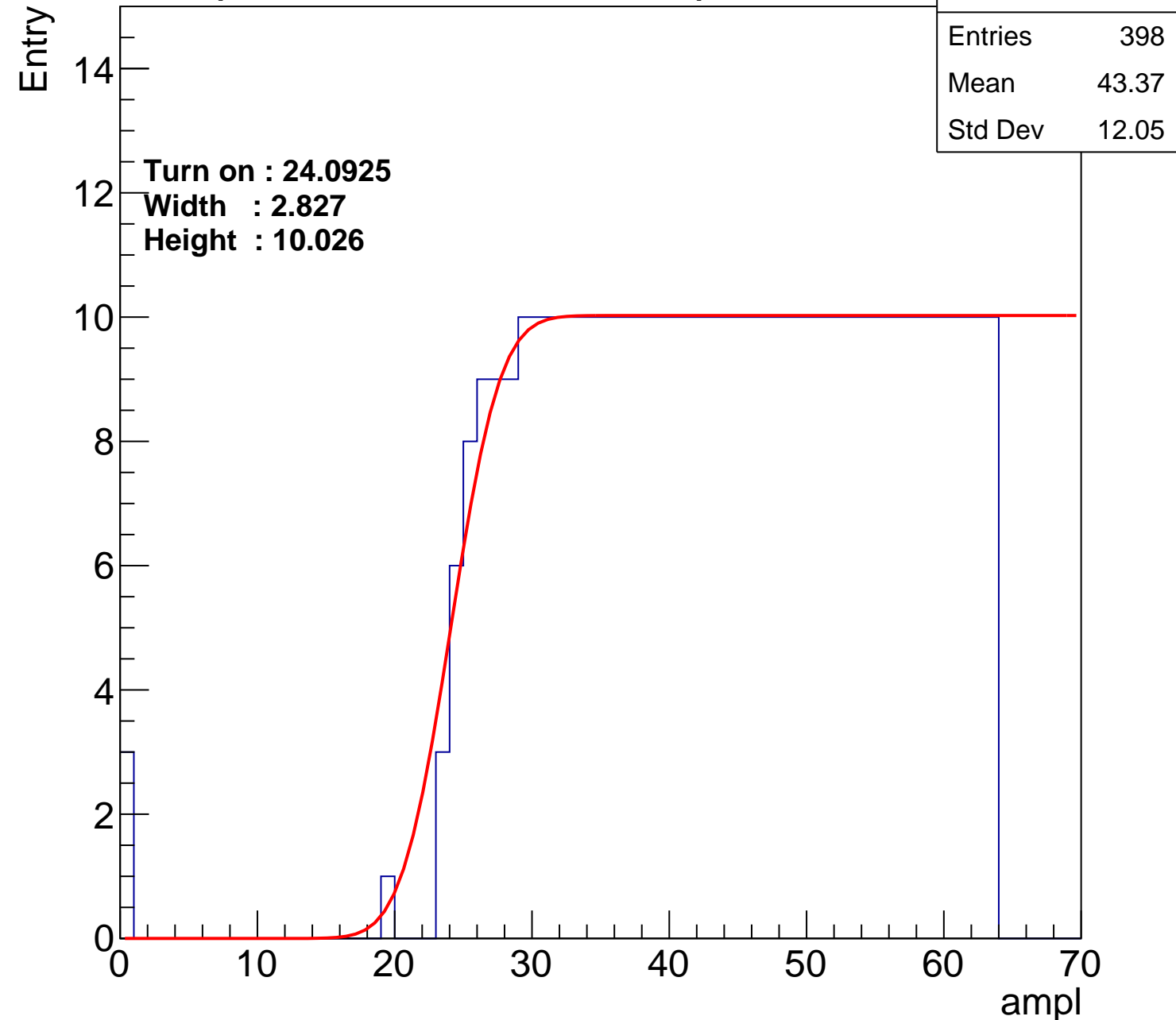
Width : 2.827

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch111

calib_packv5_042523_0143.root, FC#3, port B3

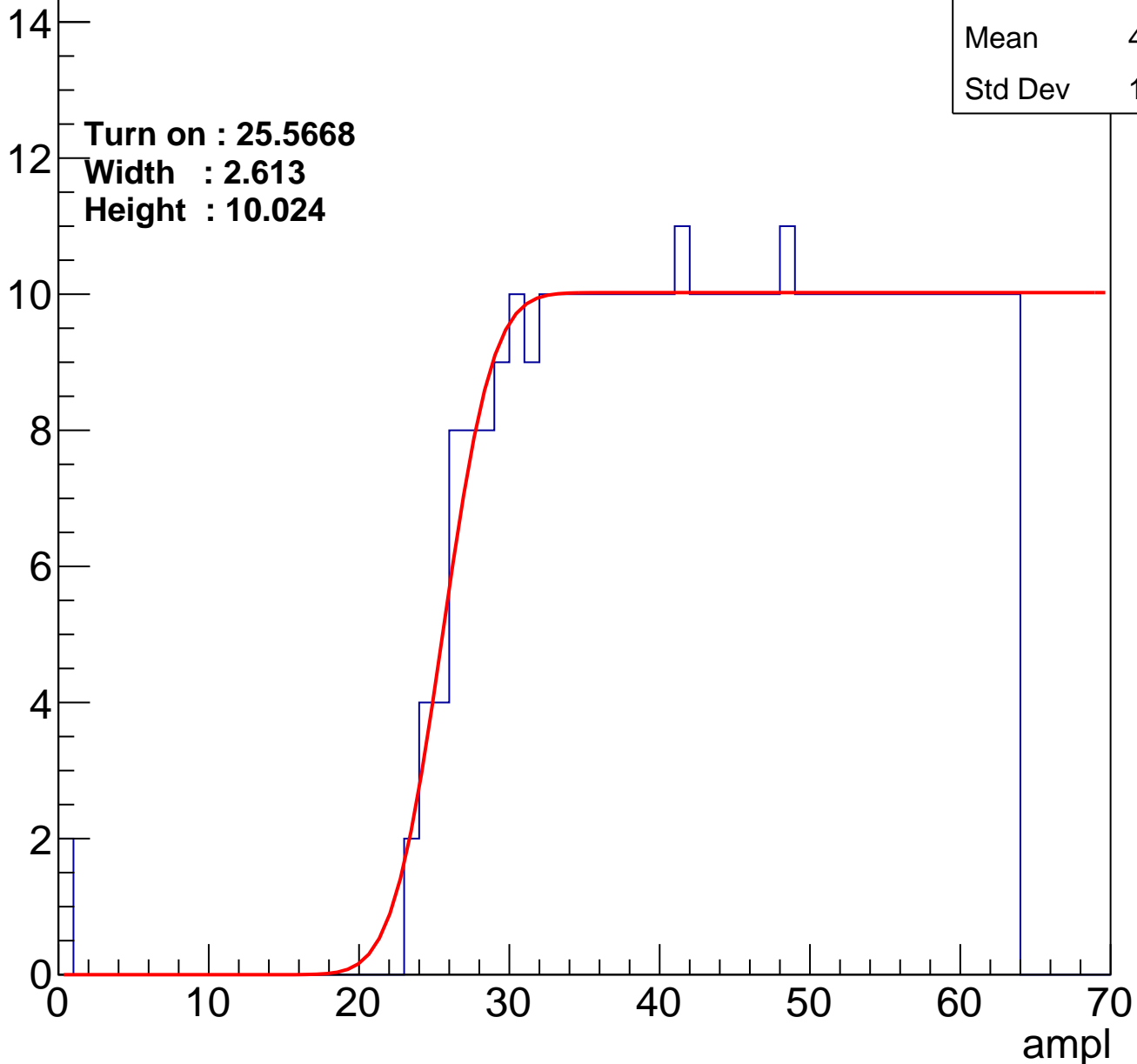
Entries	386
Mean	44.09
Std Dev	11.55

Turn on : 25.5668

Width : 2.613

Height : 10.024

Entry



B1L000S, U22-ch112

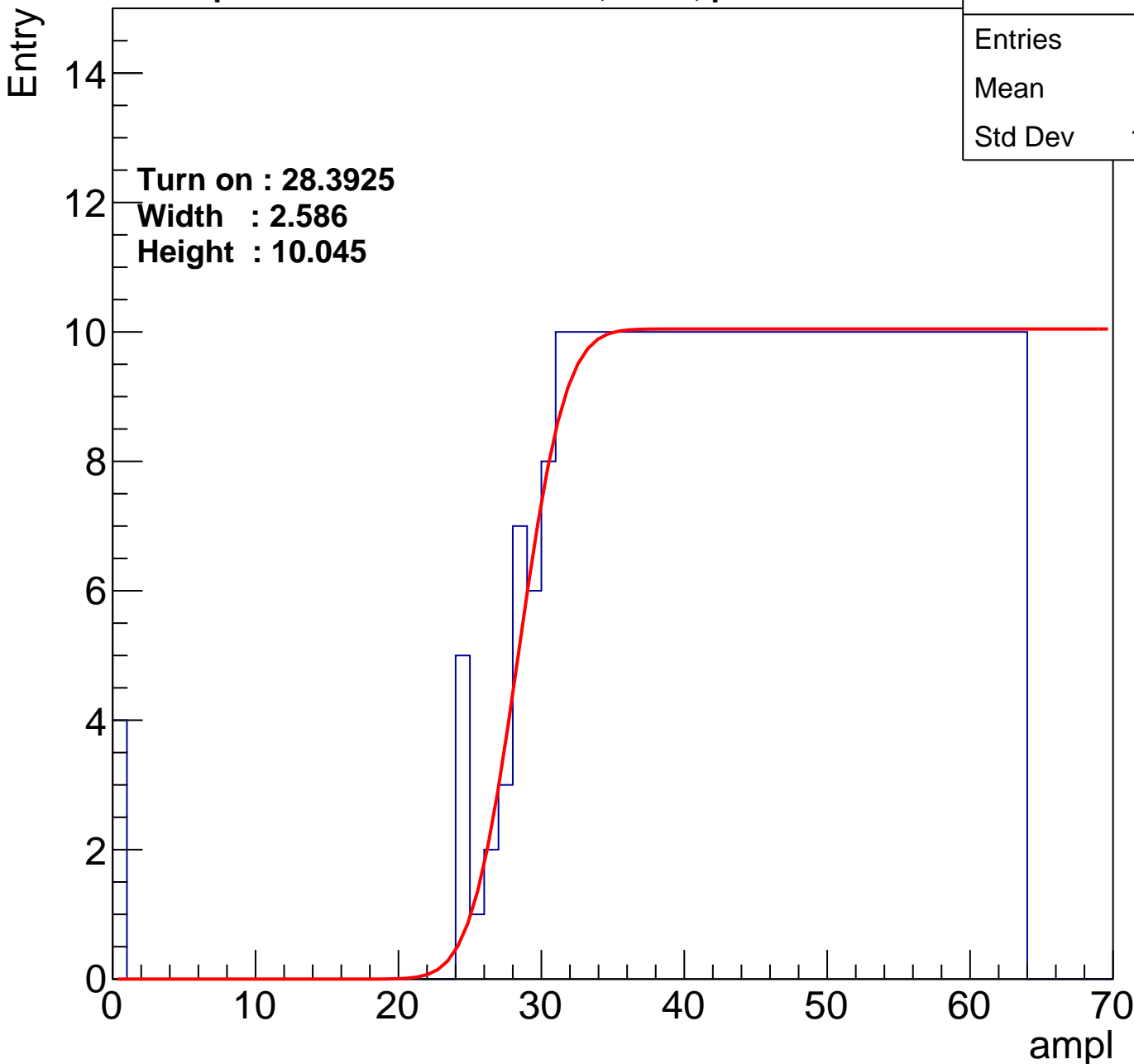
calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.8
Std Dev	11.57

Turn on : 28.3925

Width : 2.586

Height : 10.045



B1L000S, U22-ch113

calib_packv5_042523_0143.root, FC#3, port B3

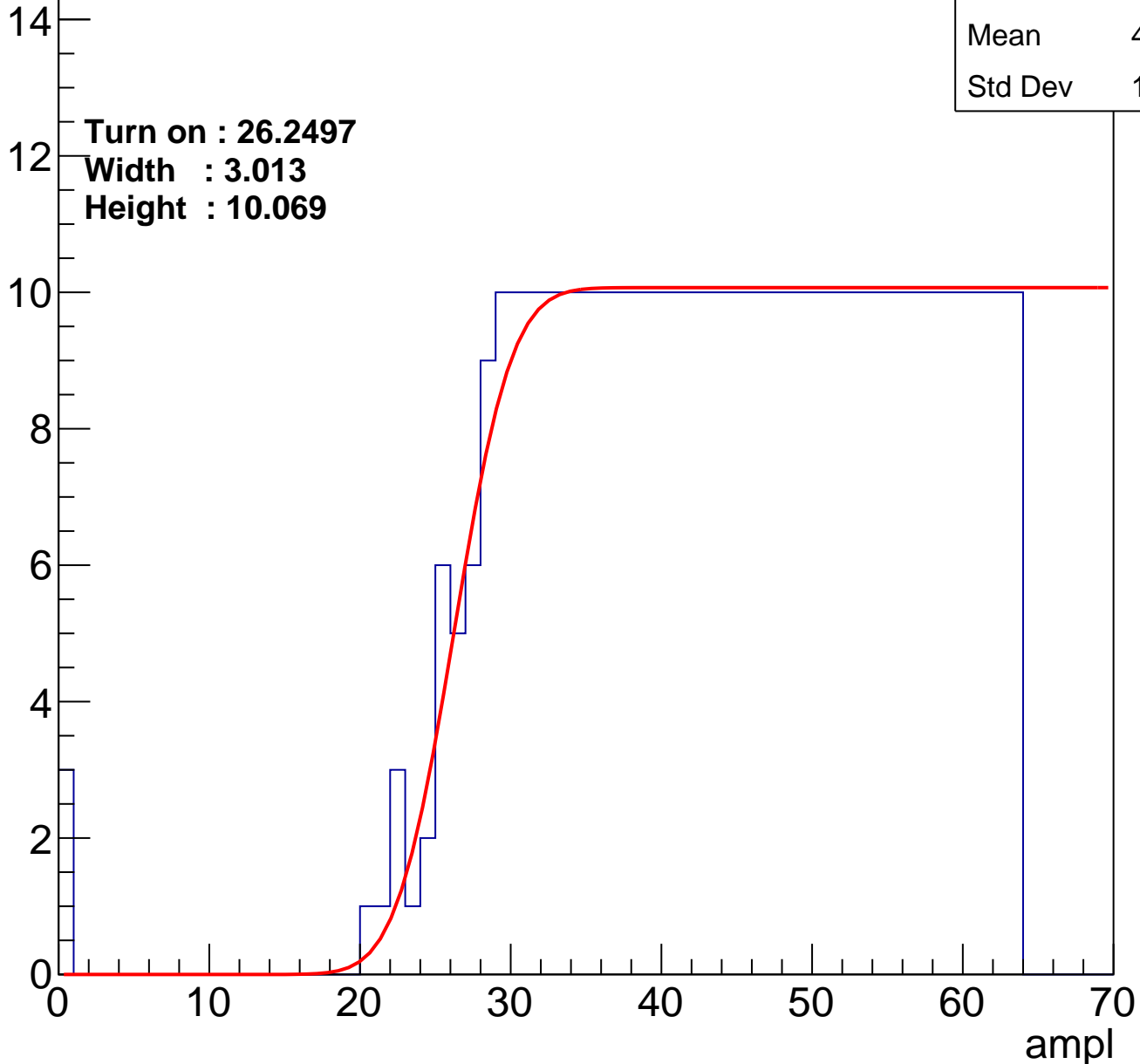
Entries	387
Mean	43.86
Std Dev	11.87

Turn on : 26.2497

Width : 3.013

Height : 10.069

Entry



B1L000S, U22-ch114

calib_packv5_042523_0143.root, FC#3, port B3

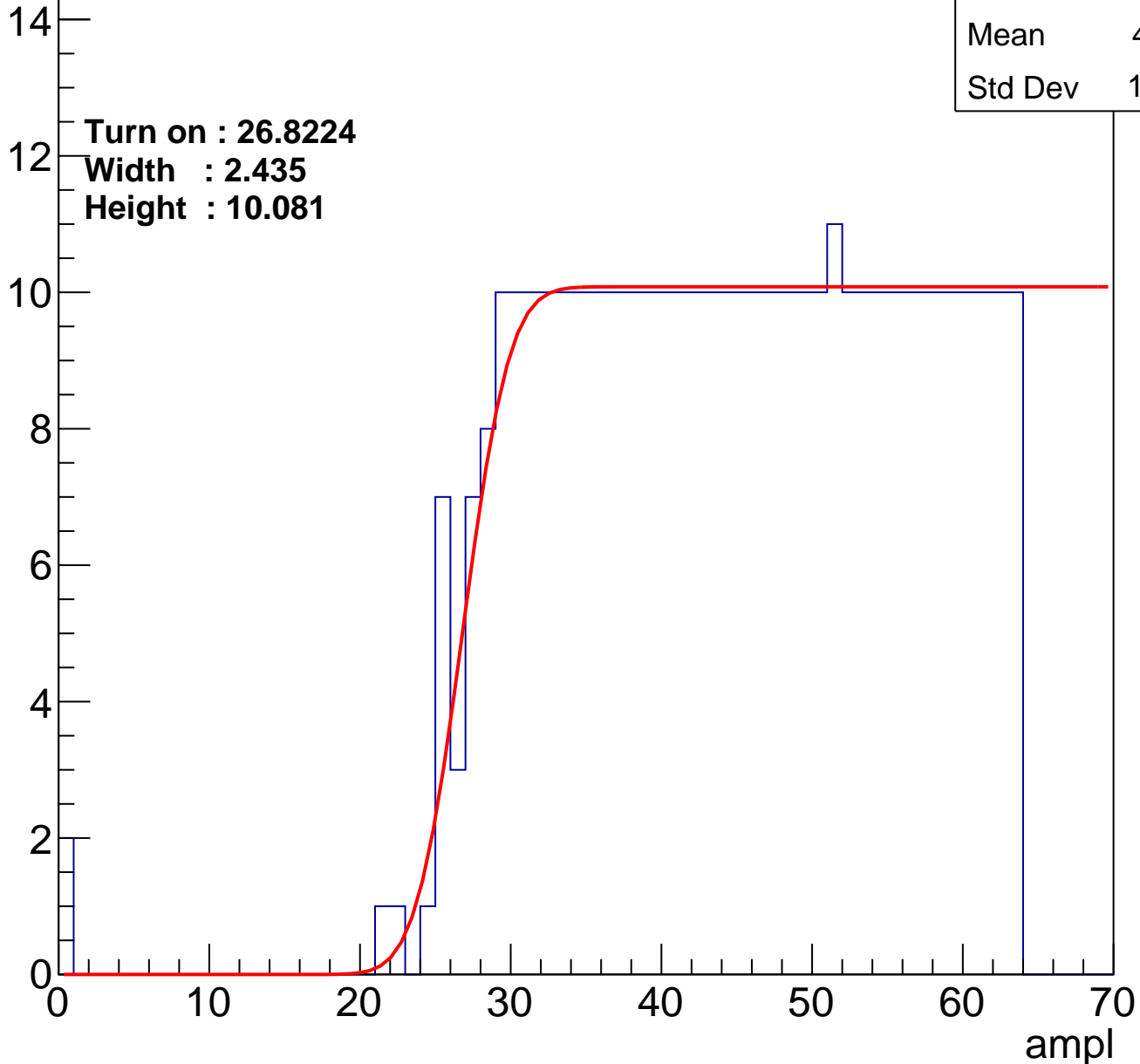
Entries	381
Mean	44.31
Std Dev	11.45

Turn on : 26.8224

Width : 2.435

Height : 10.081

Entry



B1L000S, U22-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.26
Std Dev	10.78

Turn on : 28.2194

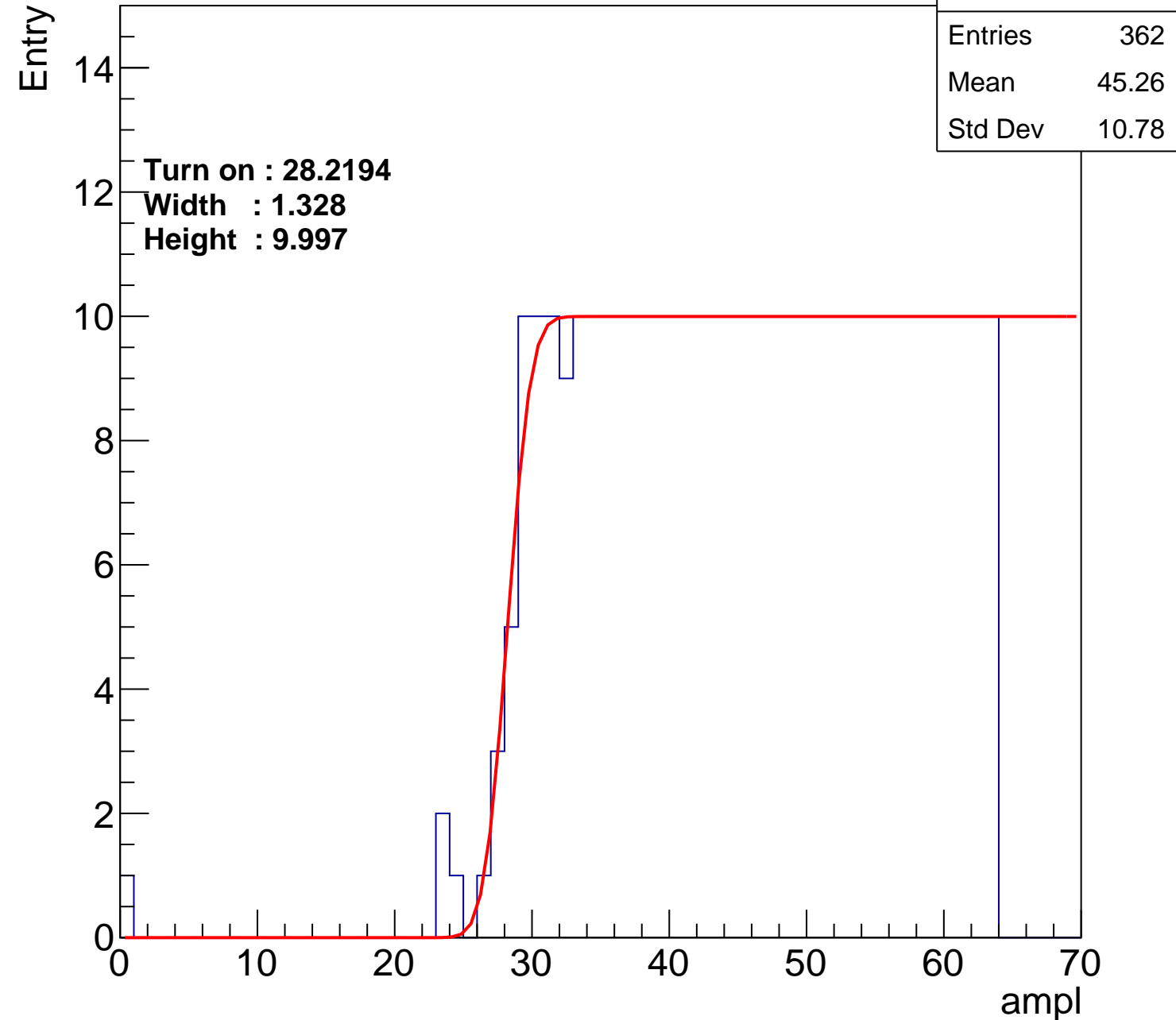
Width : 1.328

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch116

calib_packv5_042523_0143.root, FC#3, port B3

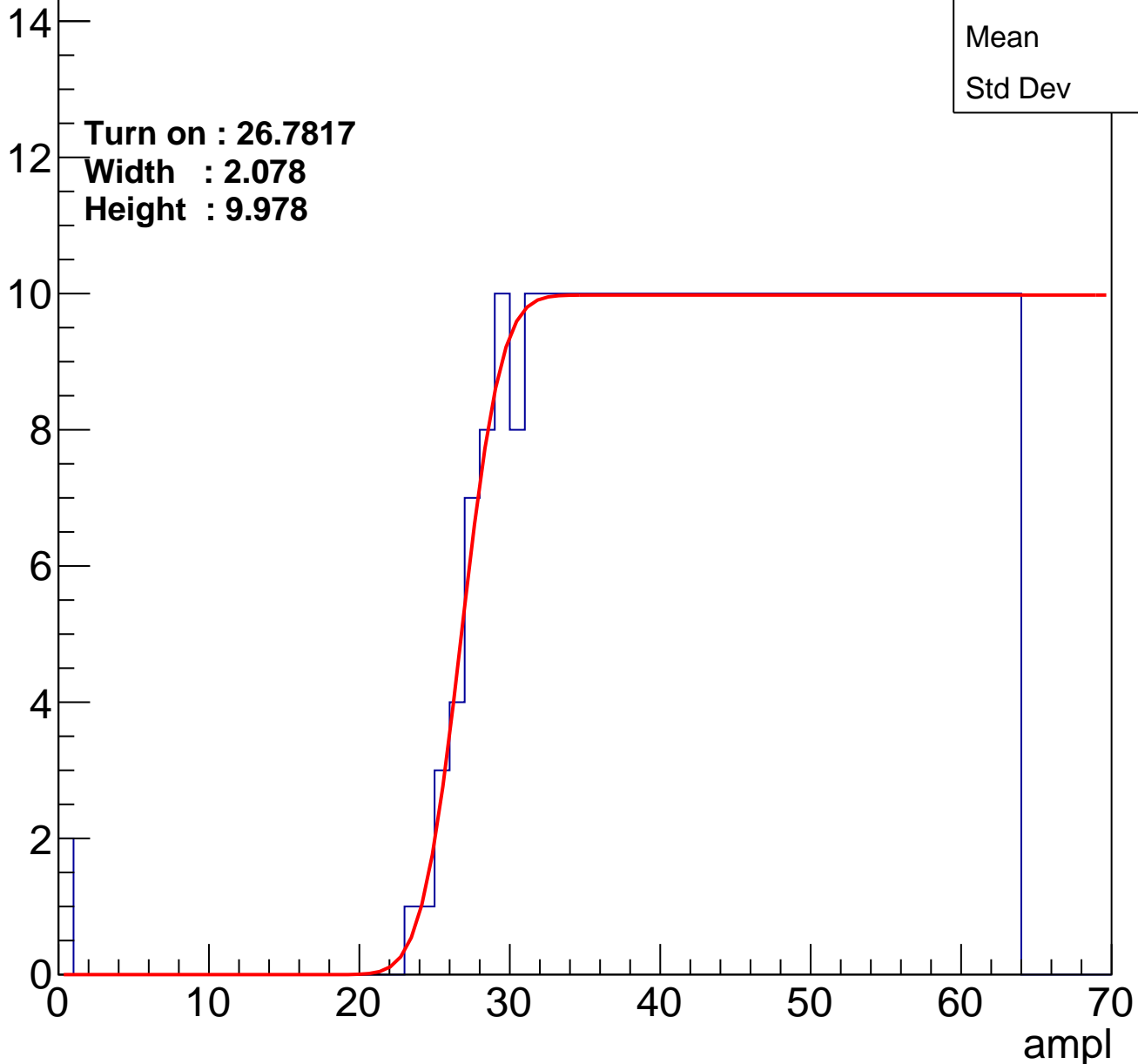
Entries	374
Mean	44.6
Std Dev	11.3

Turn on : 26.7817

Width : 2.078

Height : 9.978

Entry



B1L000S, U22-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.77
Std Dev	11.28

Turn on : 27.0716

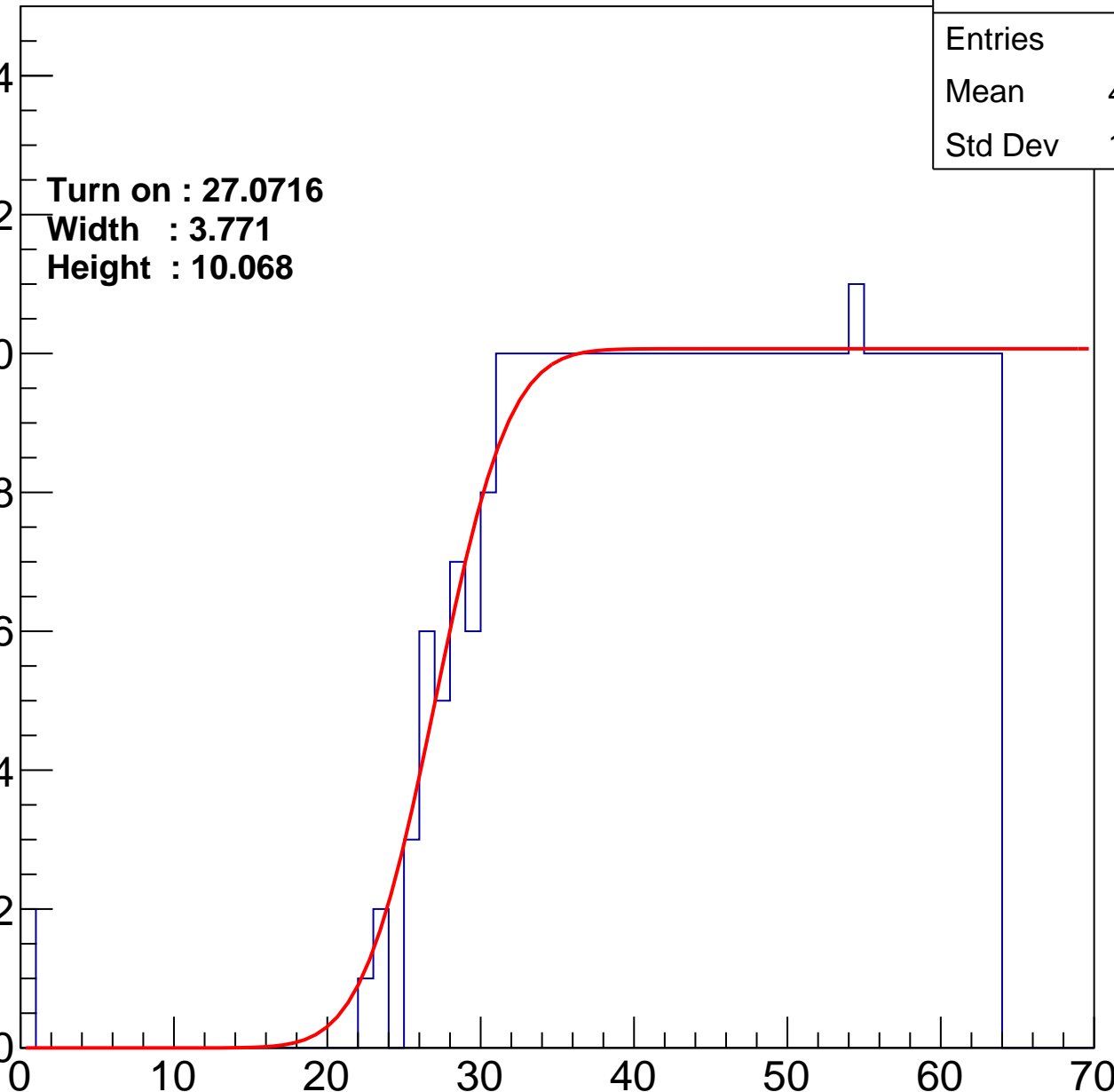
Width : 3.771

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	359
Mean	45.31
Std Dev	10.96

Turn on : 28.4760

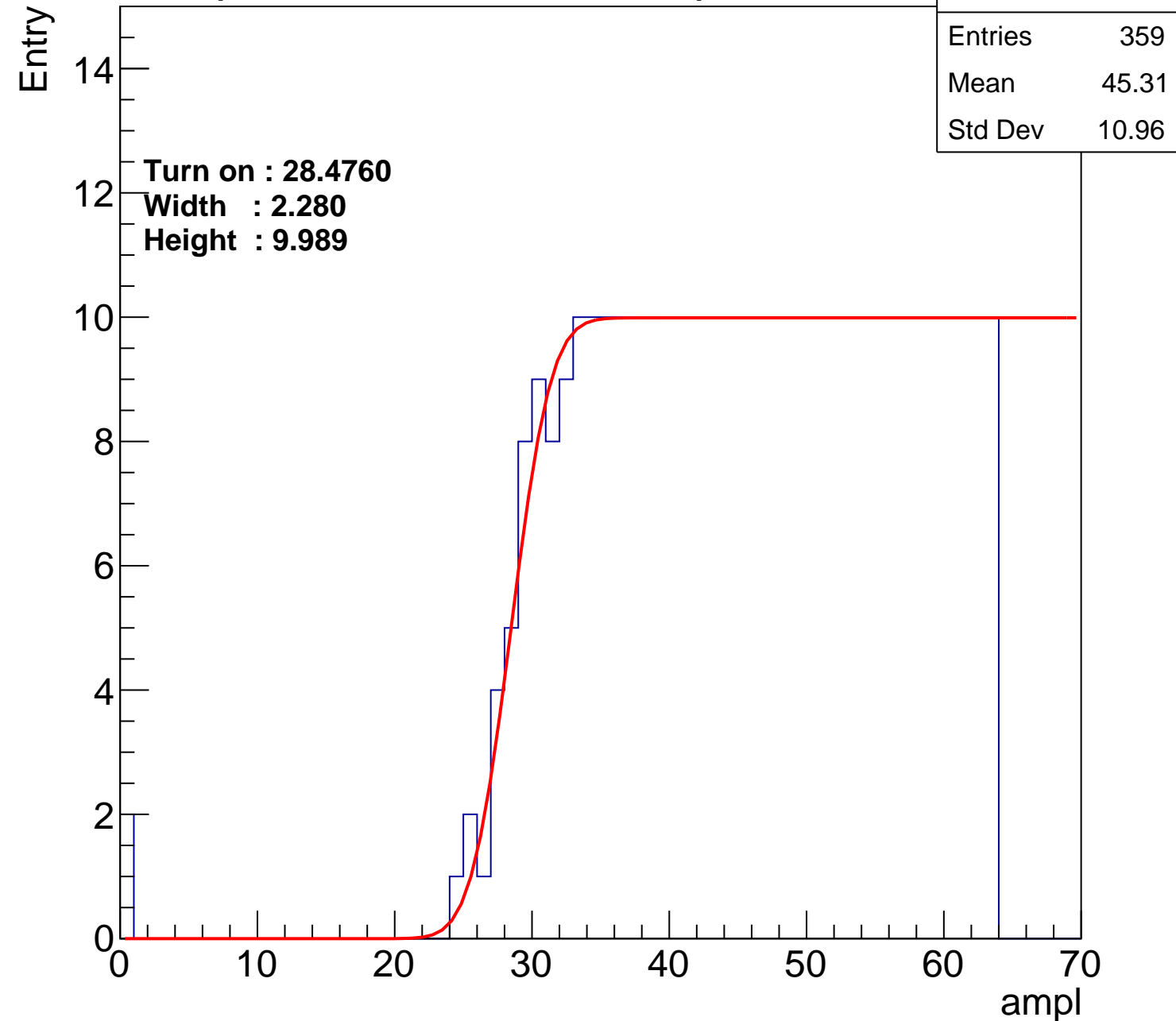
Width : 2.280

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.77
Std Dev	11.09

Turn on : 27.0141

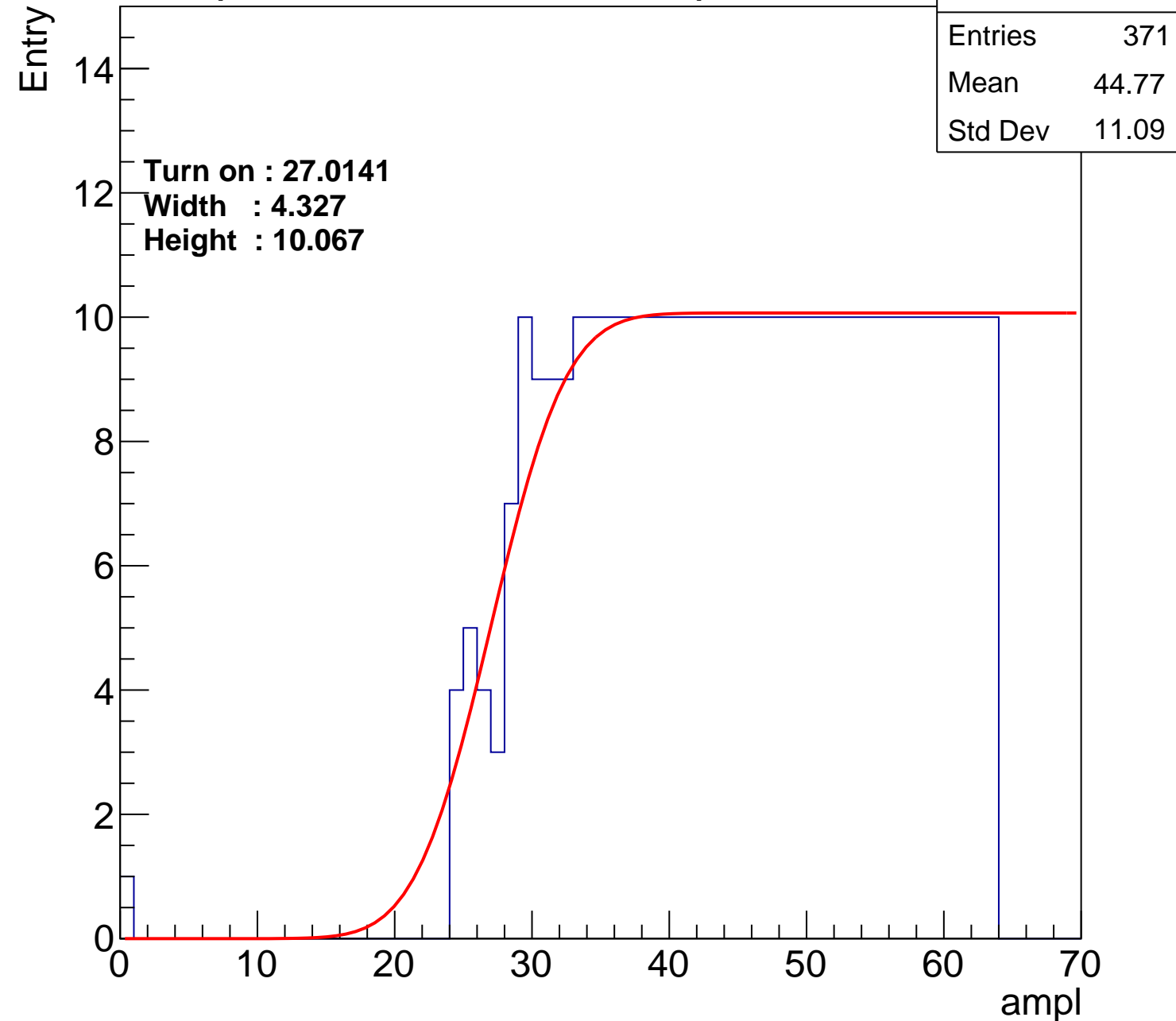
Width : 4.327

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.94
Std Dev	11.83

Turn on : 25.6099

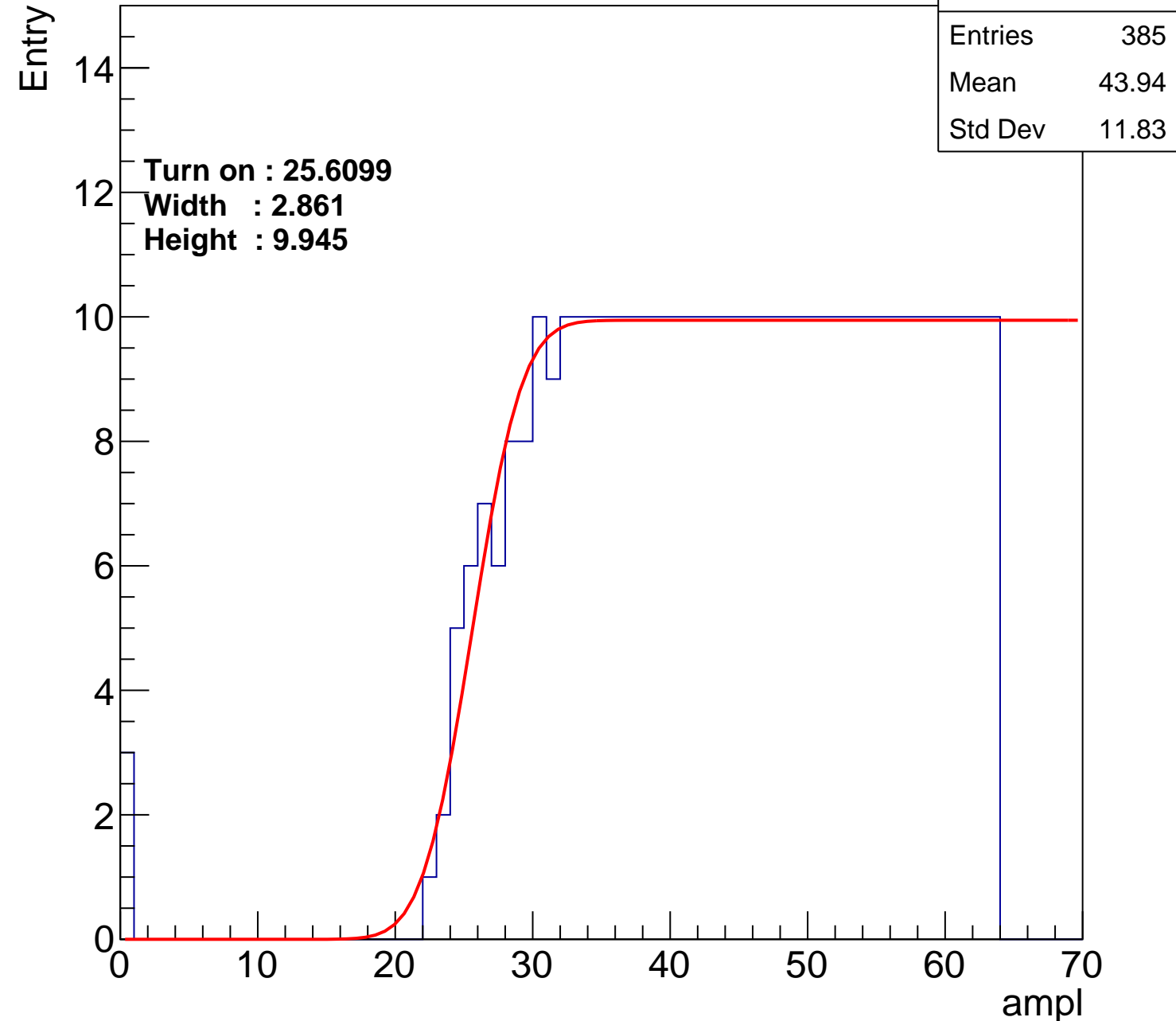
Width : 2.861

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.57
Std Dev	12

Turn on : 25.1102

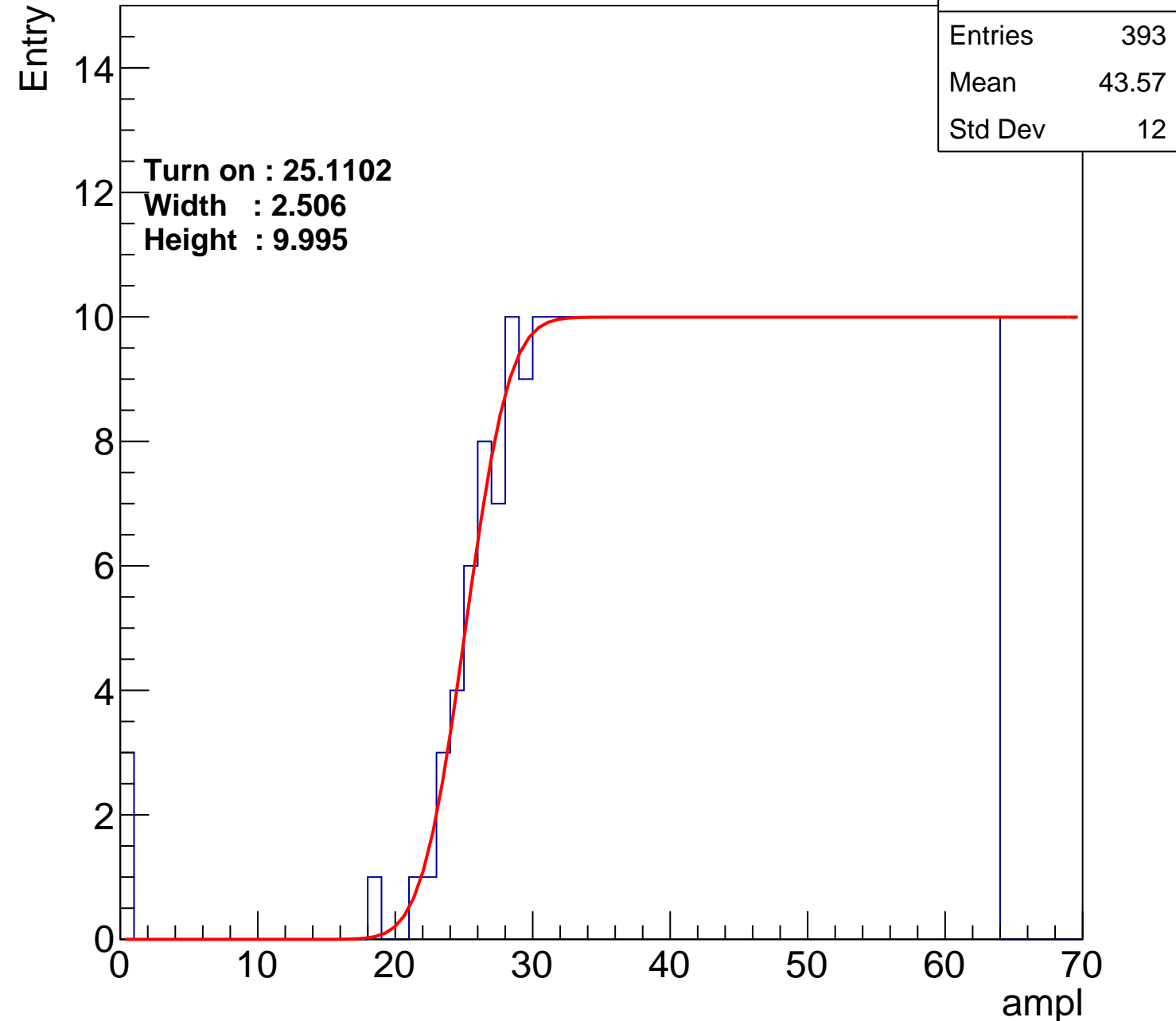
Width : 2.506

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch122

calib_packv5_042523_0143.root, FC#3, port B3

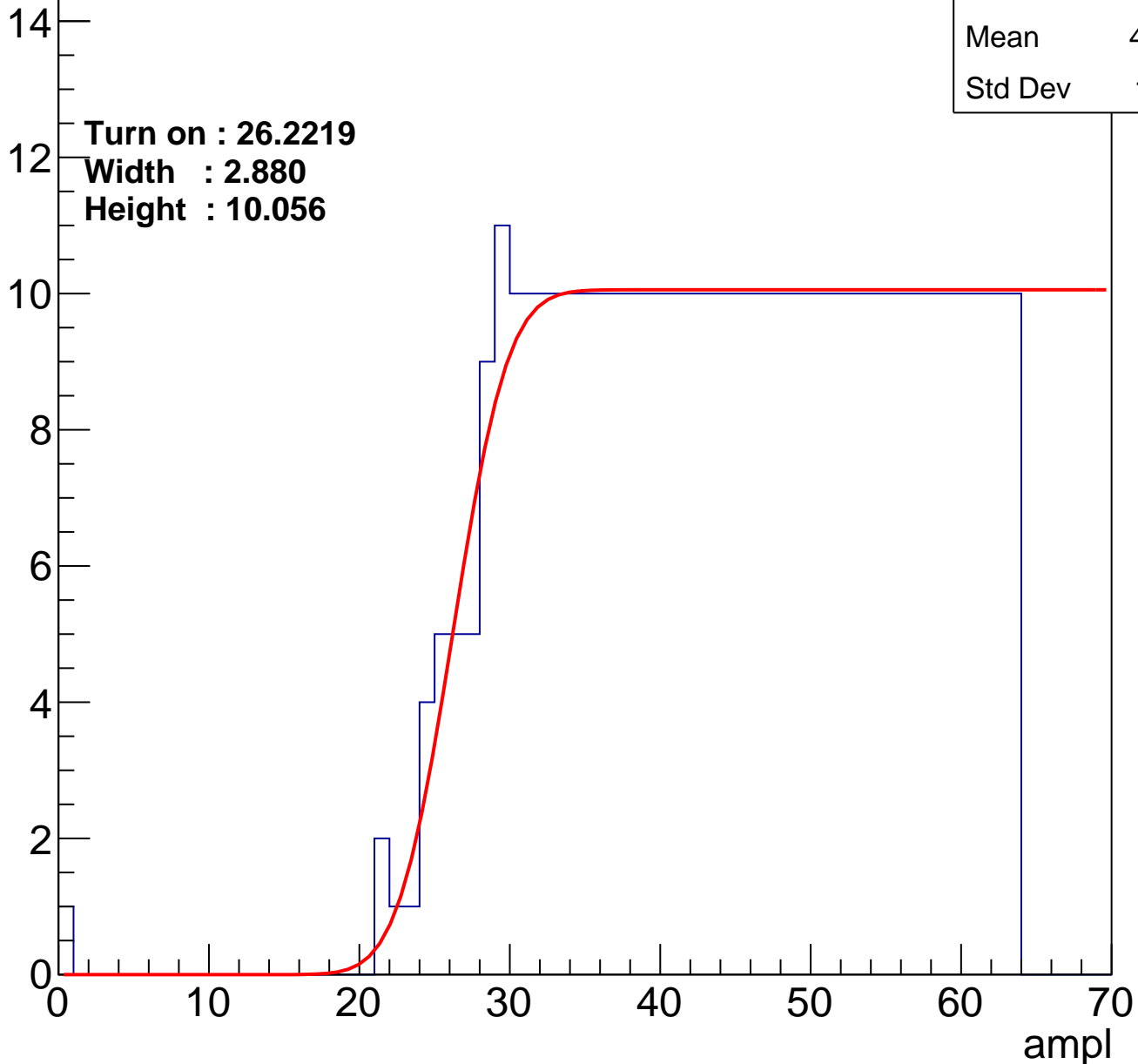
Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 26.2219

Width : 2.880

Height : 10.056

Entry



B1L000S, U22-ch123

calib_packv5_042523_0143.root, FC#3, port B3

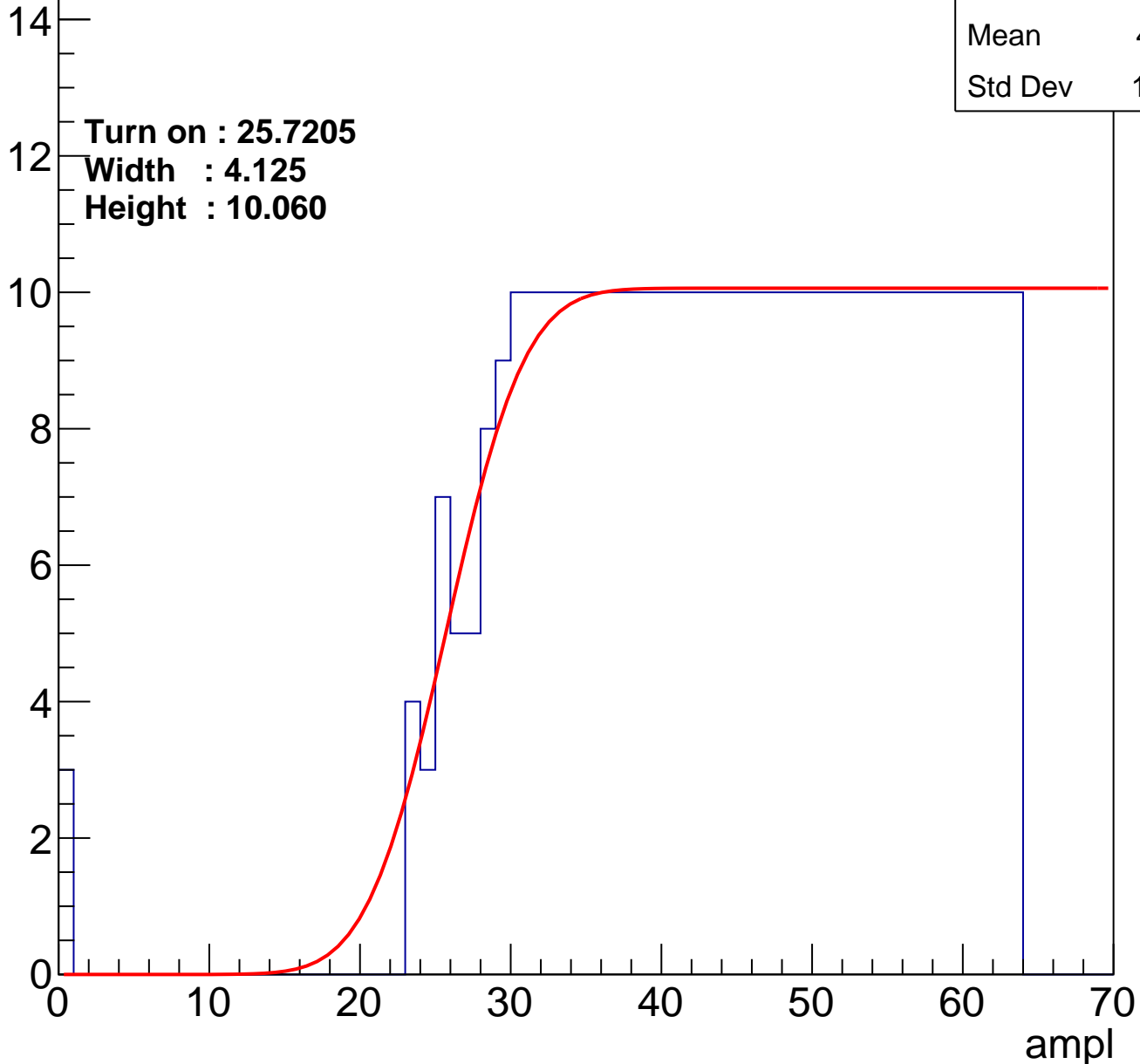
Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 25.7205

Width : 4.125

Height : 10.060

Entry



B1L000S, U22-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.81
Std Dev	11.94

Turn on : 25.8422

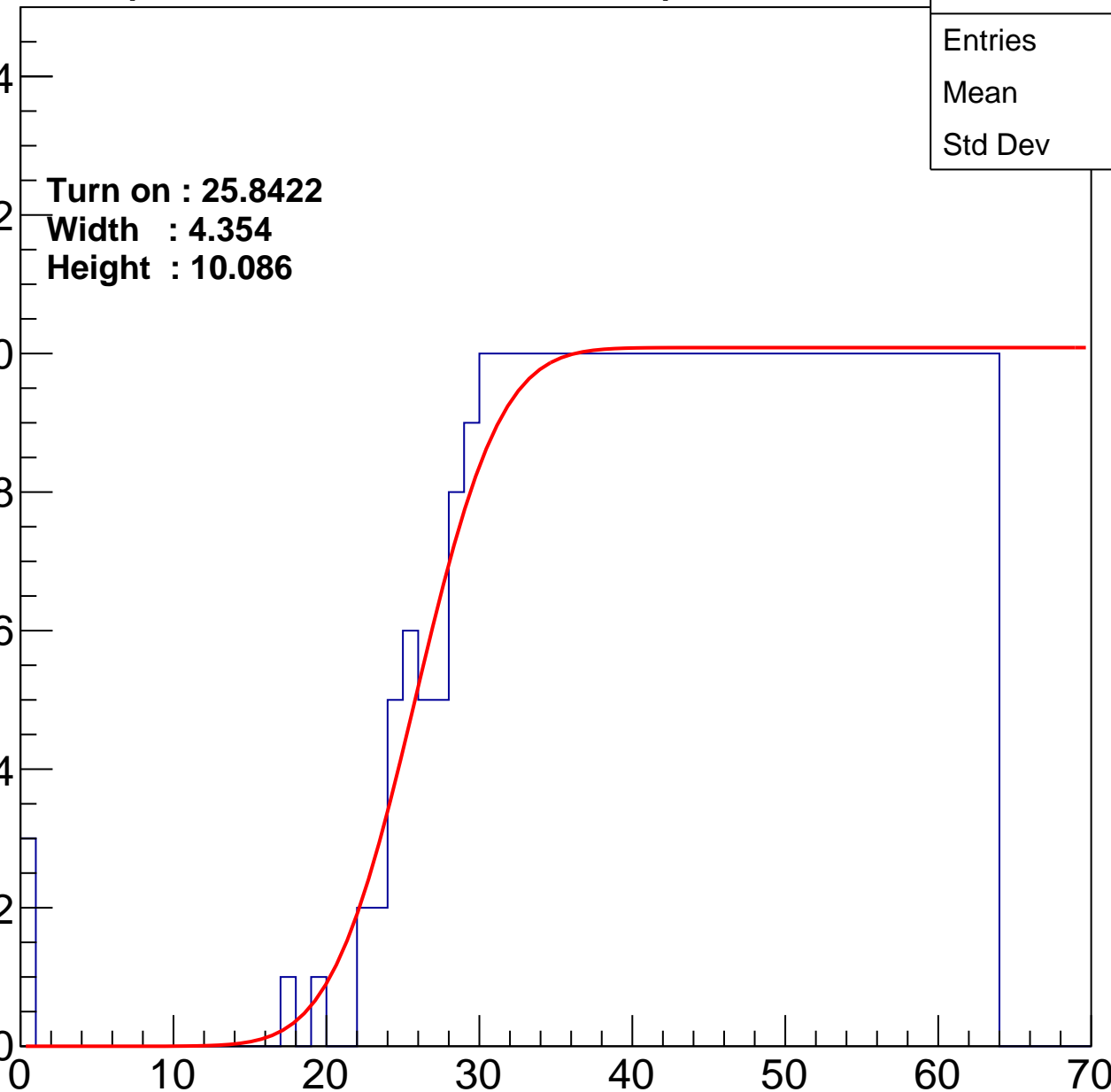
Width : 4.354

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch125

calib_packv5_042523_0143.root, FC#3, port B3

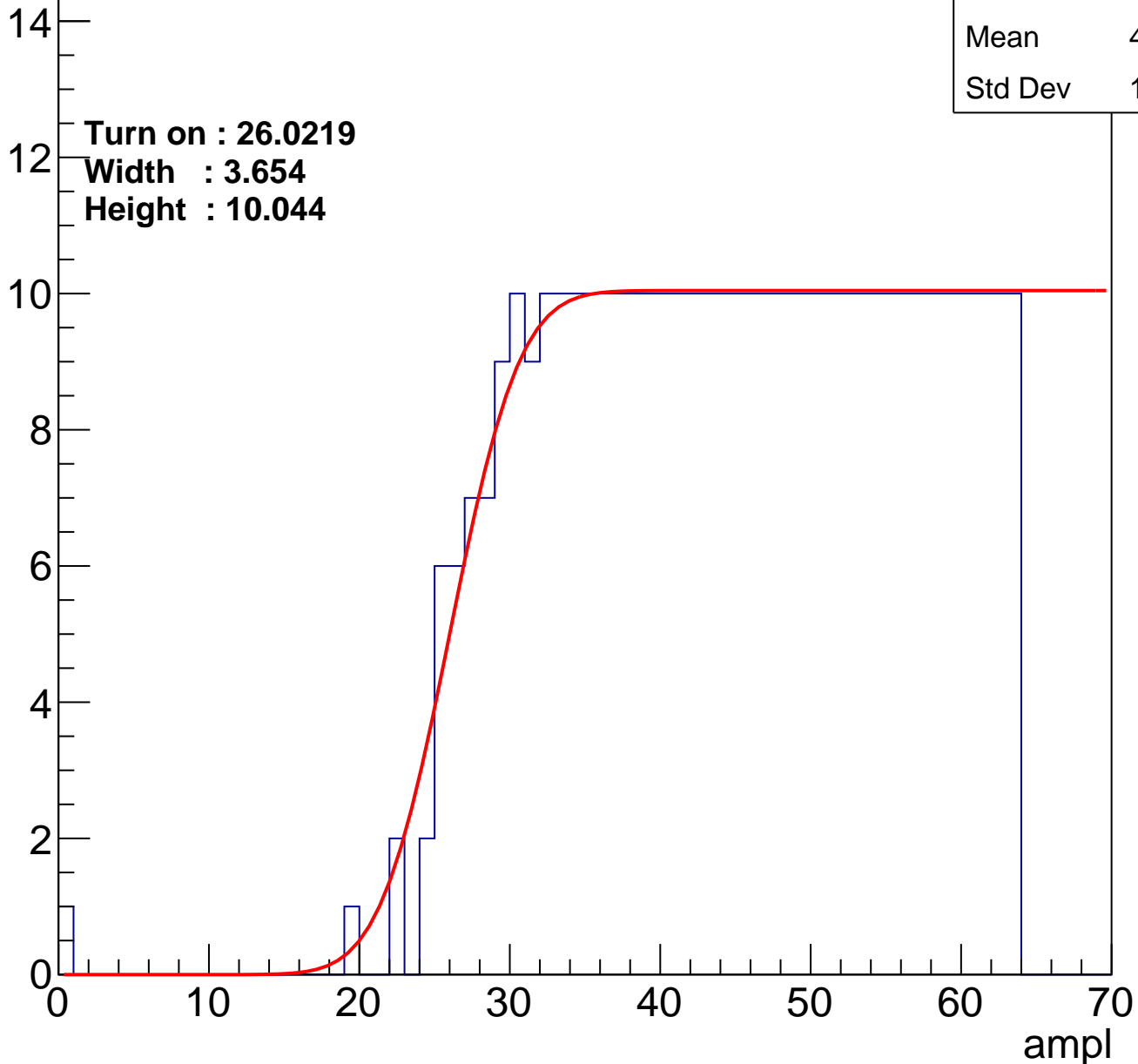
Entries	380
Mean	44.32
Std Dev	11.35

Turn on : 26.0219

Width : 3.654

Height : 10.044

Entry



B1L000S, U22-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.49
Std Dev	11.91

Turn on : 25.0159

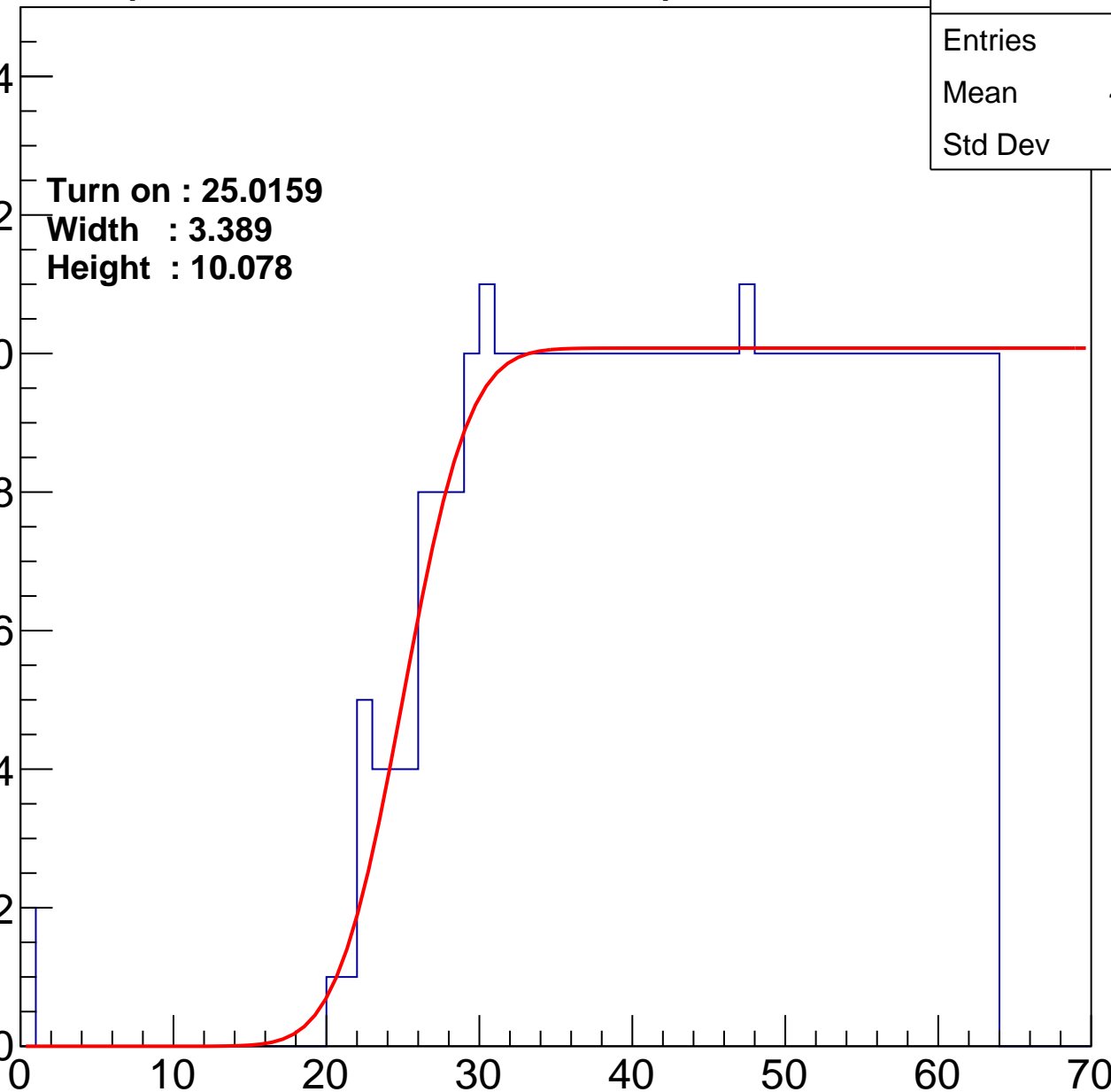
Width : 3.389

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U22-ch127

calib_packv5_042523_0143.root, FC#3, port B3

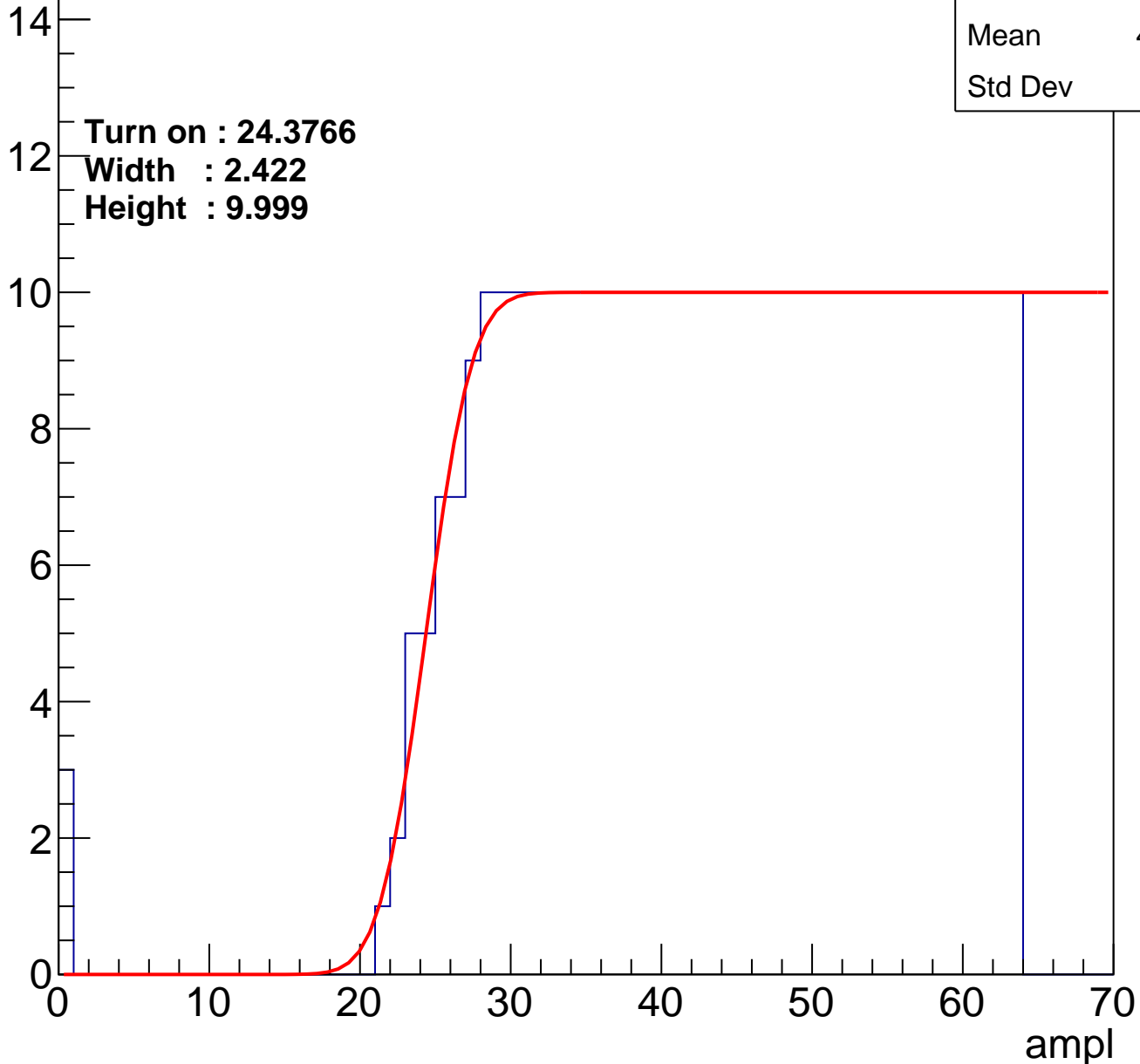
Entries	399
Mean	43.31
Std Dev	12.1

Turn on : 24.3766

Width : 2.422

Height : 9.999

Entry



B1L000S, U22-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.31
Std Dev	12.1

Turn on : 24.3766

Width : 2.422

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl

