

B0L001S, U19-ch0, adc0

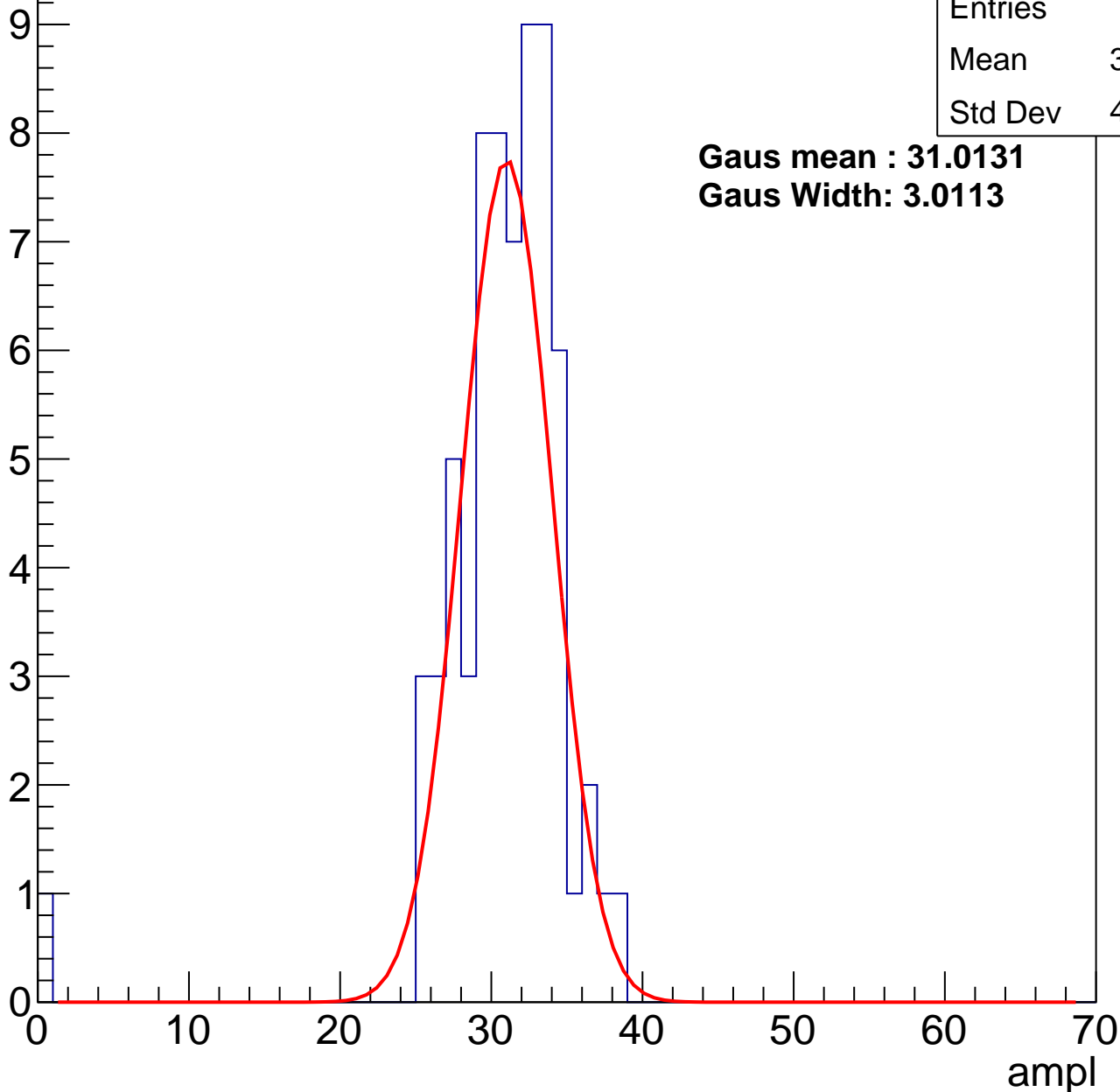
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.33
Std Dev	4.749

Gaus mean : 31.0131

Gaus Width: 3.0113



B0L001S, U19-ch0, adc1

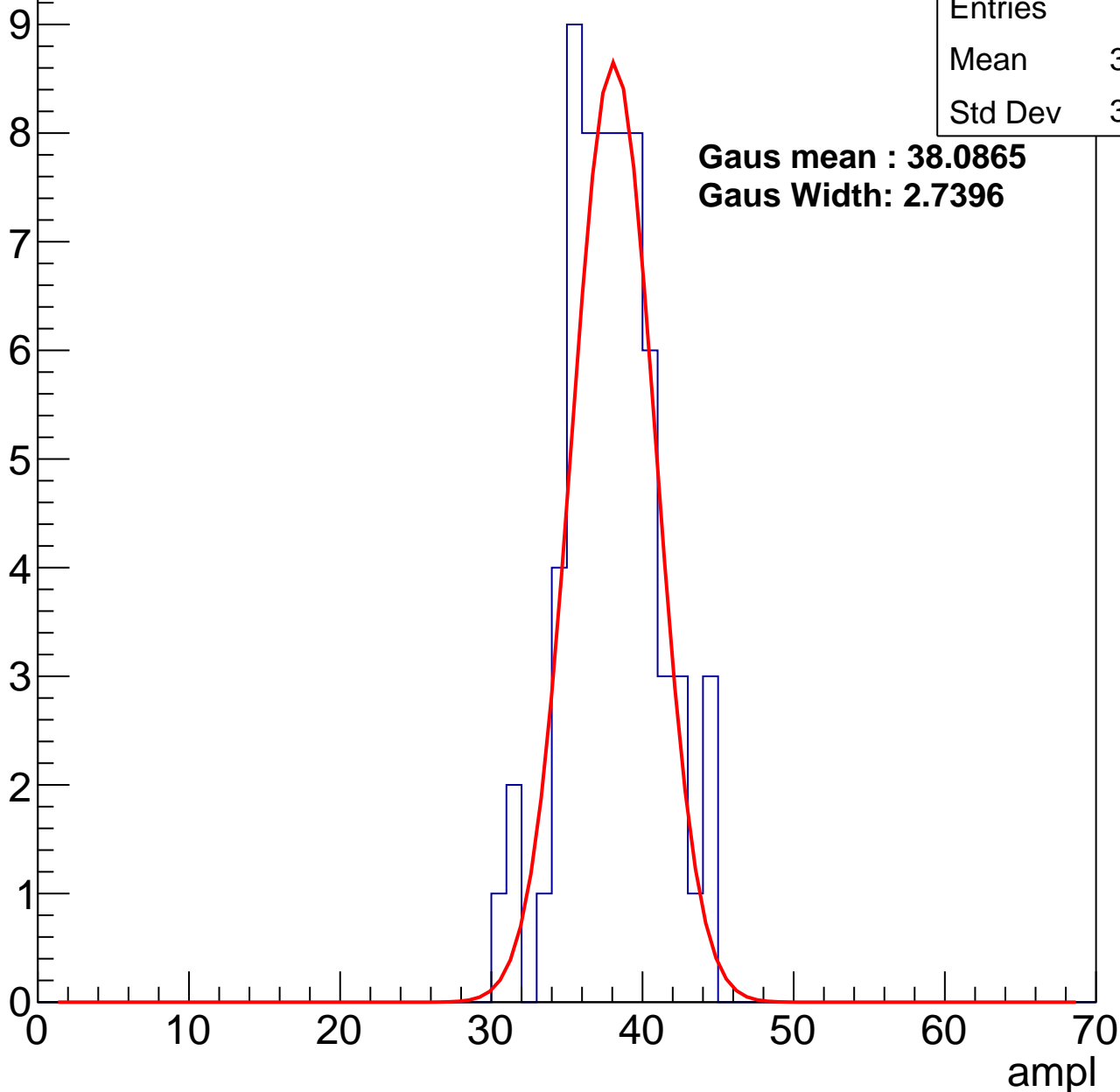
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.54
Std Dev	3.034

Gaus mean : 38.0865

Gaus Width: 2.7396



B0L001S, U19-ch0, adc2

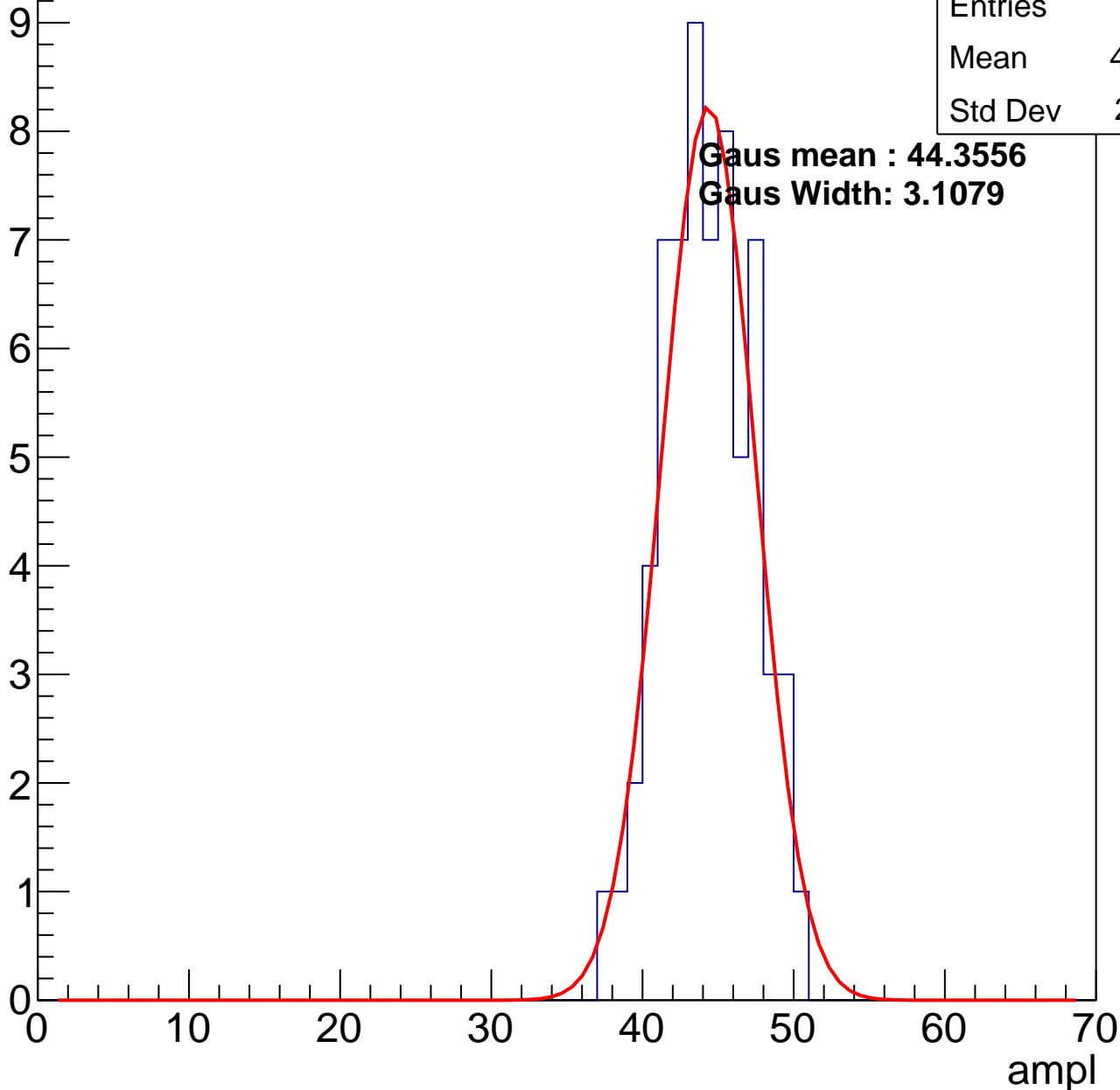
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.83
Std Dev	2.901

Gaus mean : 44.3556

Gaus Width: 3.1079

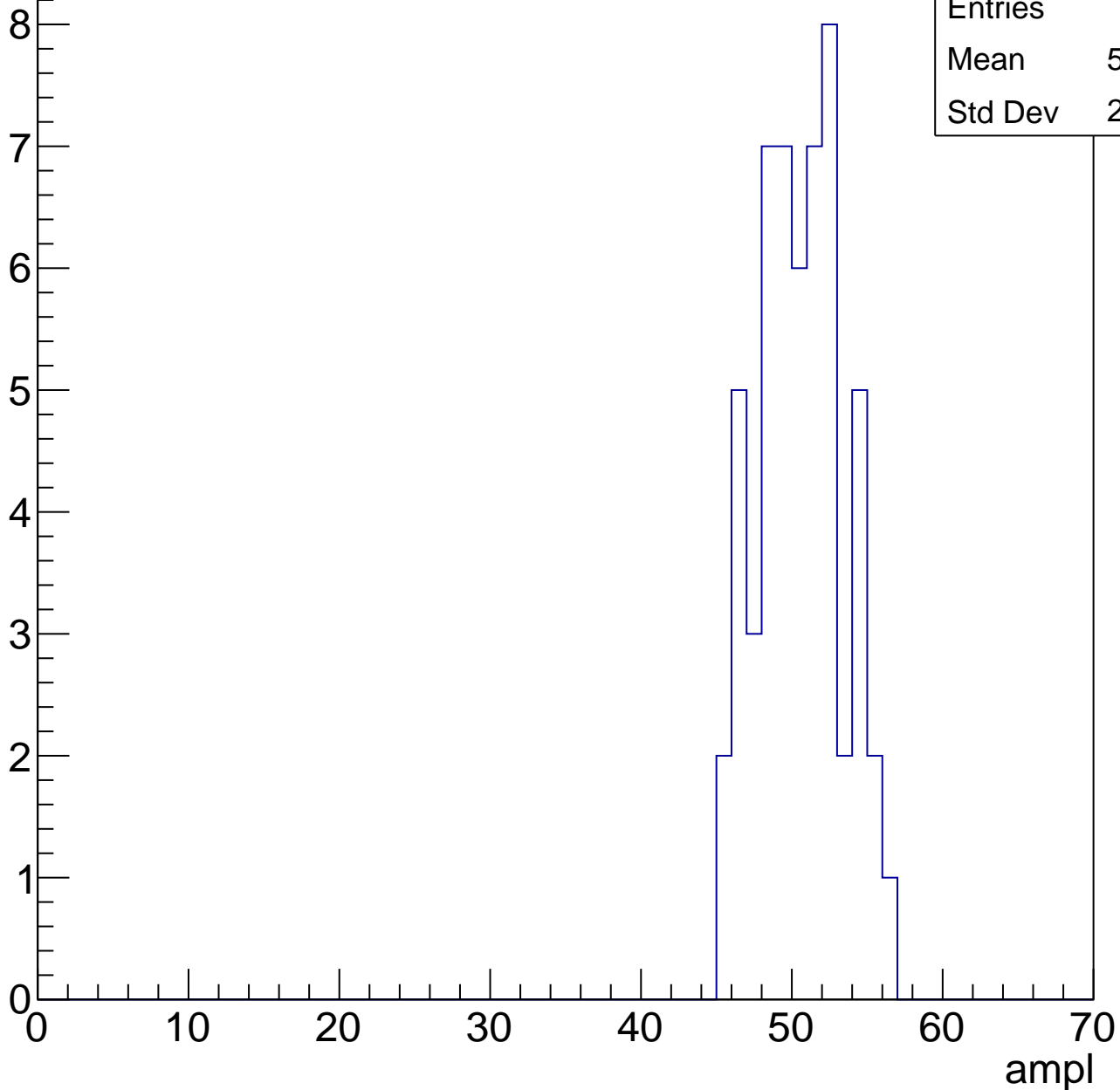


B0L001S, U19-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.09
Std Dev	2.745

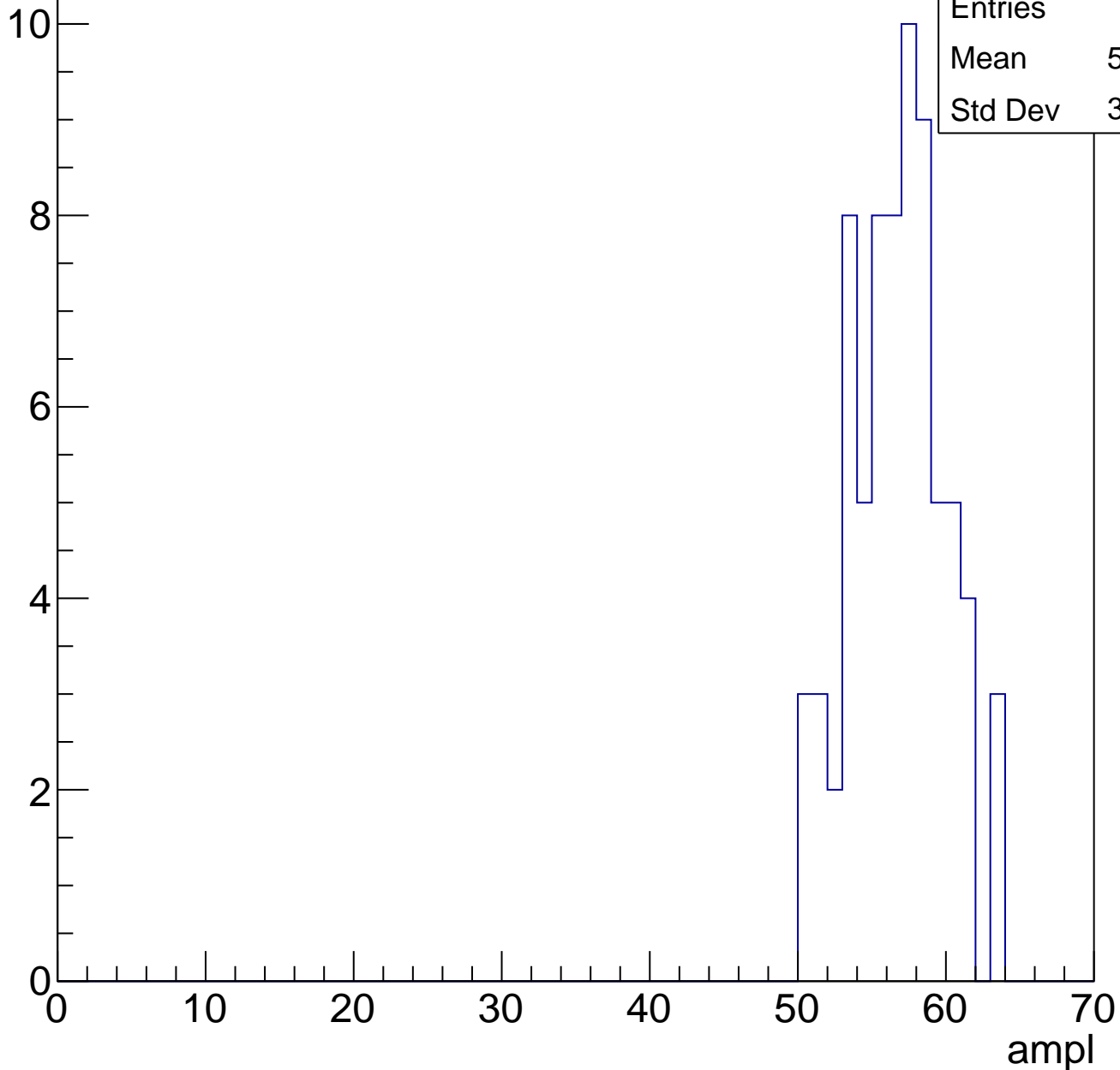


B0L001S, U19-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	56.29
Std Dev	3.156

Entry

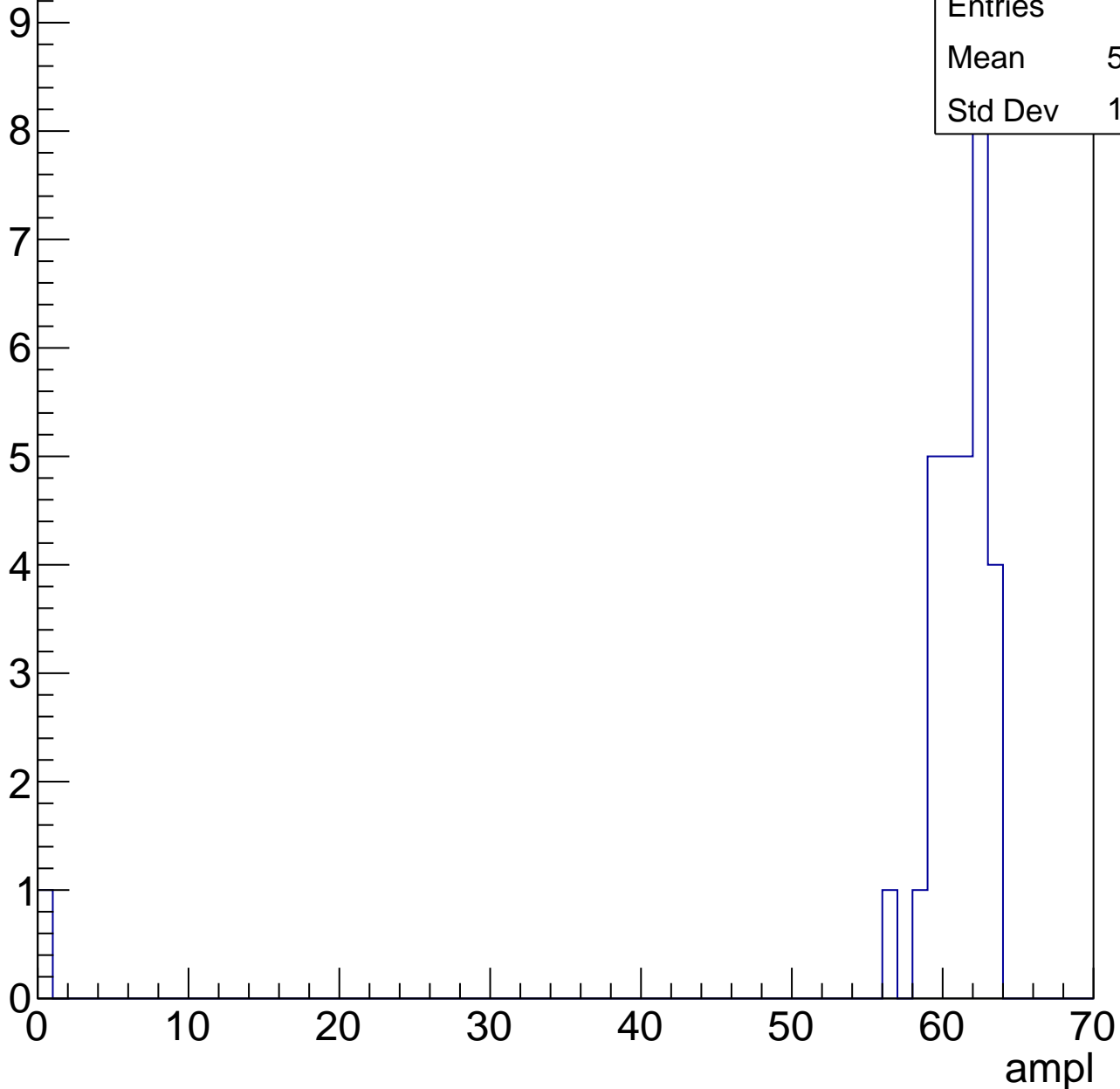


B0L001S, U19-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	58.84
Std Dev	10.87



B0L001S, U19-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch1, adc0

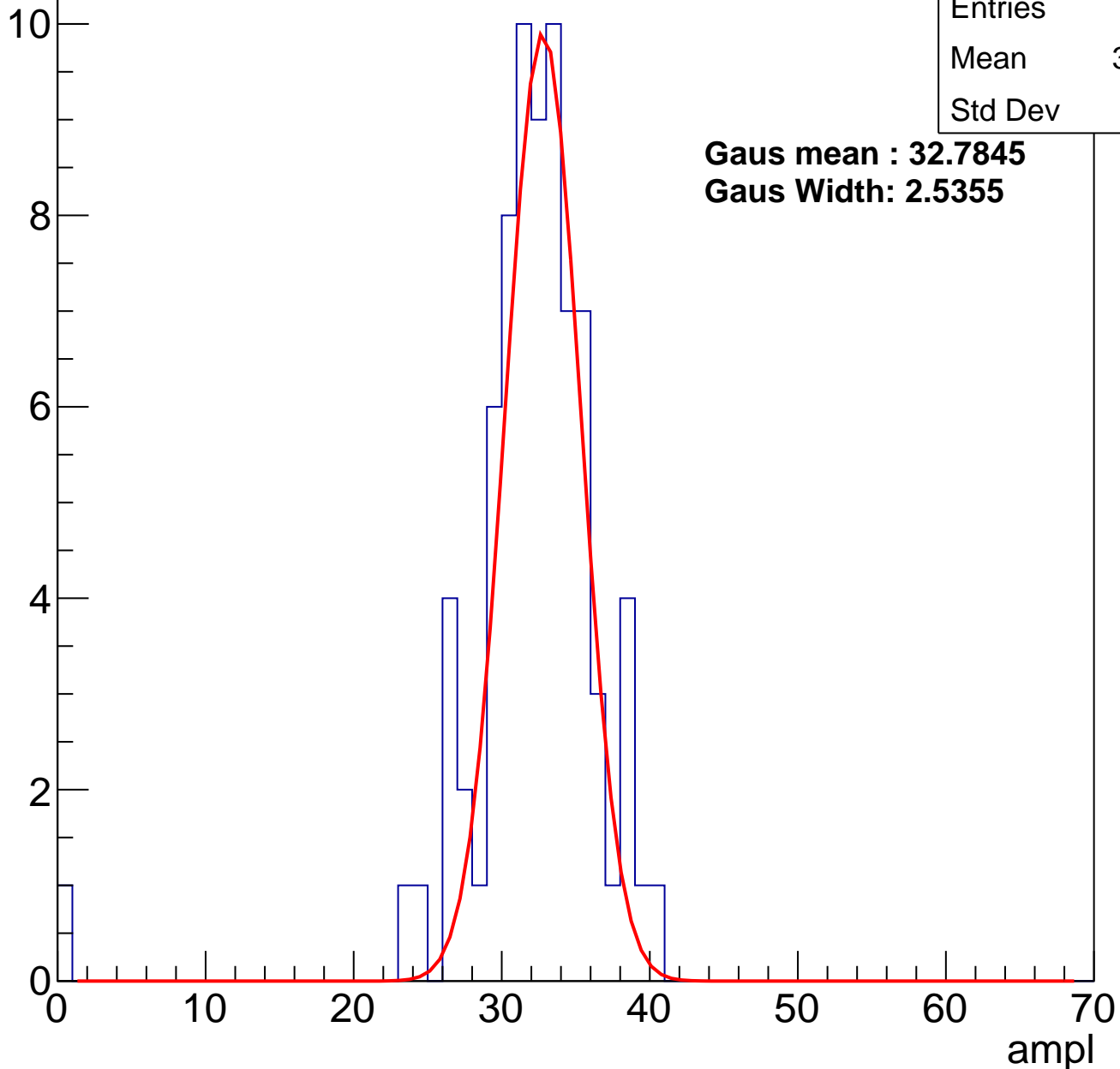
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	31.61
Std Dev	4.96

Gaus mean : 32.7845

Gaus Width: 2.5355

Entry



B0L001S, U19-ch1, adc1

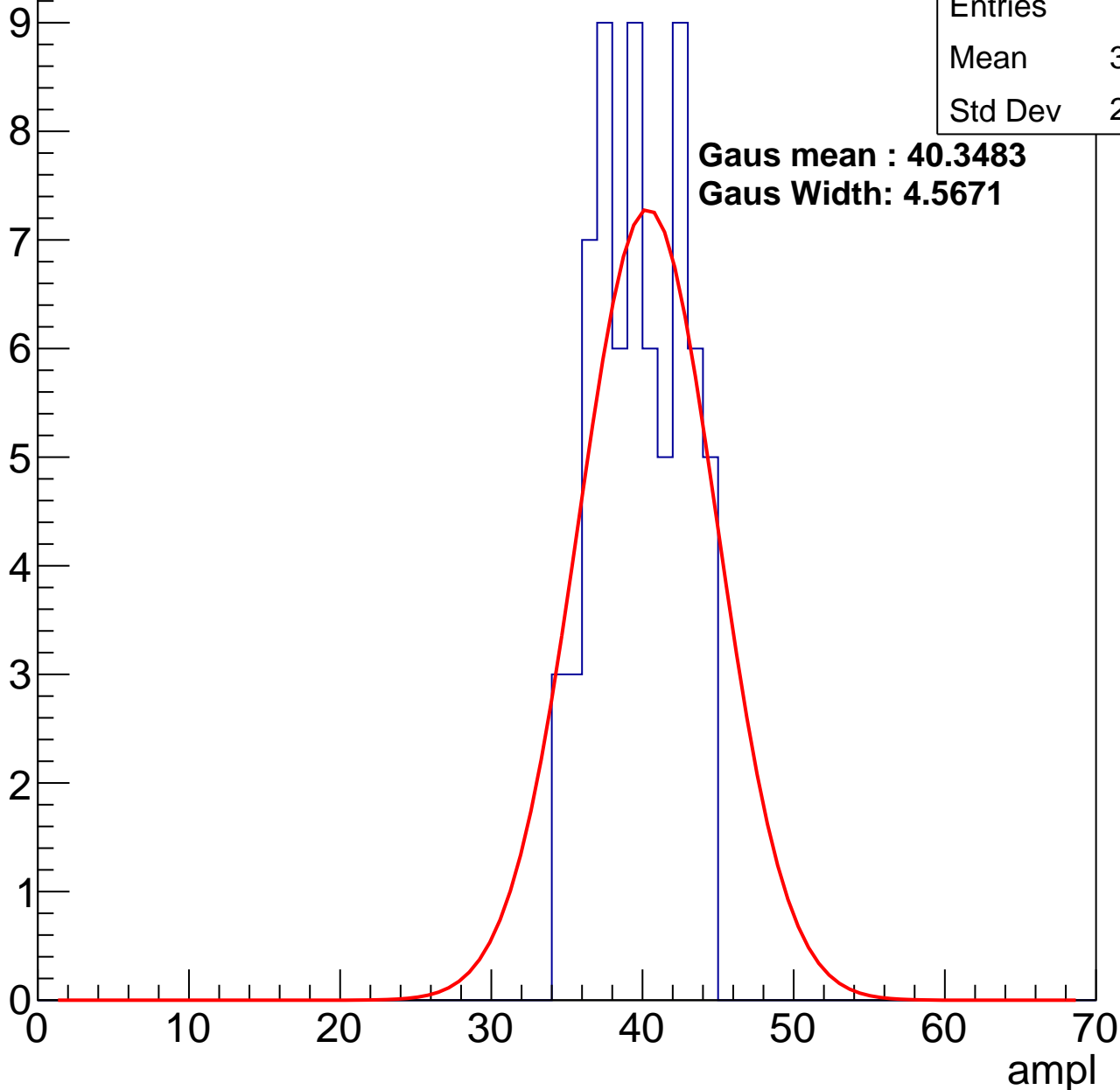
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	39.29
Std Dev	2.844

Gaus mean : 40.3483

Gaus Width: 4.5671



B0L001S, U19-ch1, adc2

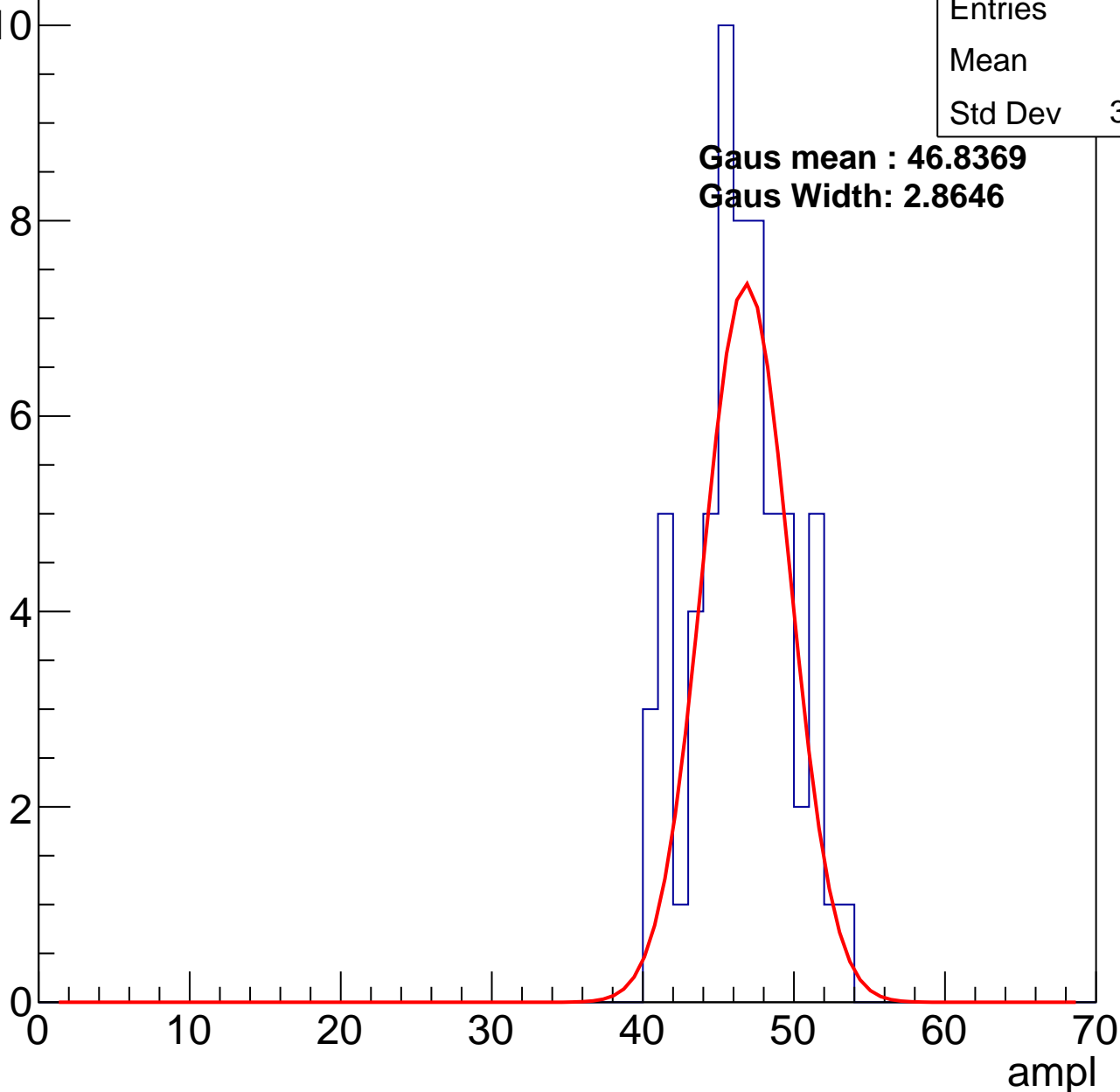
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	46
Std Dev	3.162

Gaus mean : 46.8369

Gaus Width: 2.8646

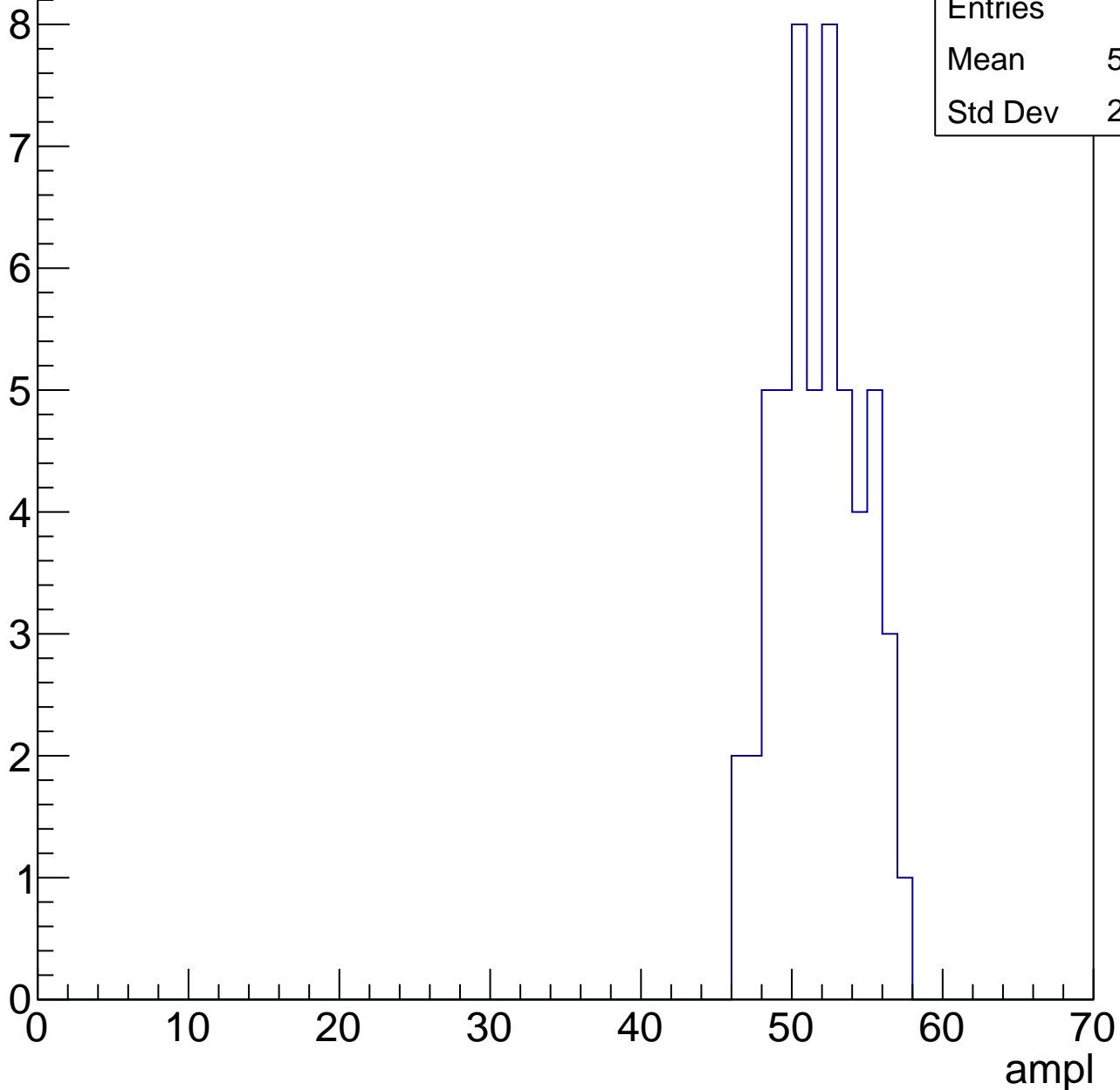


B0L001S, U19-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

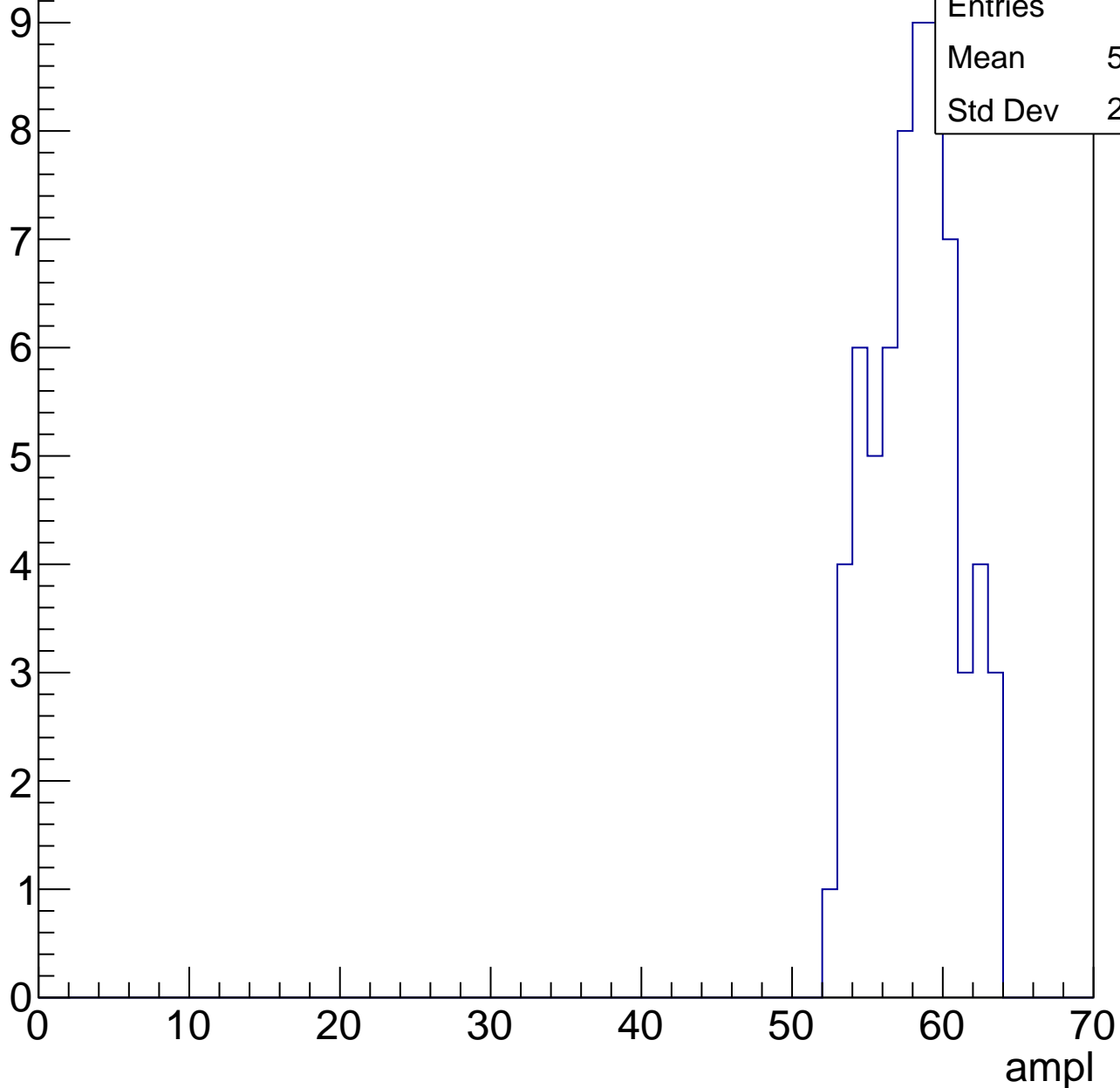
Entries	53
Mean	51.38
Std Dev	2.756



B0L001S, U19-ch1, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



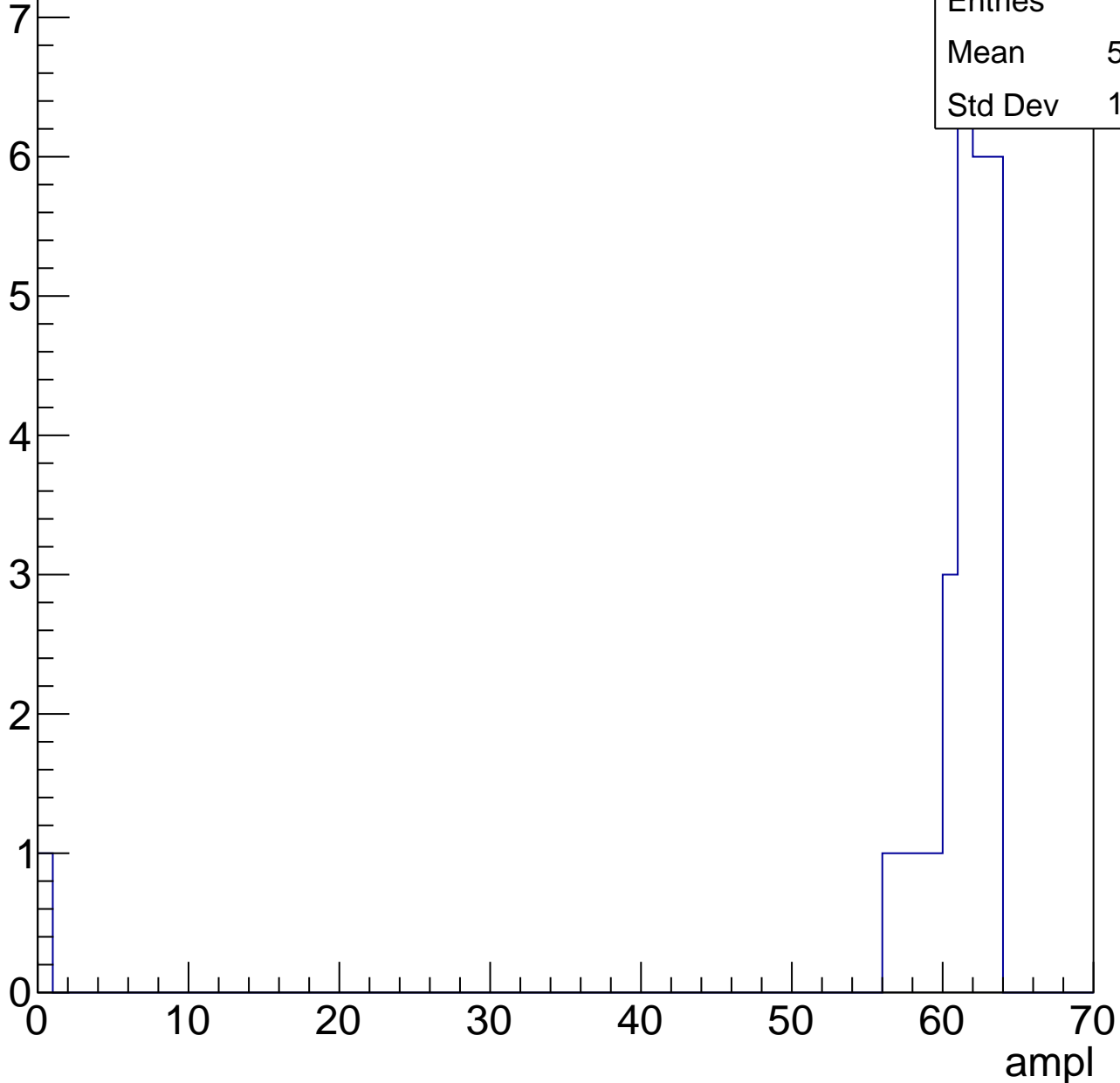
Entries	65
Mean	57.66
Std Dev	2.786

B0L001S, U19-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.78
Std Dev	11.67



B0L001S, U19-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch2, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	31.56
Std Dev	2.955

Gaus mean : 31.8264

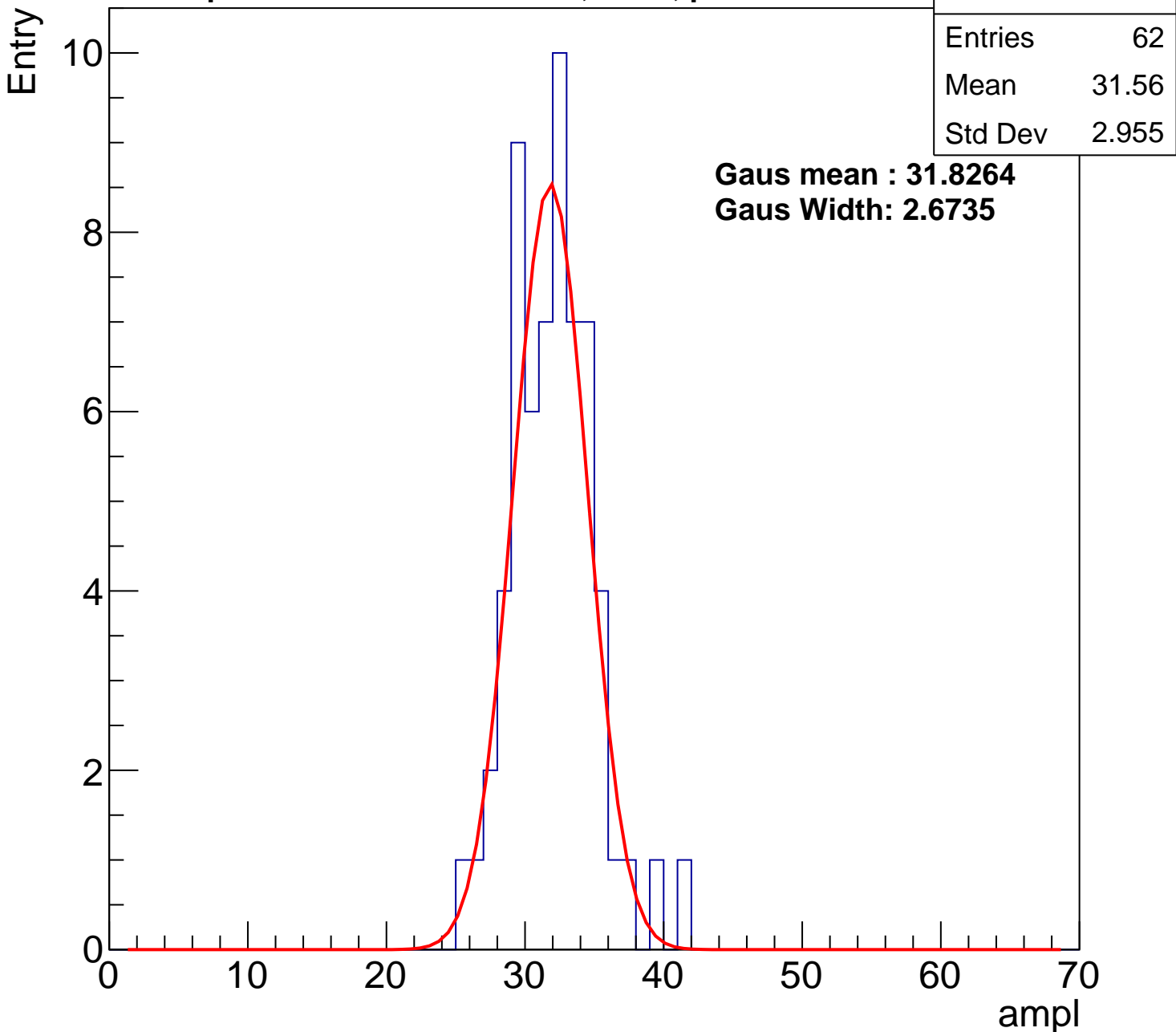
Gaus Width: 2.6735

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch2, adc1

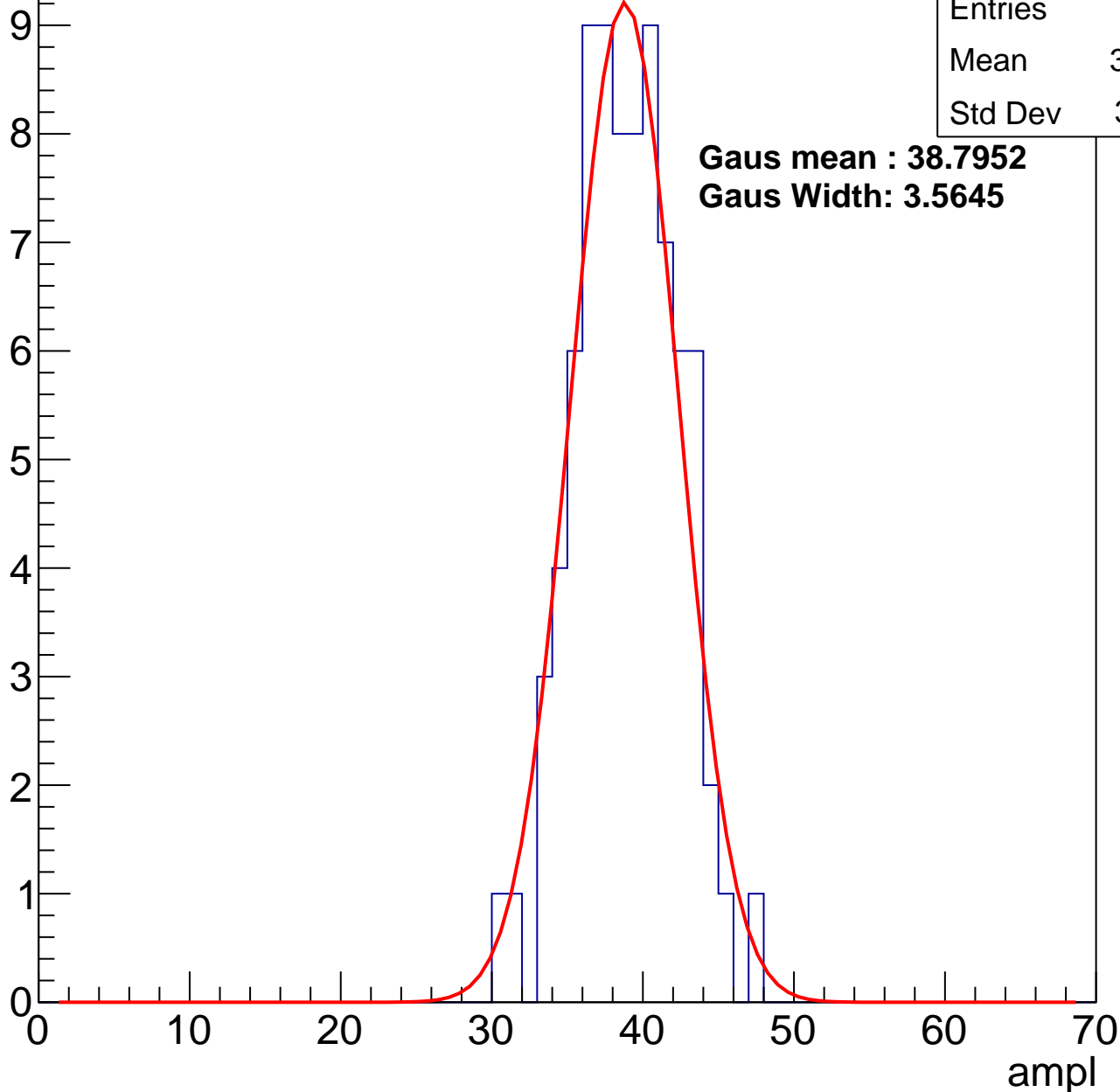
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	38.47
Std Dev	3.311

Gaus mean : 38.7952

Gaus Width: 3.5645



B0L001S, U19-ch2, adc2

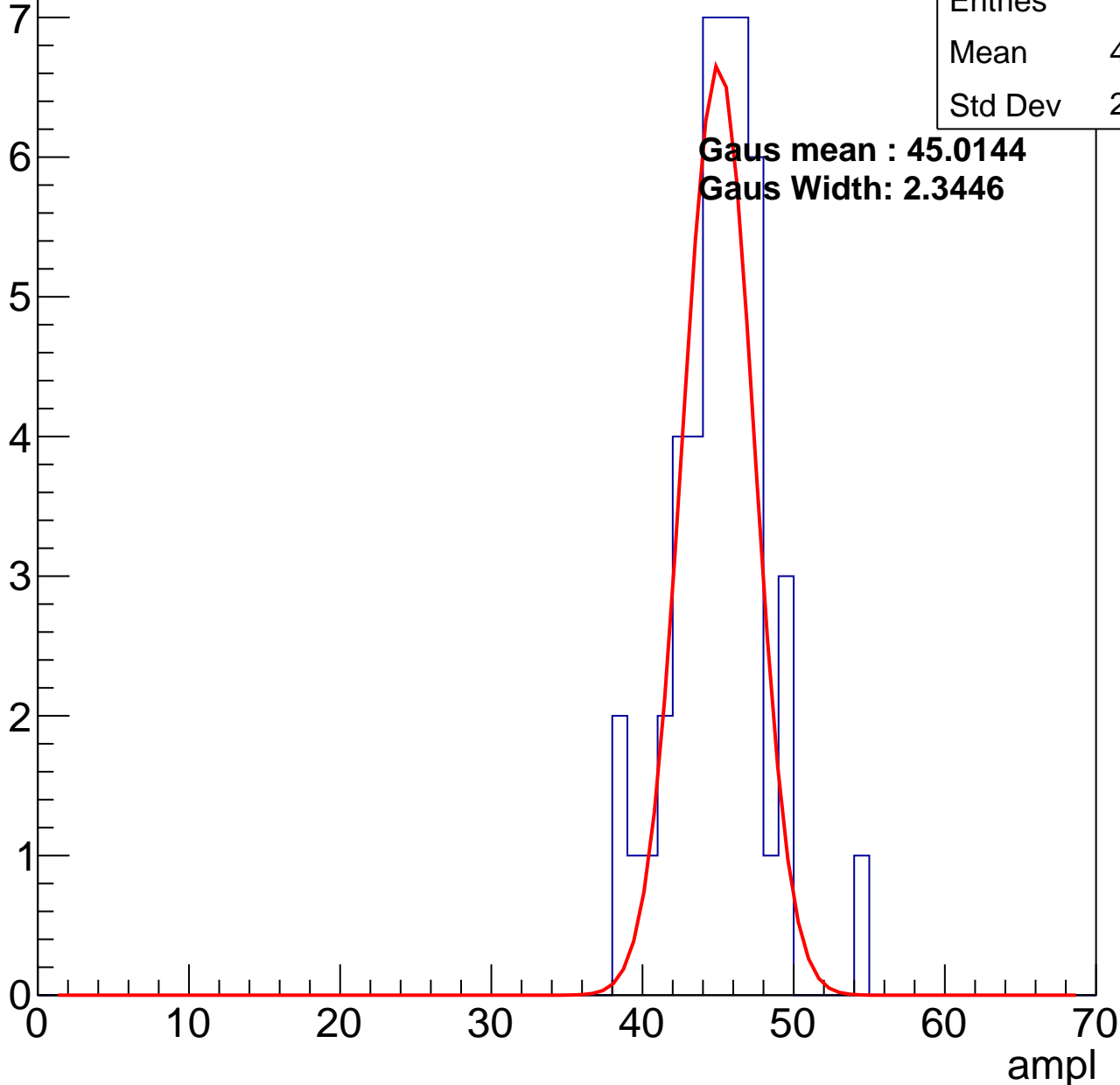
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	44.63
Std Dev	2.995

Gaus mean : 45.0144

Gaus Width: 2.3446

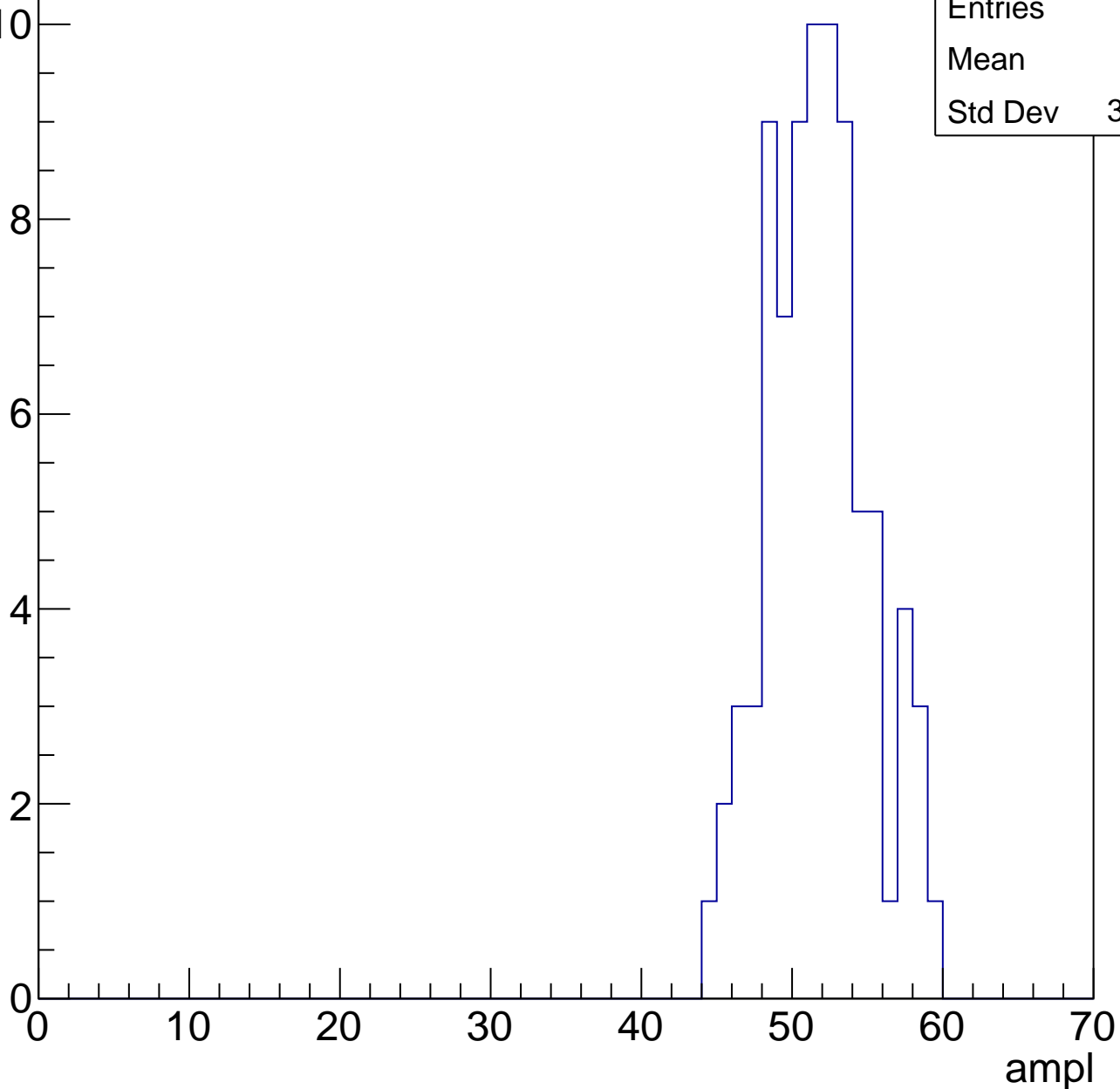


B0L001S, U19-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	51.3
Std Dev	3.323

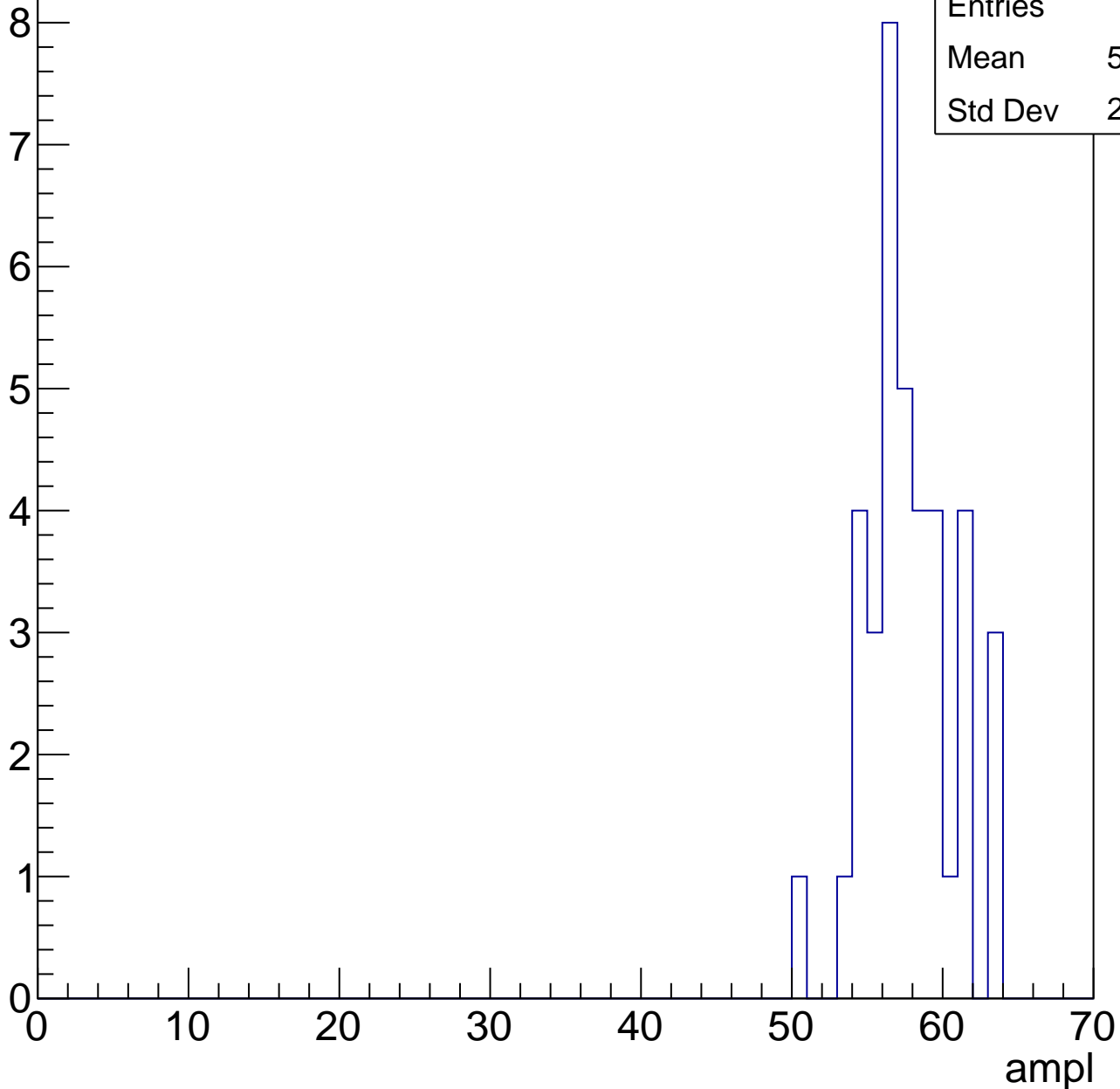


B0L001S, U19-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	57.32
Std Dev	2.894

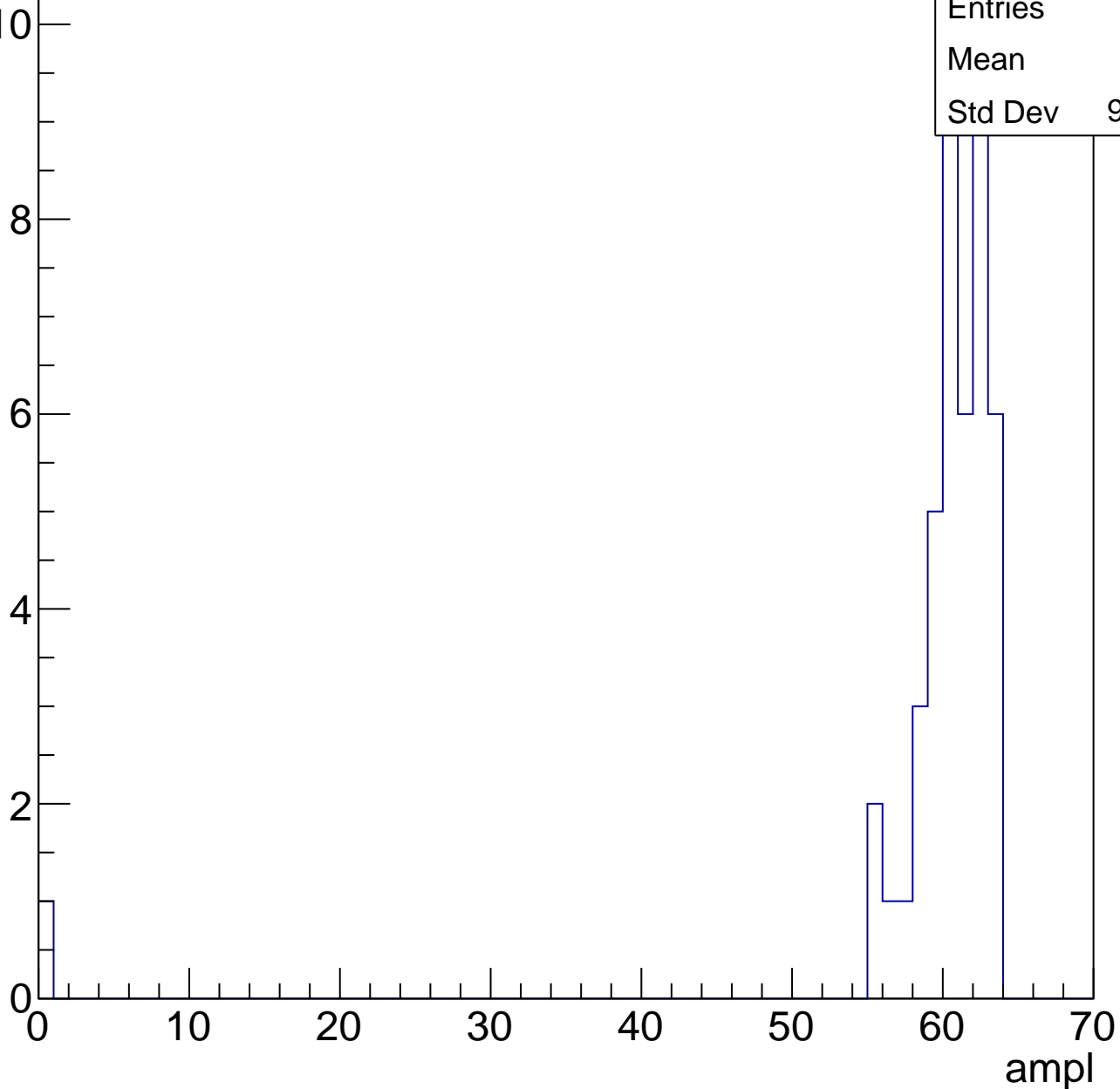


B0L001S, U19-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	59
Std Dev	9.229



B0L001S, U19-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch3, adc0

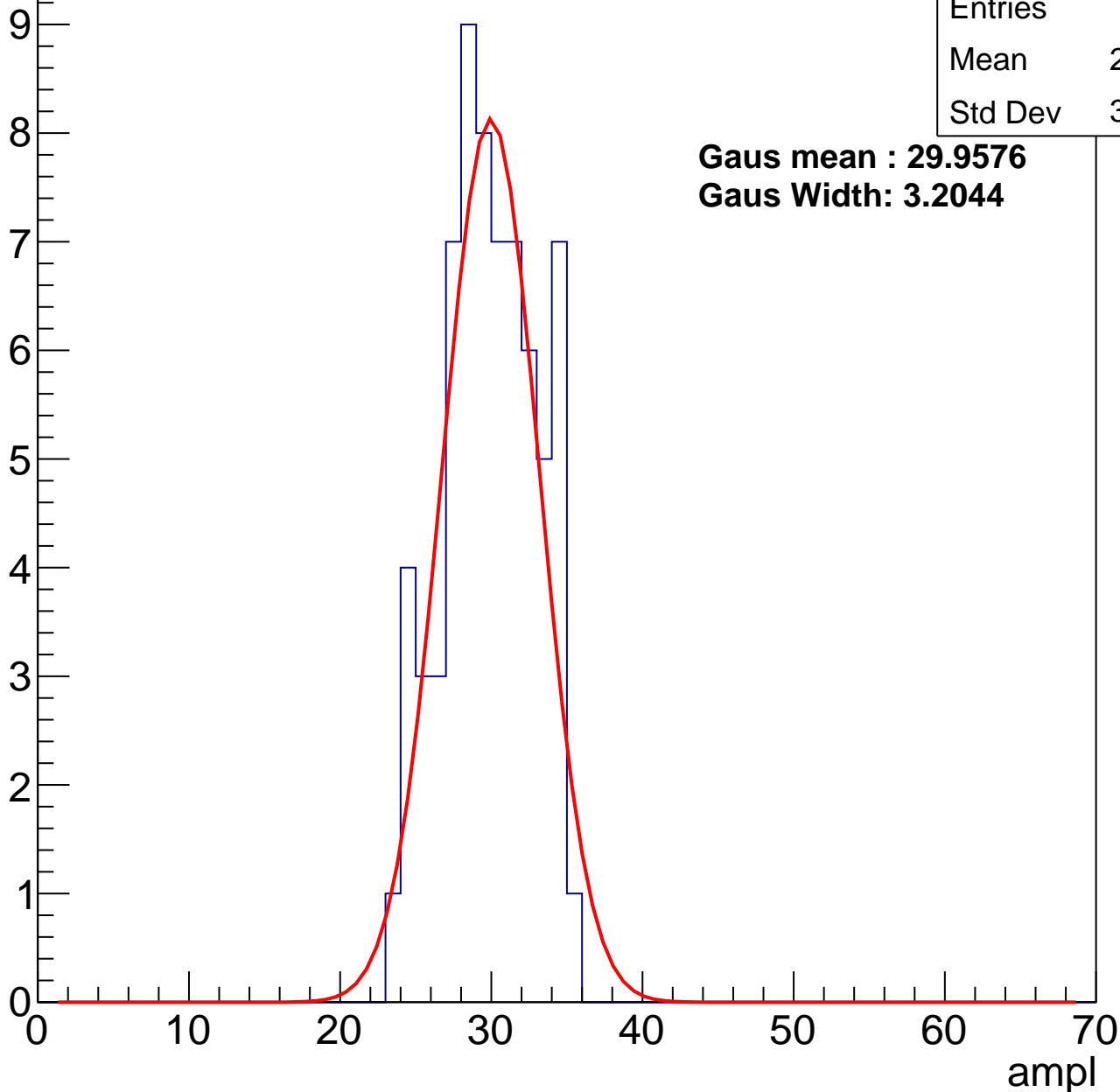
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	29.44
Std Dev	3.007

Gaus mean : 29.9576

Gaus Width: 3.2044



B0L001S, U19-ch3, adc1

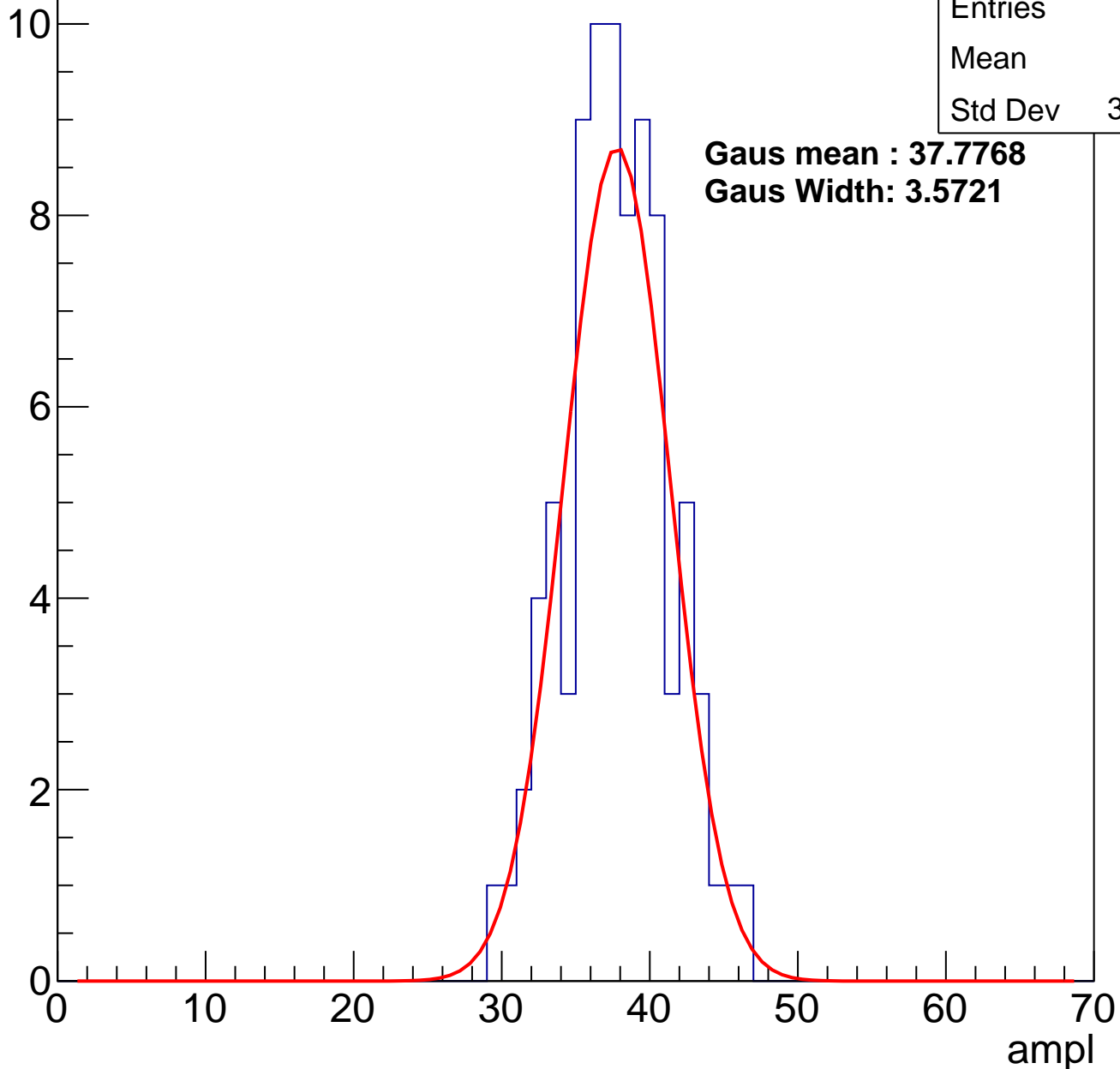
calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	37.3
Std Dev	3.487

Gaus mean : 37.7768

Gaus Width: 3.5721

Entry



B0L001S, U19-ch3, adc2

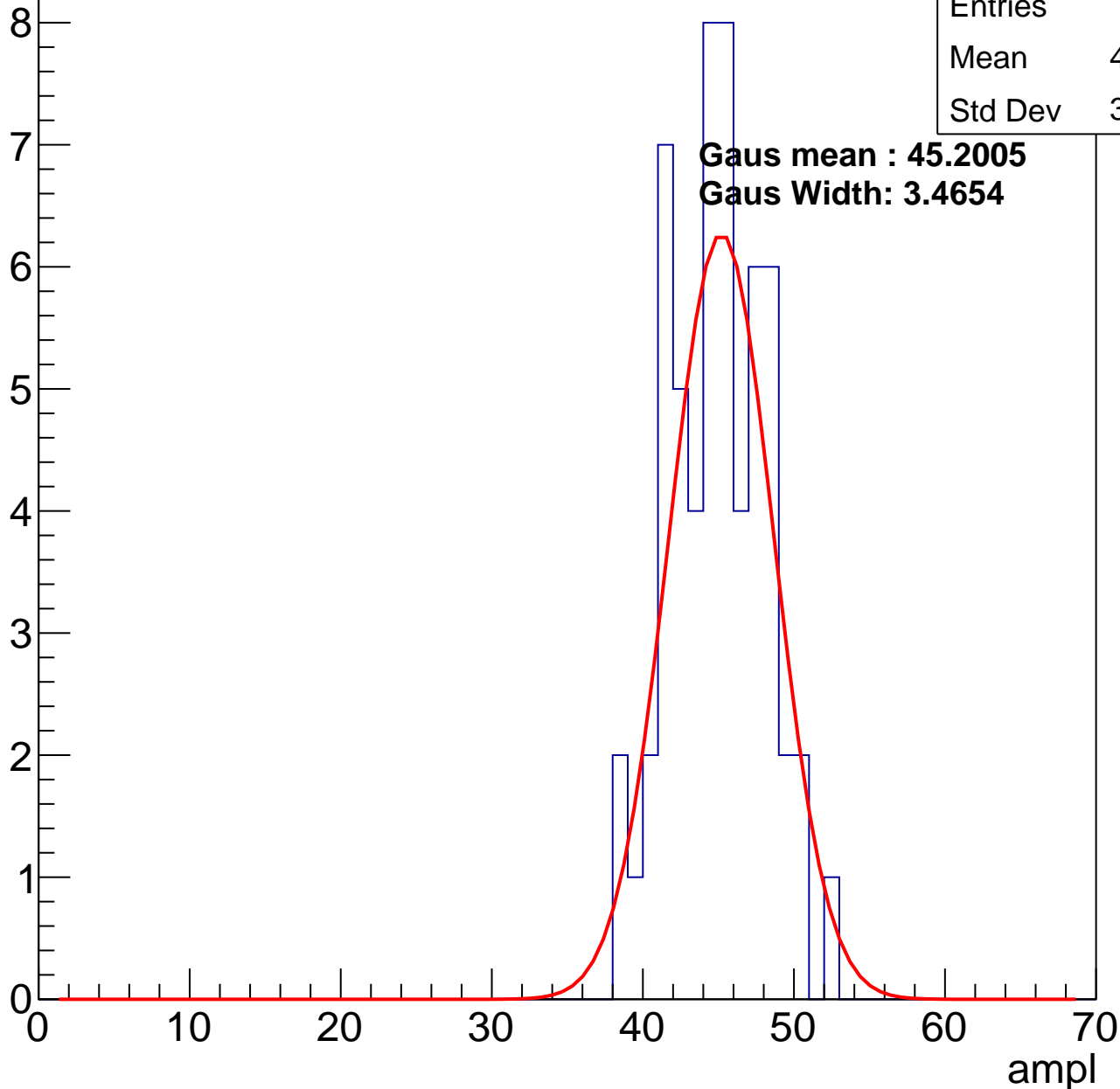
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.48
Std Dev	3.114

Gaus mean : 45.2005

Gaus Width: 3.4654

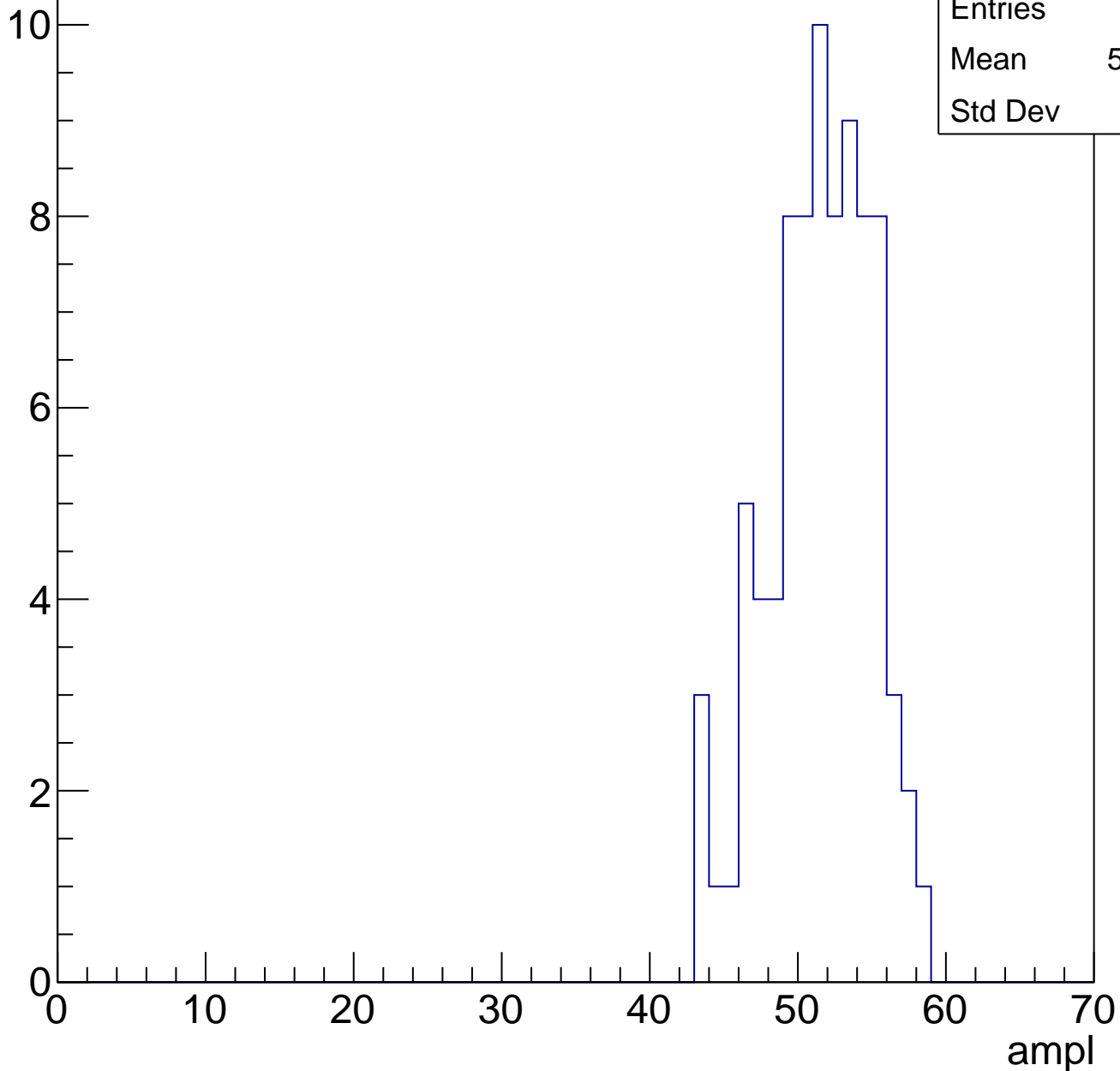


B0L001S, U19-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	51.02
Std Dev	3.44

Entry

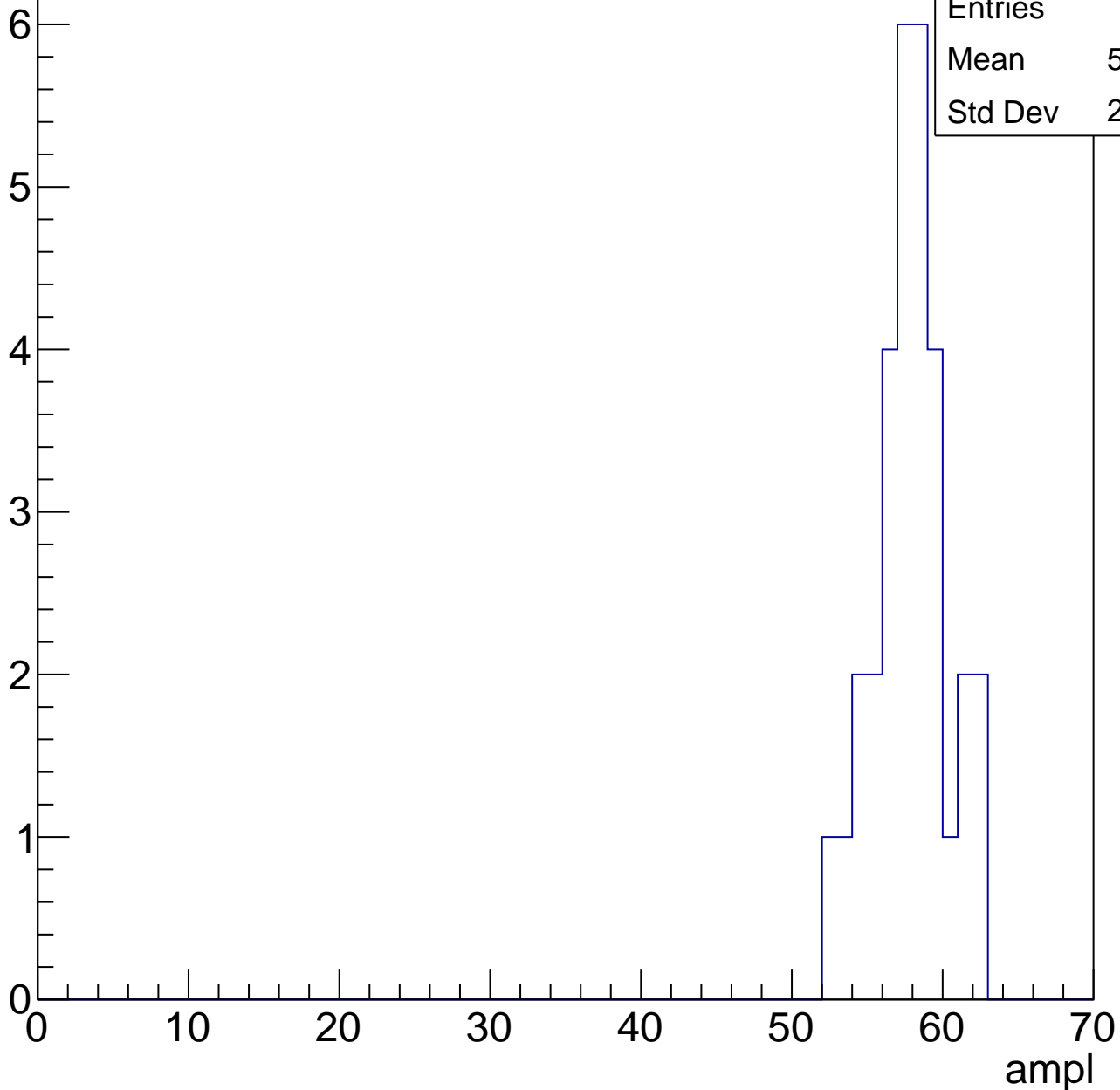


B0L001S, U19-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	57.39
Std Dev	2.405



B0L001S, U19-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

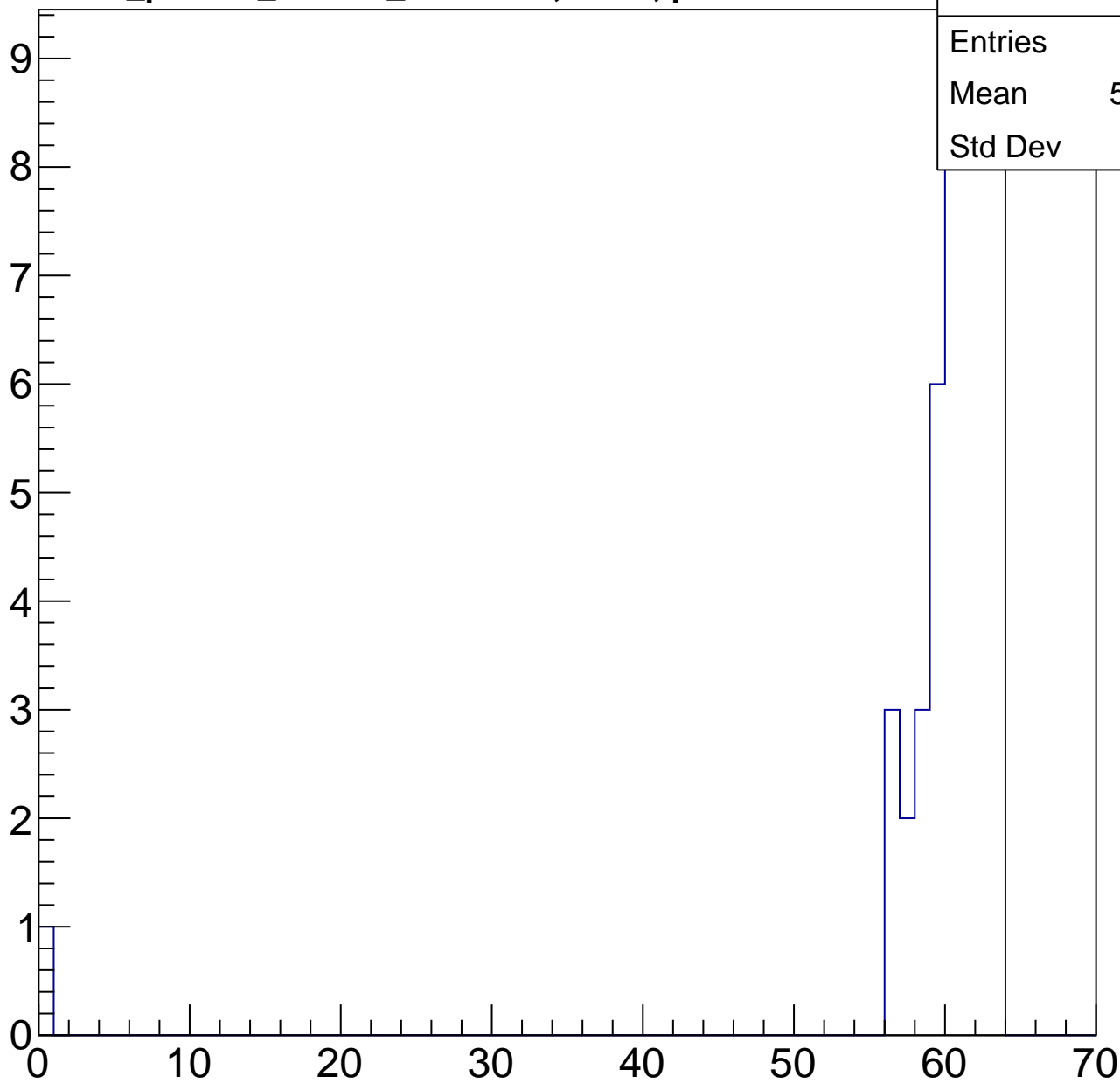
Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.12
Std Dev	8.85

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch4, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	28.8
Std Dev	4.78

Gaus mean : 29.7028

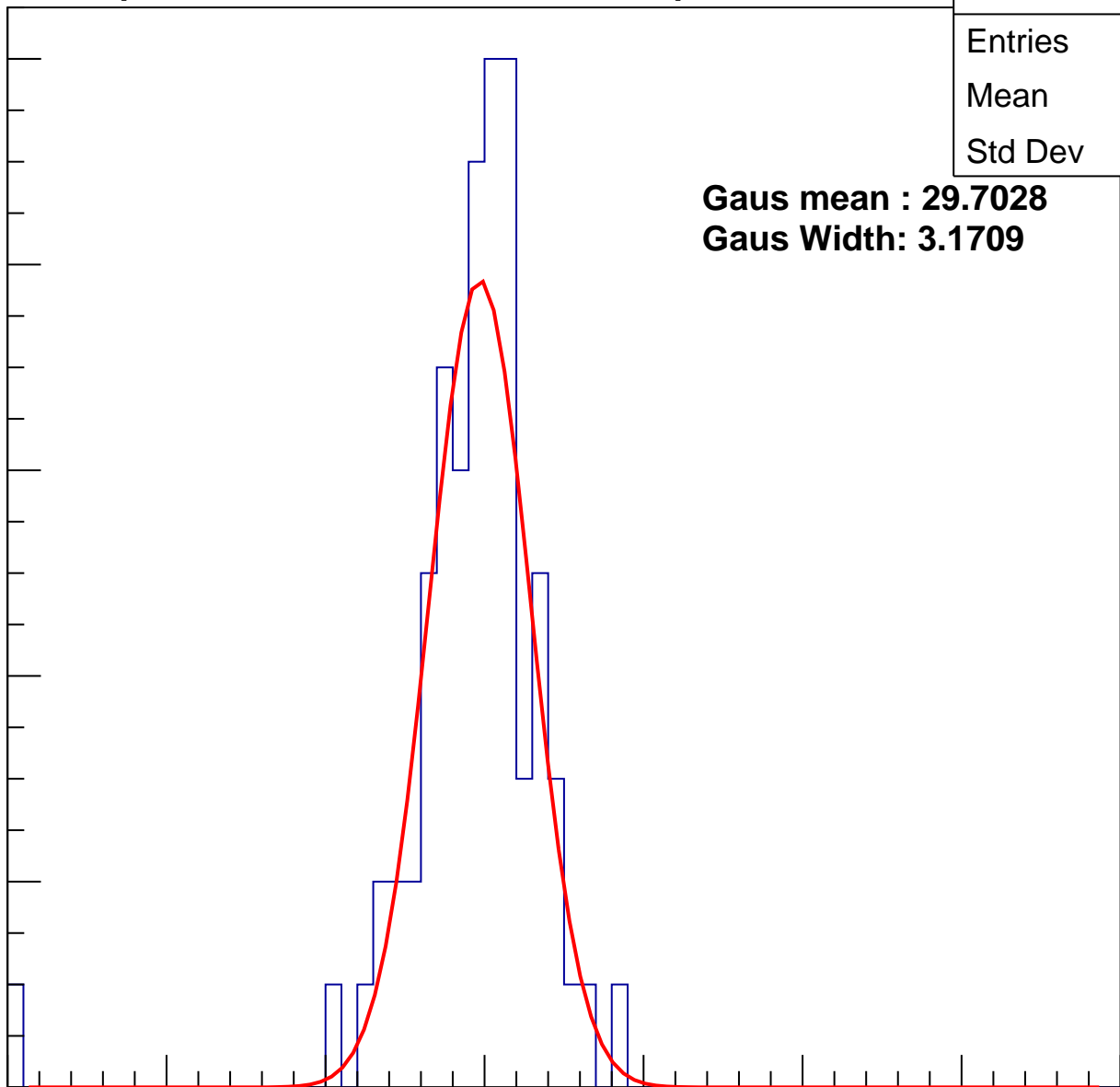
Gaus Width: 3.1709

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



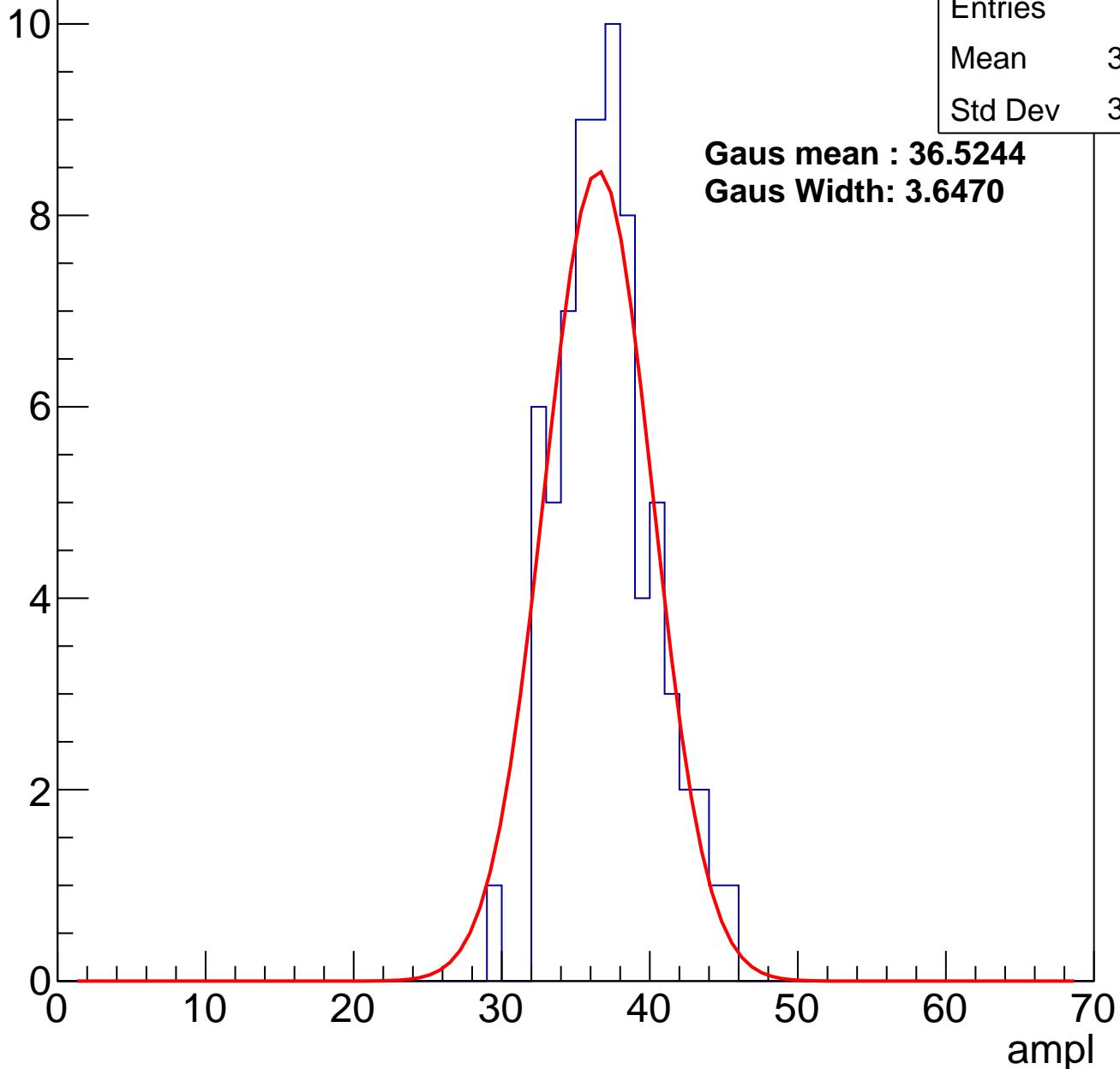
B0L001S, U19-ch4, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	36.64
Std Dev	3.194

Gaus mean : 36.5244
Gaus Width: 3.6470

Entry

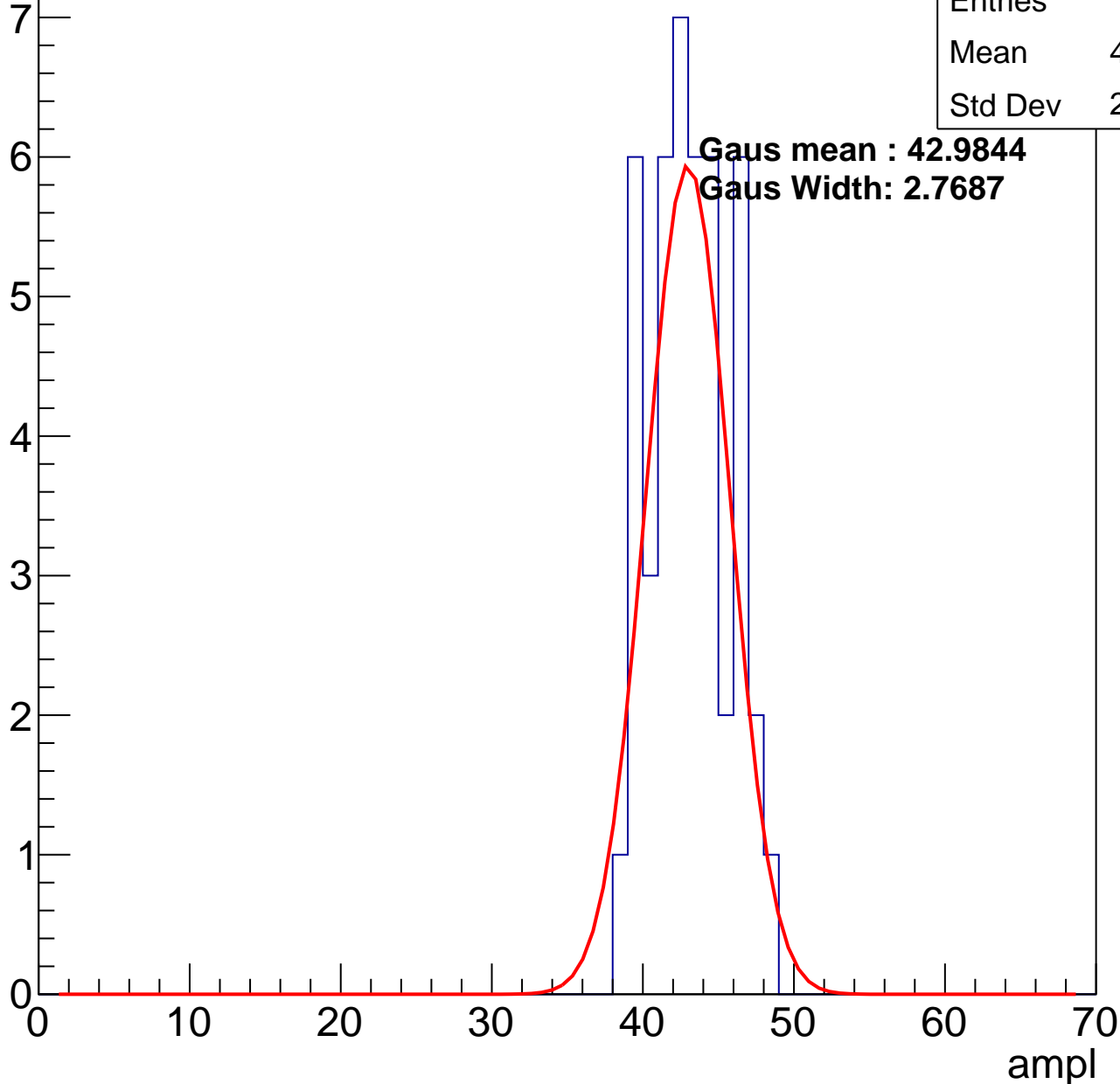


B0L001S, U19-ch4, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

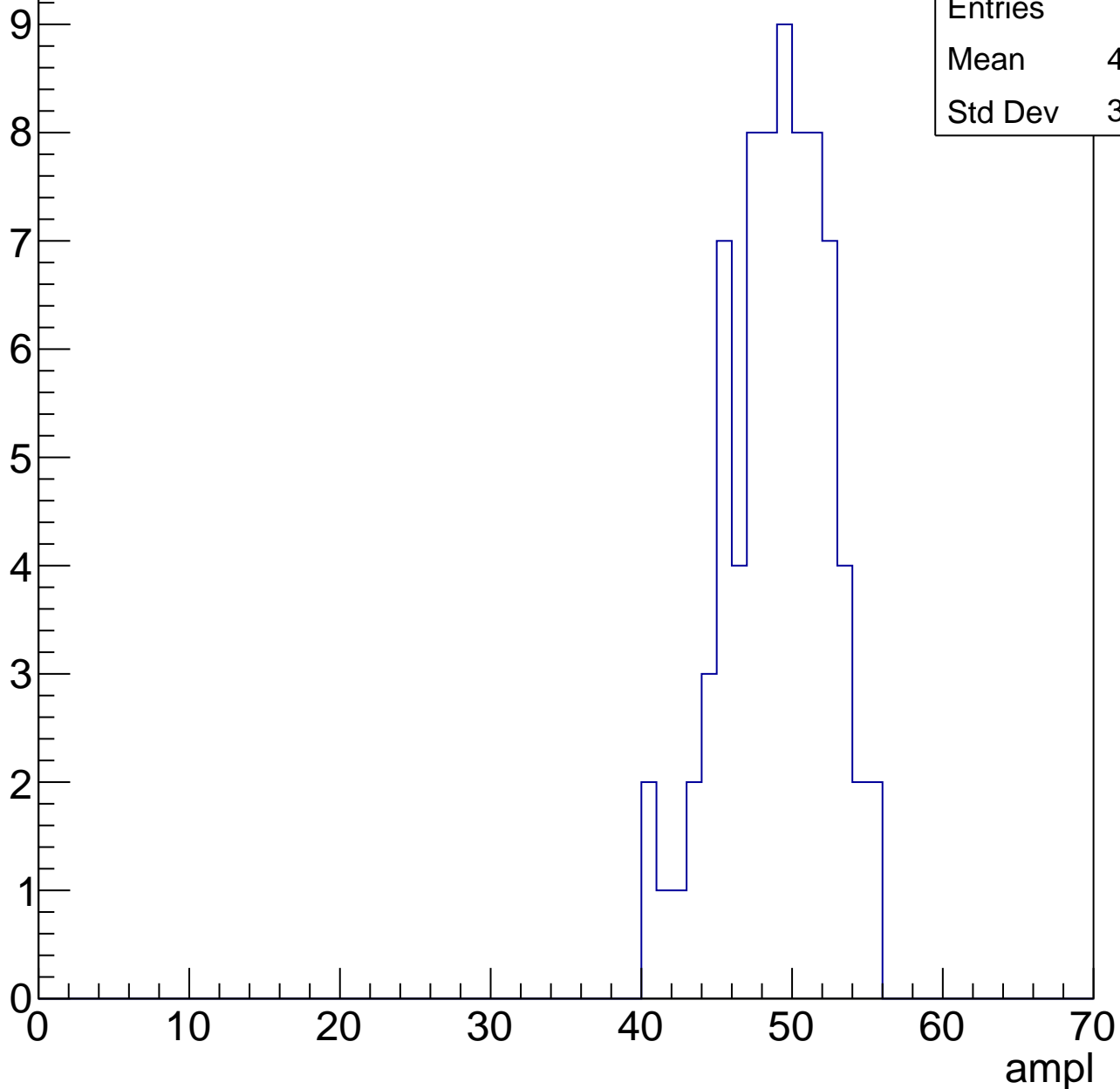
Entries	46
Mean	42.65
Std Dev	2.547



B0L001S, U19-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



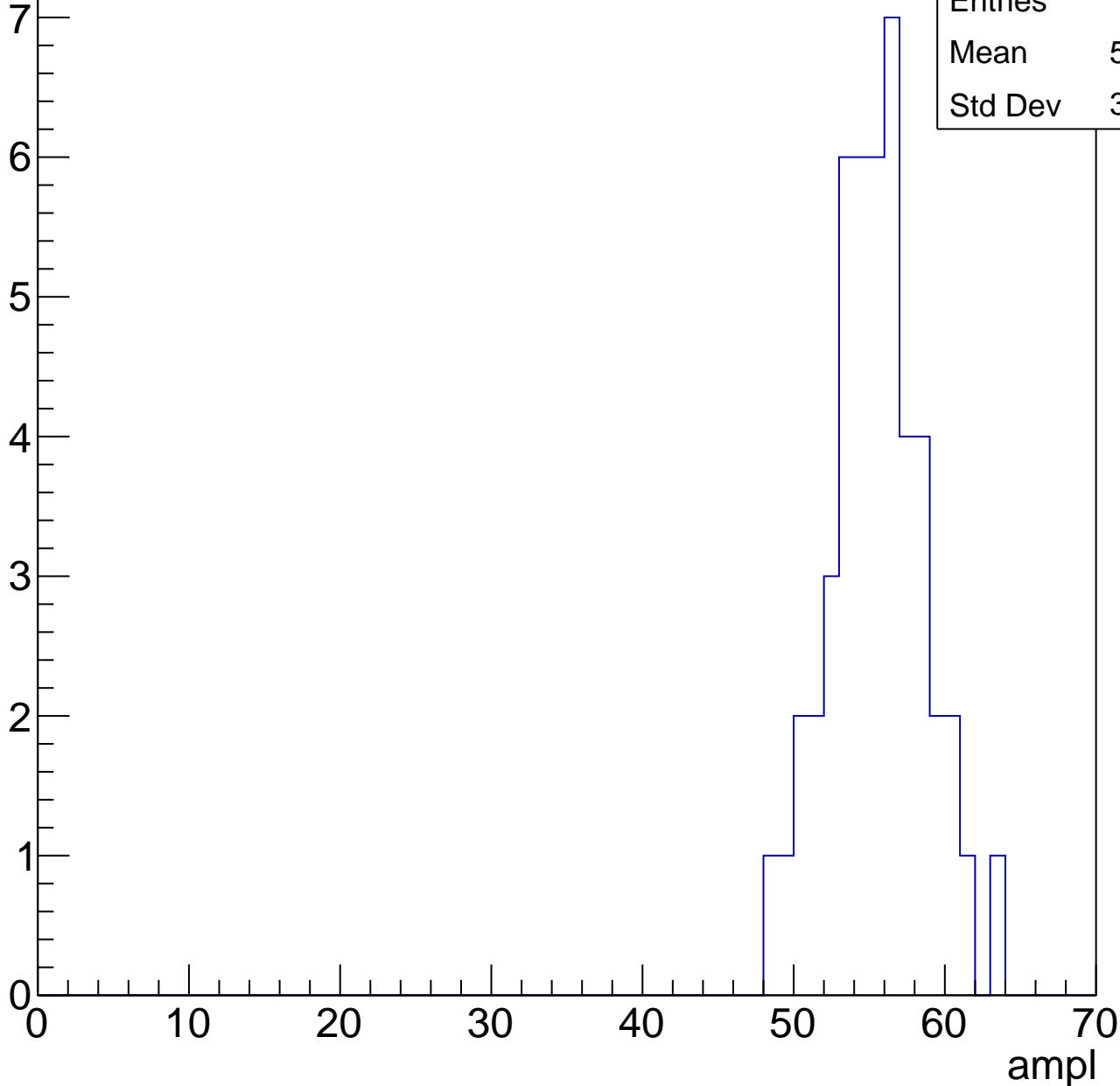
Entries	76
Mean	48.46
Std Dev	3.397

B0L001S, U19-ch4, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	55.02
Std Dev	3.112



B0L001S, U19-ch4, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries	52
Mean	59.37
Std Dev	2.496

ampl

0

10

20

30

40

50

60

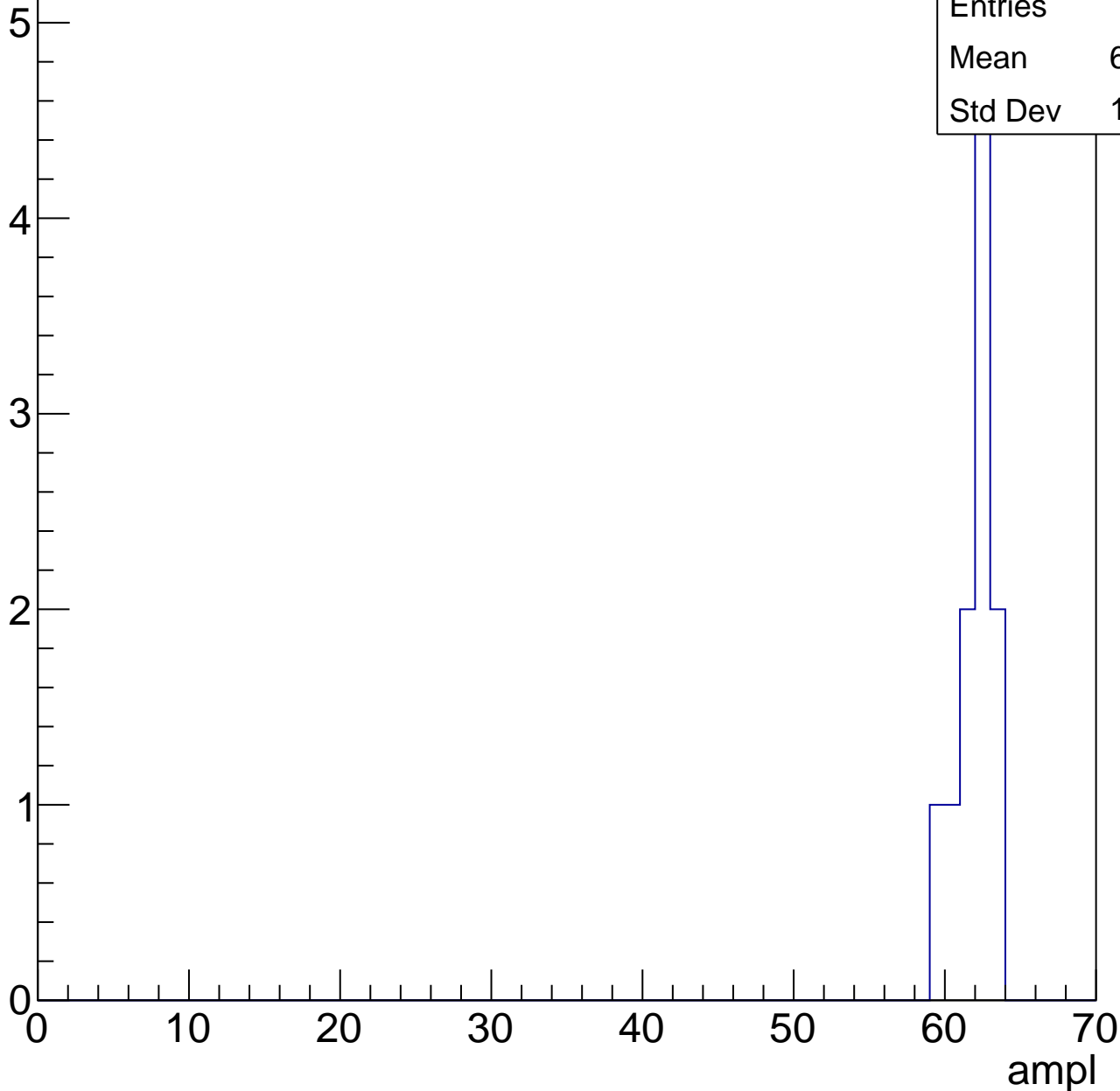
70

B0L001S, U19-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	11
Mean	61.55
Std Dev	1.157



B0L001S, U19-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch5, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	31.68
Std Dev	5.008

Gaus mean : 32.3896

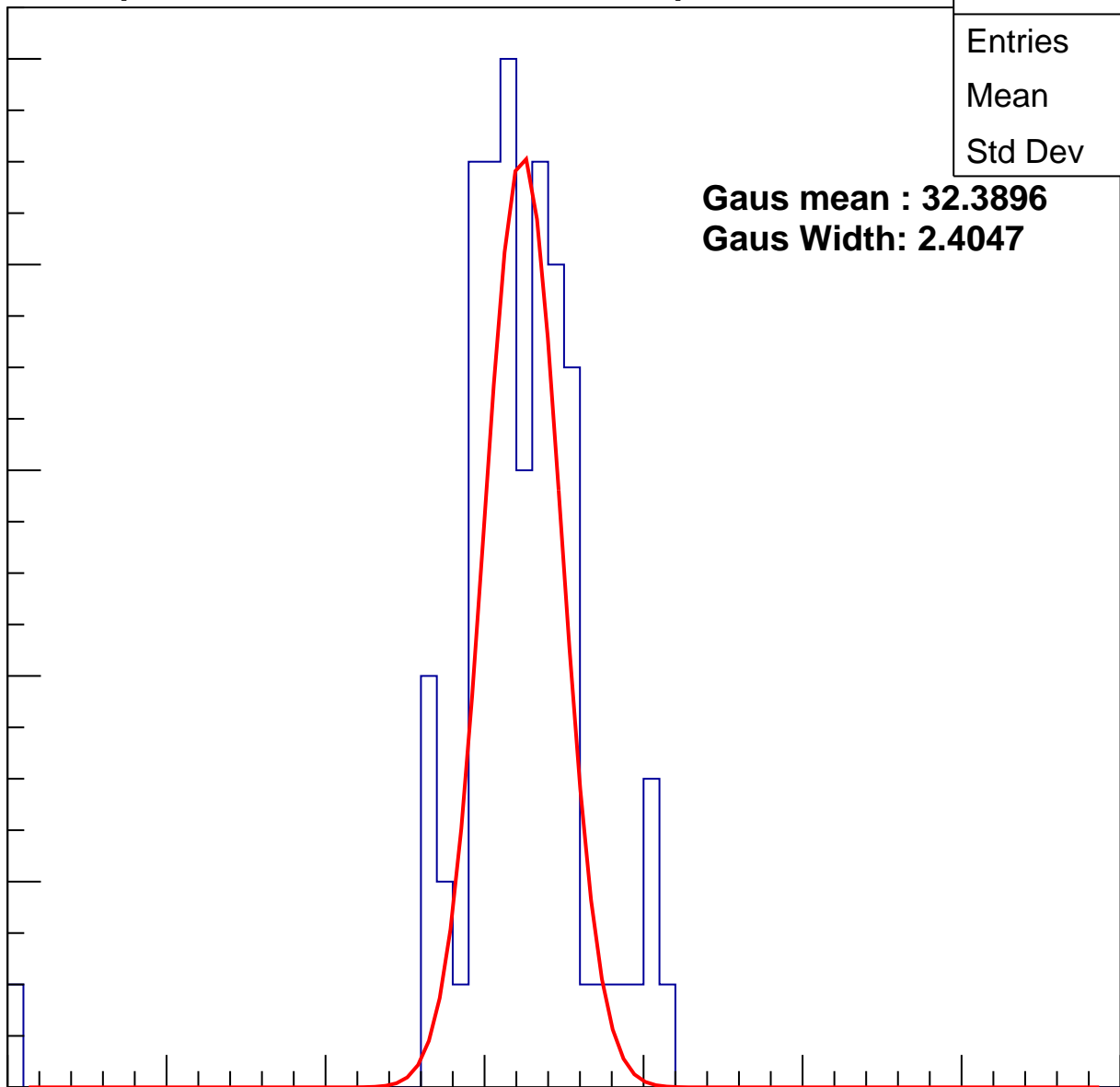
Gaus Width: 2.4047

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch5, adc1

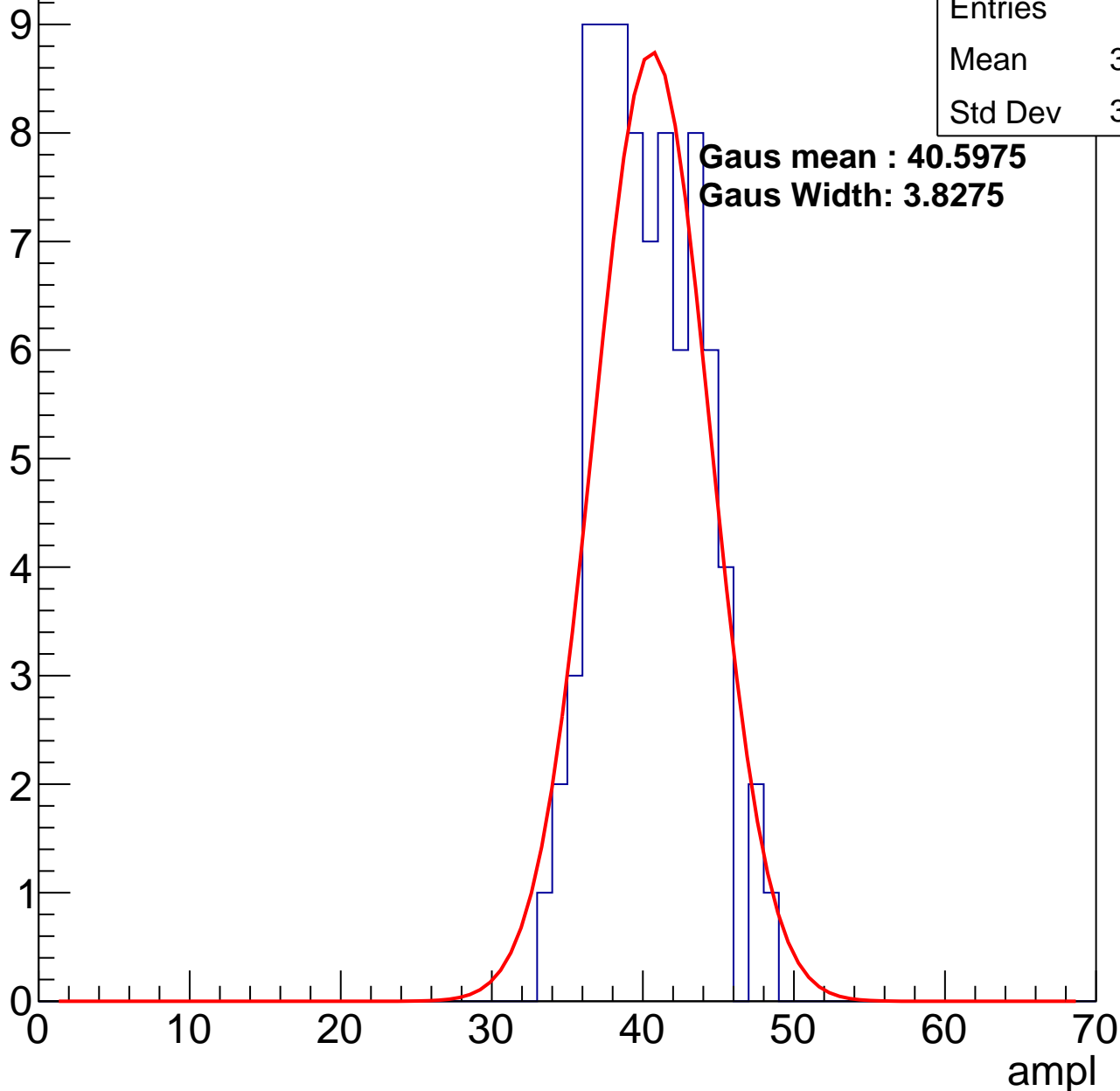
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	39.84
Std Dev	3.338

Gaus mean : 40.5975

Gaus Width: 3.8275



B0L001S, U19-ch5, adc2

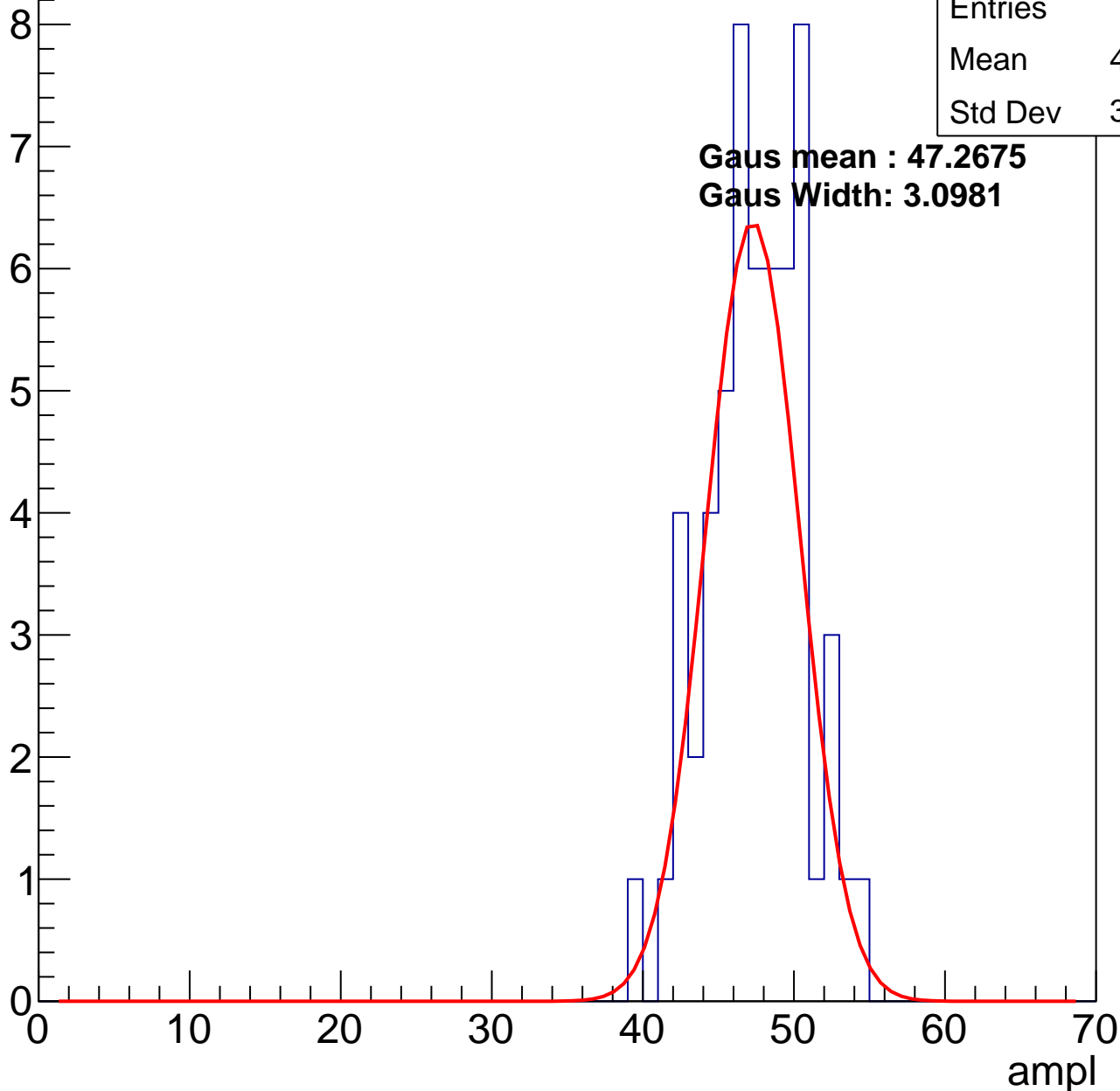
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	47.04
Std Dev	3.173

Gaus mean : 47.2675

Gaus Width: 3.0981

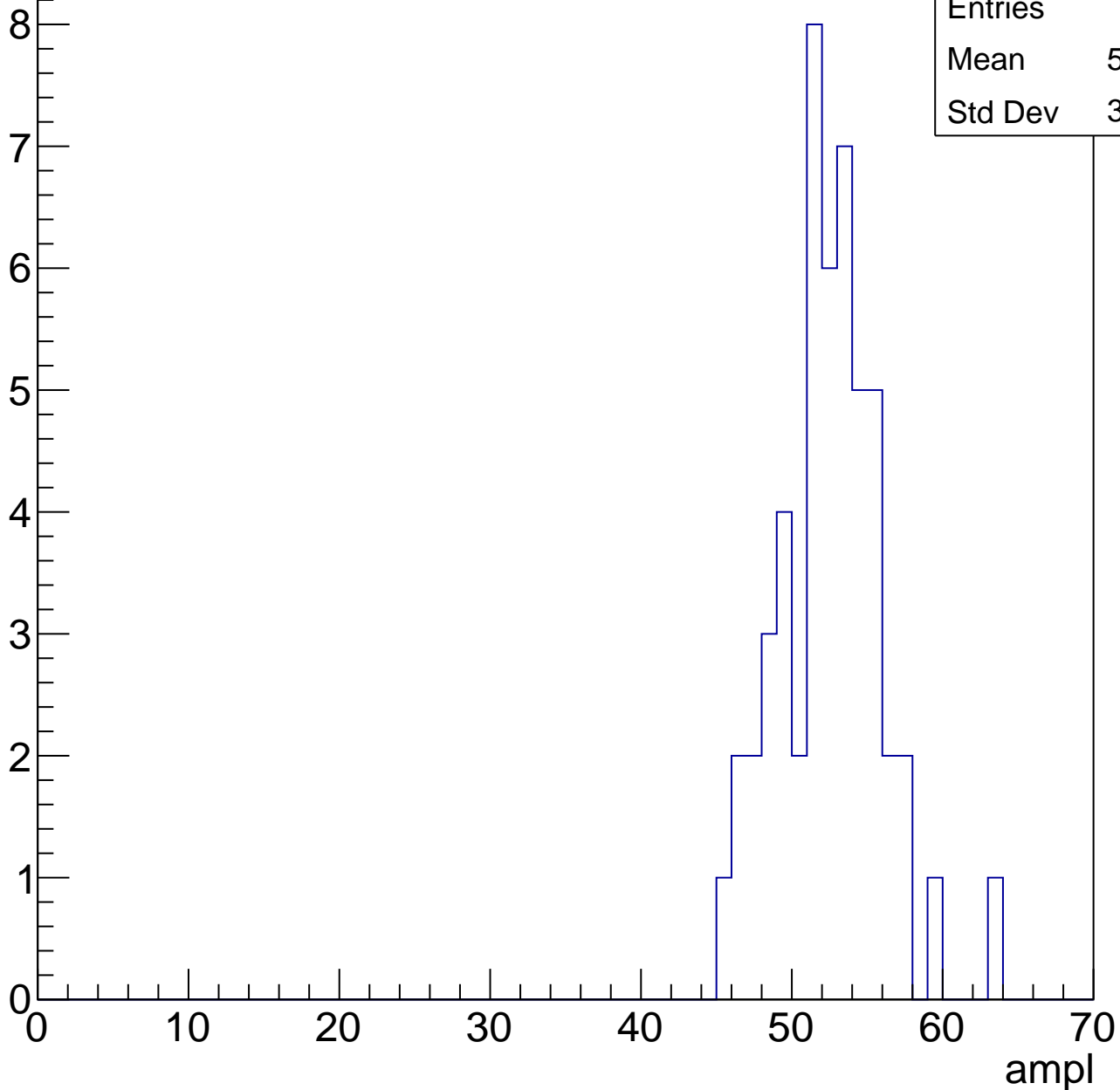


B0L001S, U19-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

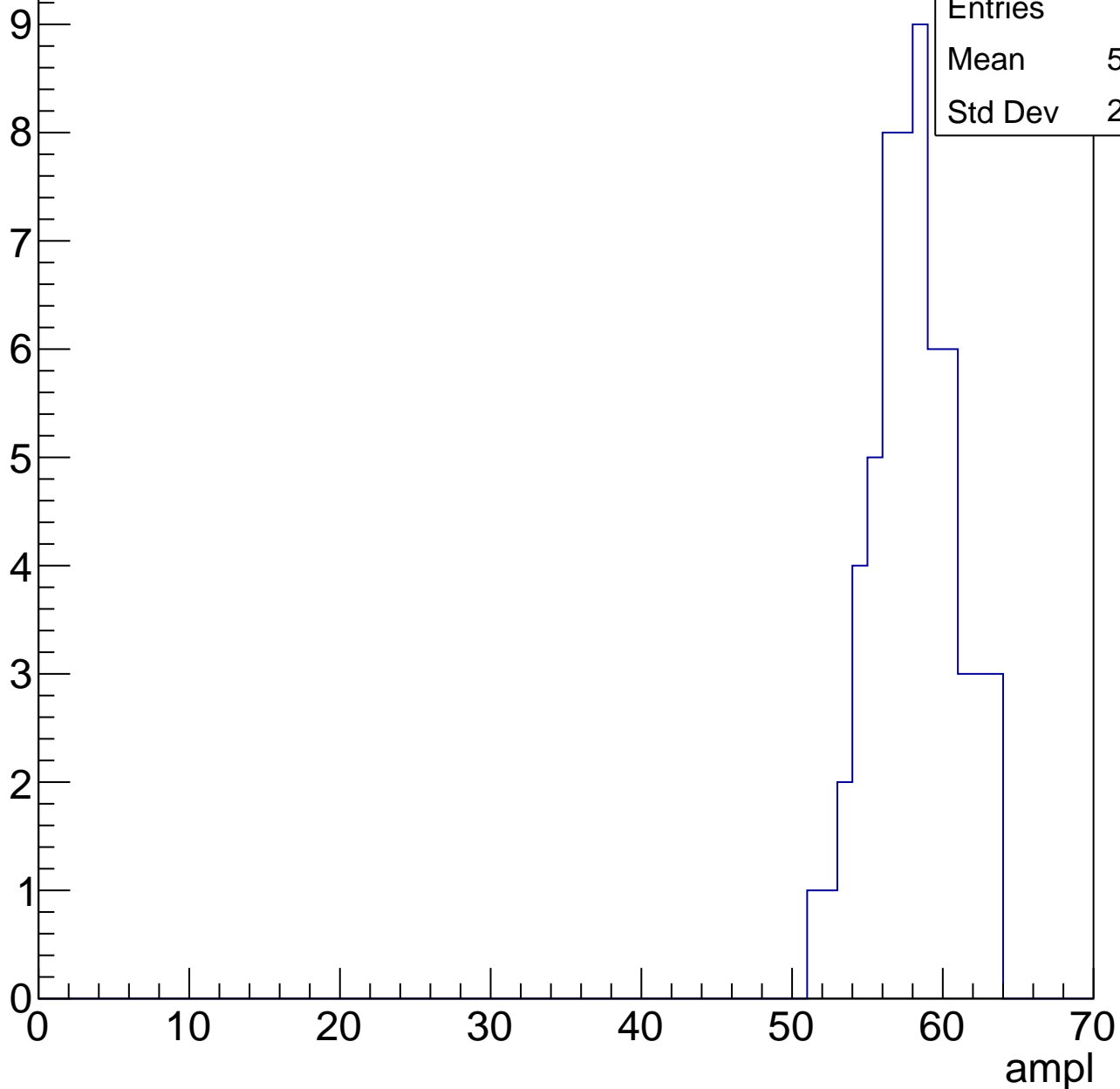
Entries	51
Mean	52.06
Std Dev	3.415



B0L001S, U19-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

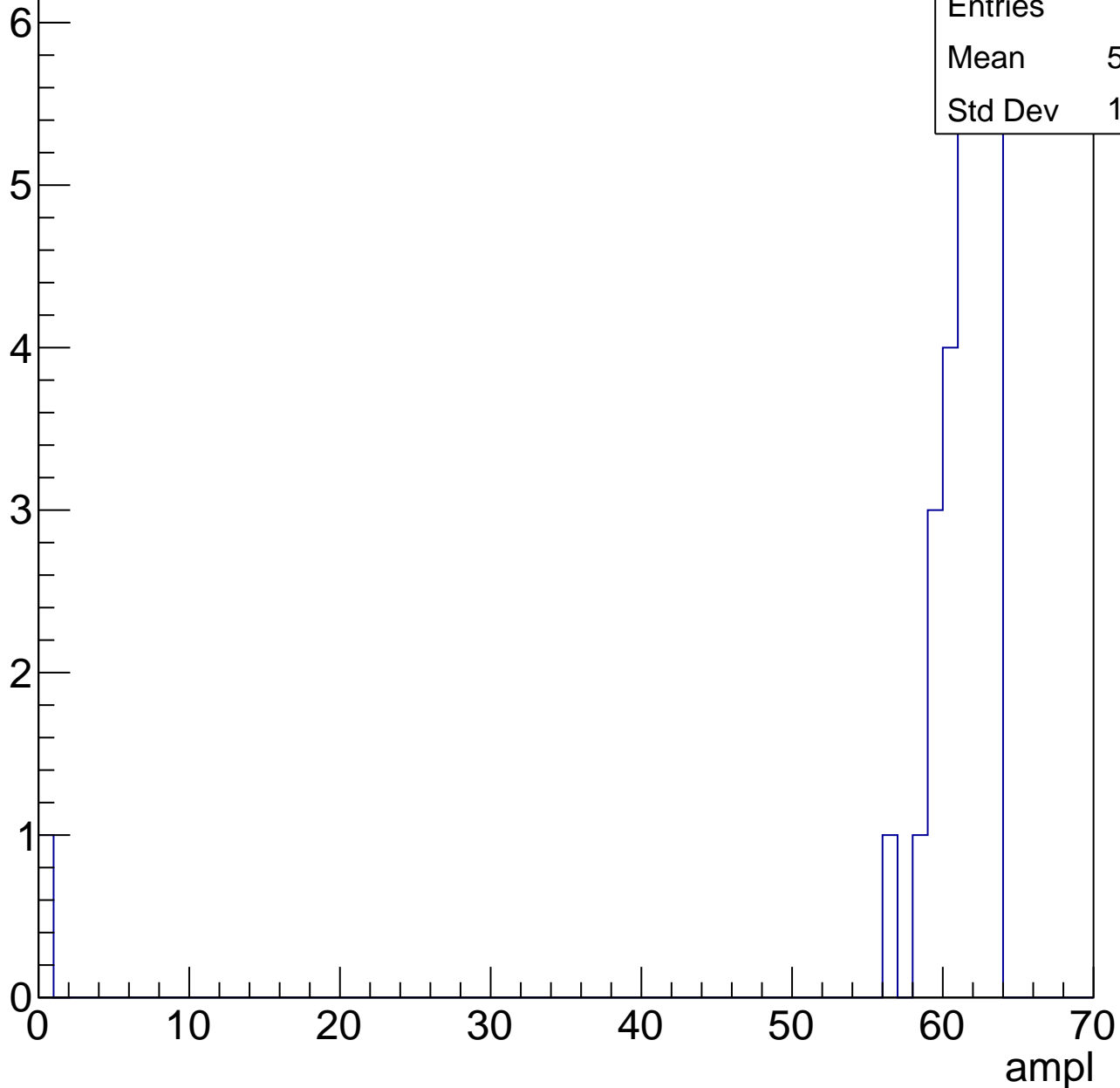


Entries	59
Mean	57.59
Std Dev	2.775

B0L001S, U19-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	26
Std Dev	0

ampl

B0L001S, U19-ch6, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	31.07
Std Dev	6.303

Gaus mean : 32.5397

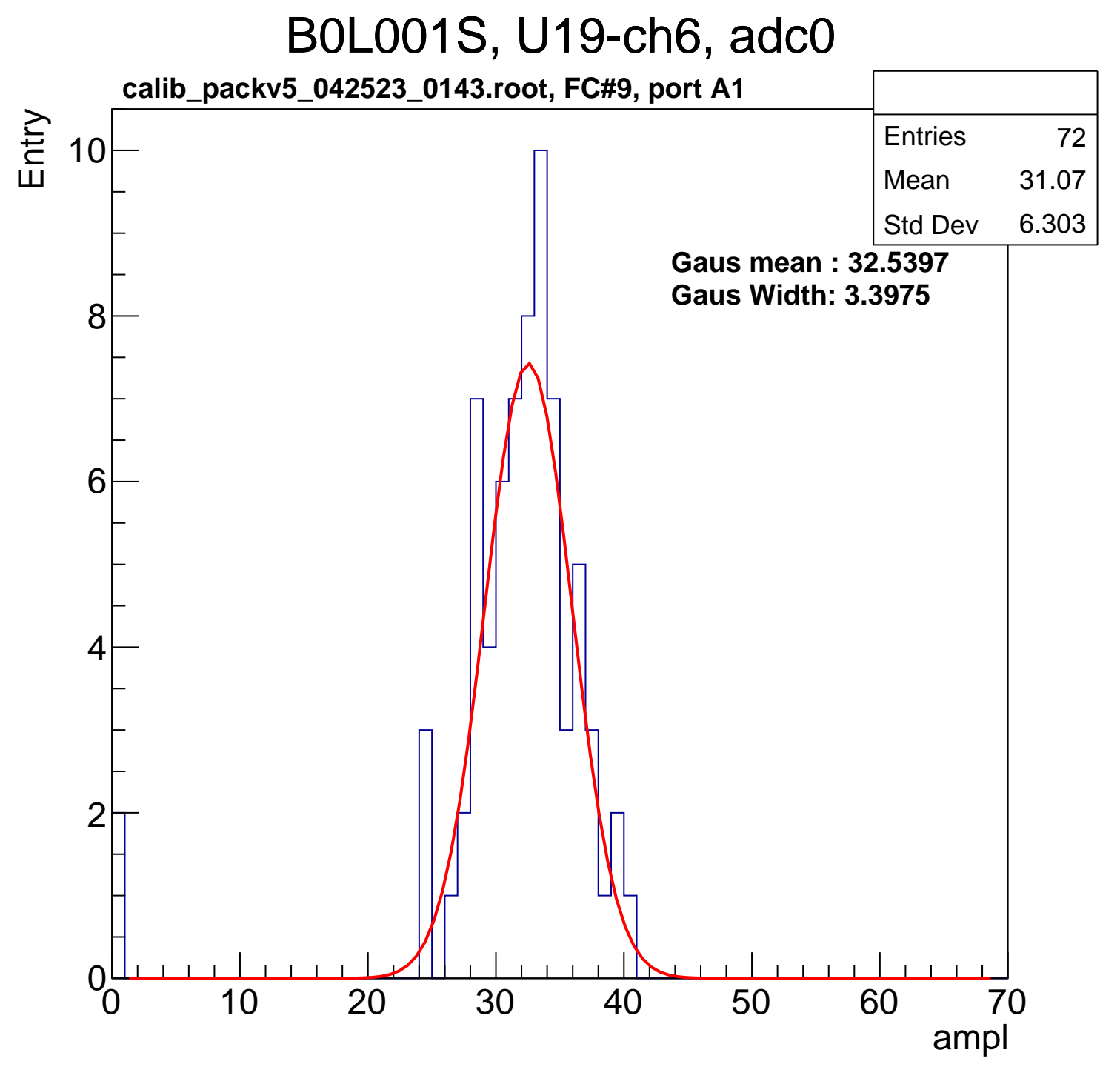
Gaus Width: 3.3975

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch6, adc1

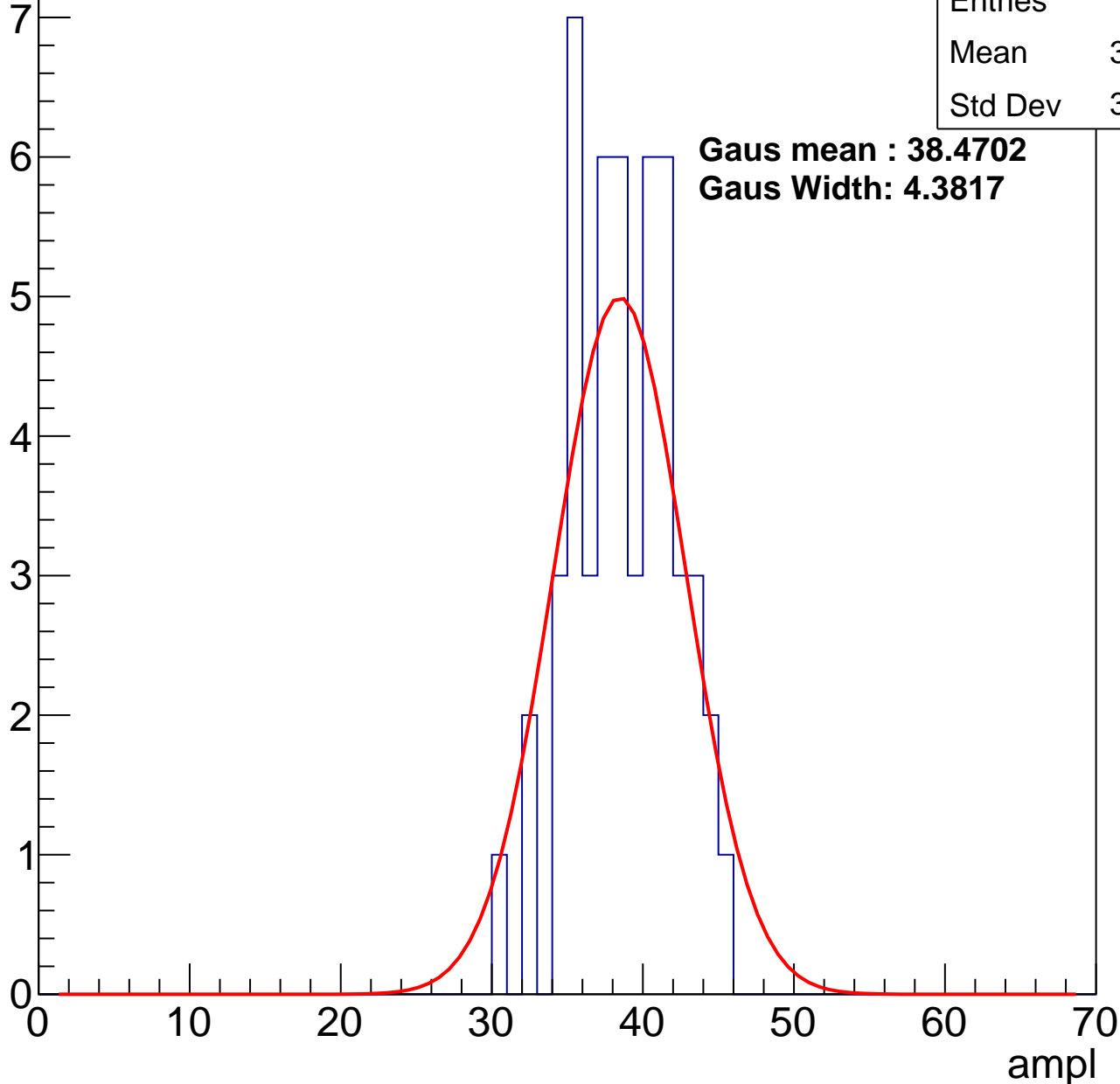
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	38.27
Std Dev	3.358

Gaus mean : 38.4702

Gaus Width: 4.3817



B0L001S, U19-ch6, adc2

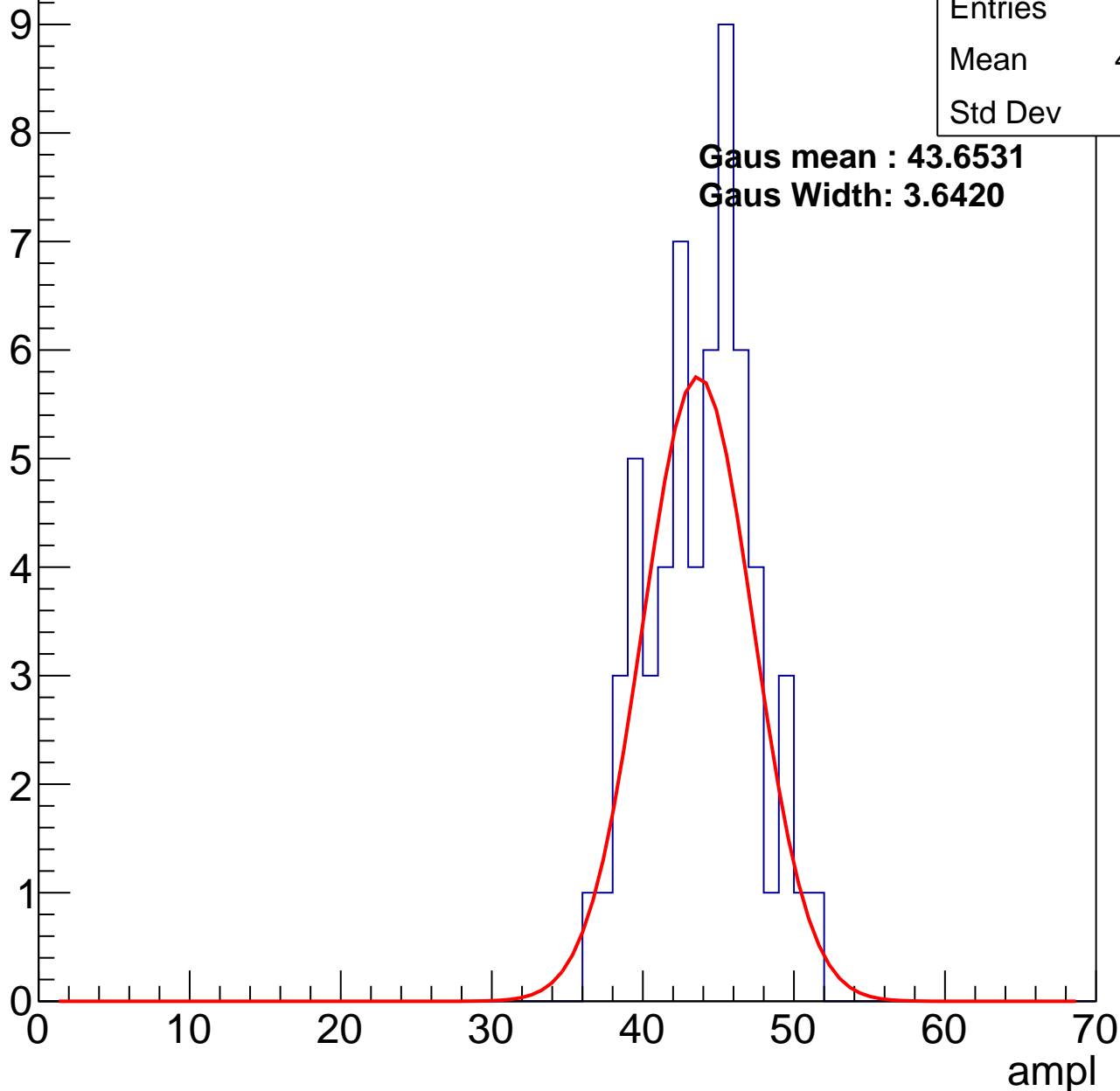
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.41
Std Dev	3.41

Gaus mean : 43.6531

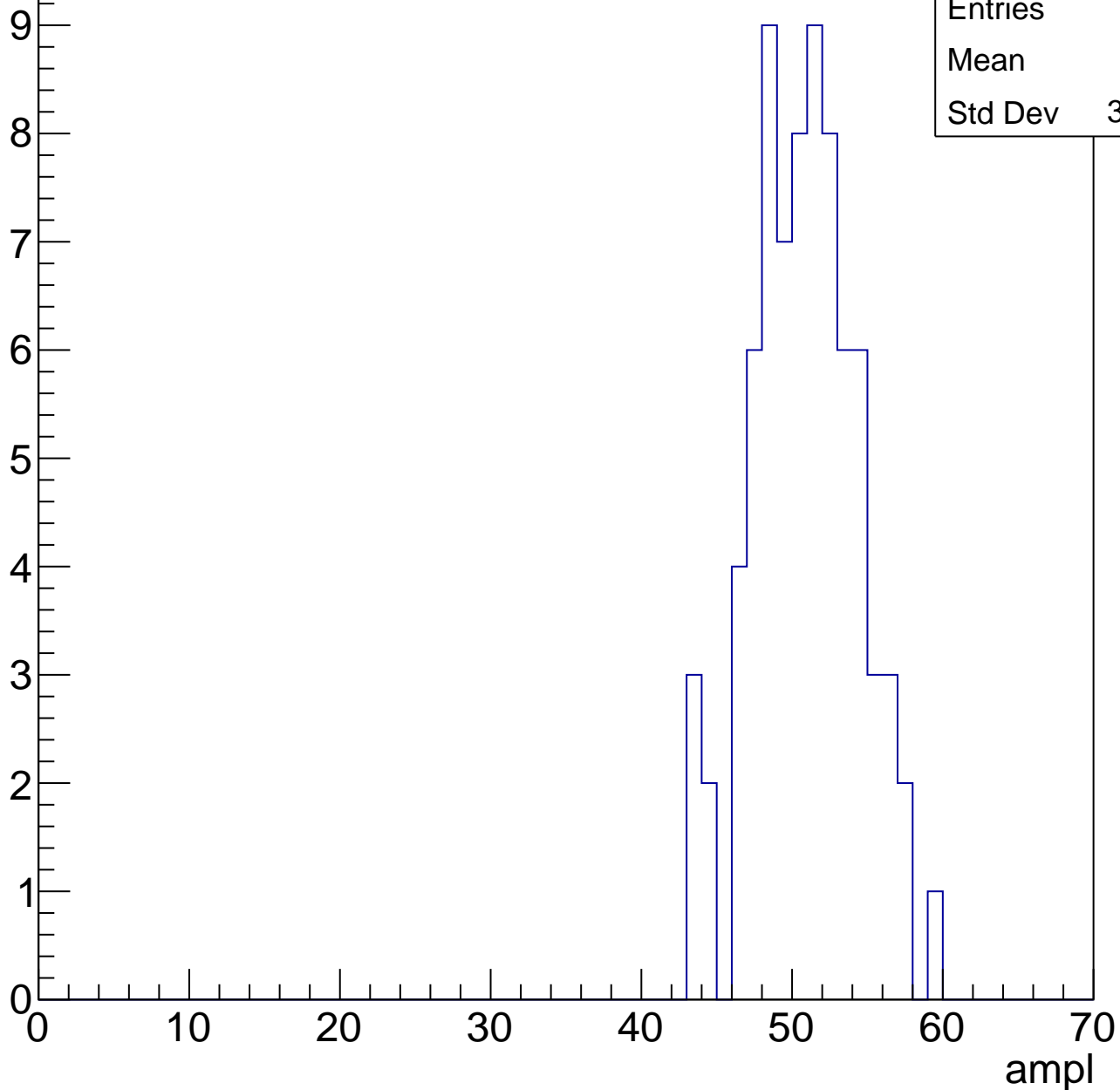
Gaus Width: 3.6420



B0L001S, U19-ch6, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



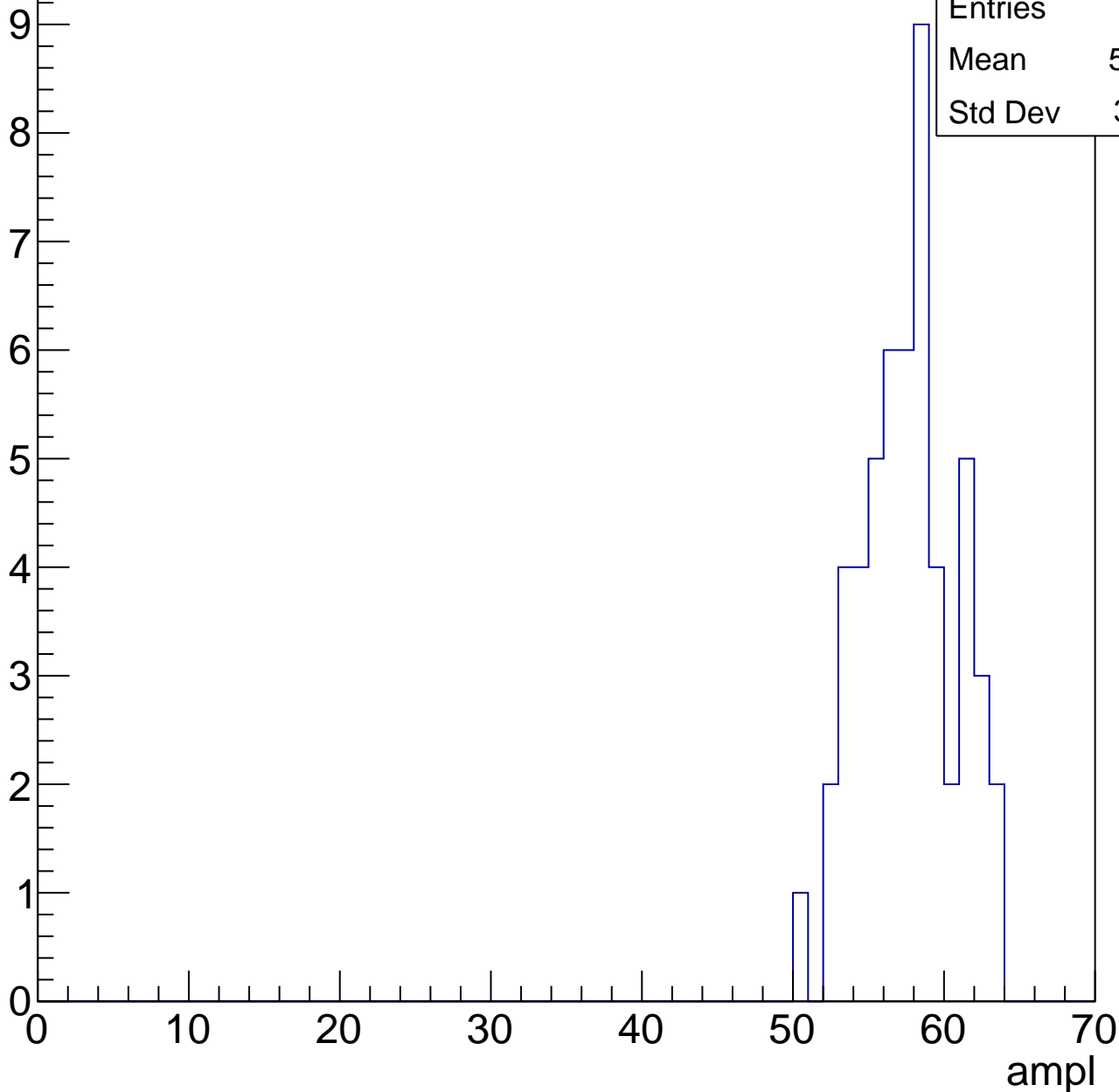
Entries	77
Mean	50.4
Std Dev	3.458

B0L001S, U19-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

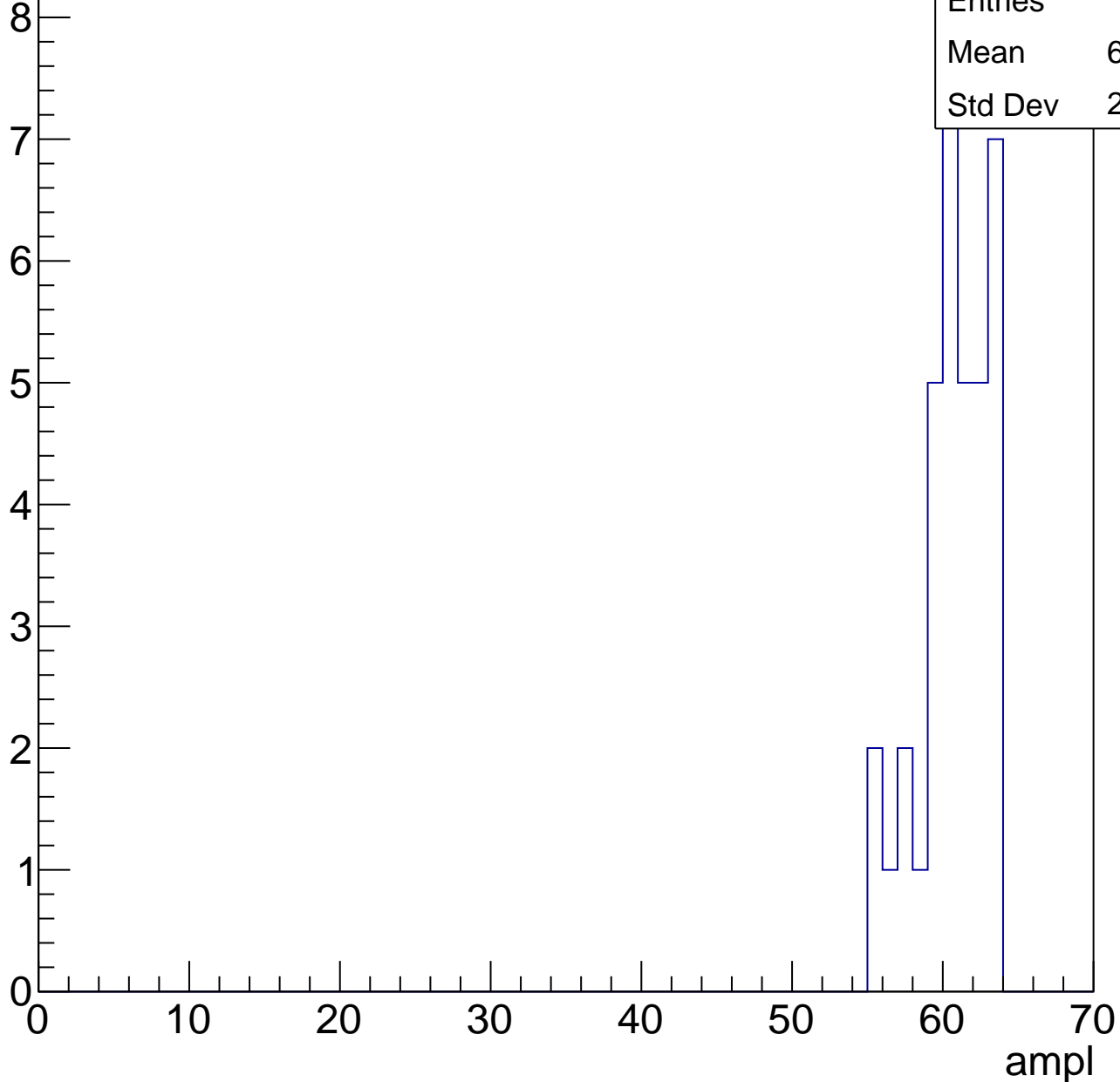
Entries	53
Mean	57.17
Std Dev	3.051



B0L001S, U19-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	36
Mean	60.25
Std Dev	2.228

B0L001S, U19-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch7, adc0

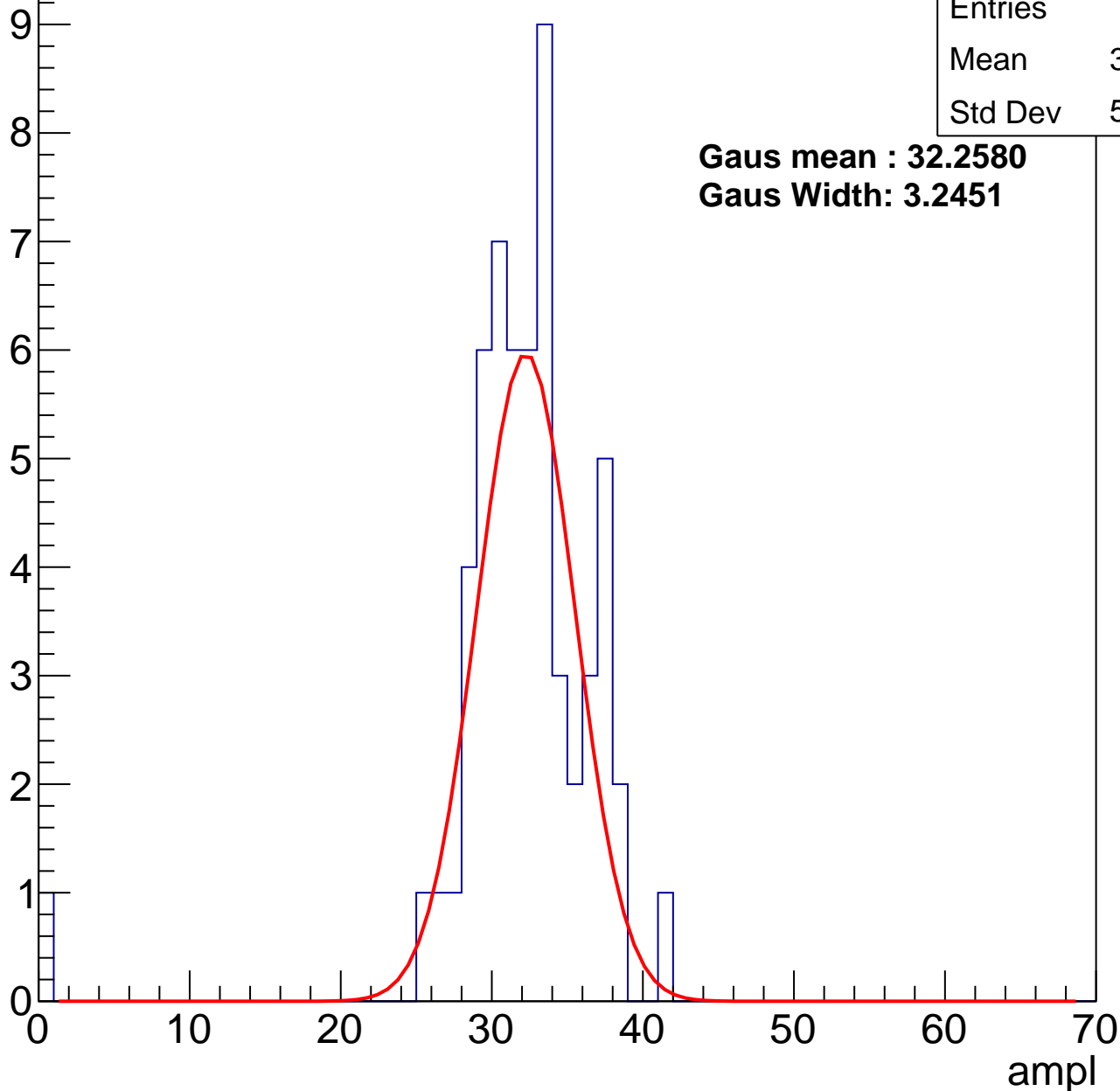
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.57
Std Dev	5.318

Gaus mean : 32.2580

Gaus Width: 3.2451



B0L001S, U19-ch7, adc1

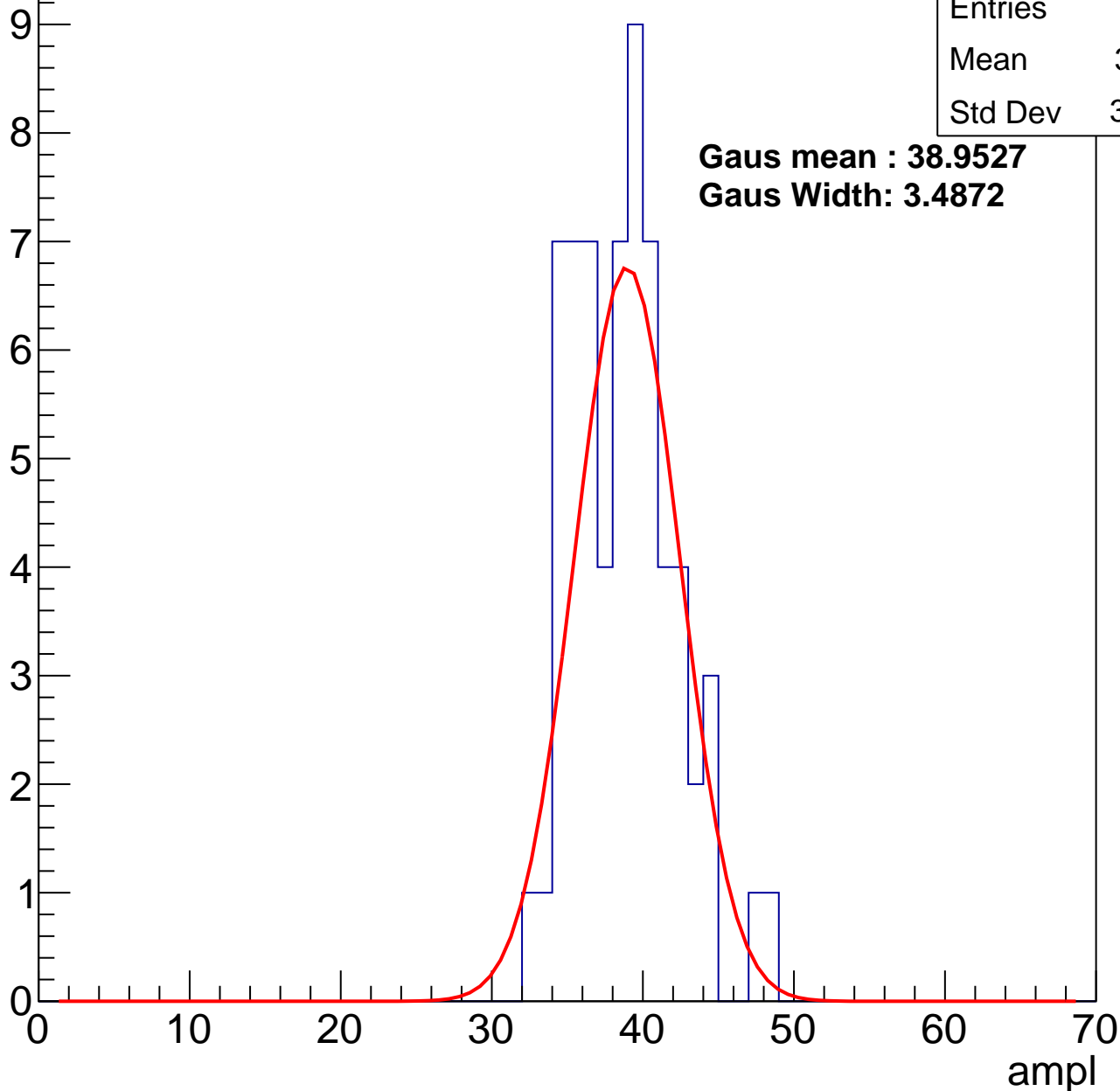
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.31
Std Dev	3.369

Gaus mean : 38.9527

Gaus Width: 3.4872



B0L001S, U19-ch7, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	44.58
Std Dev	3.087

Gaus mean : 45.5215

Gaus Width: 3.4988

Entry

10

8

6

4

2

0

0

10

20

30

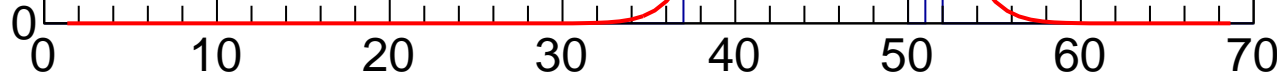
40

50

60

70

ampl

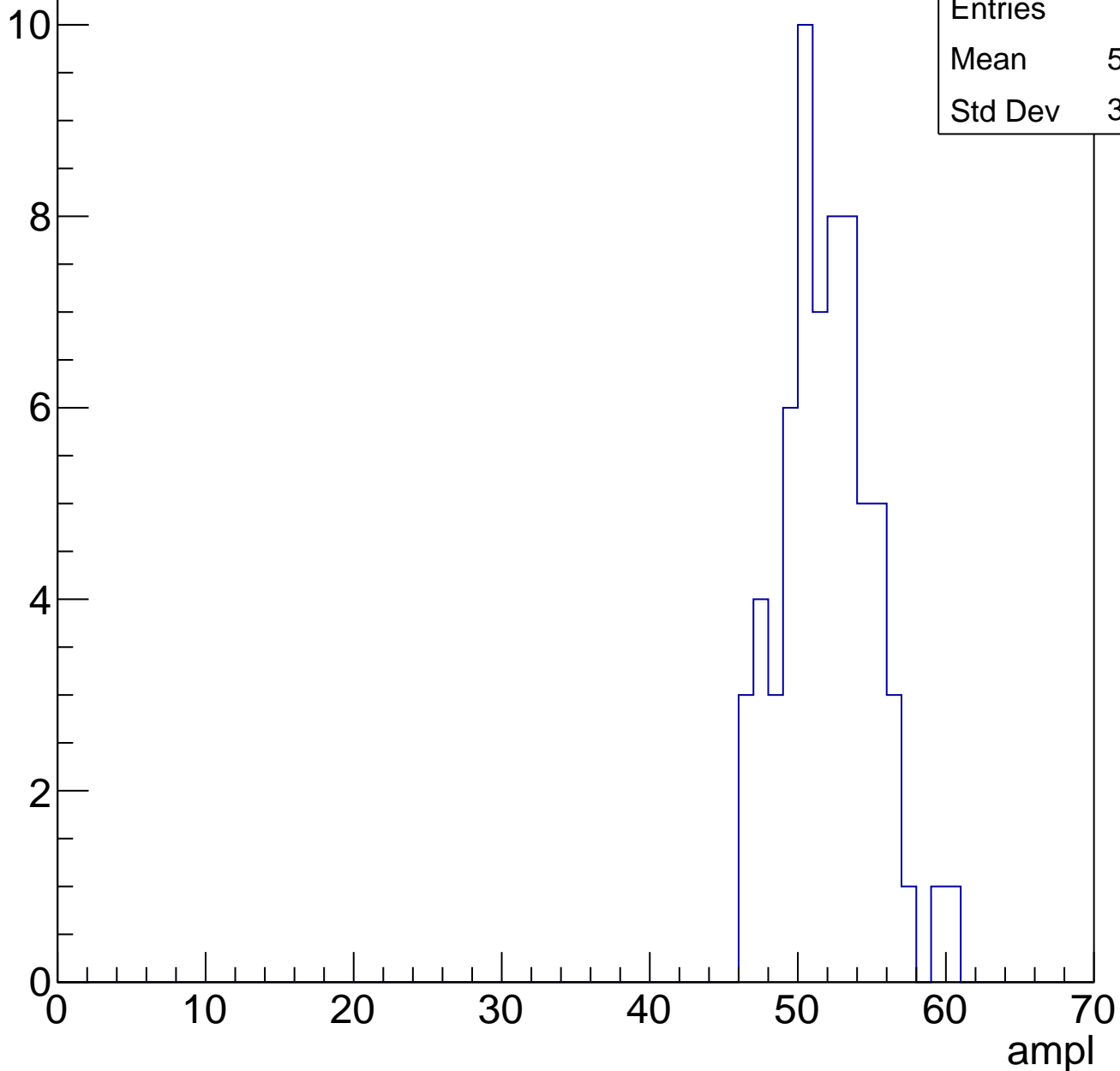


B0L001S, U19-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

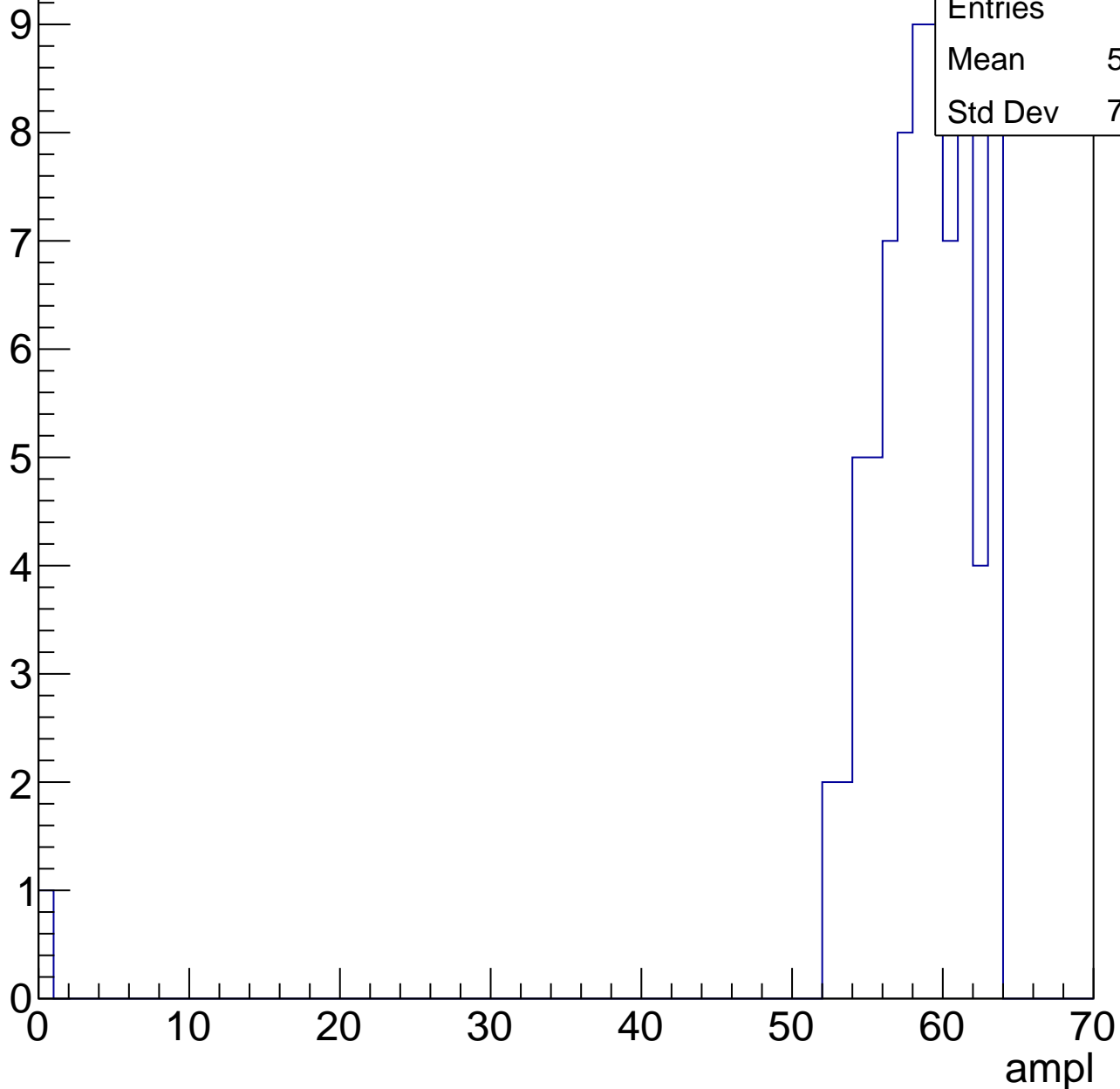
Entries	65
Mean	51.54
Std Dev	3.044



B0L001S, U19-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

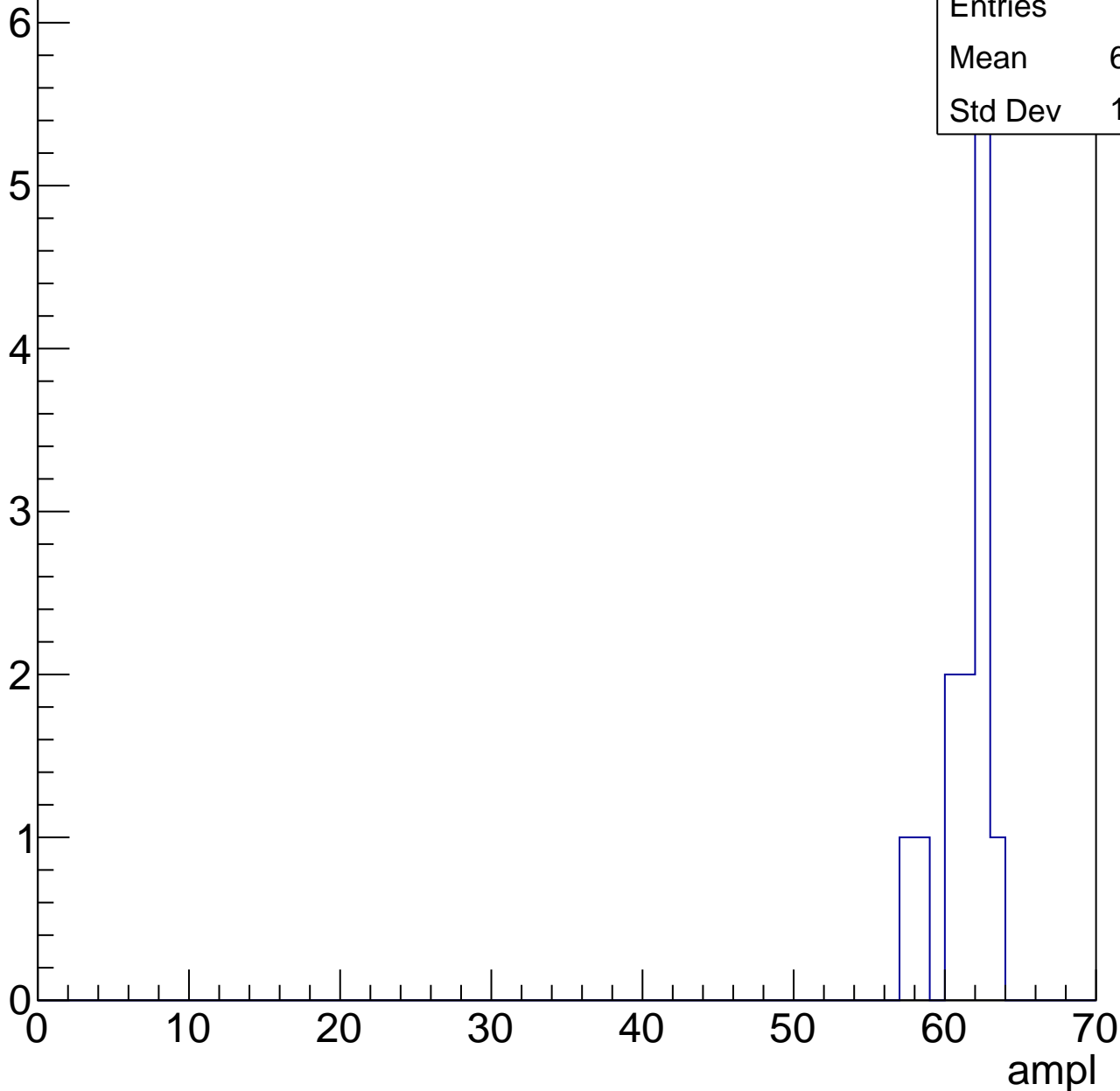


B0L001S, U19-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	13
Mean	60.92
Std Dev	1.685



B0L001S, U19-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch8, adc0

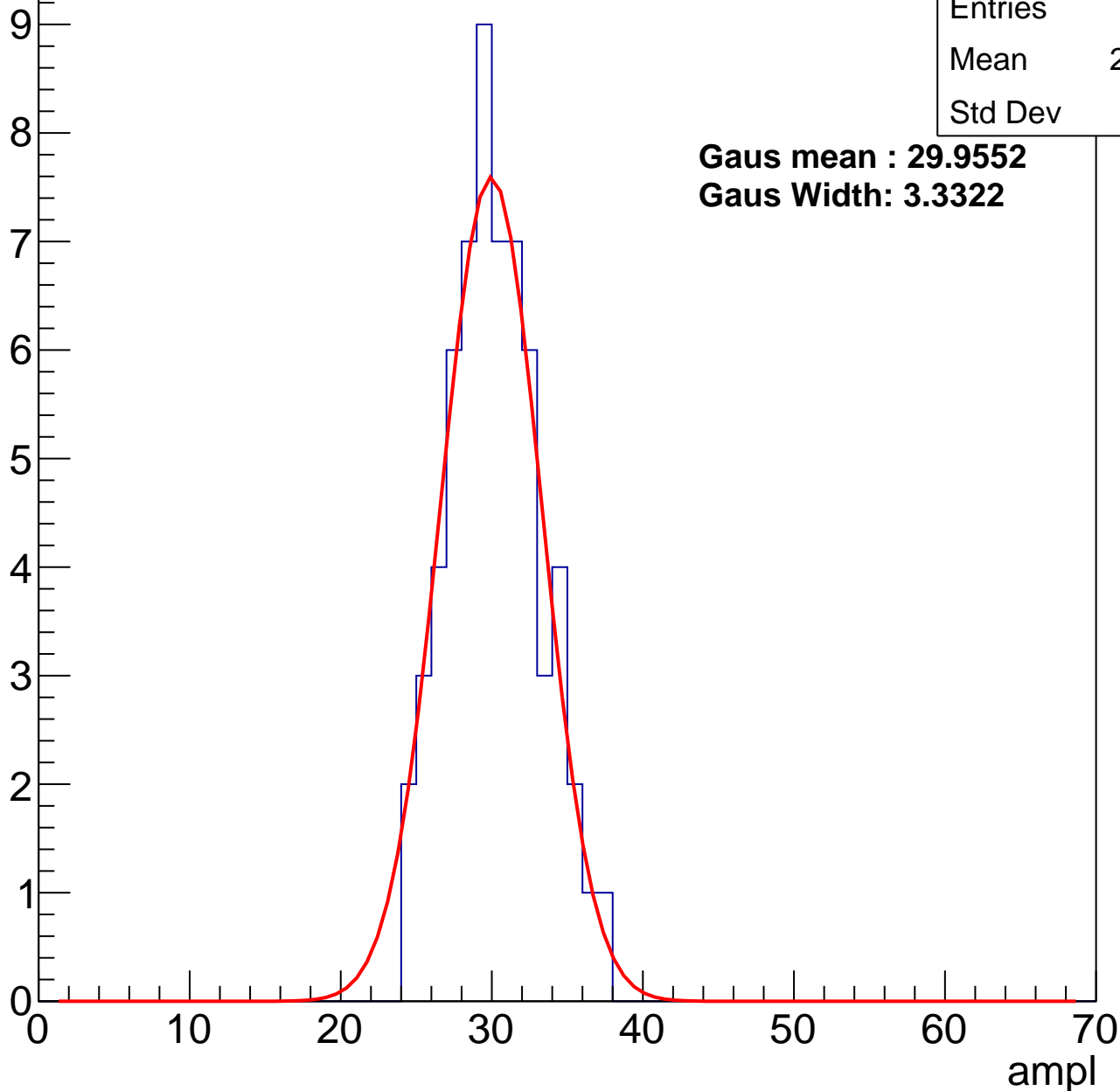
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.73
Std Dev	2.99

Gaus mean : 29.9552

Gaus Width: 3.3322



B0L001S, U19-ch8, adc1

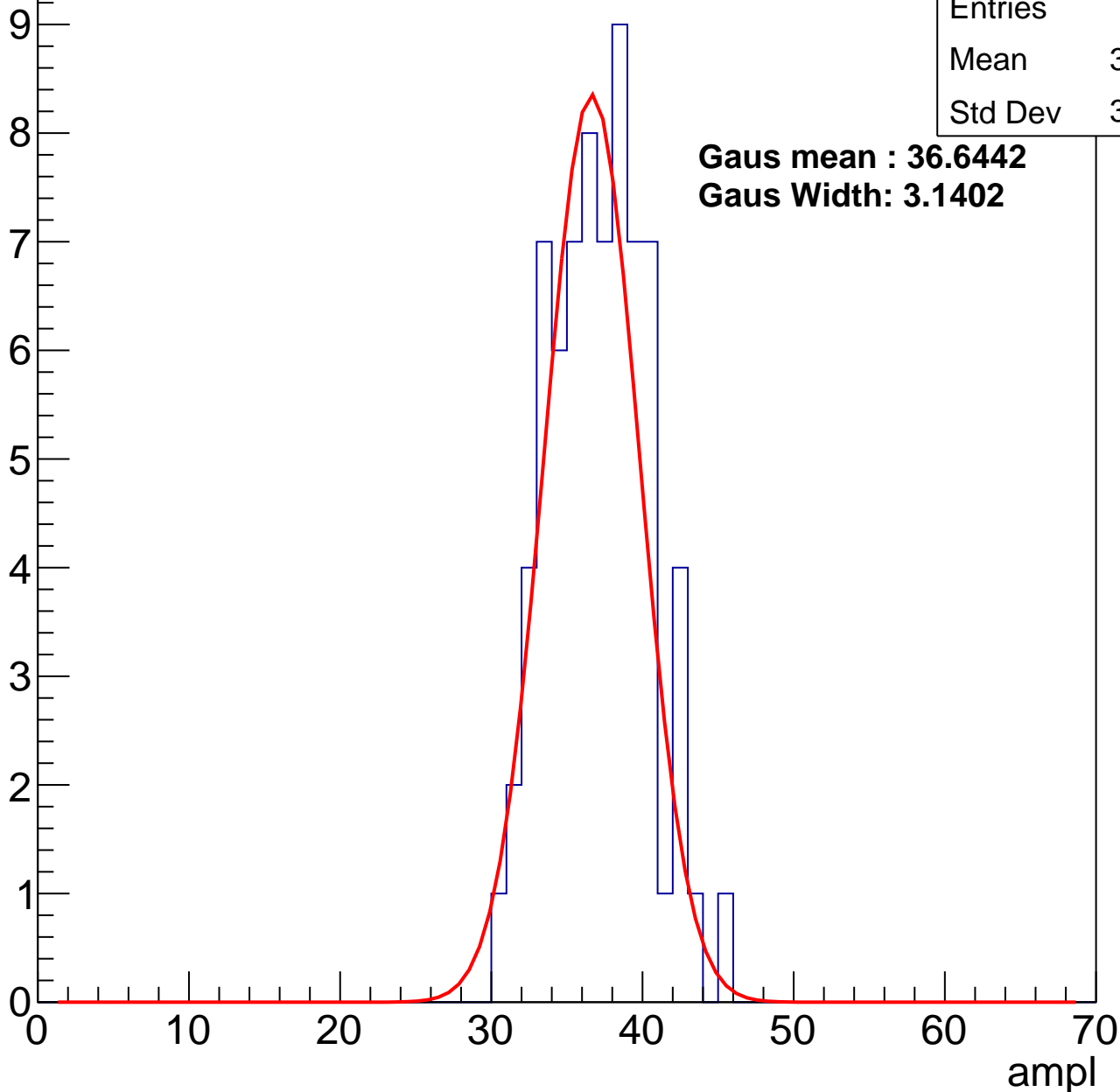
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.65
Std Dev	3.185

Gaus mean : 36.6442

Gaus Width: 3.1402



B0L001S, U19-ch8, adc2

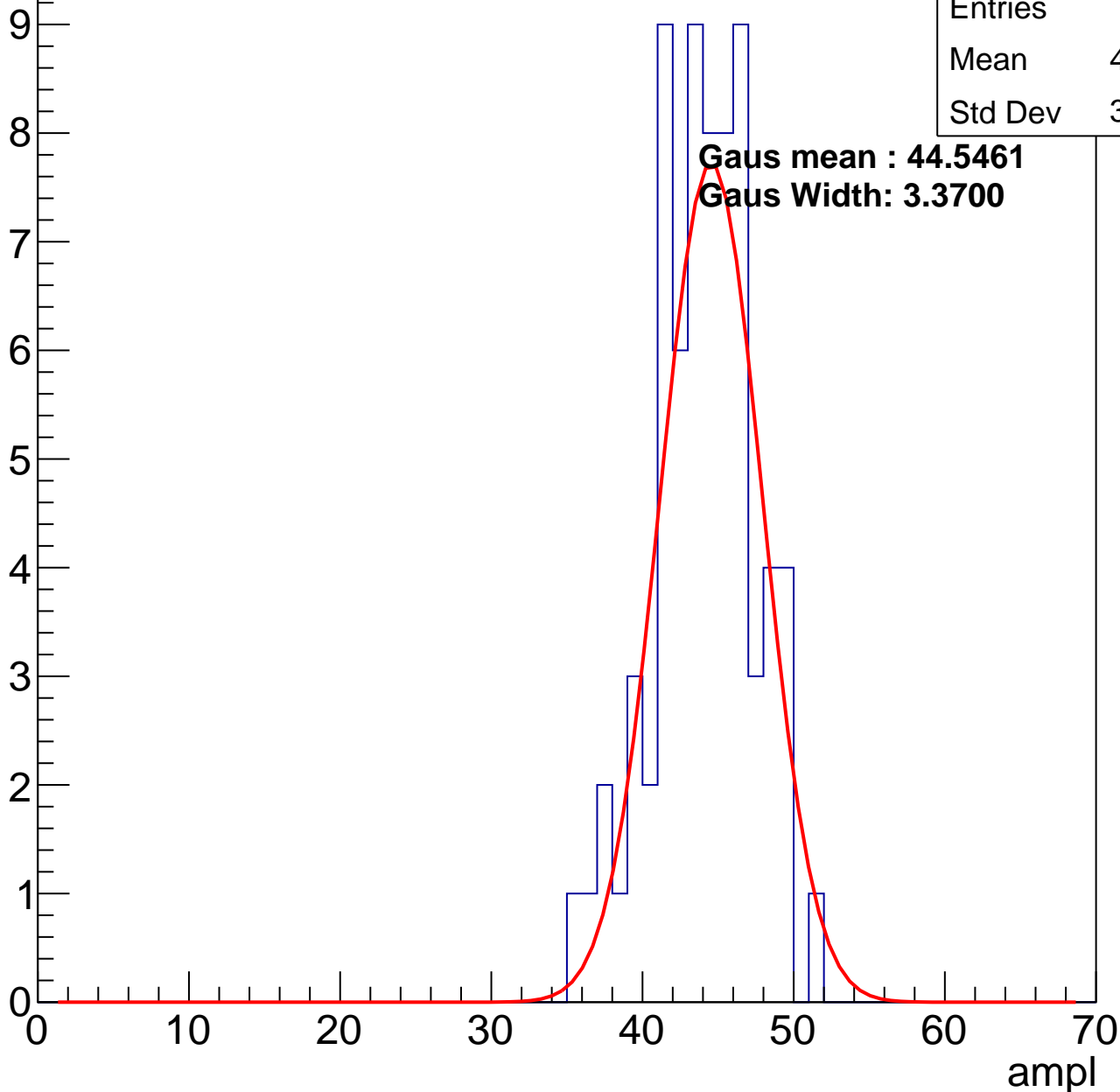
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	43.58
Std Dev	3.296

Gaus mean : 44.5461

Gaus Width: 3.3700

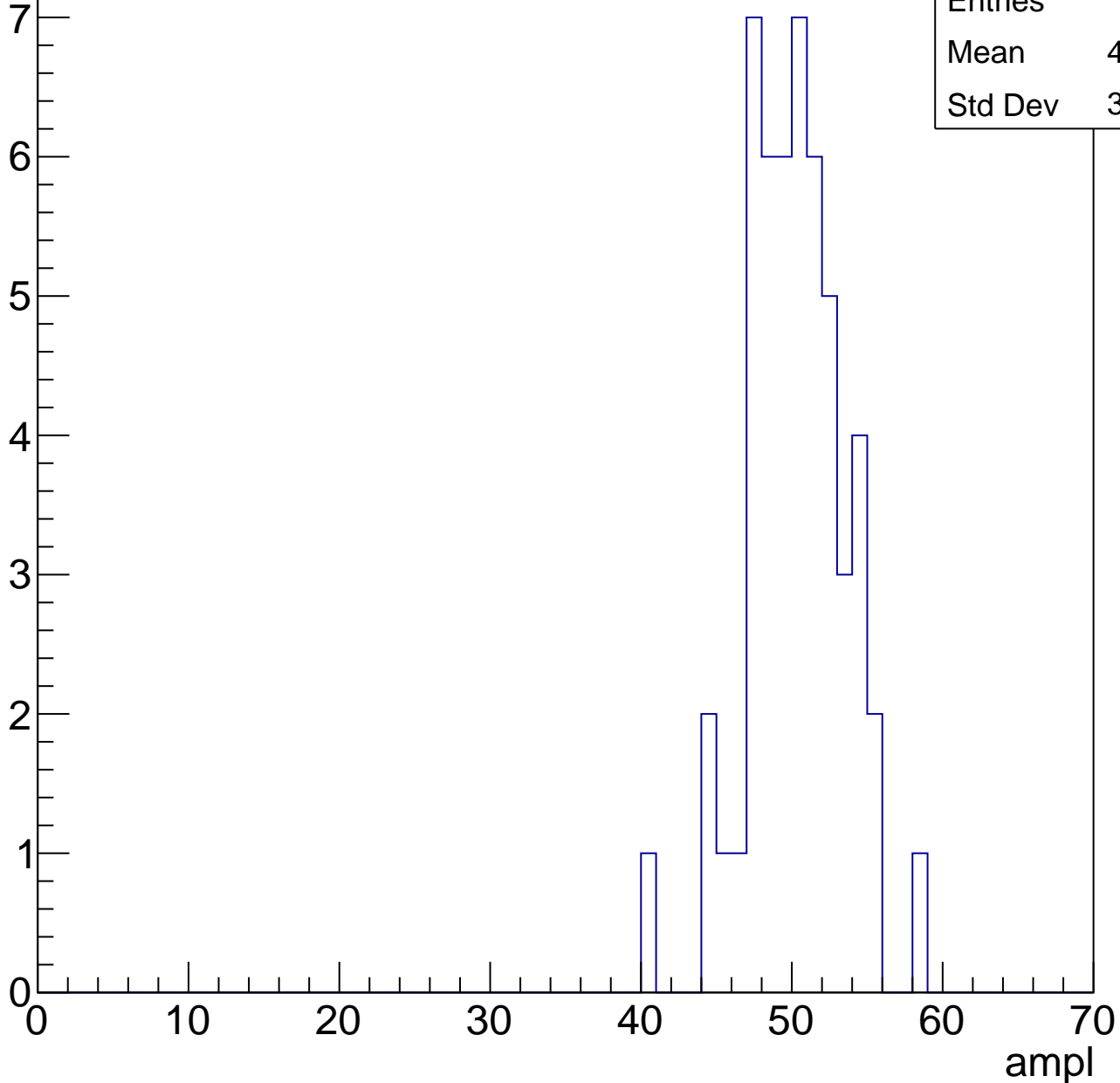


B0L001S, U19-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

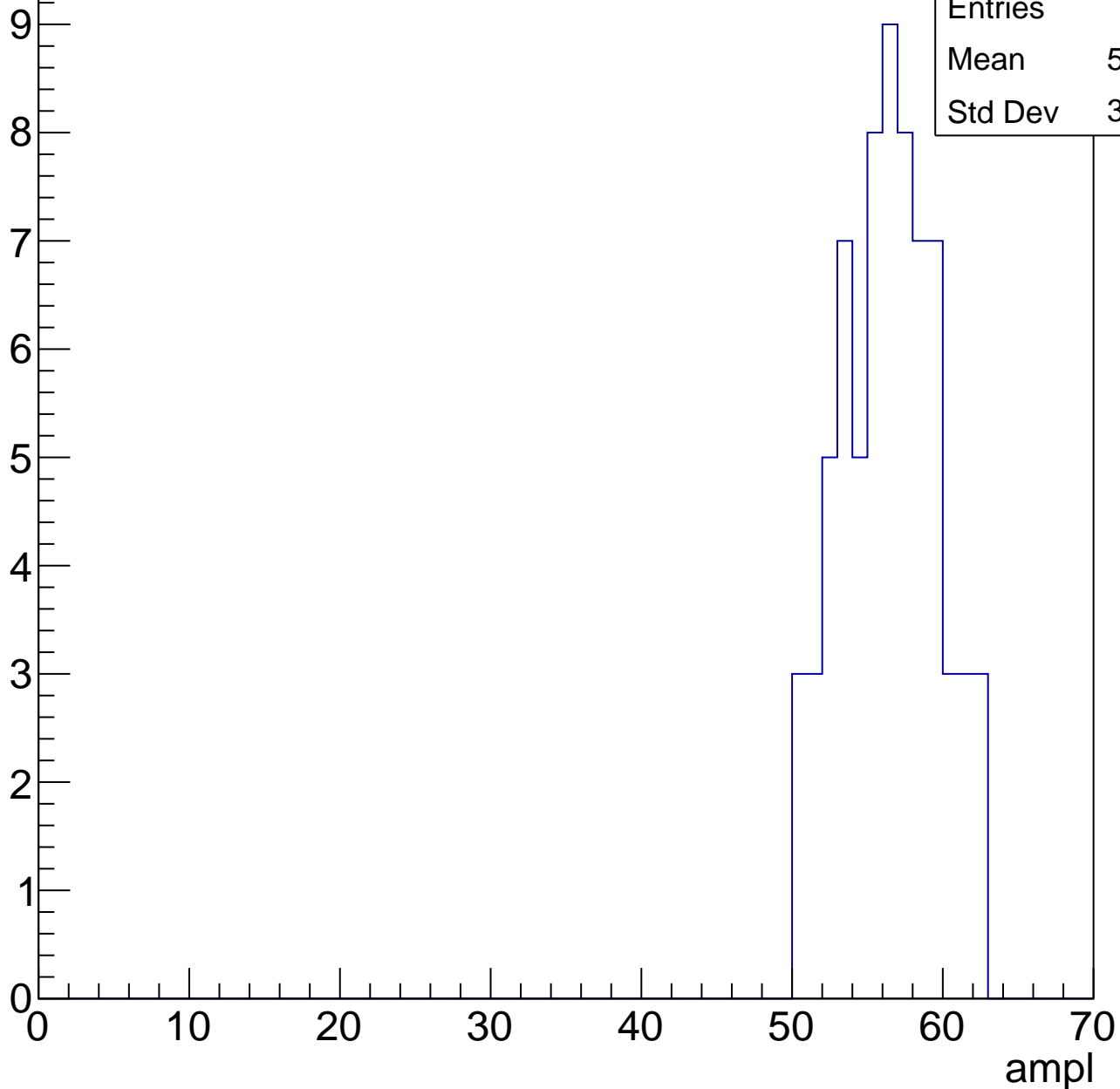
Entries	52
Mean	49.79
Std Dev	3.207



B0L001S, U19-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



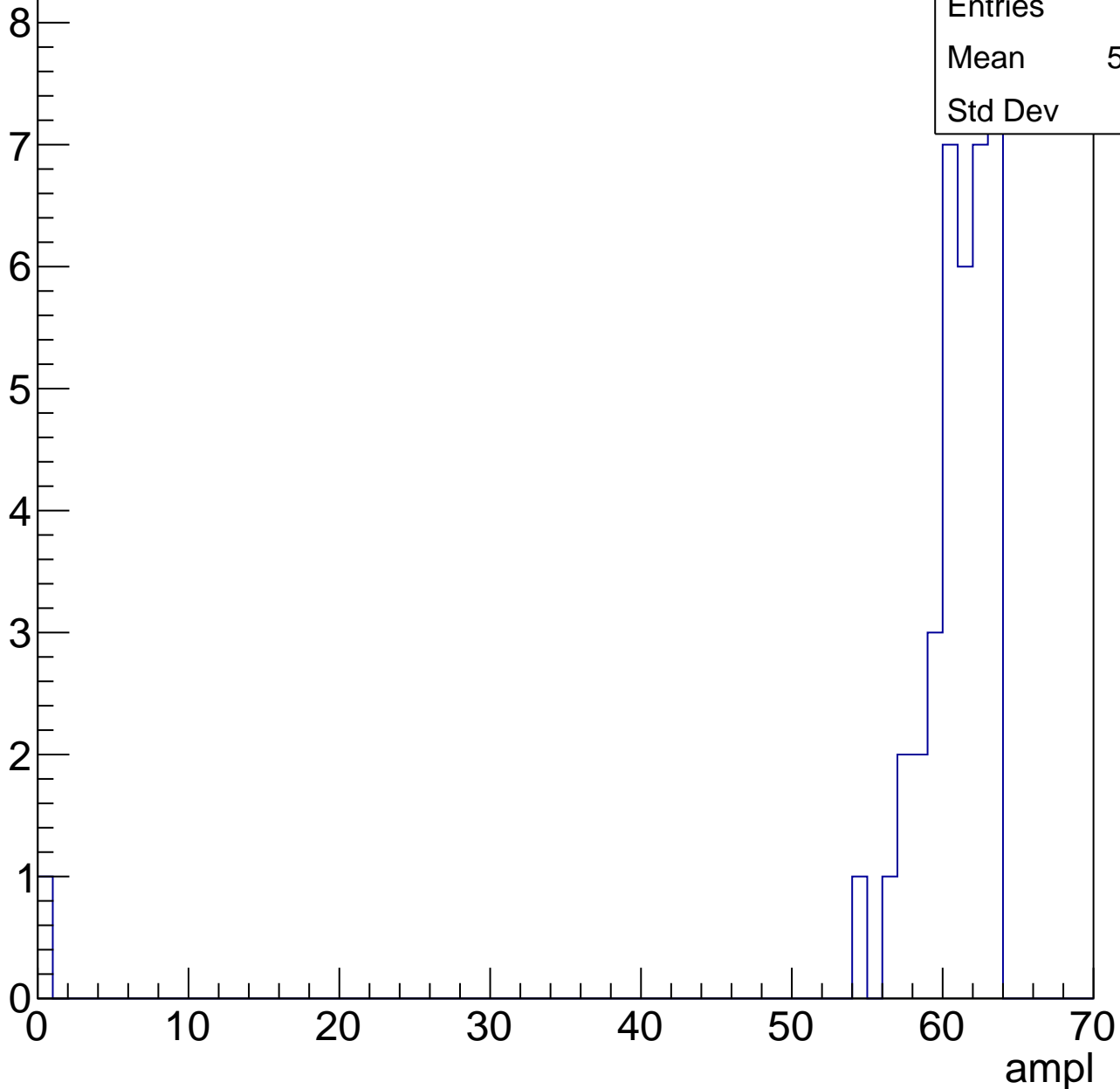
Entries	71
Mean	55.94
Std Dev	3.103

B0L001S, U19-ch8, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.97
Std Dev	9.93



B0L001S, U19-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch9, adc0

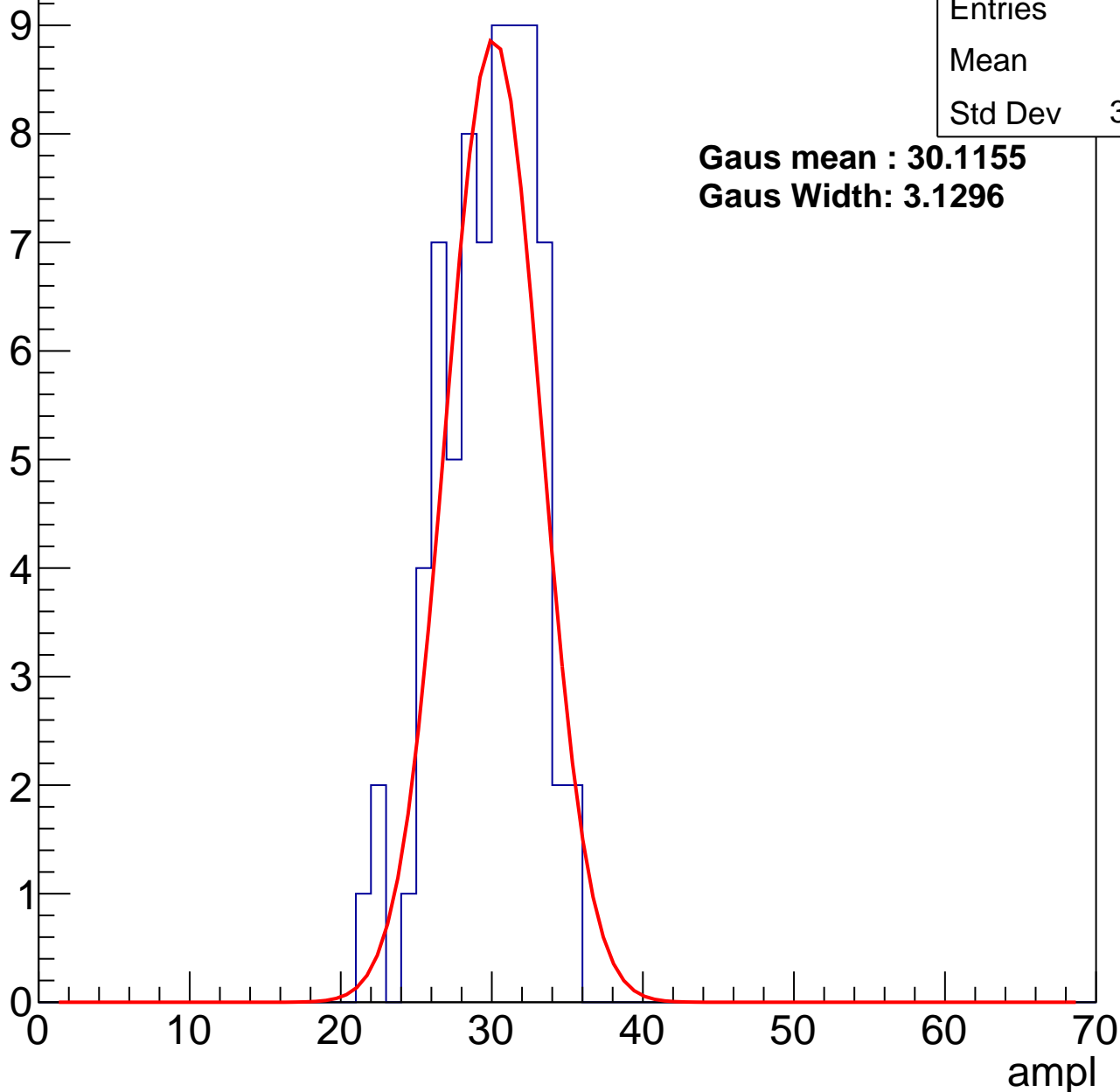
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	29.3
Std Dev	3.086

Gaus mean : 30.1155

Gaus Width: 3.1296



B0L001S, U19-ch9, adc1

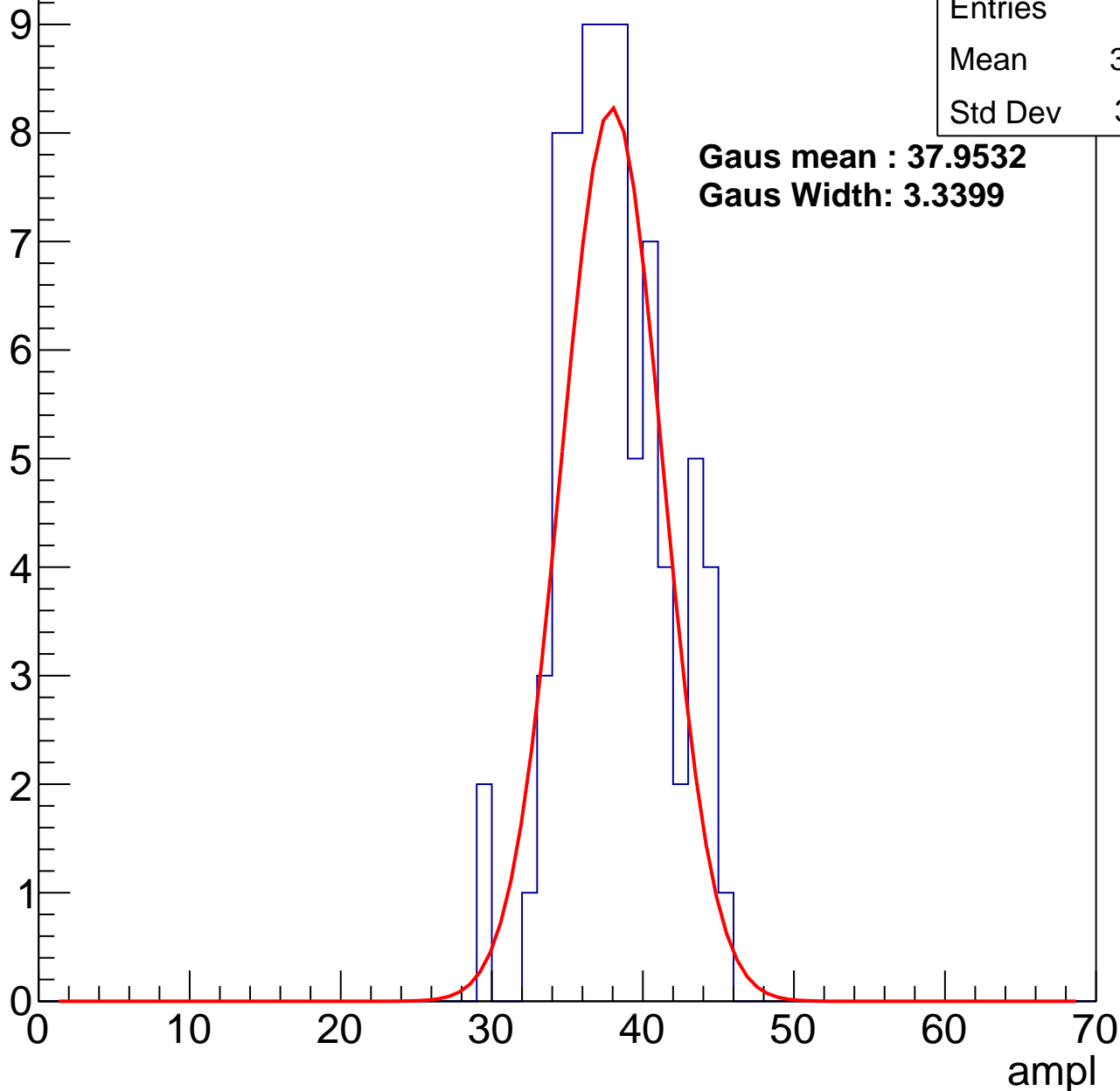
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	37.65
Std Dev	3.471

Gaus mean : 37.9532

Gaus Width: 3.3399



B0L001S, U19-ch9, adc2

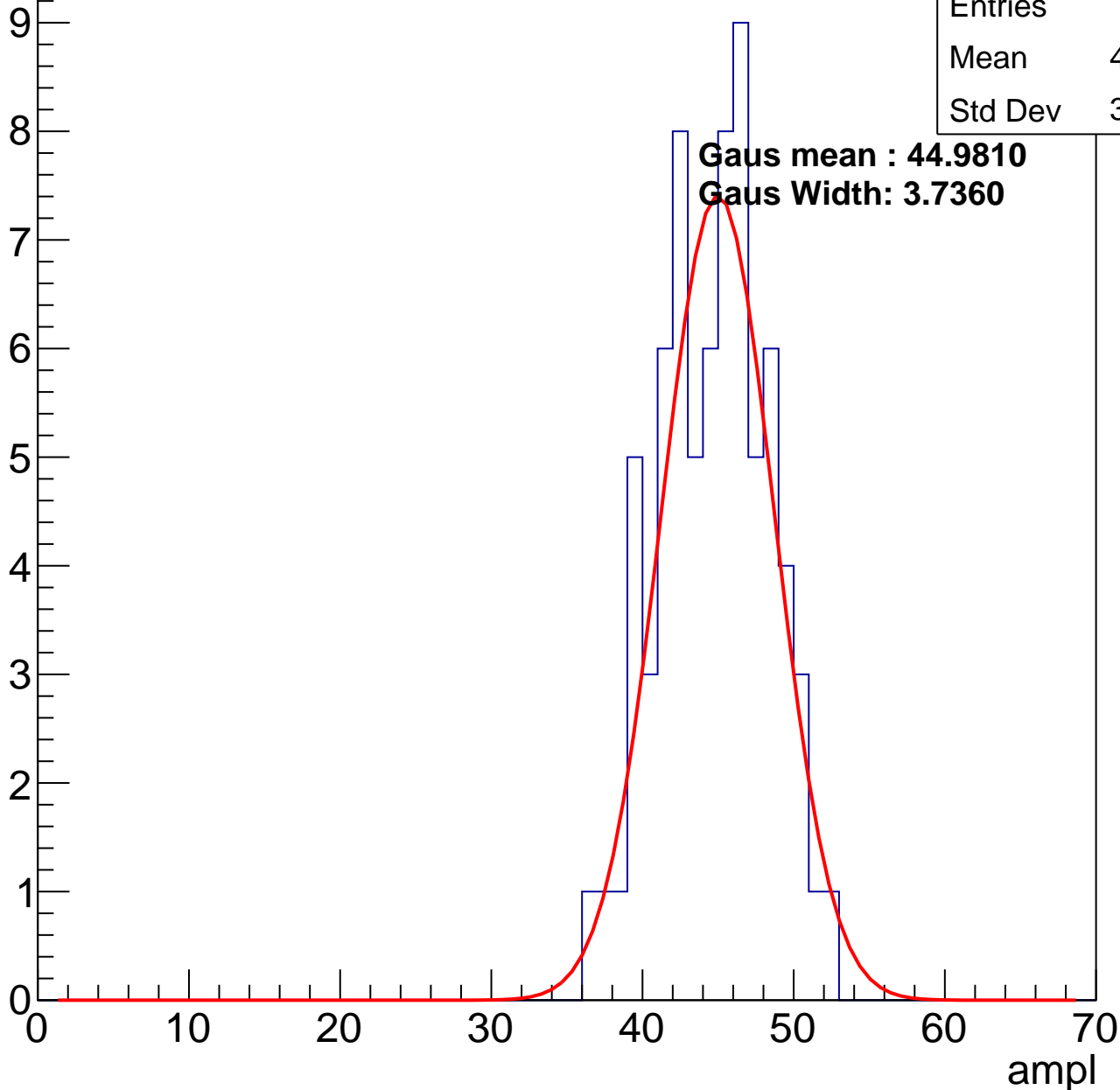
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	44.29
Std Dev	3.536

Gaus mean : 44.9810

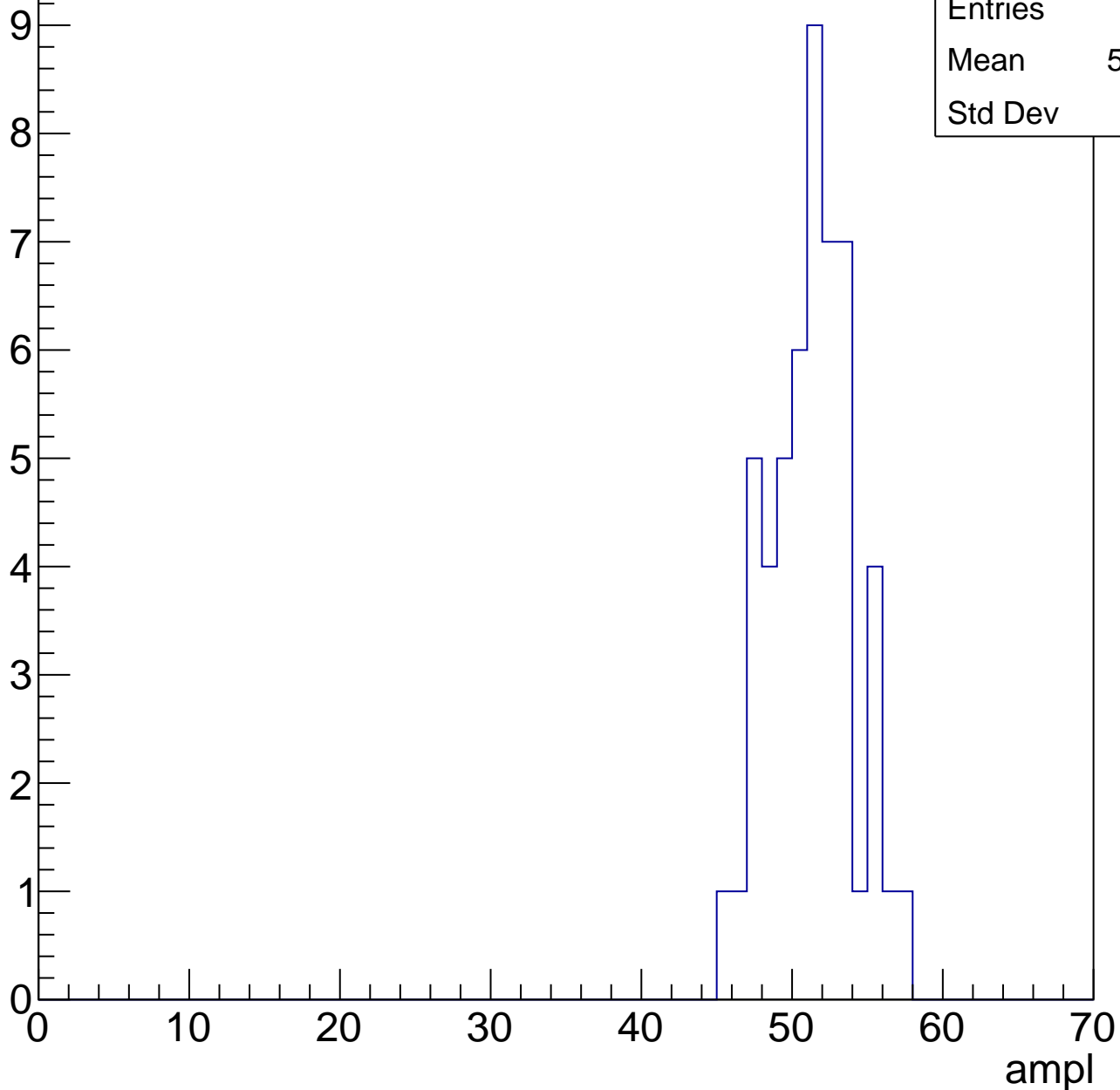
Gaus Width: 3.7360



B0L001S, U19-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



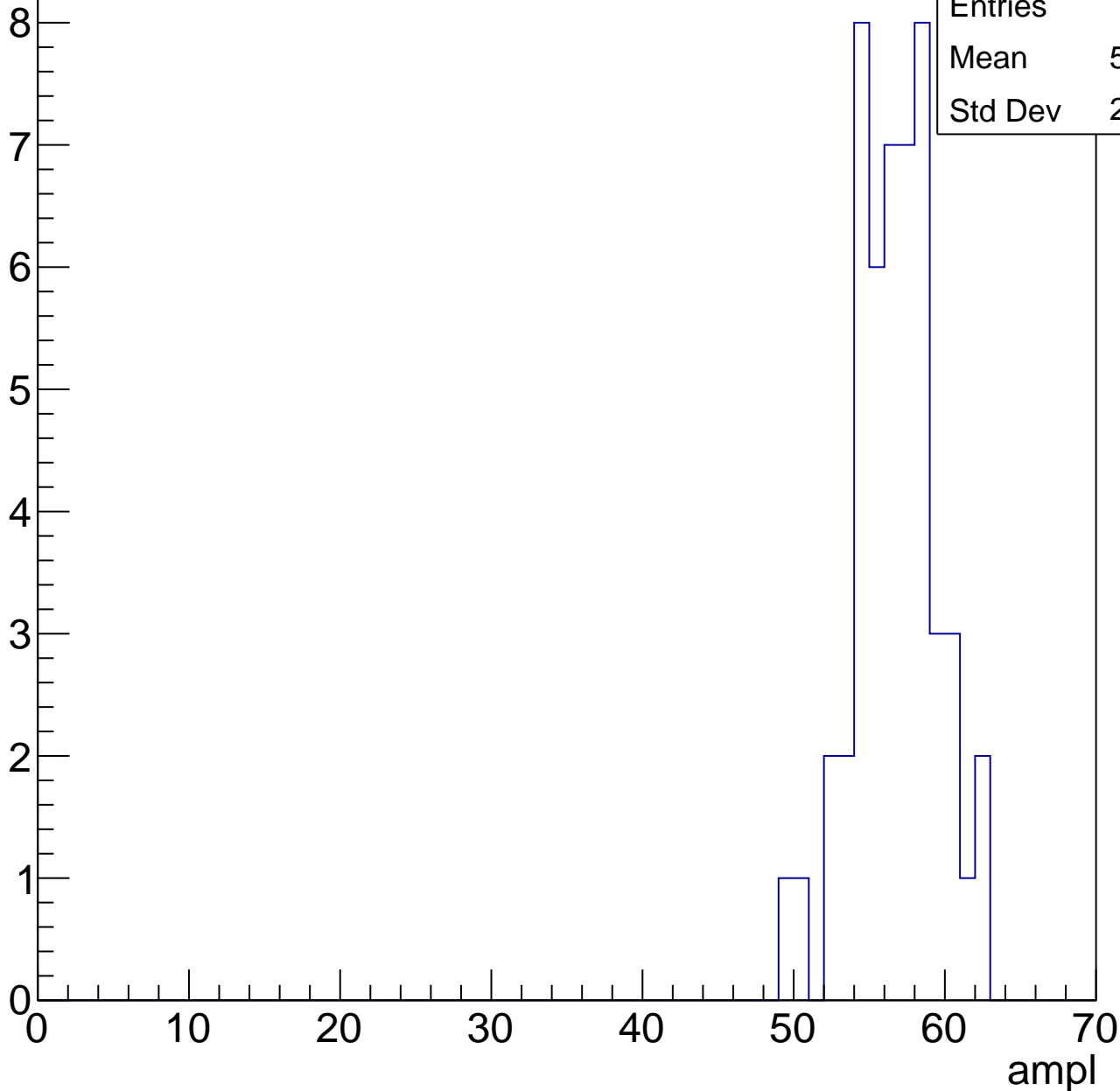
Entries	52
Mean	50.85
Std Dev	2.67

B0L001S, U19-ch9, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.24
Std Dev	2.734

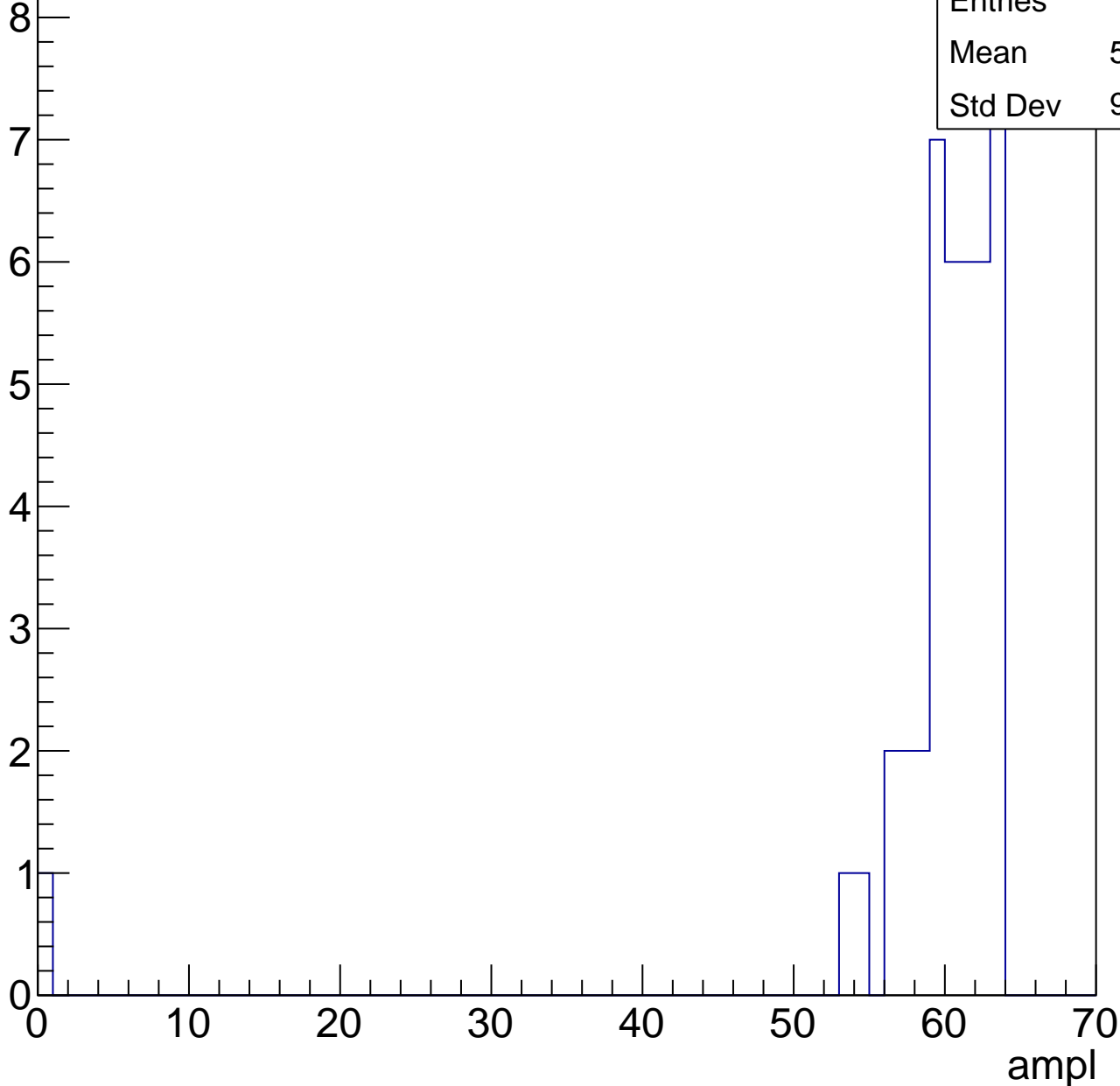


B0L001S, U19-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.67
Std Dev	9.483



B0L001S, U19-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

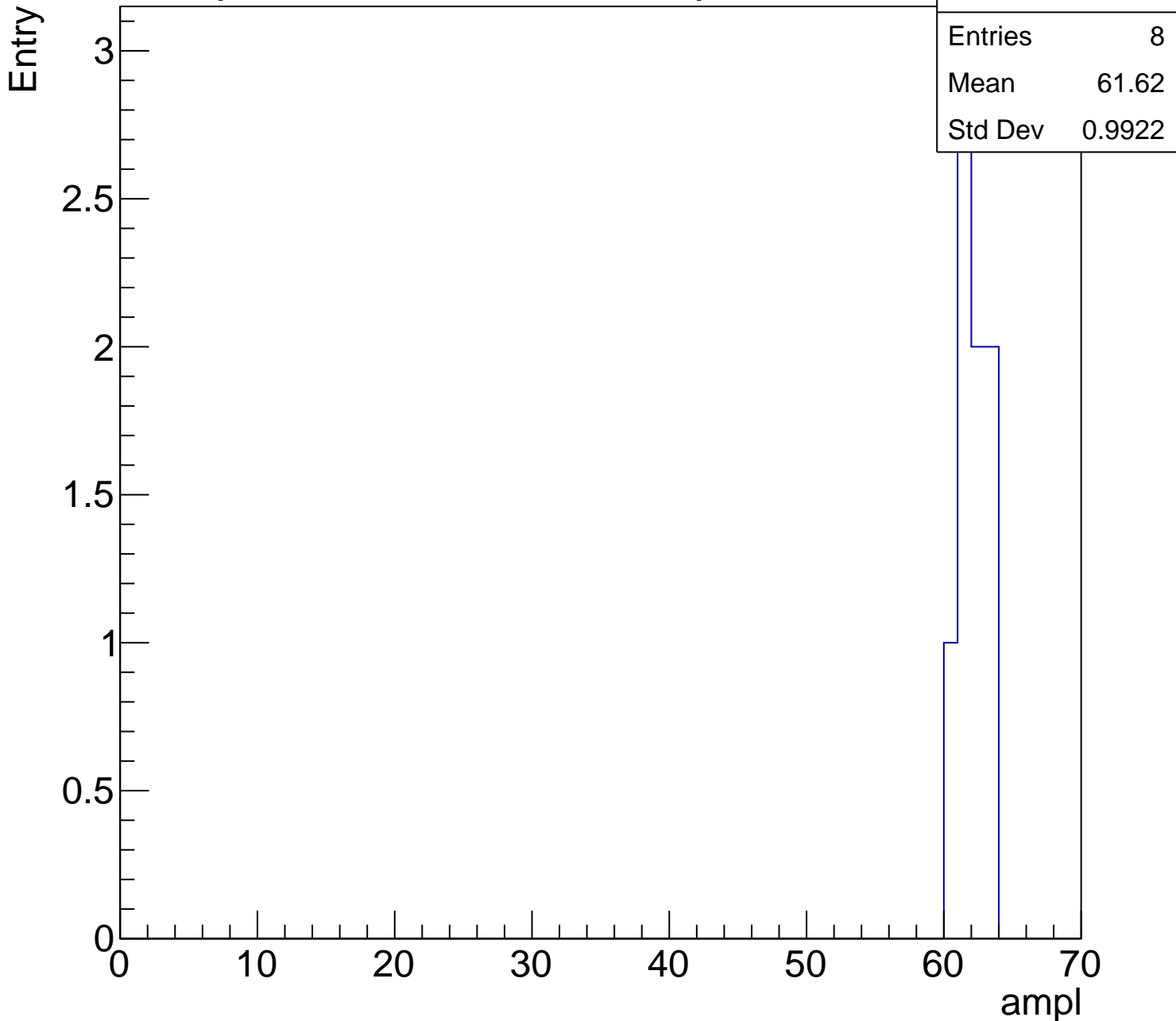
8

Mean

61.62

Std Dev

0.9922



B0L001S, U19-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch10, adc0

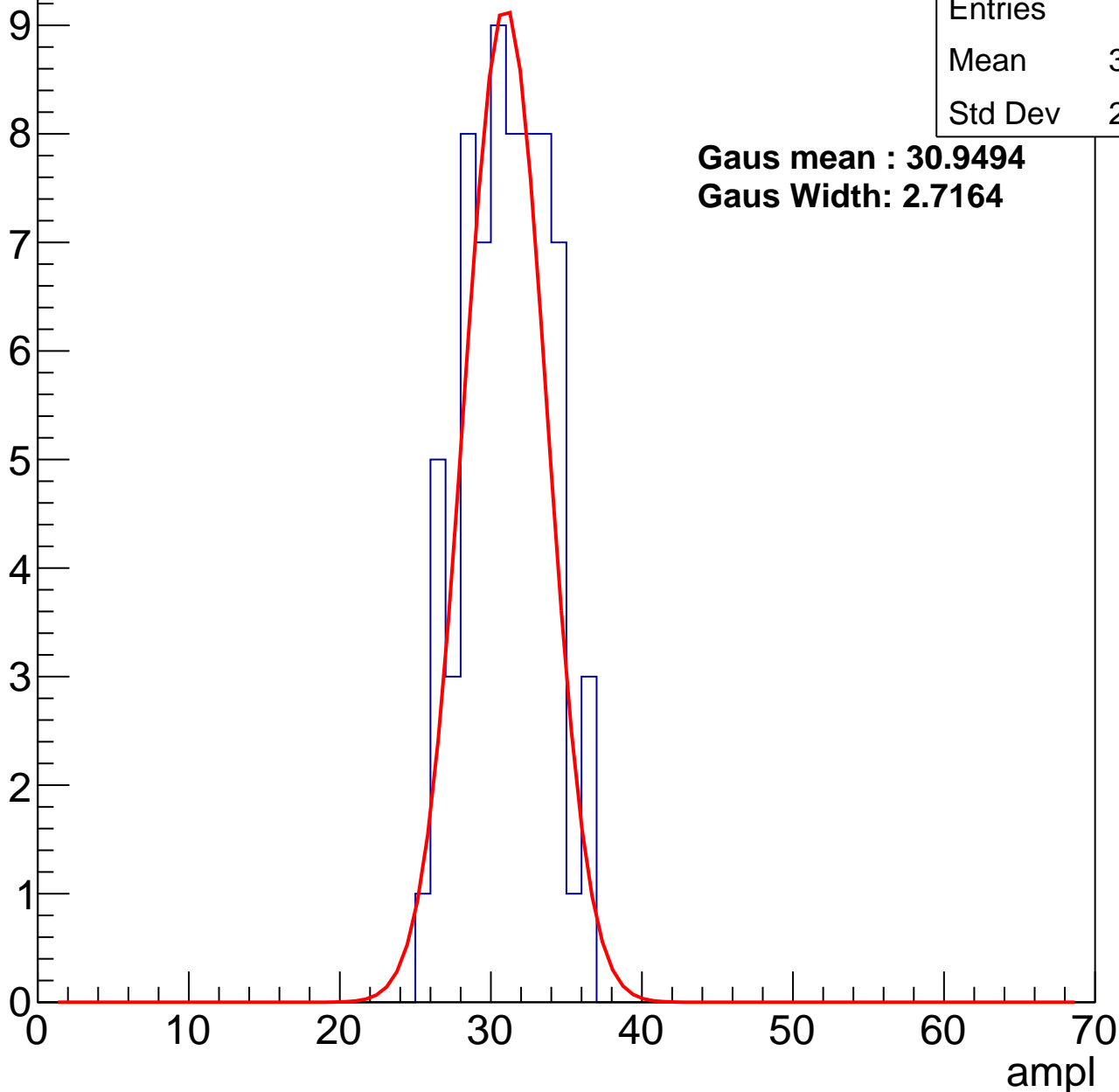
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	30.62
Std Dev	2.717

Gaus mean : 30.9494

Gaus Width: 2.7164



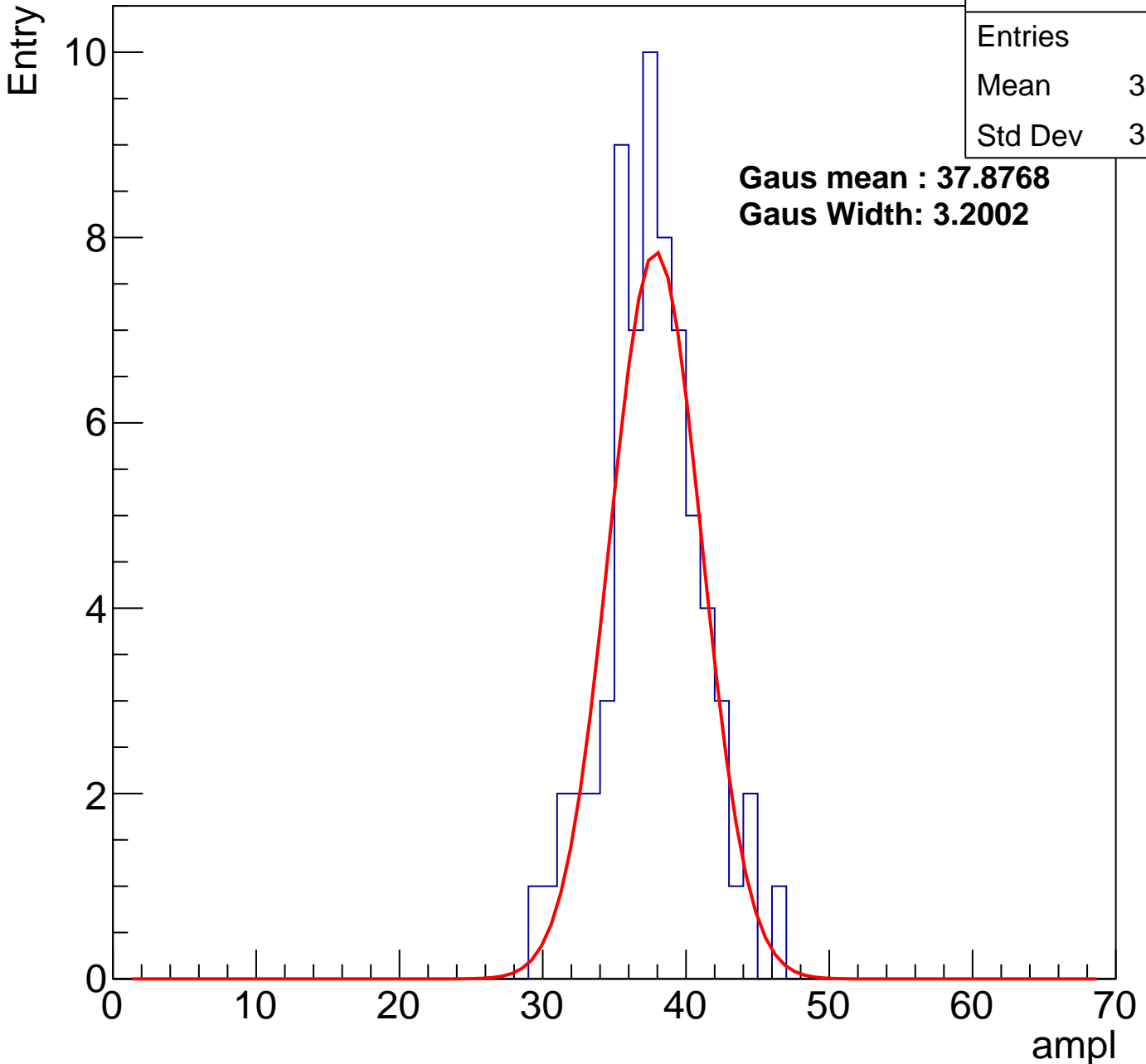
B0L001S, U19-ch10, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	37.26
Std Dev	3.359

Gaus mean : 37.8768

Gaus Width: 3.2002



B0L001S, U19-ch10, adc2

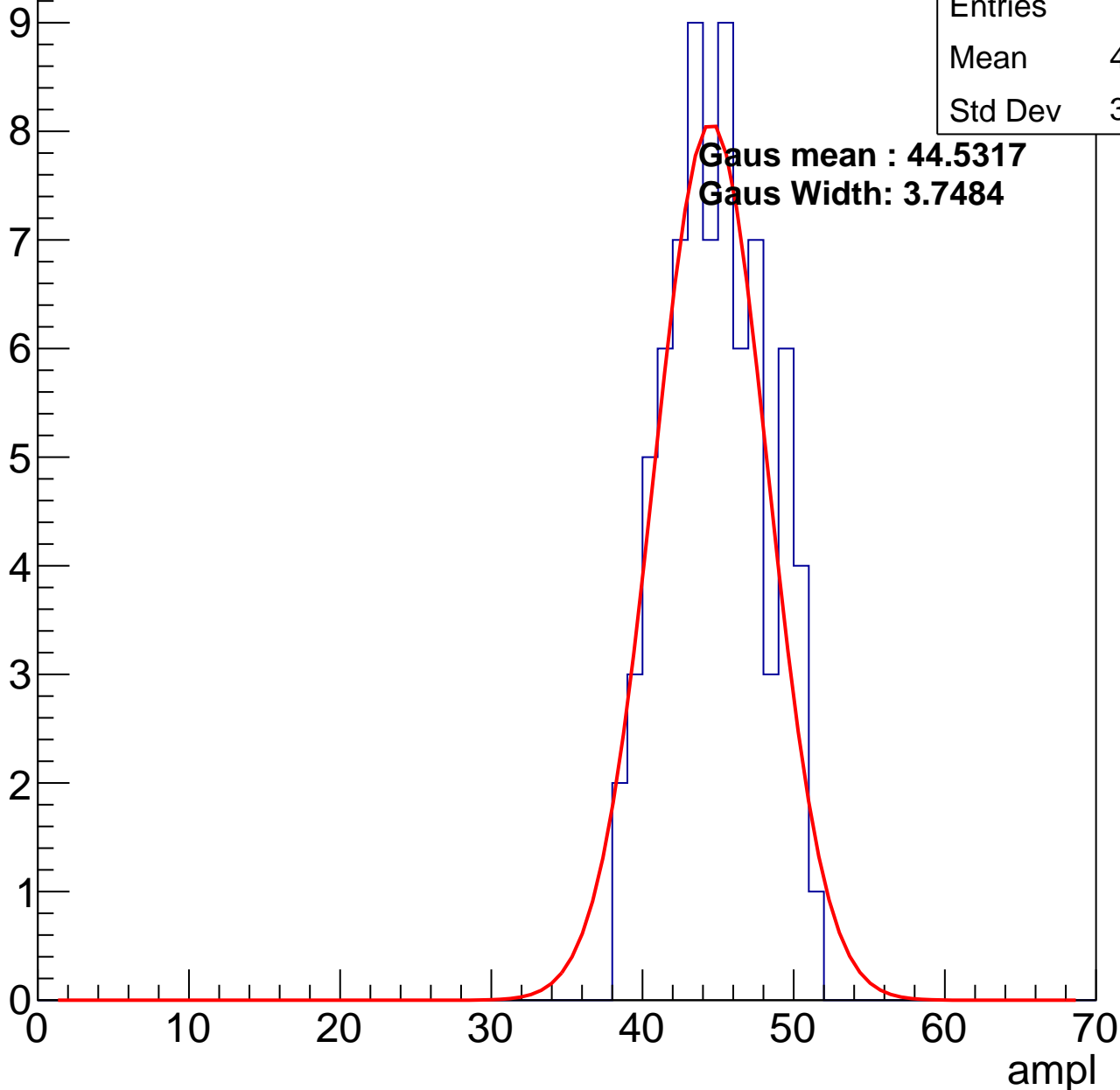
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	44.36
Std Dev	3.256

Gaus mean : 44.5317

Gaus Width: 3.7484

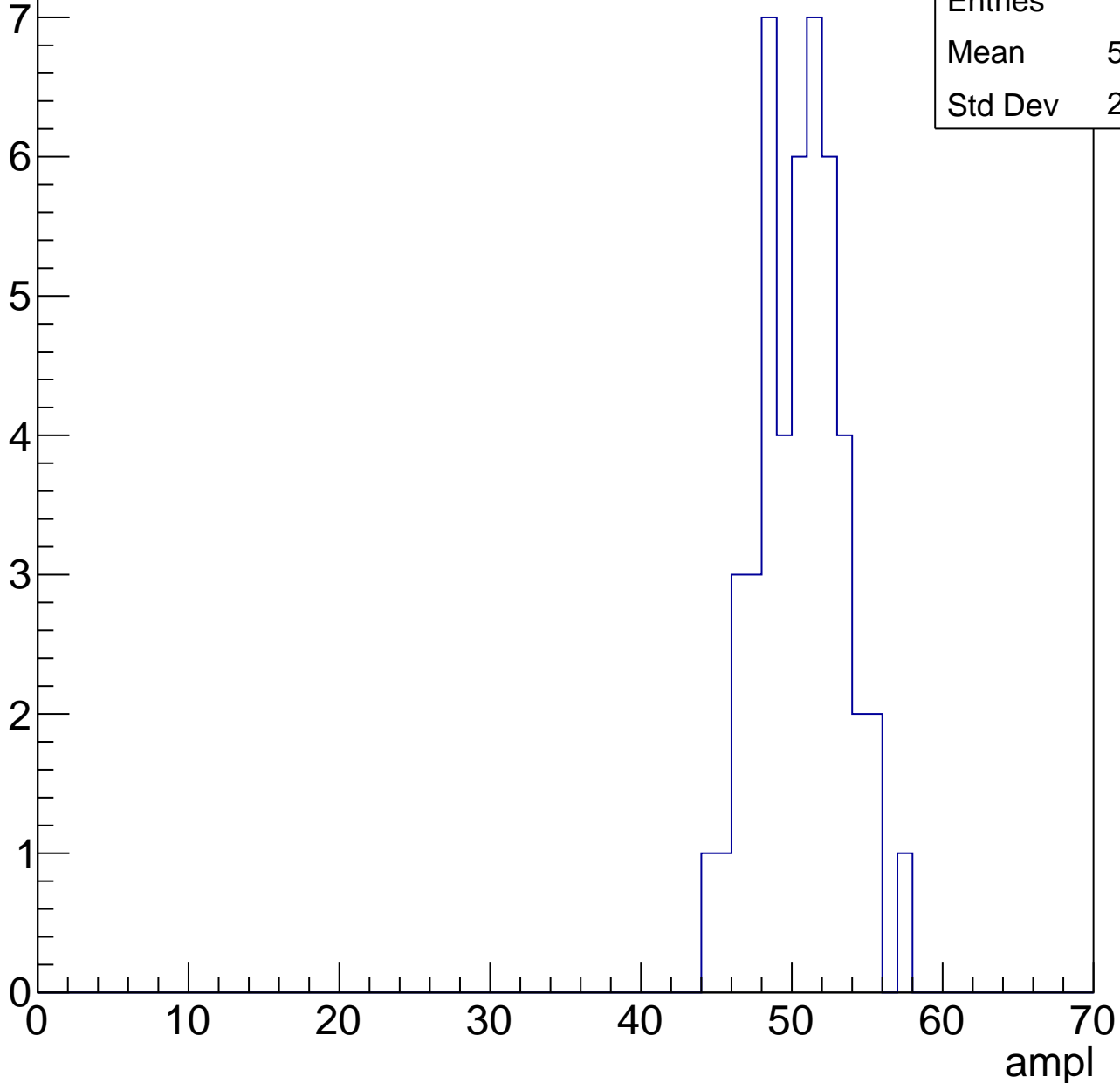


B0L001S, U19-ch10, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	50.13
Std Dev	2.788

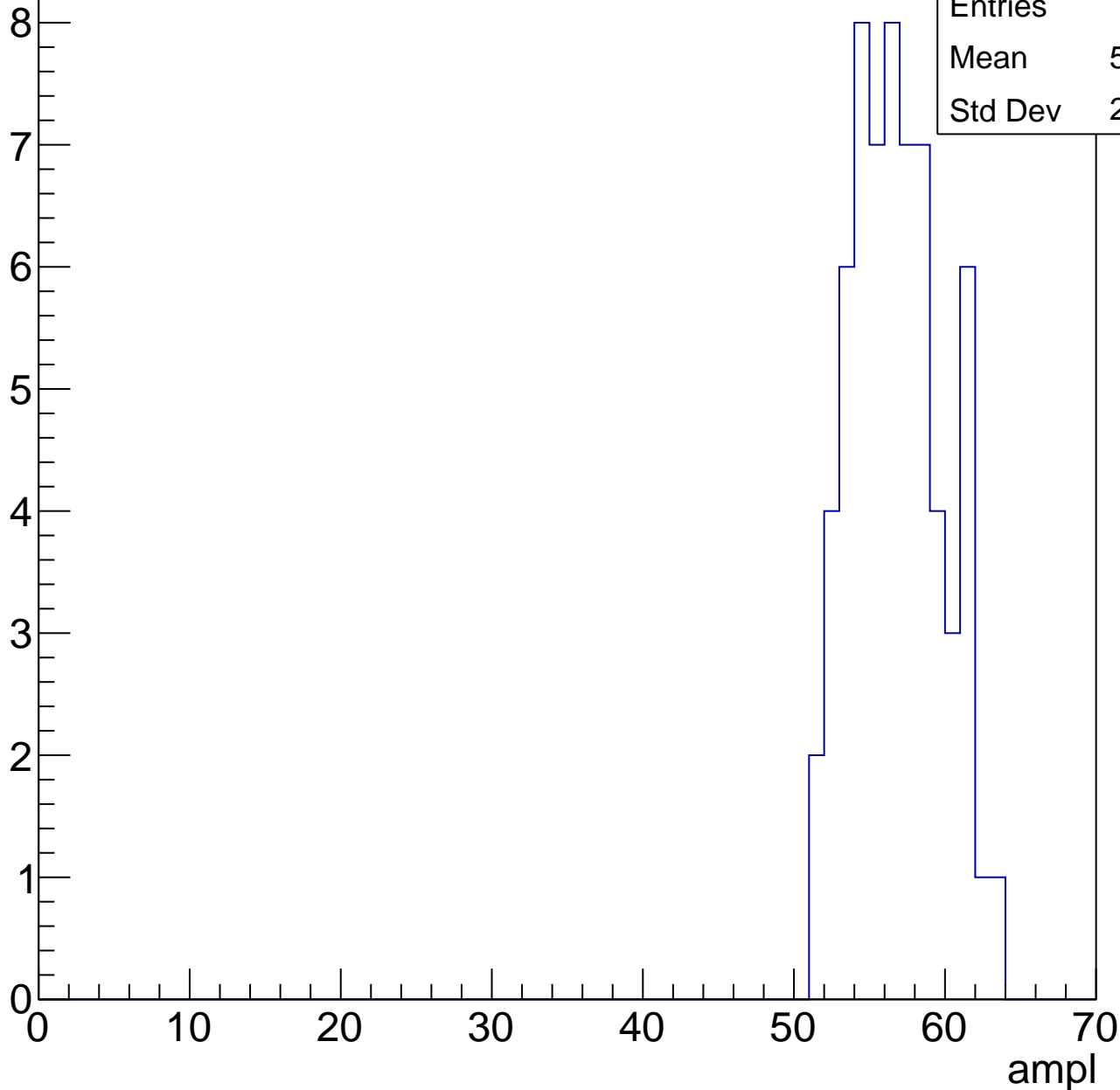


B0L001S, U19-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.33
Std Dev	2.942



B0L001S, U19-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries	36
Mean	60.19
Std Dev	2.246

ampl

0

10

20

30

40

50

60

70

B0L001S, U19-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

Entries

8

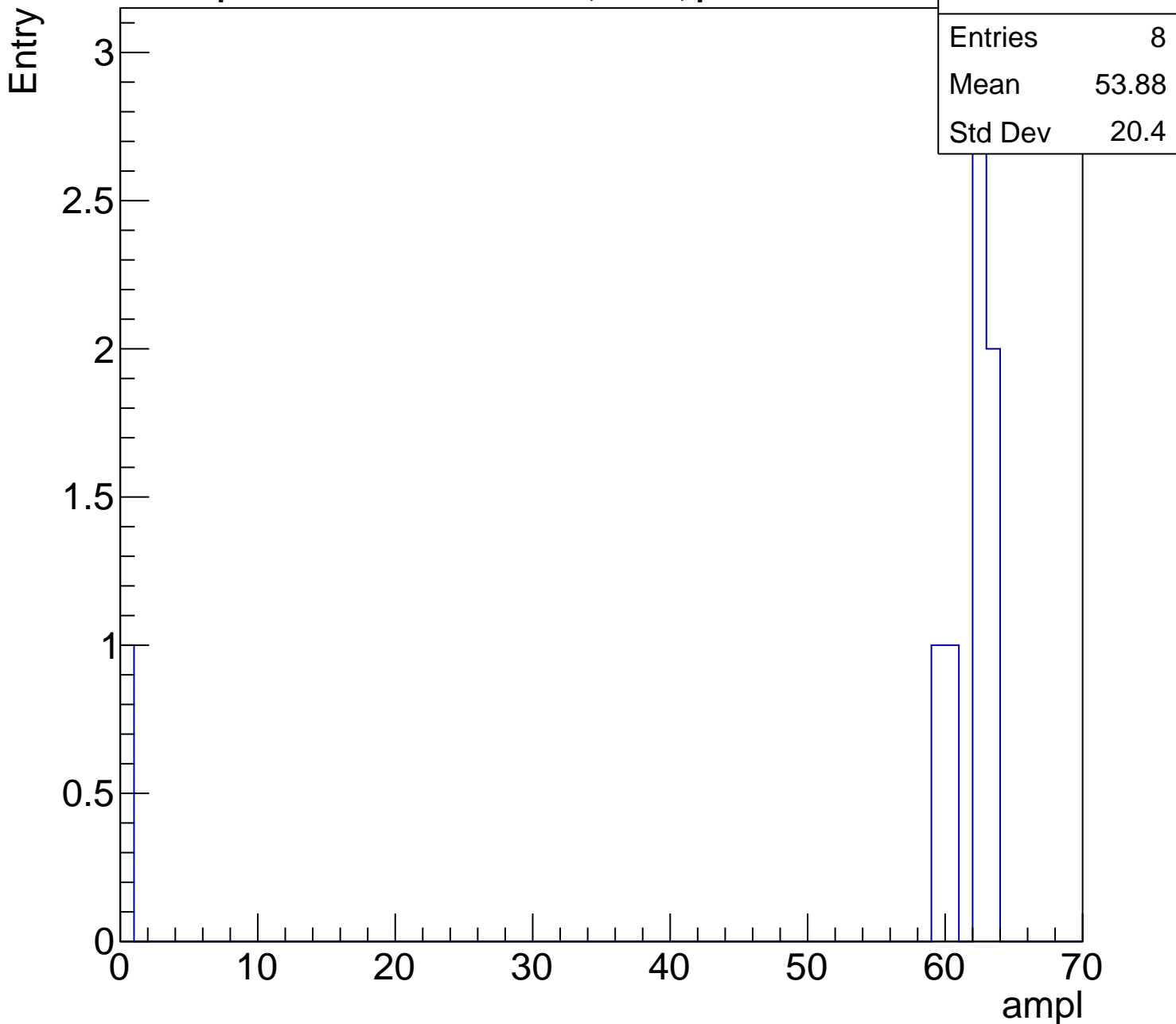
Mean

53.88

Std Dev

20.4

ampl



B0L001S, U19-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch11, adc0

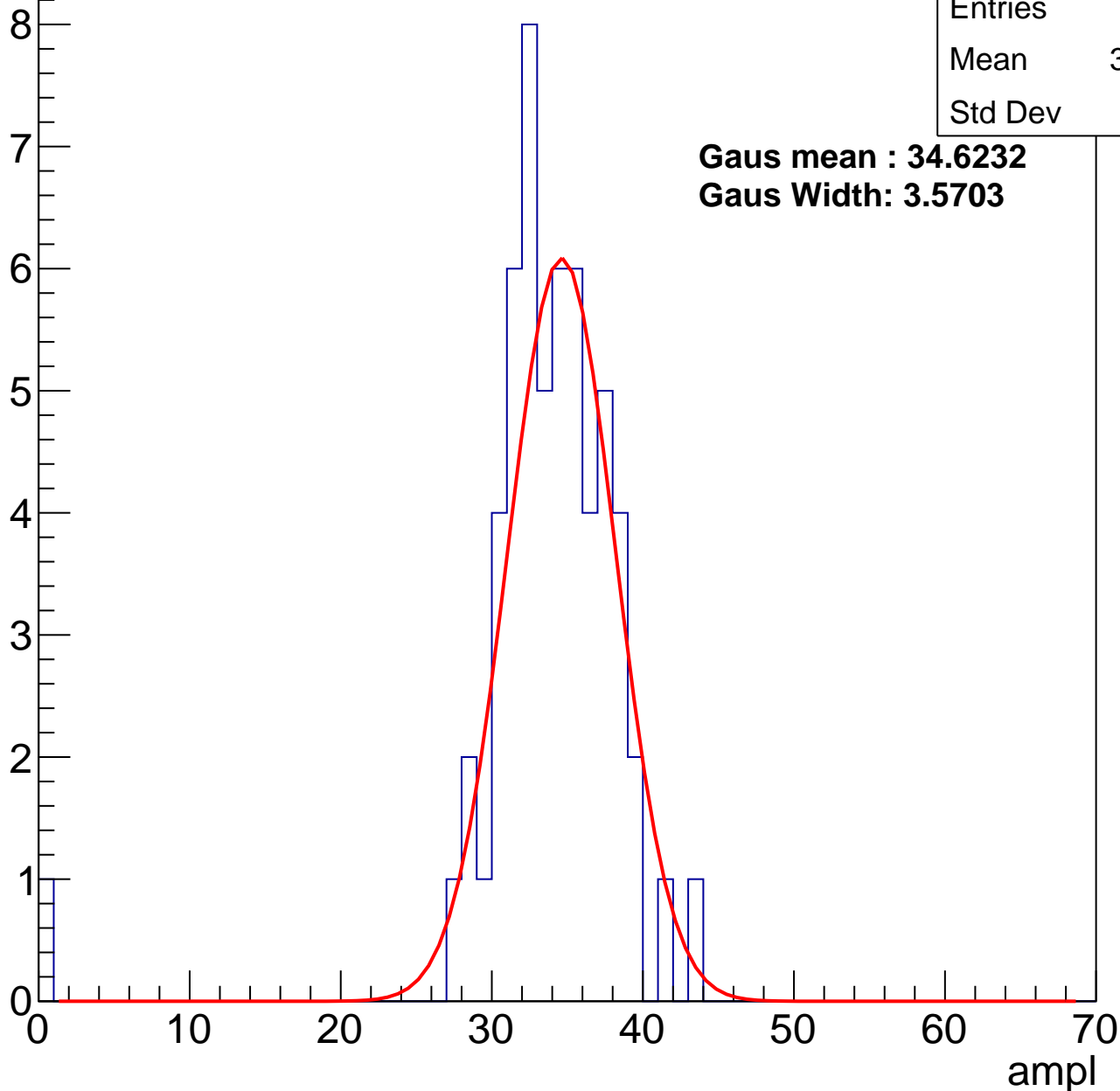
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	33.26
Std Dev	5.52

Gaus mean : 34.6232

Gaus Width: 3.5703



B0L001S, U19-ch11, adc1

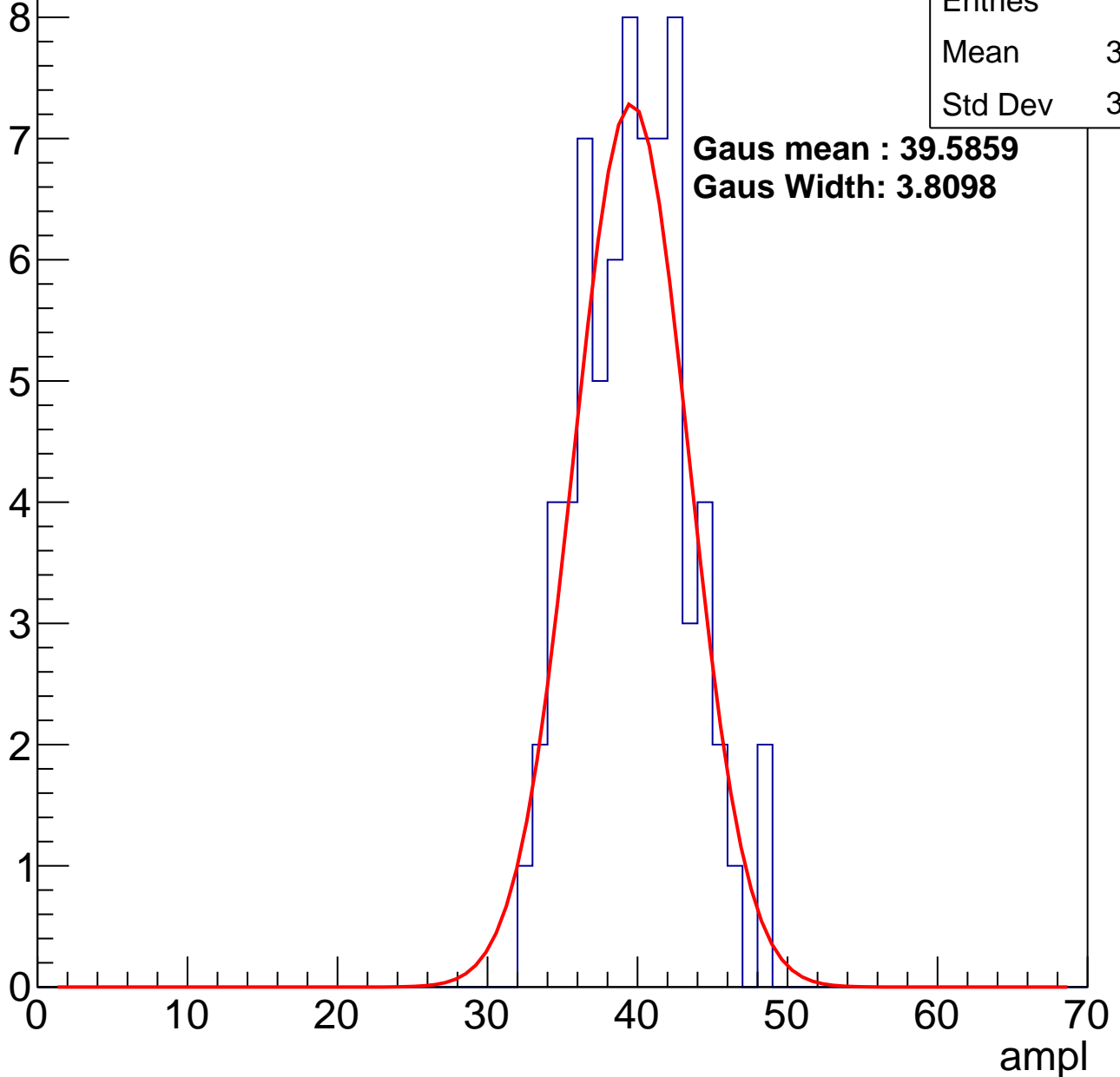
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	39.31
Std Dev	3.571

Gaus mean : 39.5859

Gaus Width: 3.8098



B0L001S, U19-ch11, adc2

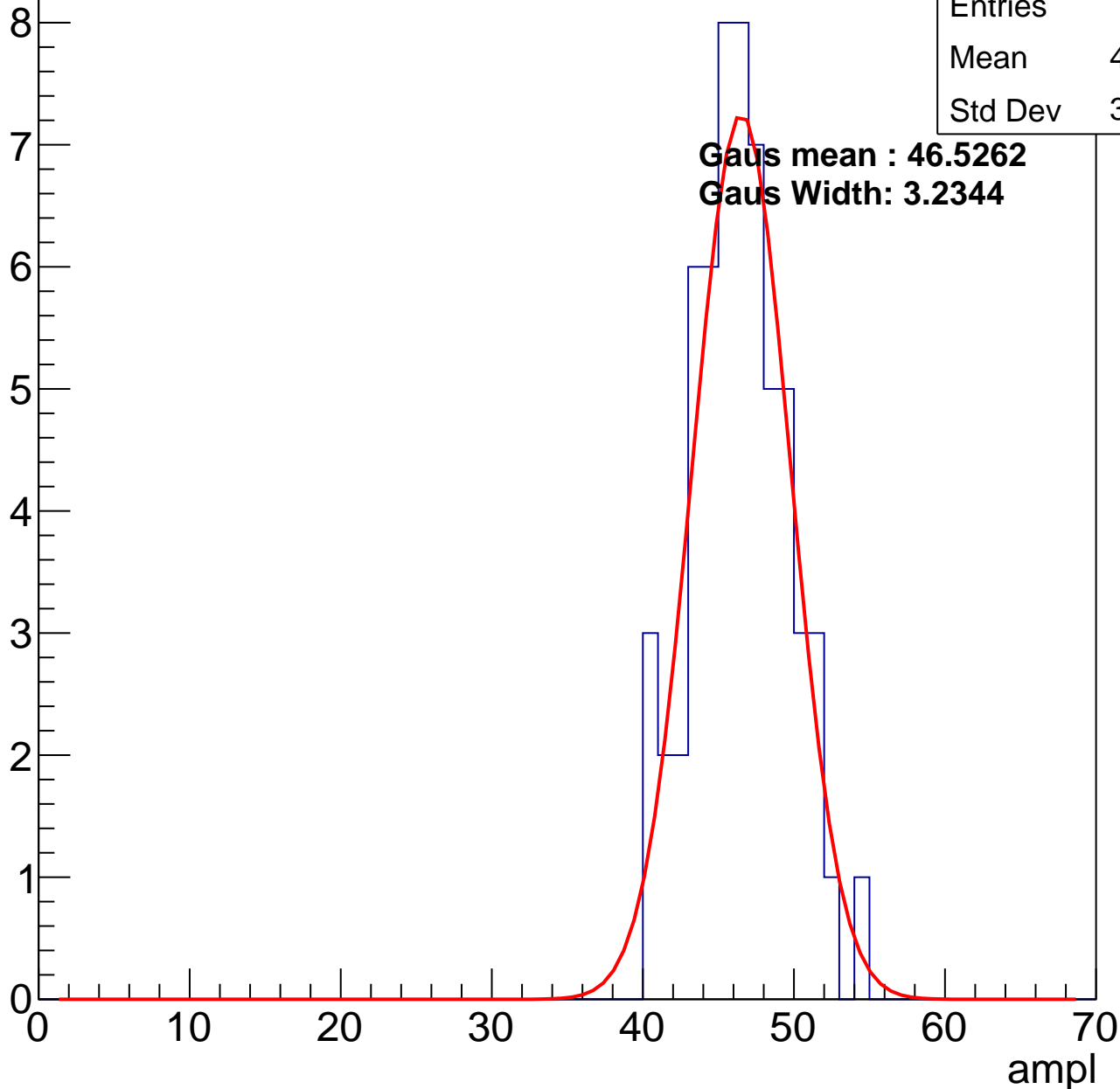
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	45.98
Std Dev	3.085

Gaus mean : 46.5262

Gaus Width: 3.2344

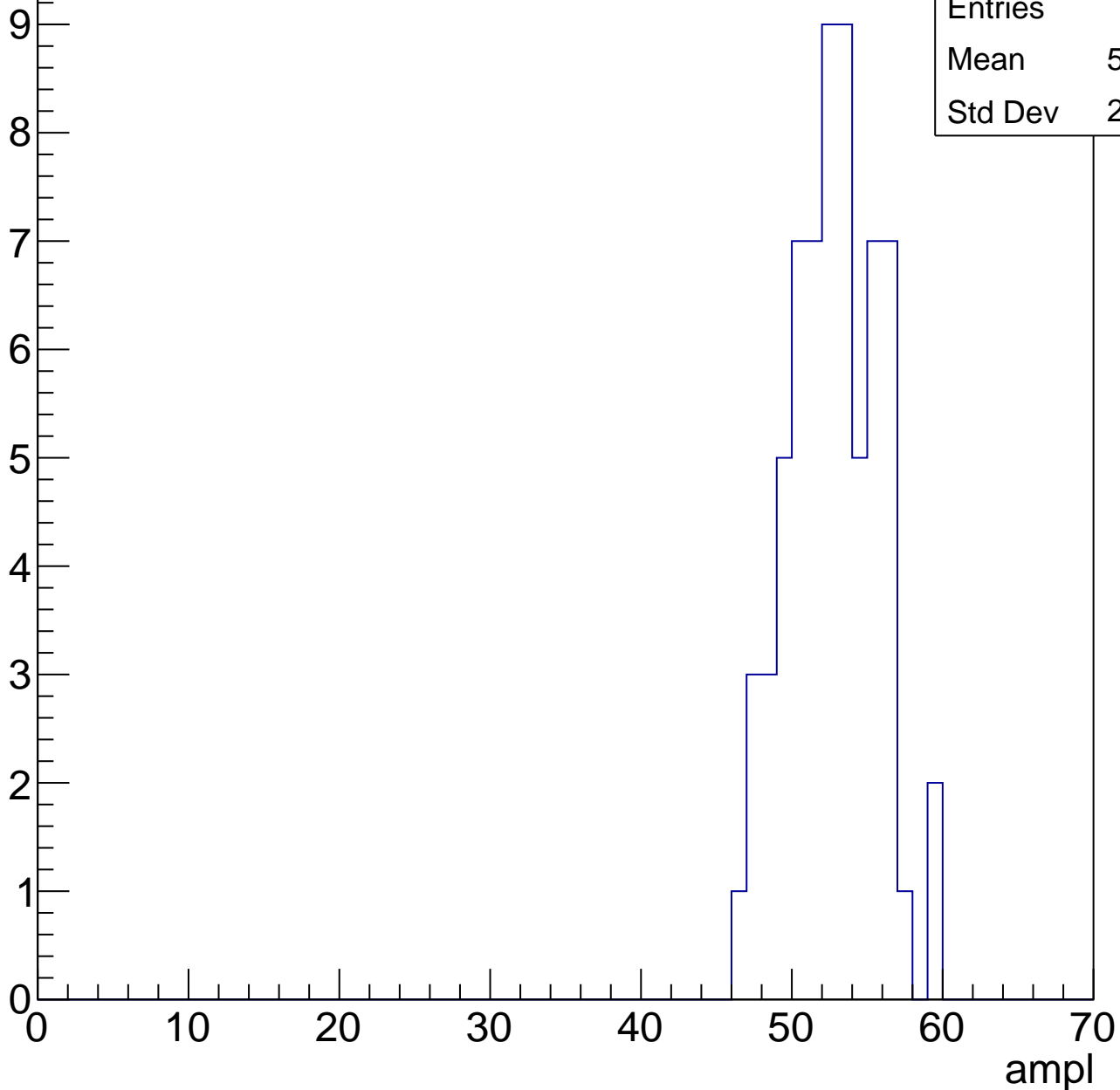


B0L001S, U19-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	52.27
Std Dev	2.916



B0L001S, U19-ch11, adc4

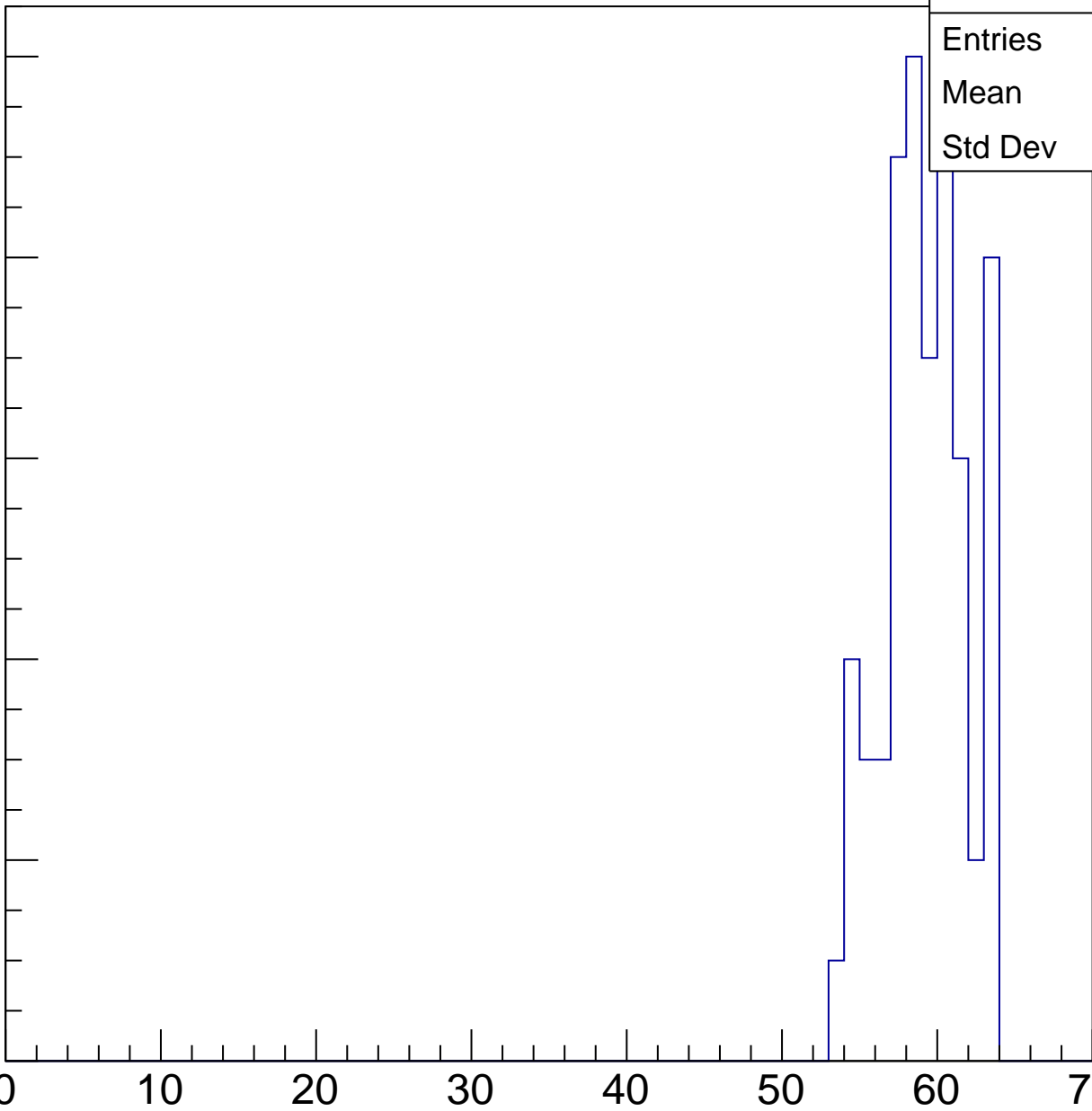
calib_packv5_042523_0143.root, FC#9, port A1

Entry

10
8
6
4
2
0

Entries	62
Mean	58.74
Std Dev	2.639

ampl

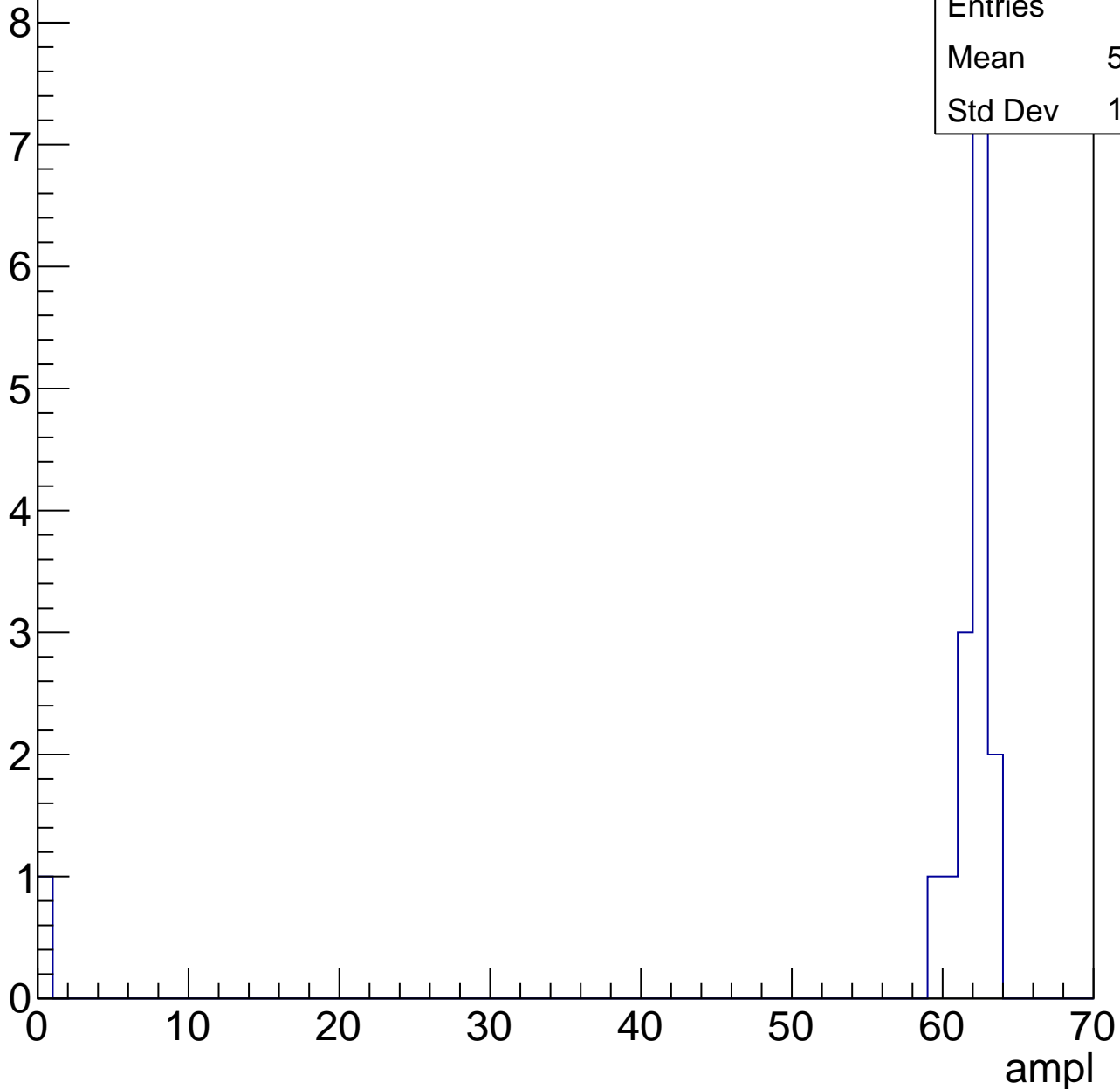


B0L001S, U19-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	16
Mean	57.75
Std Dev	14.94



B0L001S, U19-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch12, adc0

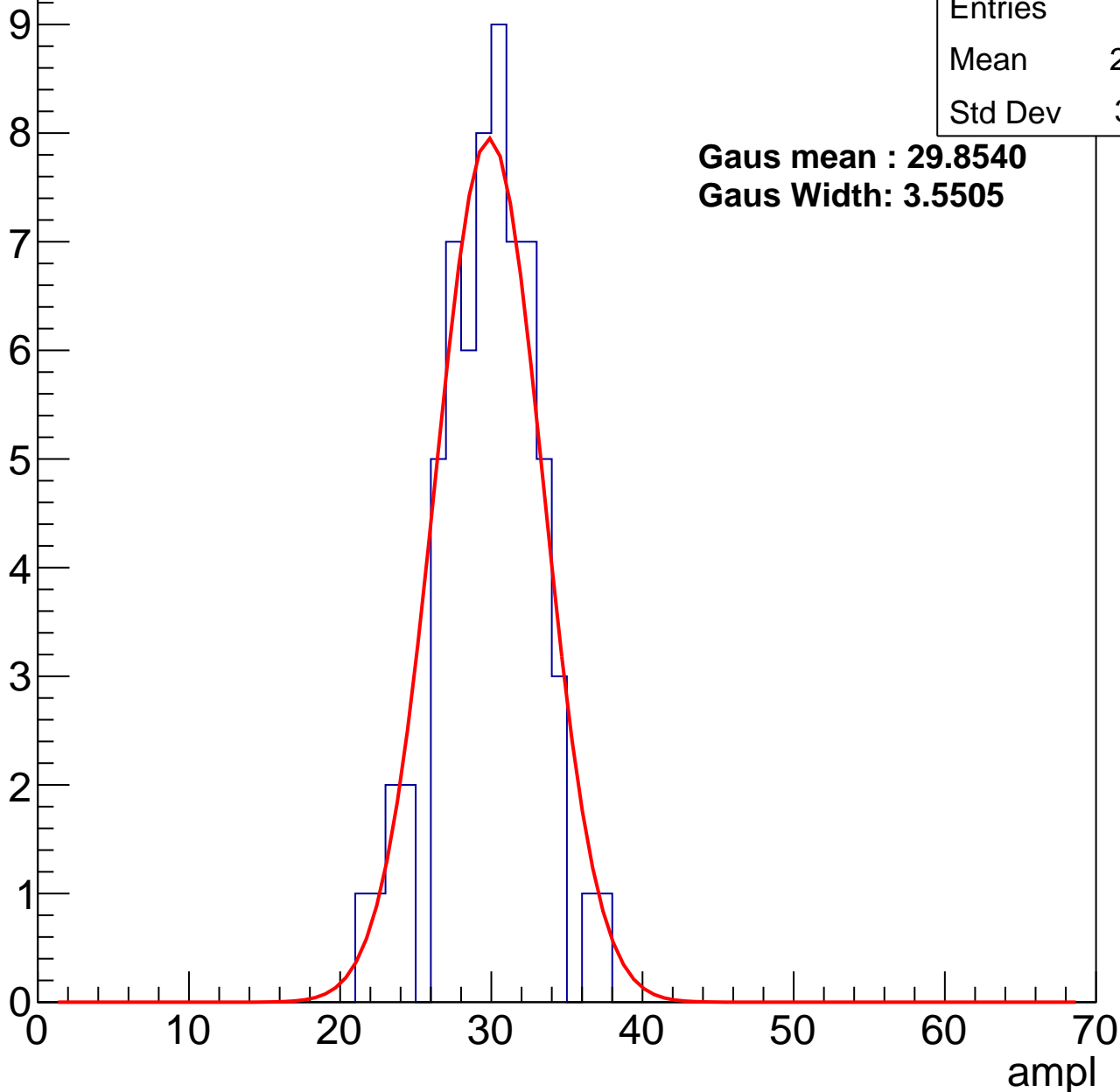
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.34
Std Dev	3.221

Gaus mean : 29.8540

Gaus Width: 3.5505

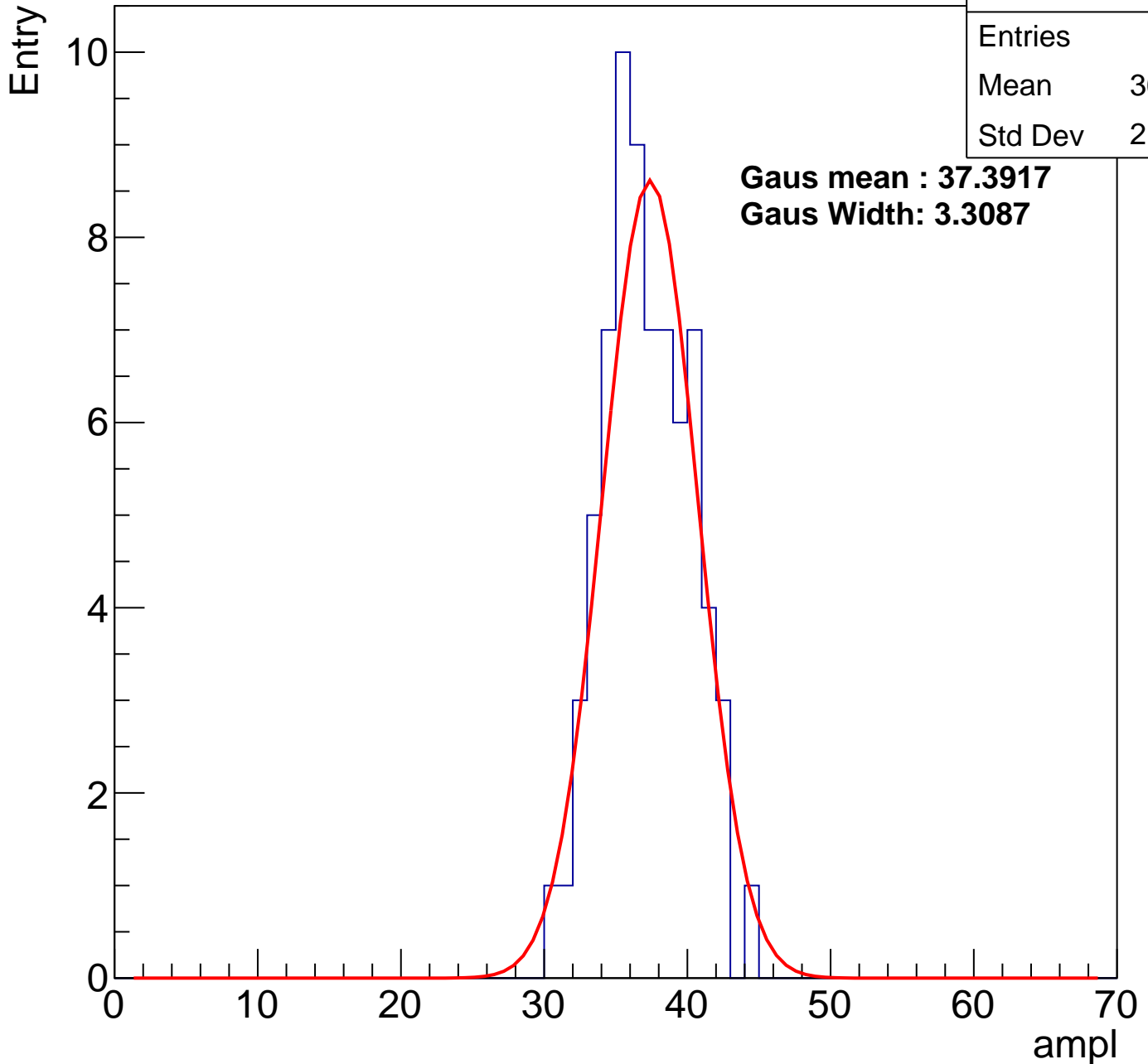


B0L001S, U19-ch12, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	36.72
Std Dev	2.975

Gaus mean : 37.3917
Gaus Width: 3.3087



B0L001S, U19-ch12, adc2

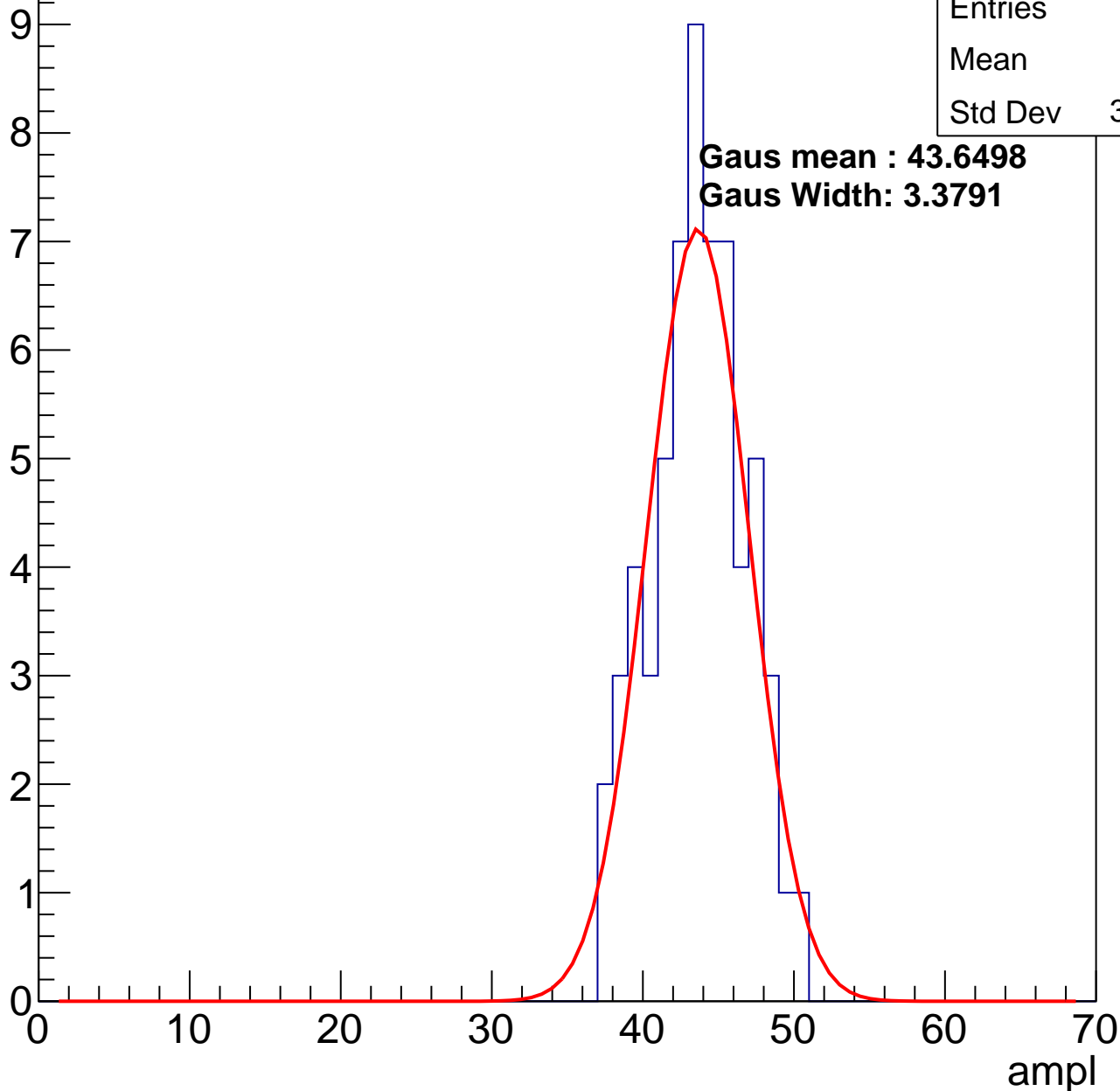
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.2
Std Dev	3.067

Gaus mean : 43.6498

Gaus Width: 3.3791

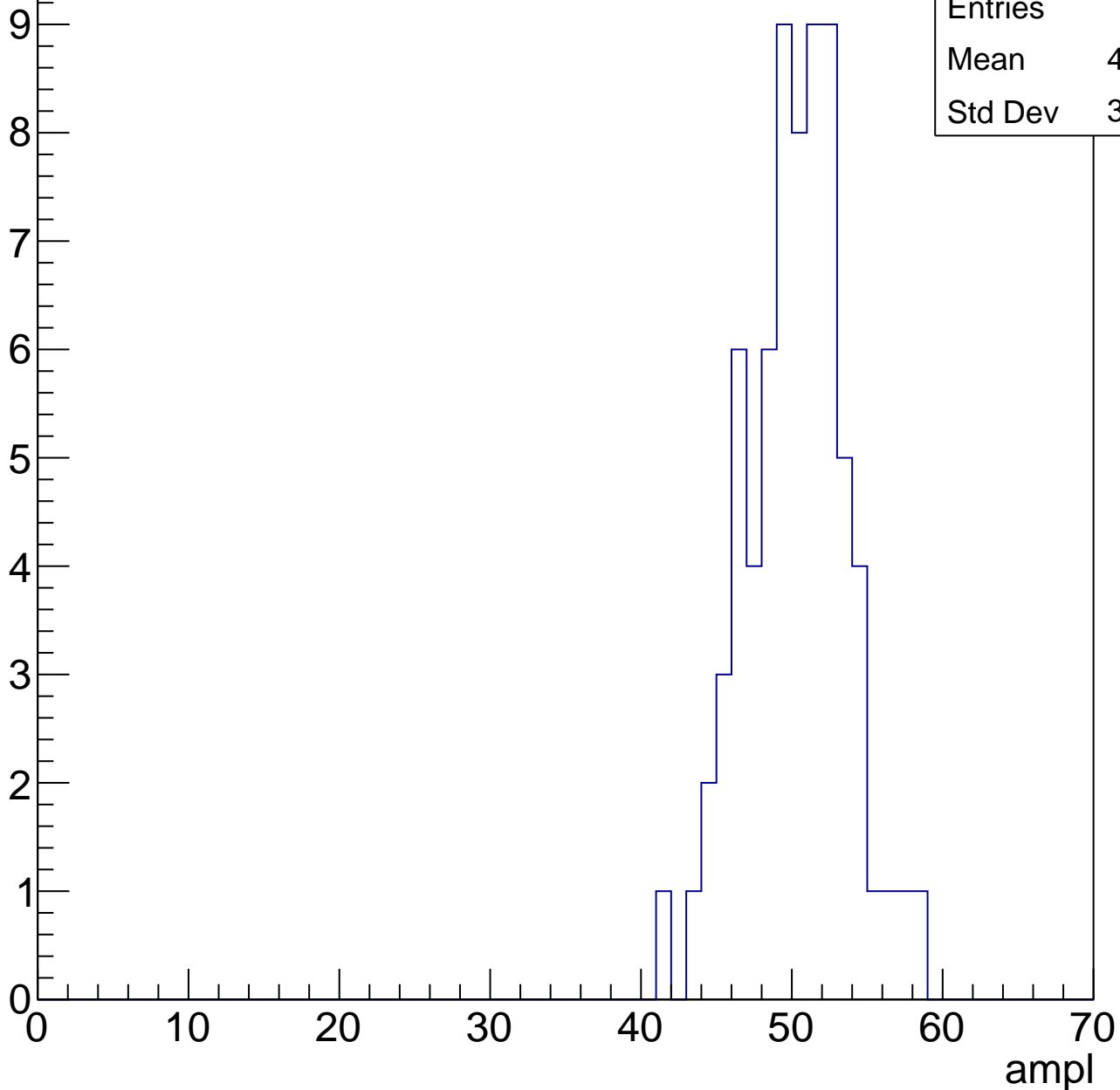


B0L001S, U19-ch12, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	49.77
Std Dev	3.285

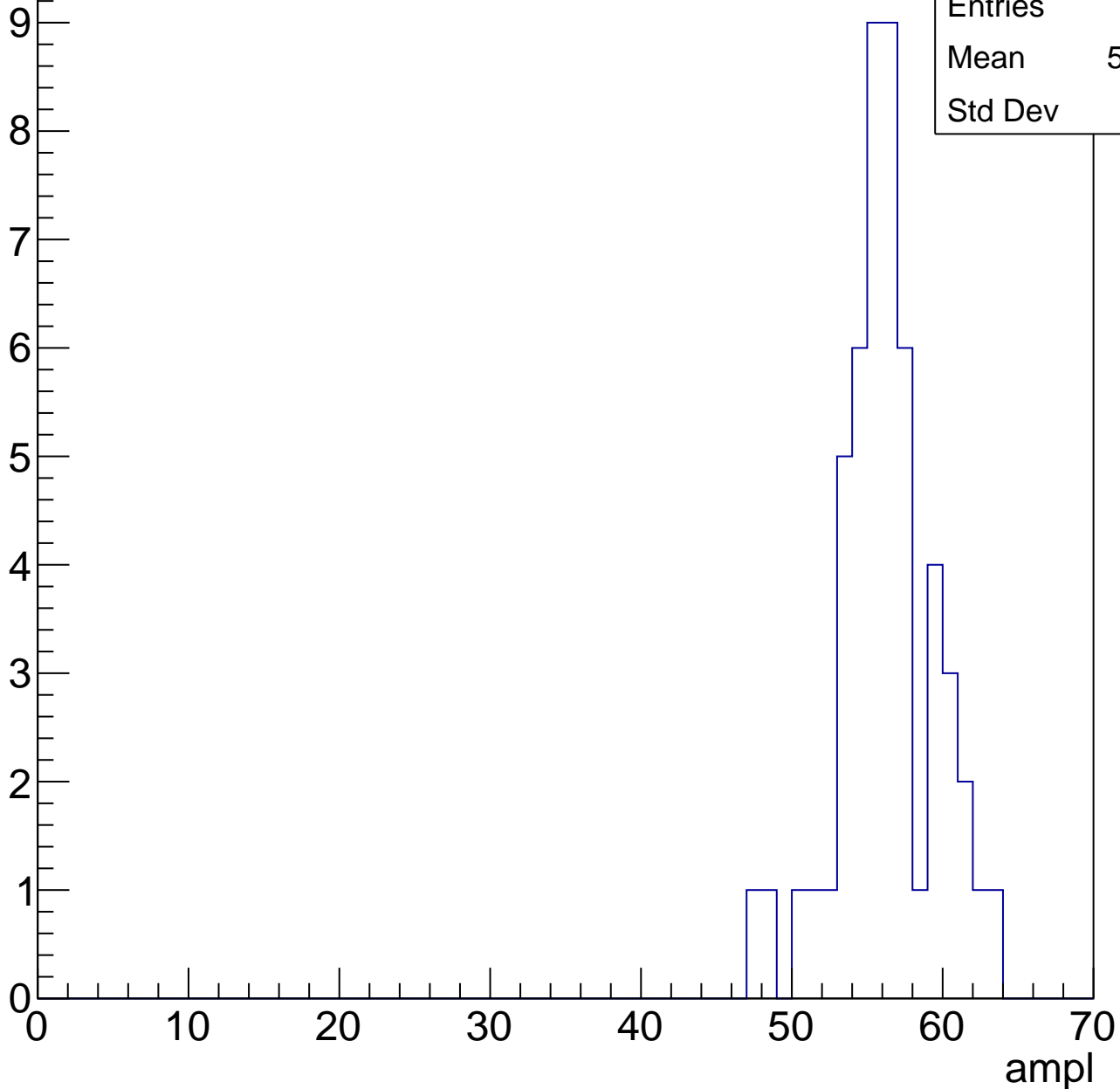


B0L001S, U19-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	55.75
Std Dev	3.18

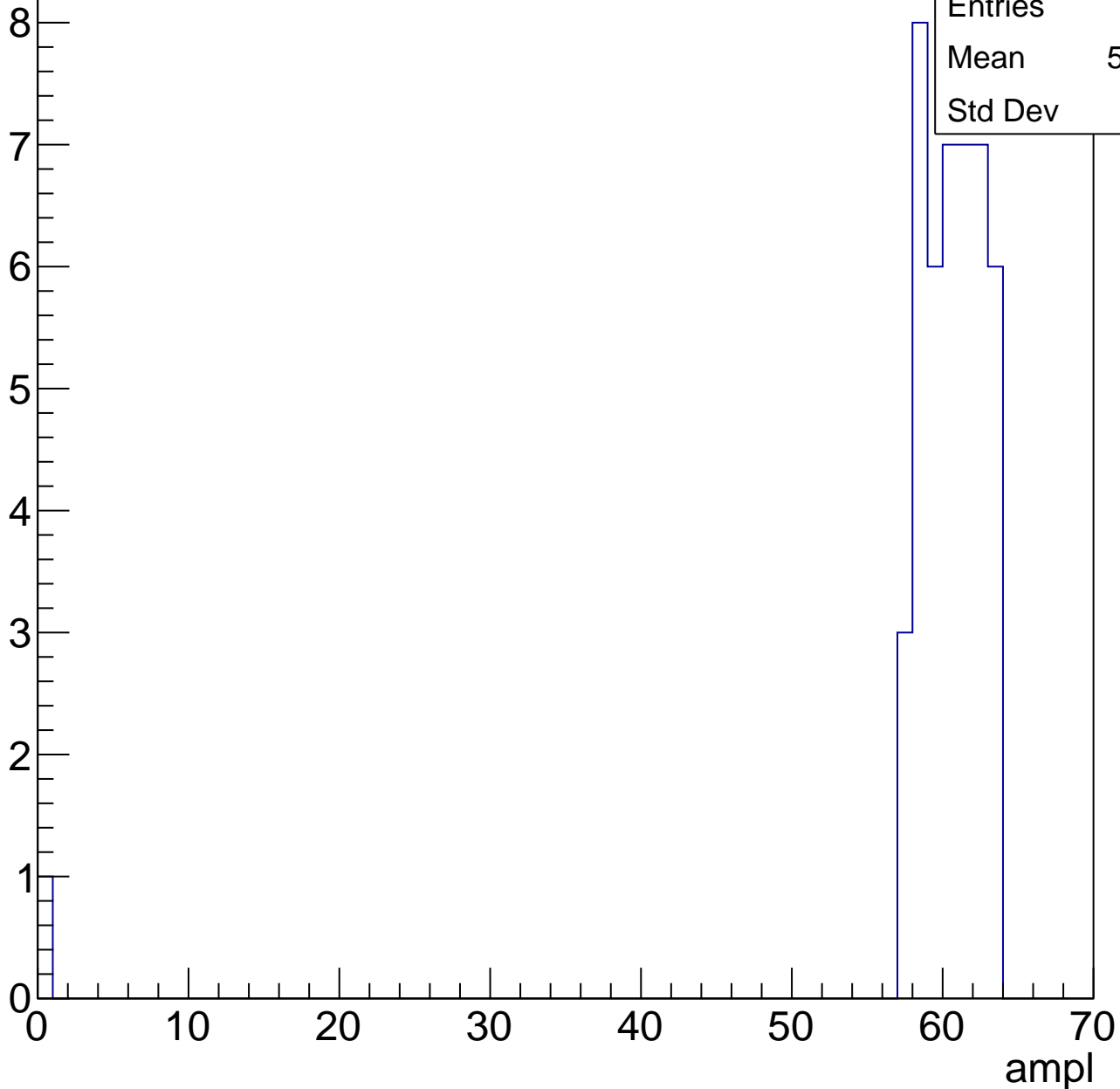


B0L001S, U19-ch12, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.84
Std Dev	9.06



B0L001S, U19-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch13, adc0

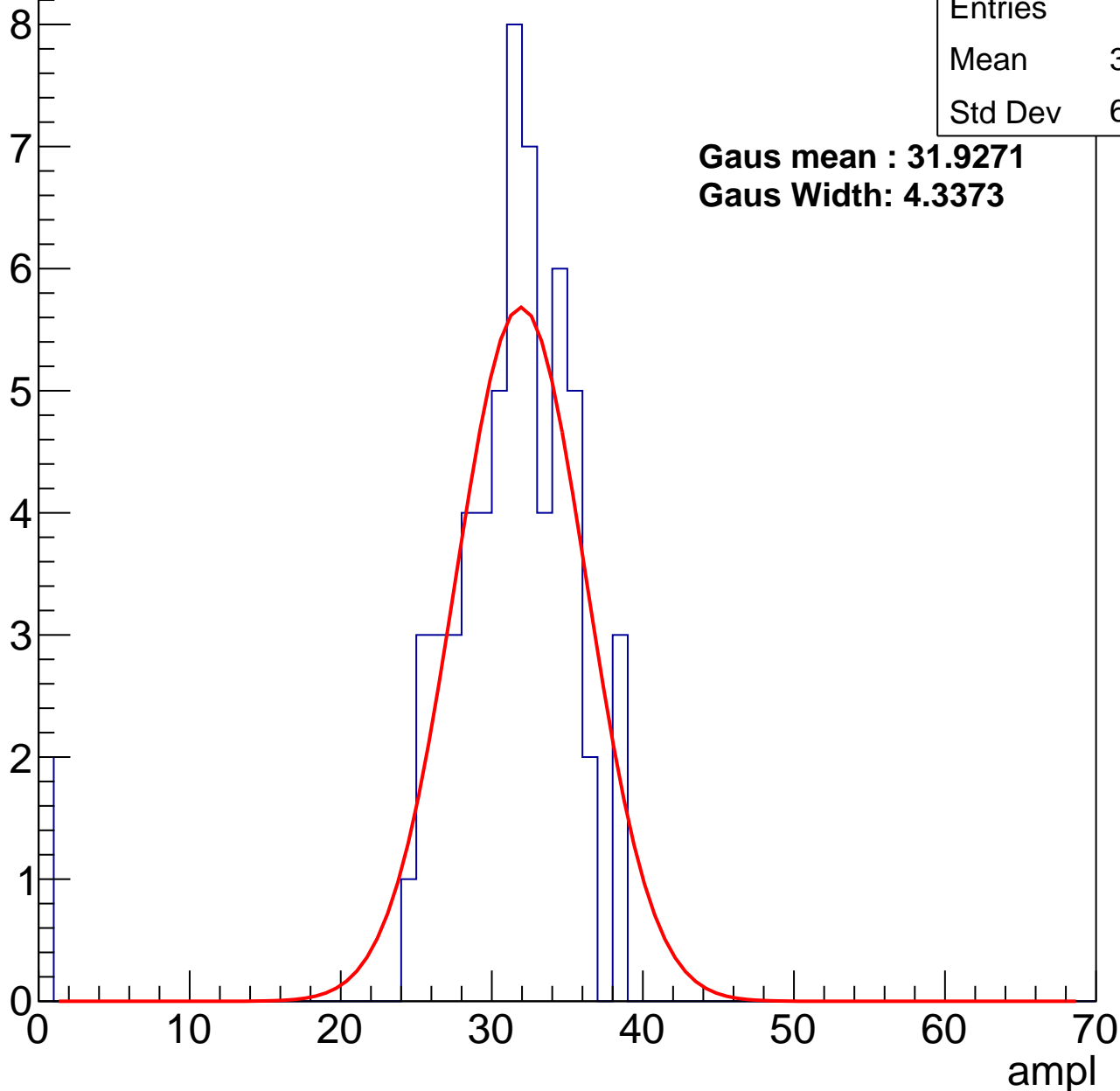
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.08
Std Dev	6.535

Gaus mean : 31.9271

Gaus Width: 4.3373



B0L001S, U19-ch13, adc1

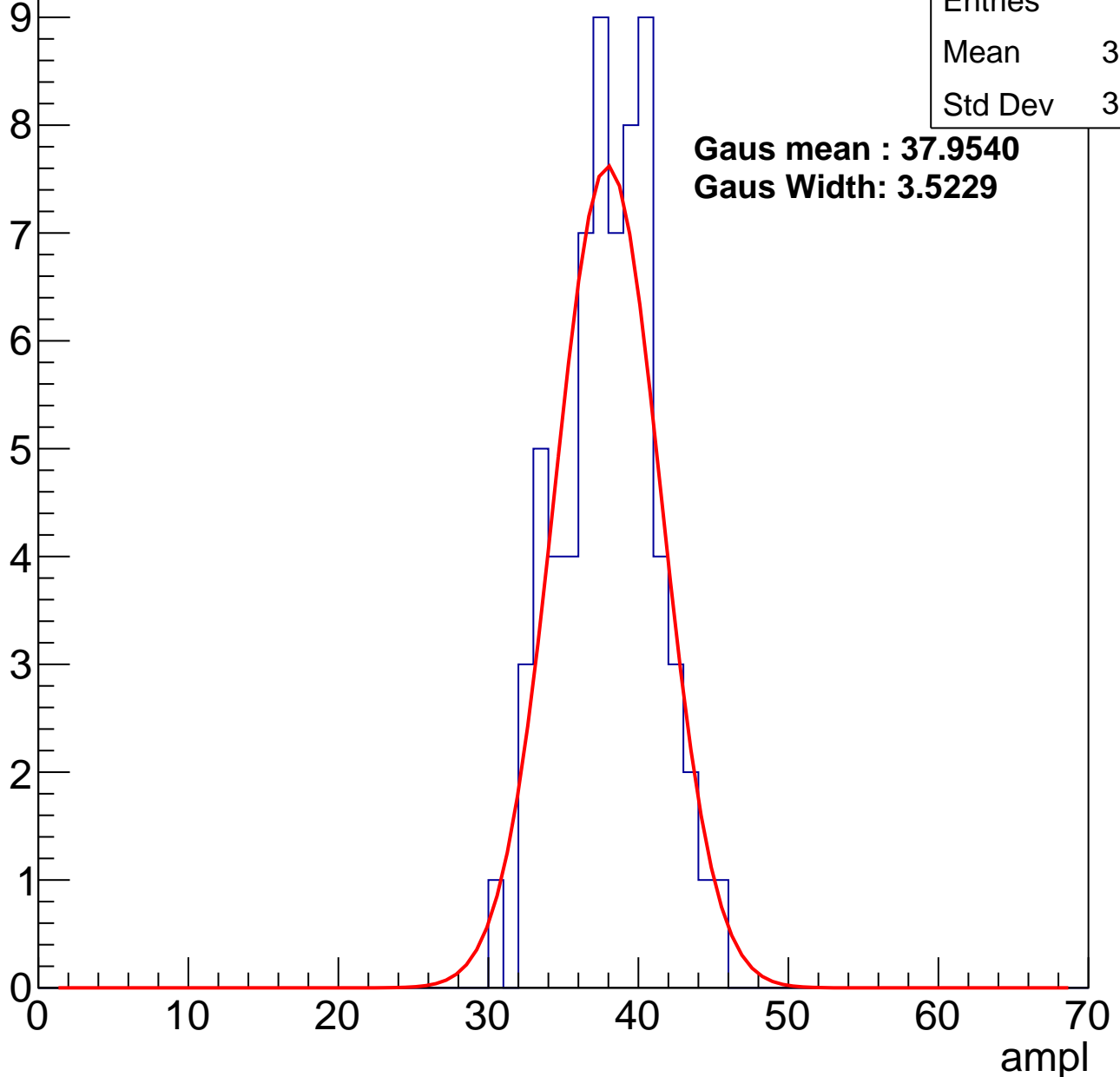
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.57
Std Dev	3.173

Gaus mean : 37.9540

Gaus Width: 3.5229



B0L001S, U19-ch13, adc2

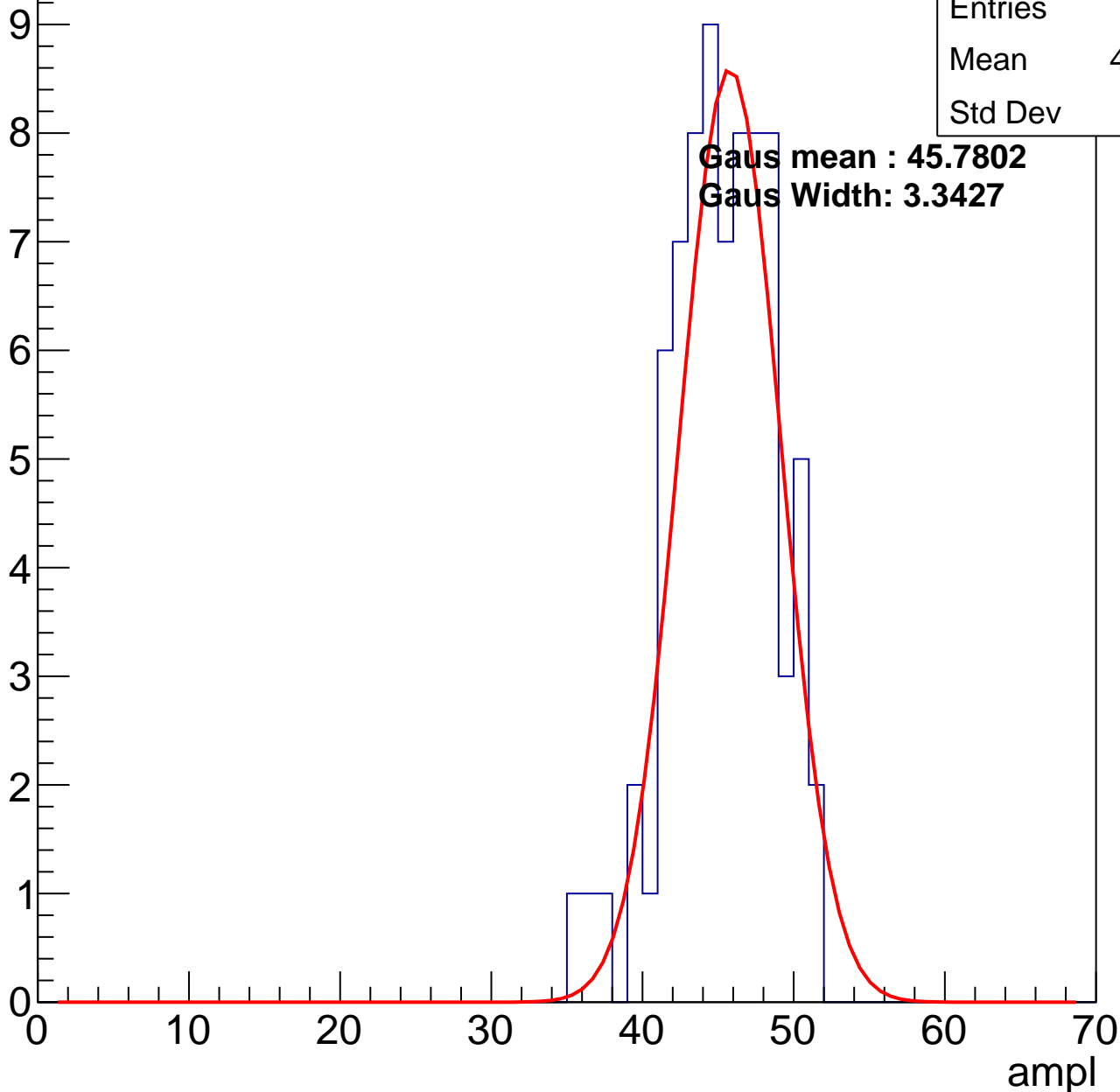
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.78
Std Dev	3.41

Gaus mean : 45.7802

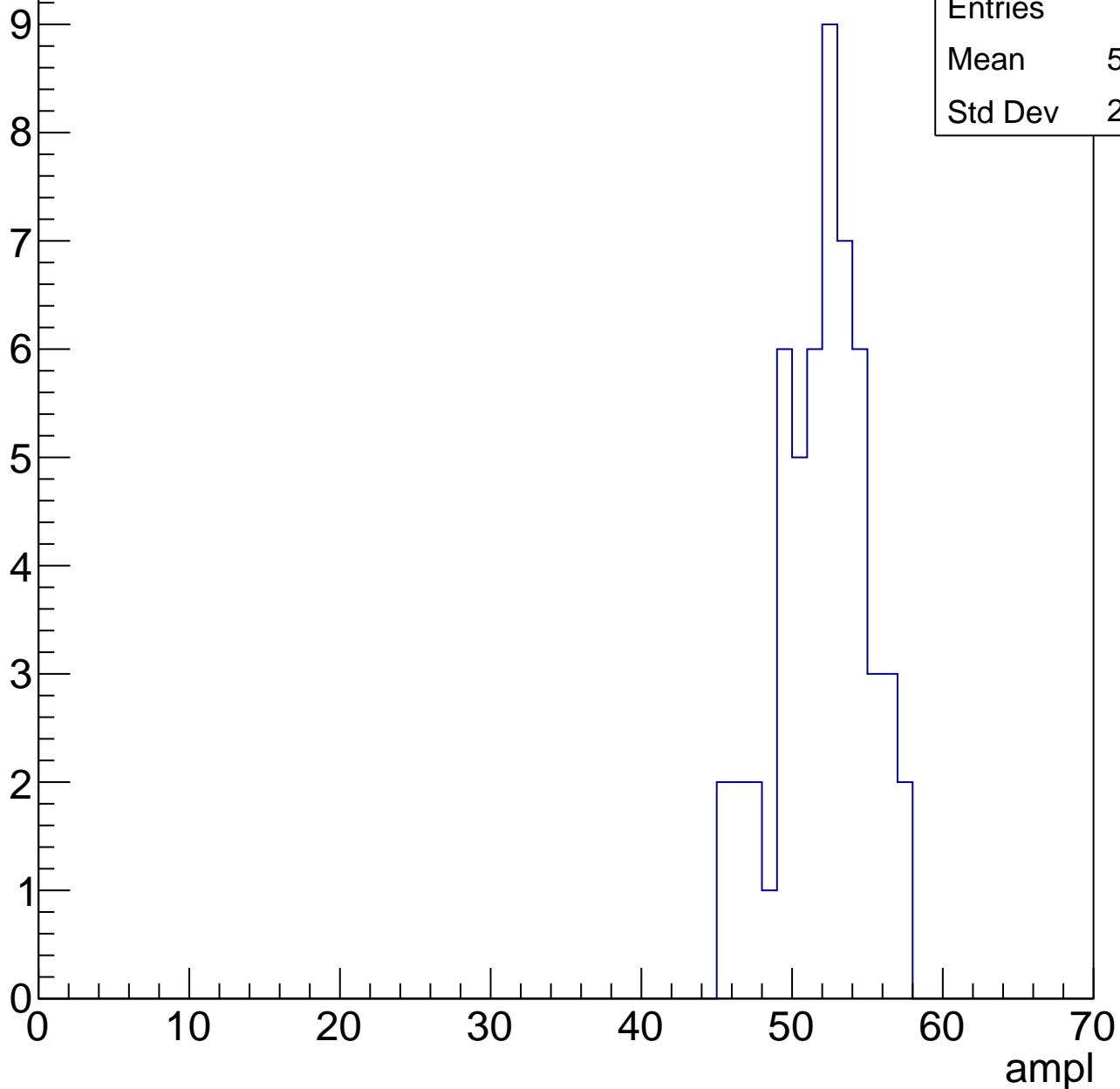
Gaus Width: 3.3427



B0L001S, U19-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



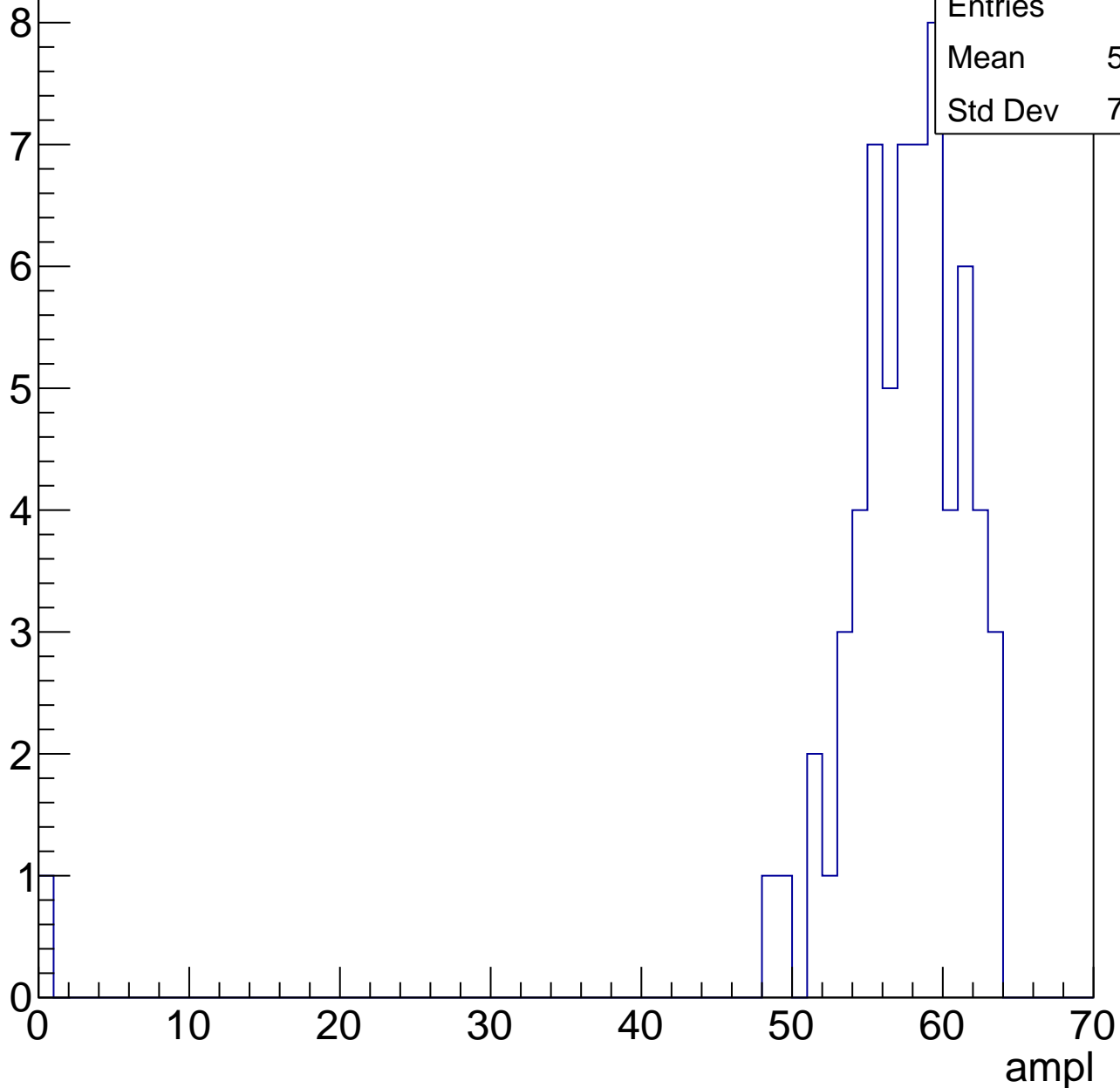
Entries	54
Mean	51.56
Std Dev	2.923

B0L001S, U19-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

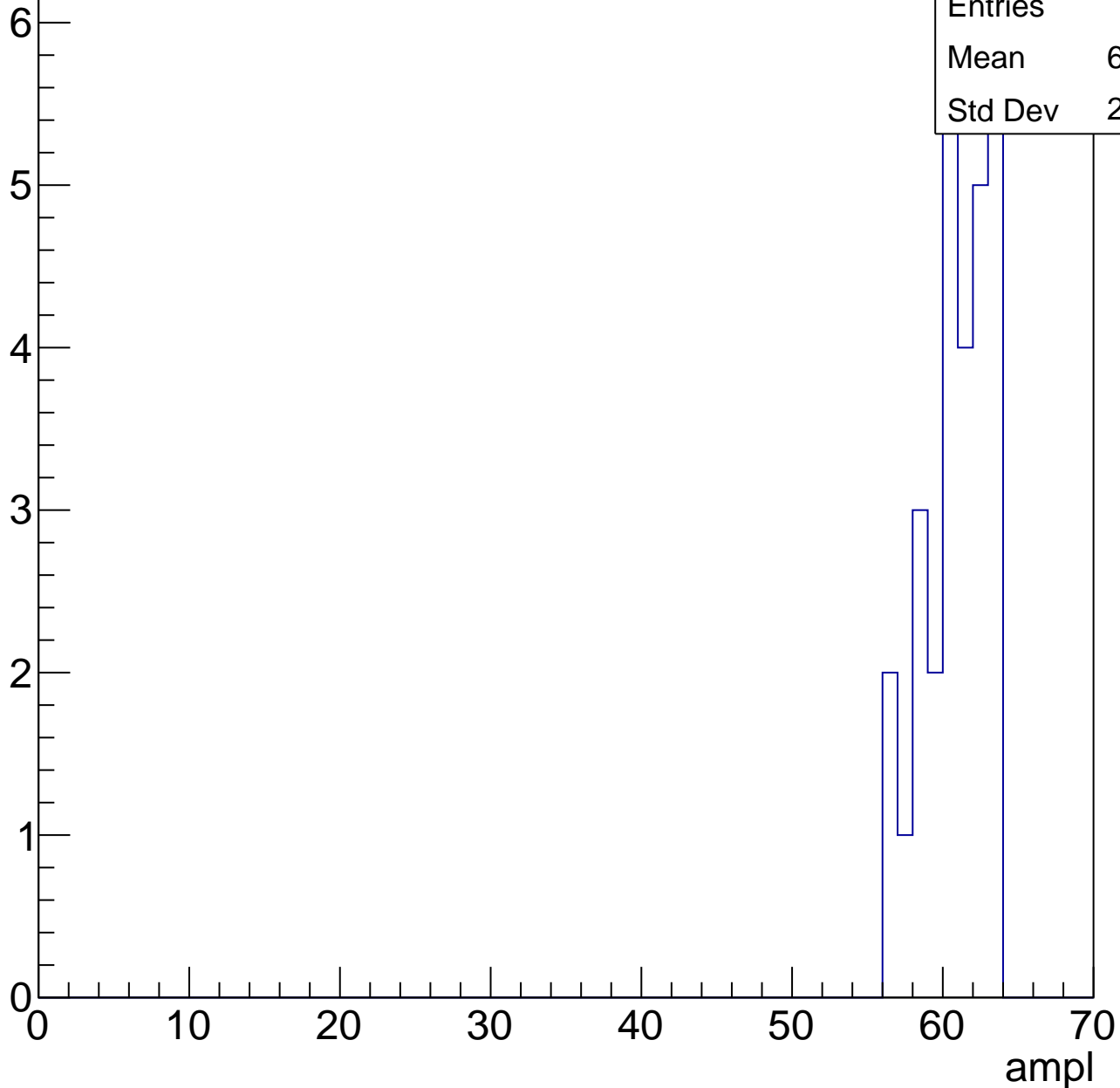
Entries	64
Mean	56.42
Std Dev	7.872



B0L001S, U19-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch14, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	29.91
Std Dev	4.644

Gaus mean : 30.7885

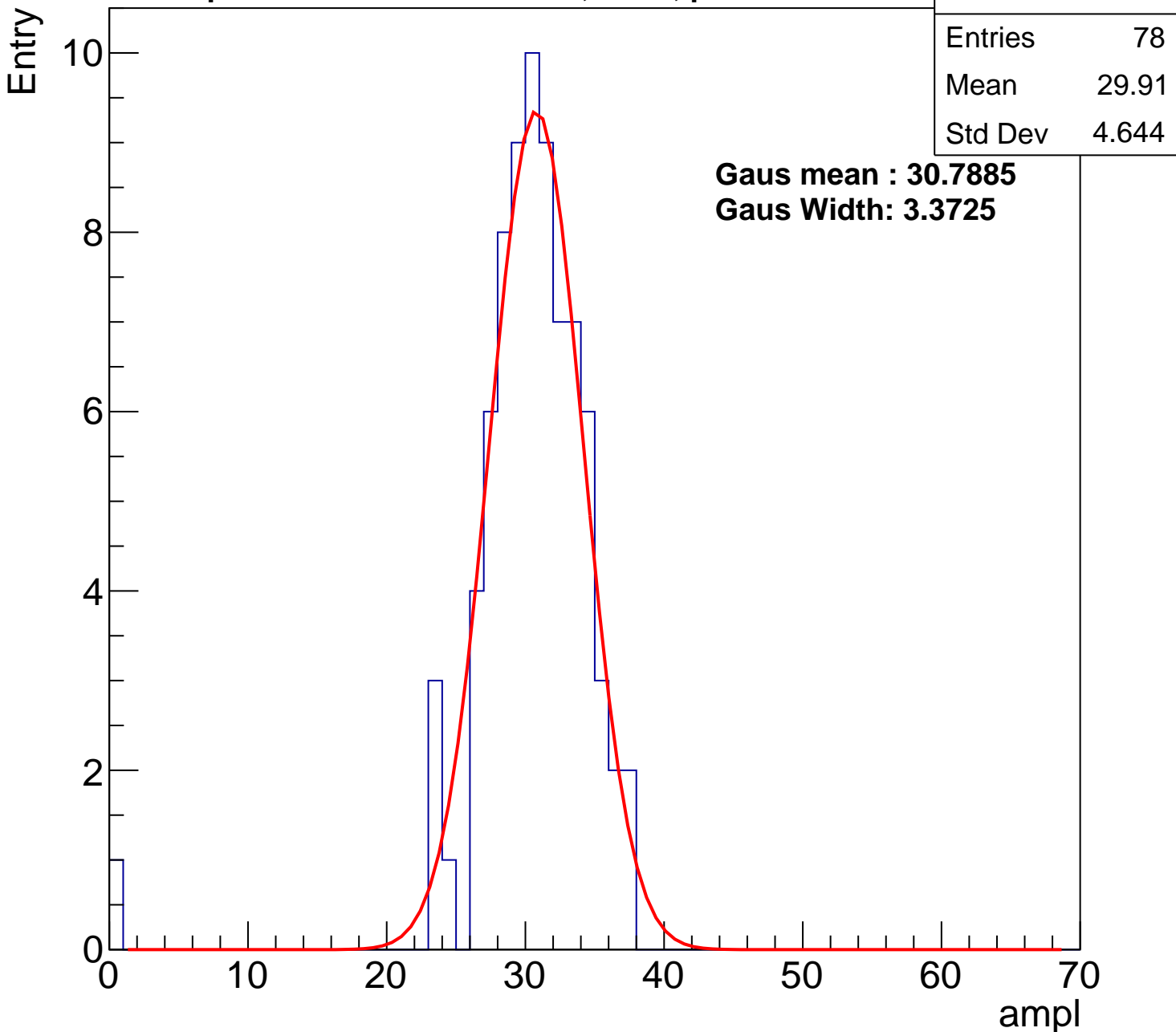
Gaus Width: 3.3725

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



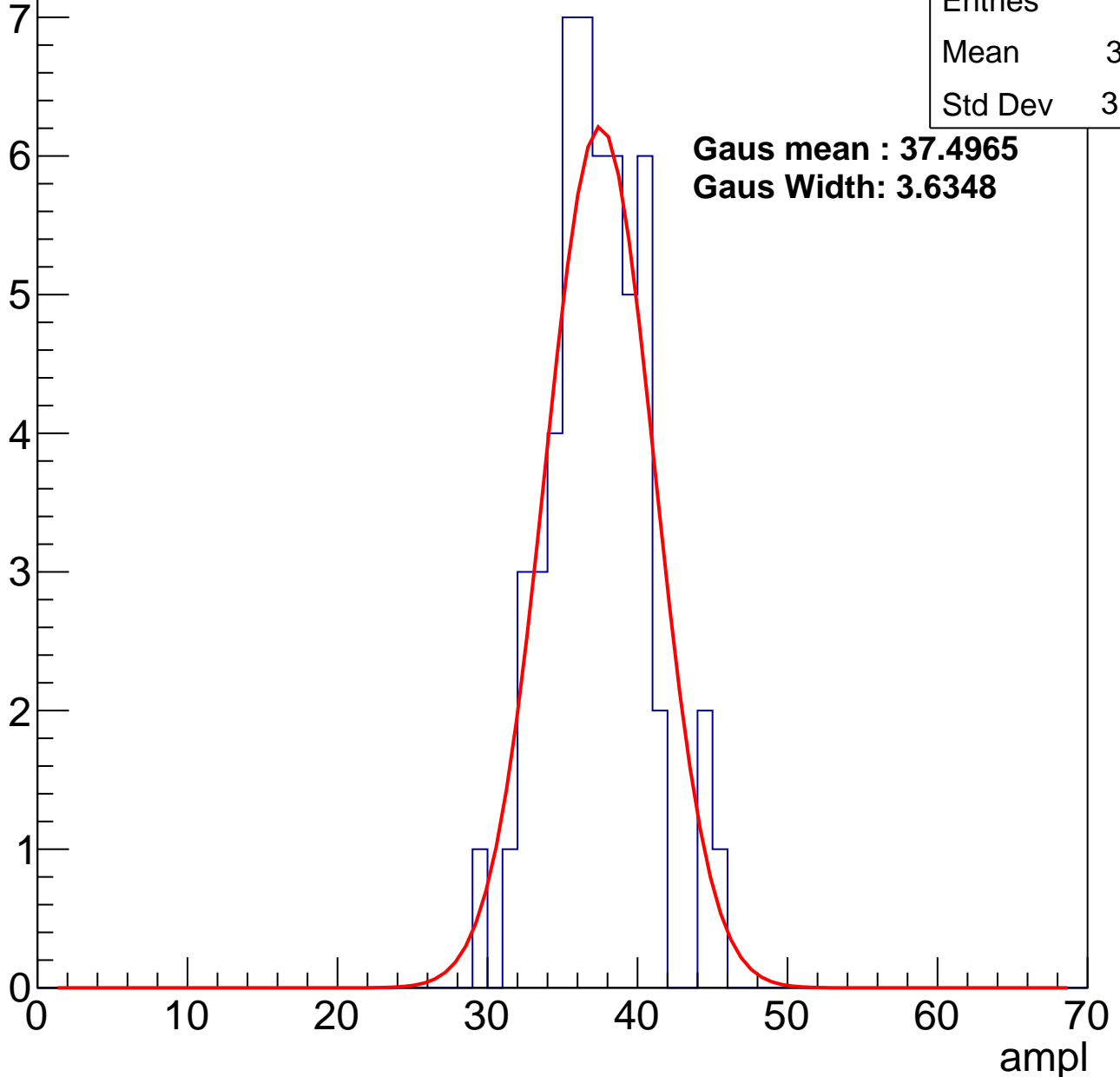
B0L001S, U19-ch14, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	36.81
Std Dev	3.238

Gaus mean : 37.4965
Gaus Width: 3.6348



B0L001S, U19-ch14, adc2

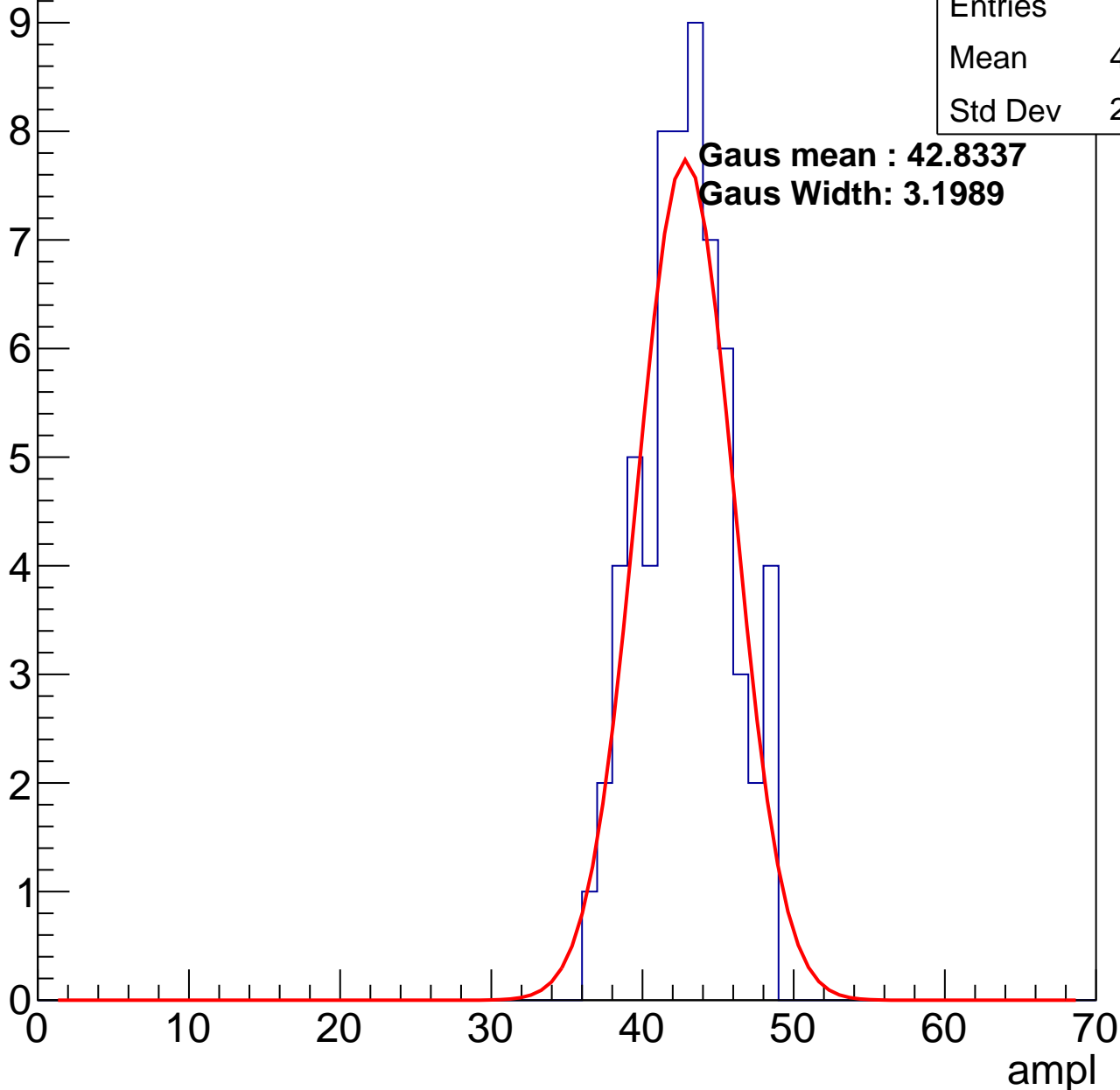
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	42.38
Std Dev	2.935

Gaus mean : 42.8337

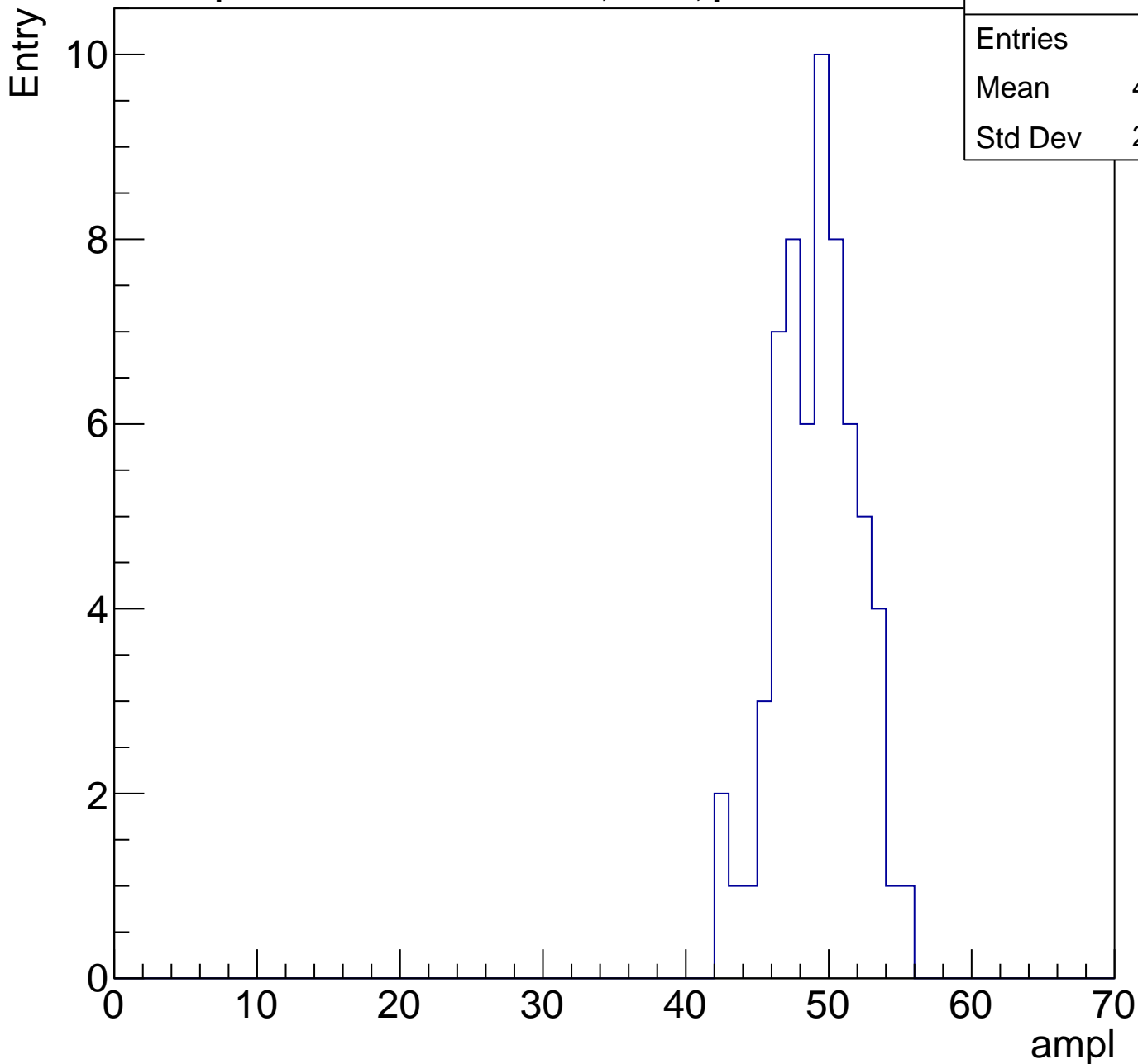
Gaus Width: 3.1989



B0L001S, U19-ch14, adc3

calib_packv5_042523_0143.root, FC#9, port A1

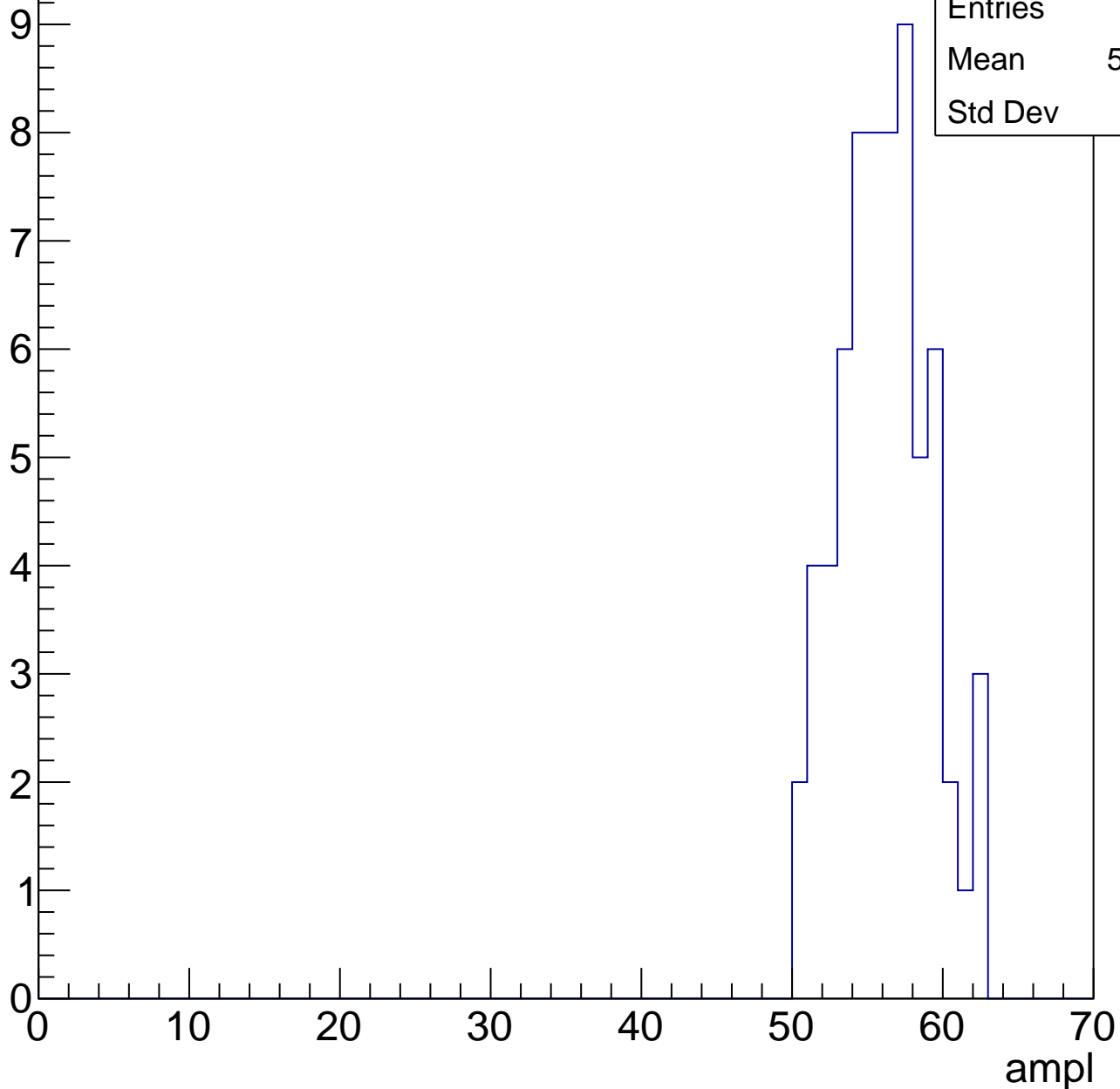
Entries	63
Mean	48.71
Std Dev	2.831



B0L001S, U19-ch14, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch14, adc5

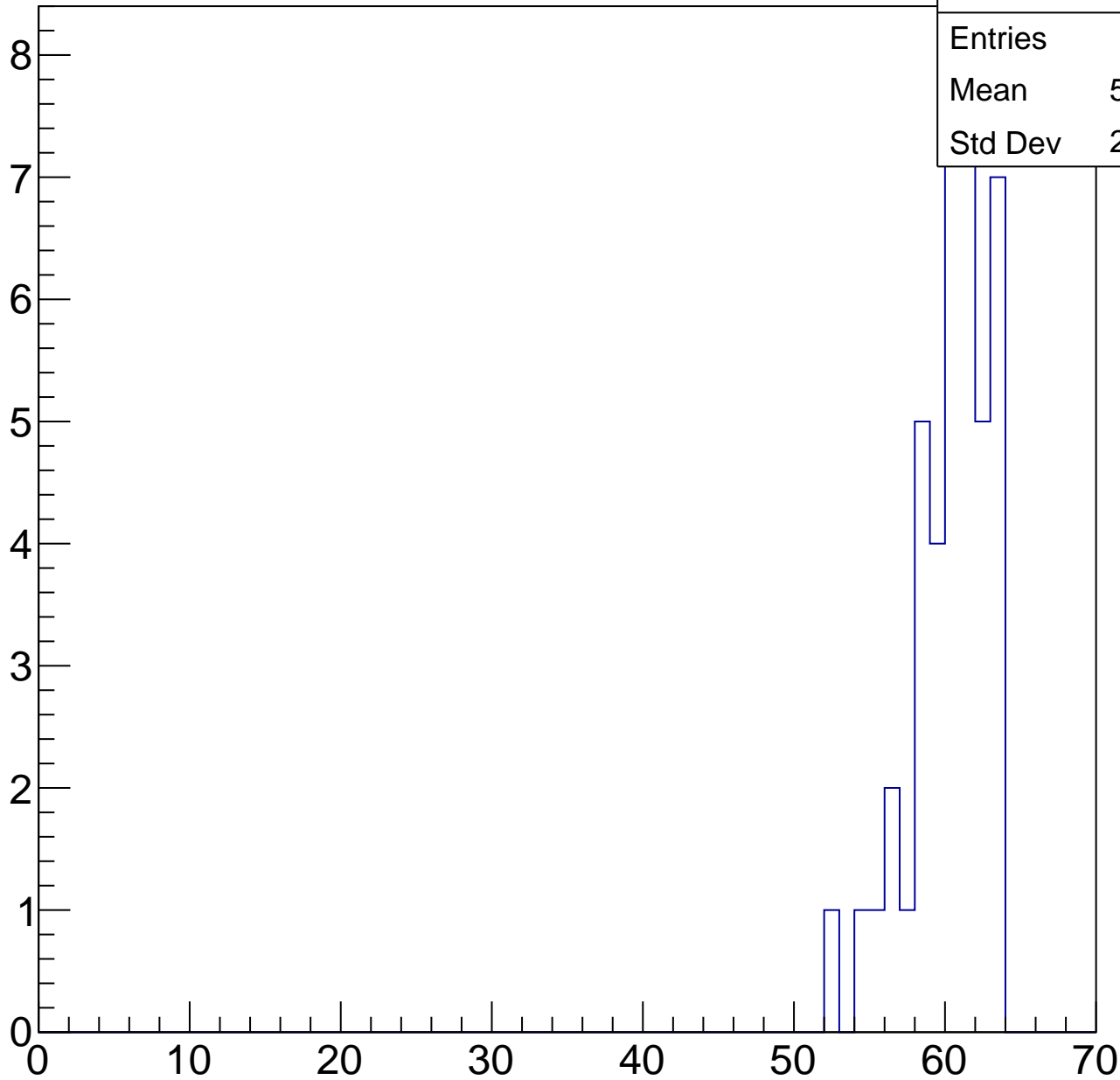
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.88
Std Dev	2.554

ampl



B0L001S, U19-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch15, adc0

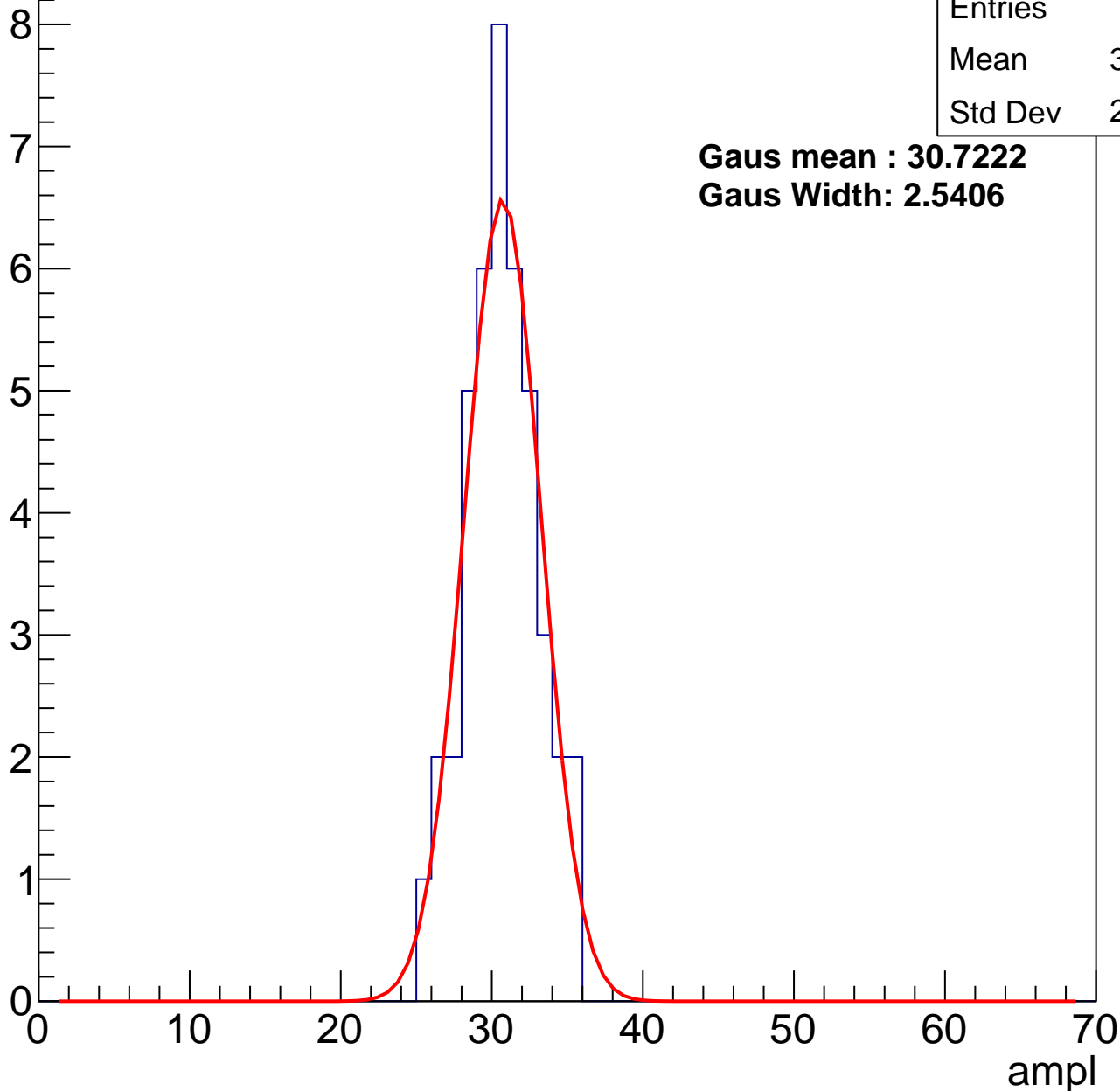
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	30.19
Std Dev	2.363

Gaus mean : 30.7222

Gaus Width: 2.5406



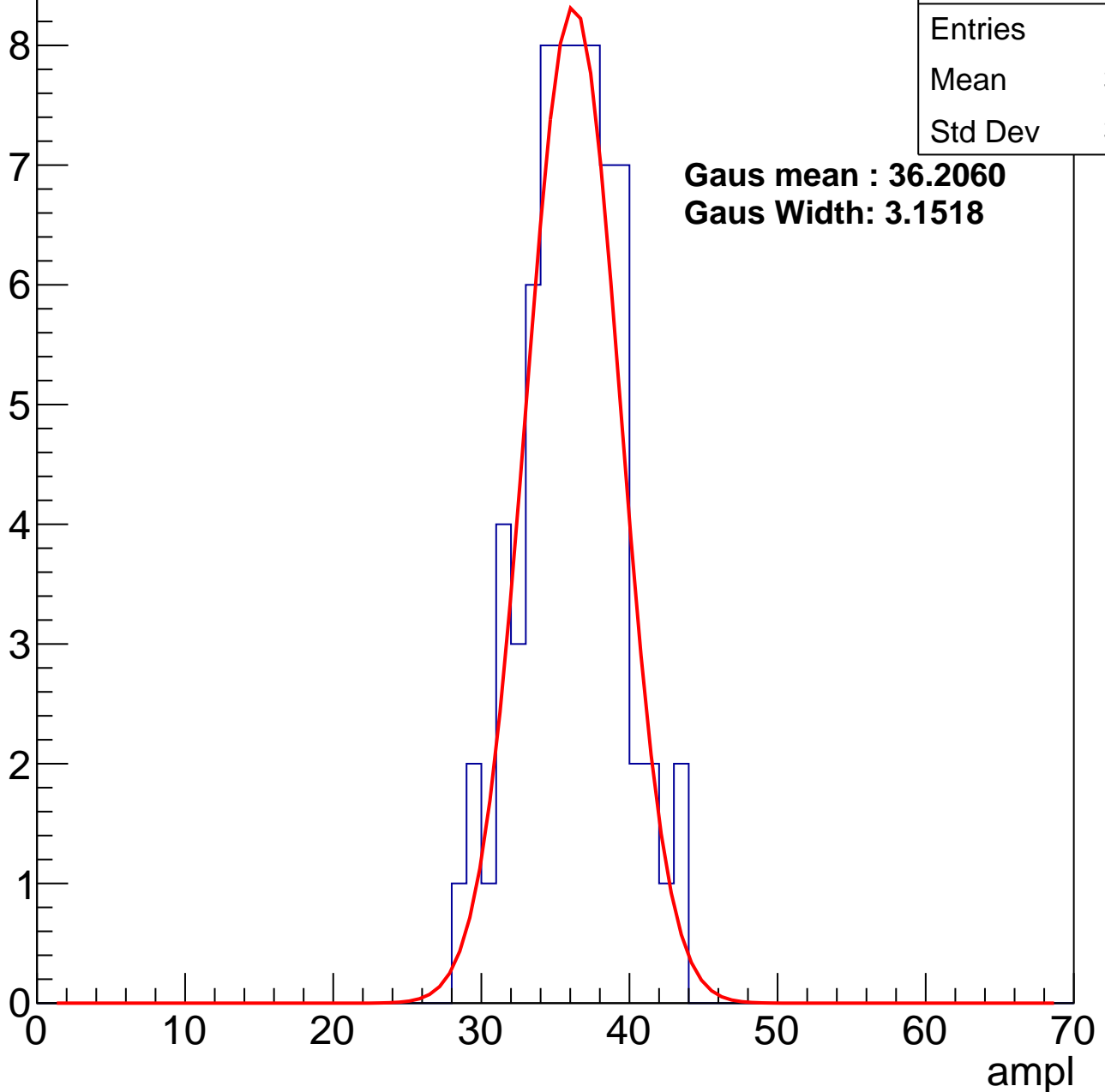
B0L001S, U19-ch15, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	35.7
Std Dev	3.27

Gaus mean : 36.2060
Gaus Width: 3.1518



B0L001S, U19-ch15, adc2

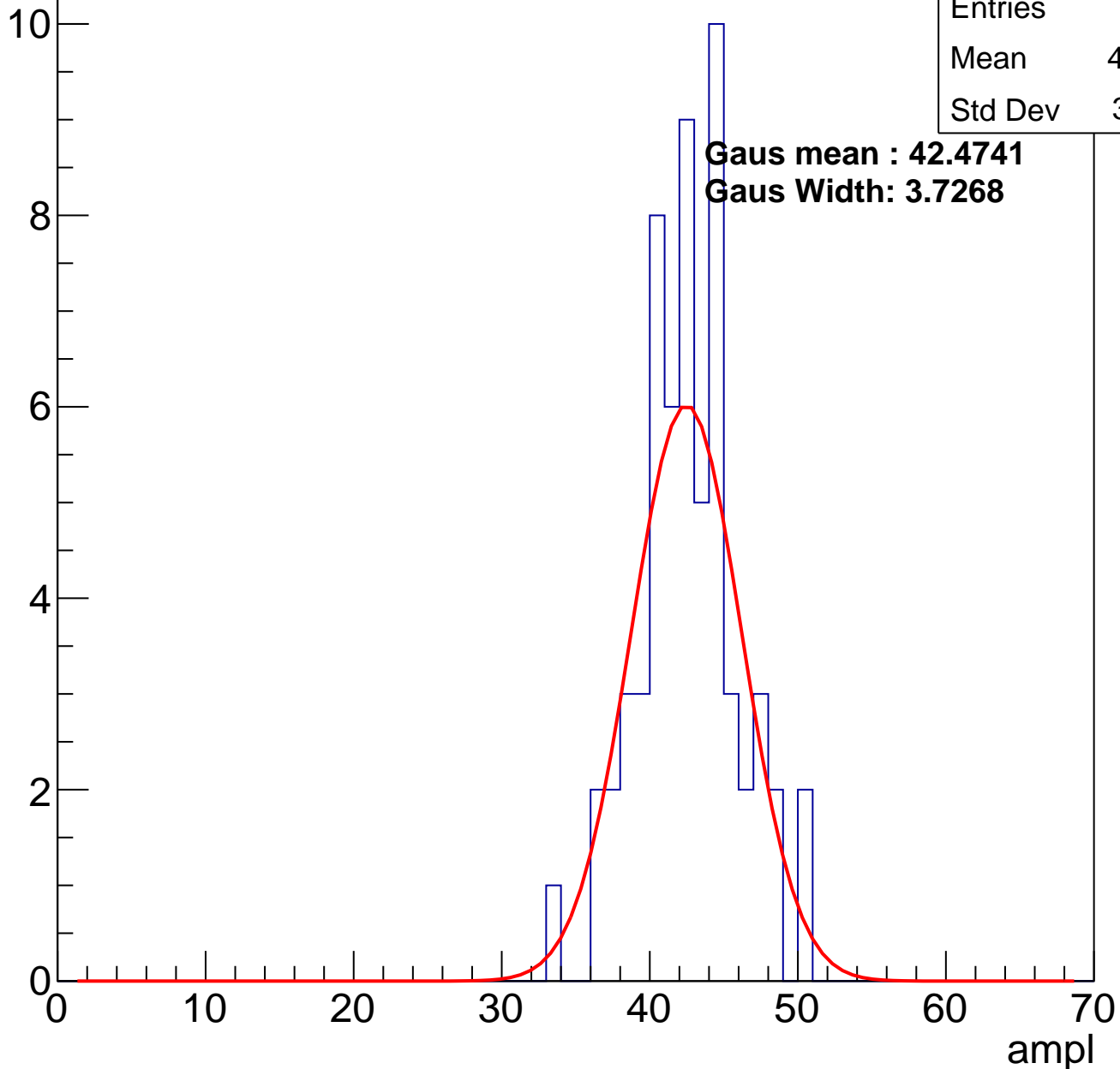
calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	42.18
Std Dev	3.371

Gaus mean : 42.4741

Gaus Width: 3.7268

Entry

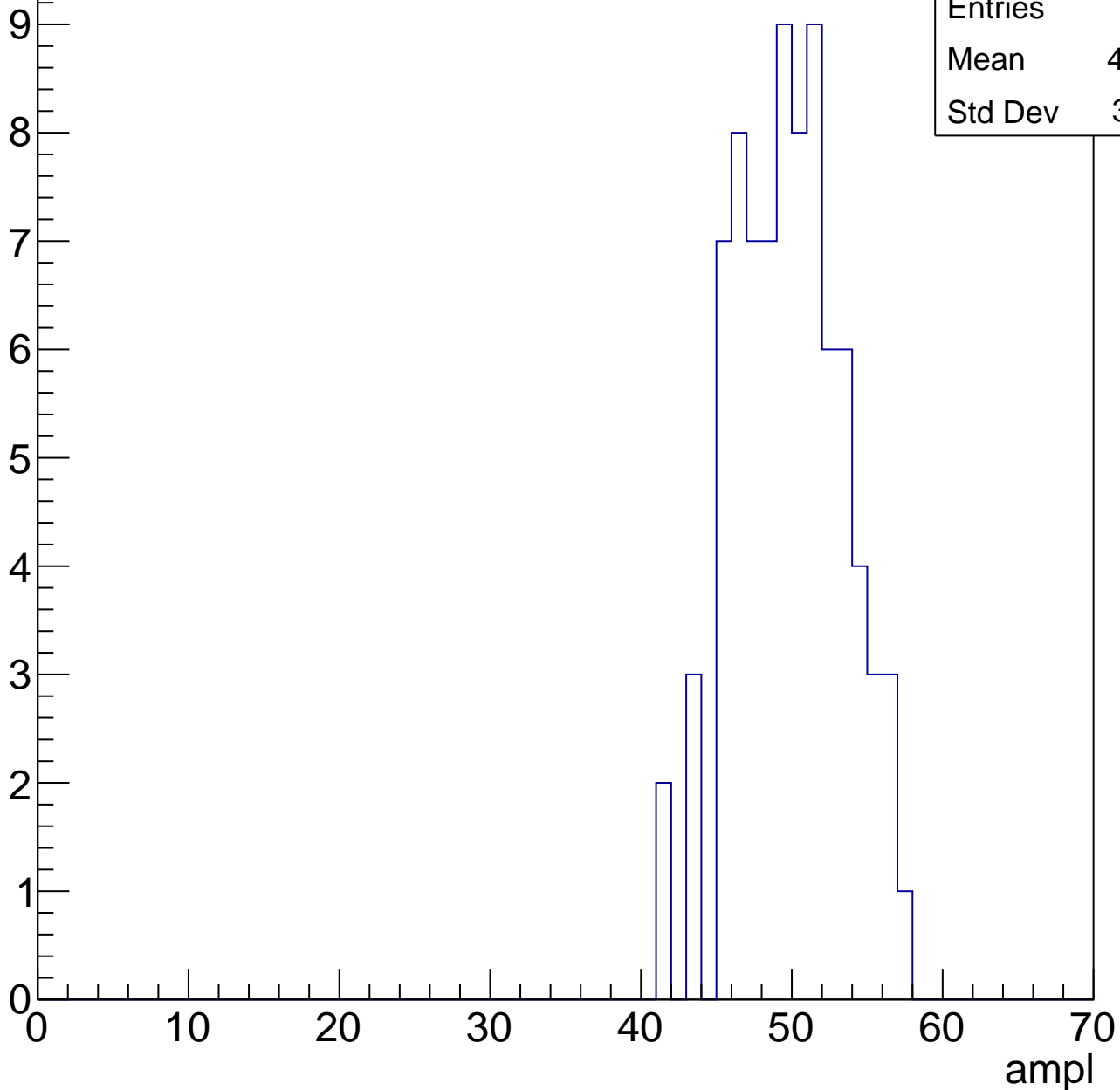


B0L001S, U19-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	49.34
Std Dev	3.571

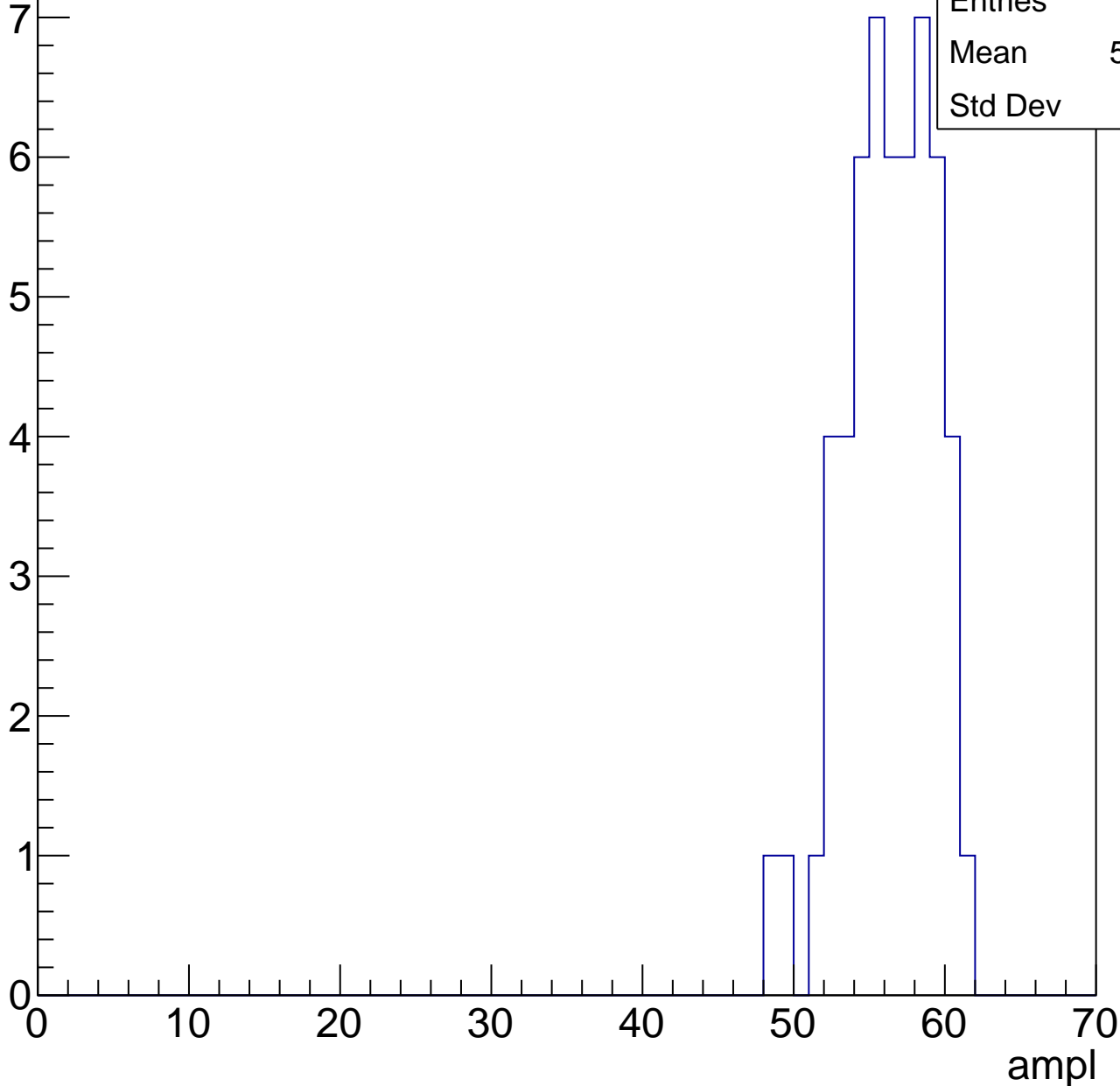


B0L001S, U19-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	55.85
Std Dev	2.87

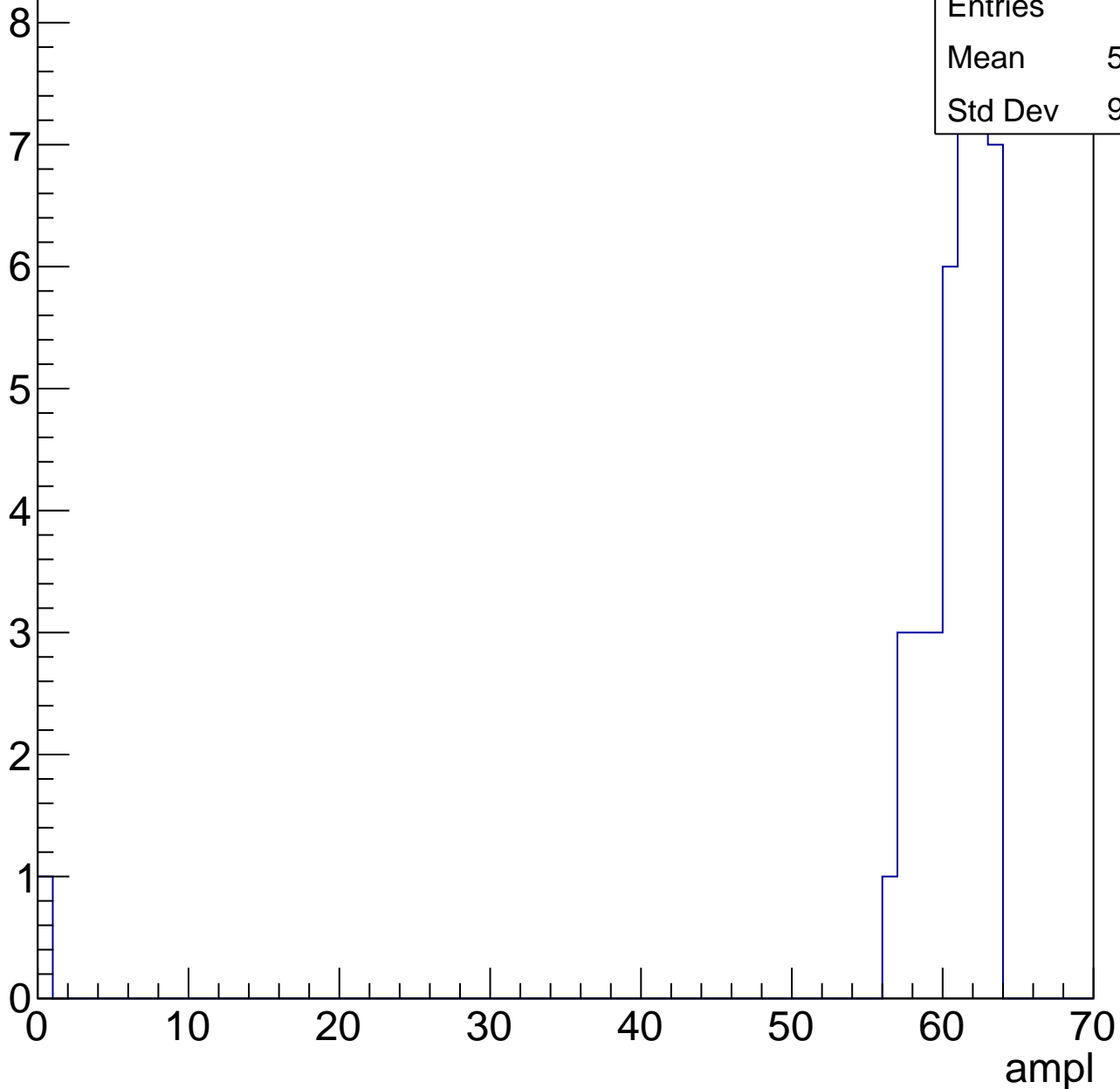


B0L001S, U19-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	59.08
Std Dev	9.652



B0L001S, U19-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch16, adc0

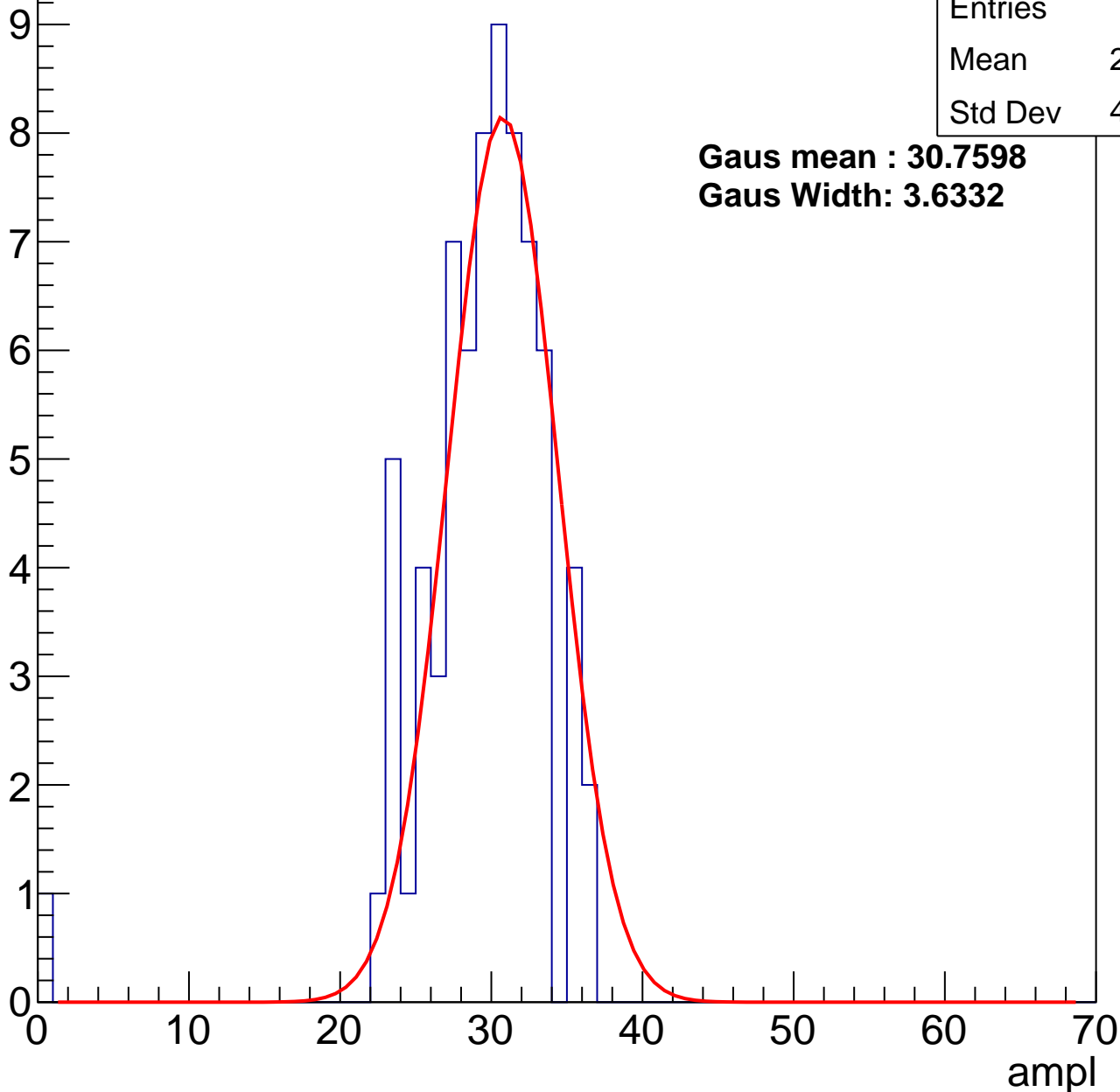
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	28.89
Std Dev	4.803

Gaus mean : 30.7598

Gaus Width: 3.6332

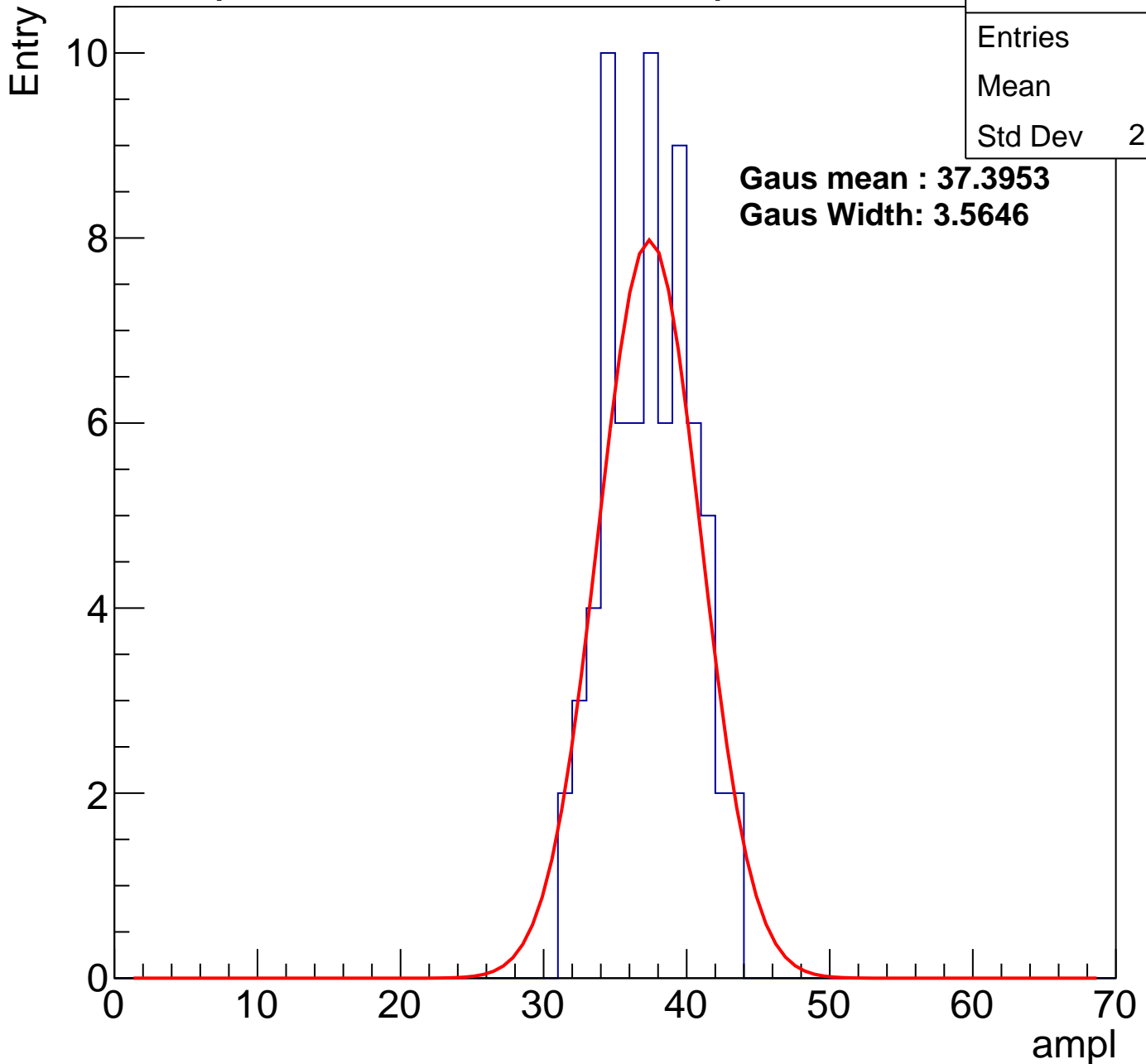


B0L001S, U19-ch16, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	36.9
Std Dev	2.975

Gaus mean : 37.3953
Gaus Width: 3.5646



B0L001S, U19-ch16, adc2

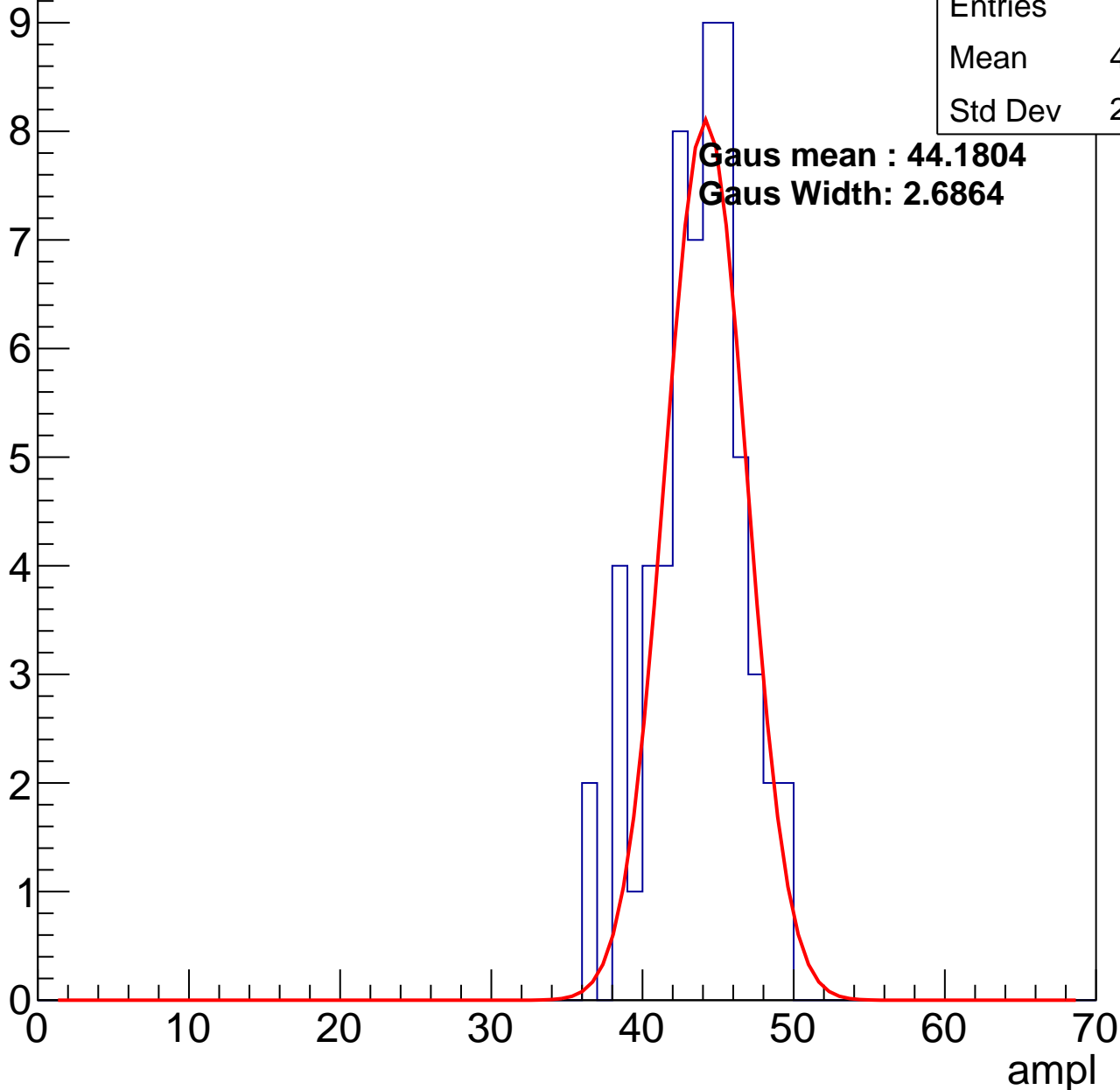
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.17
Std Dev	2.979

Gaus mean : 44.1804

Gaus Width: 2.6864

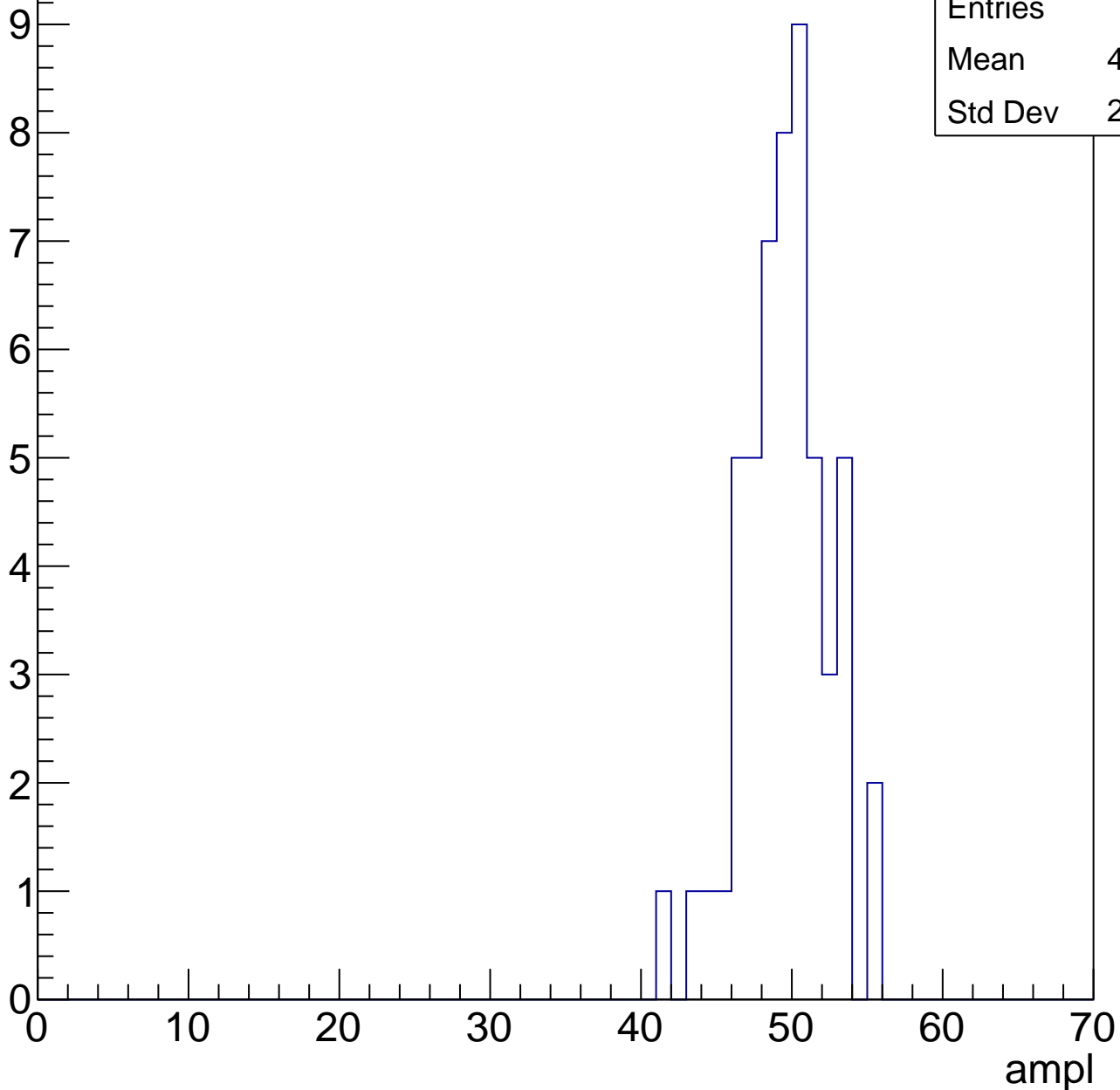


B0L001S, U19-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	49.09
Std Dev	2.817

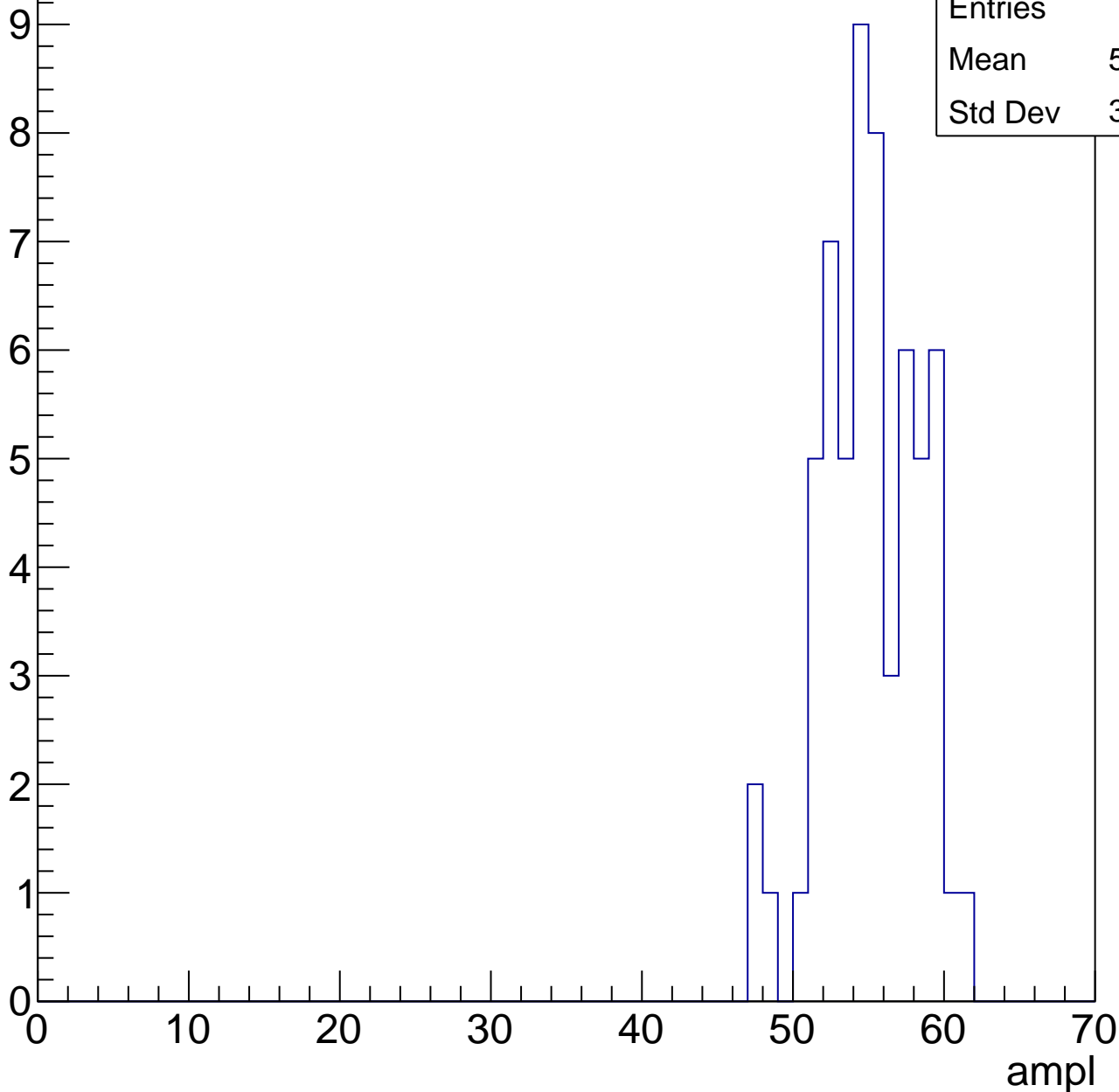


B0L001S, U19-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	54.62
Std Dev	3.147



B0L001S, U19-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

10

Entries 55

Mean 58.73

Std Dev 8.326

8

6

4

2

0

0

10

20

30

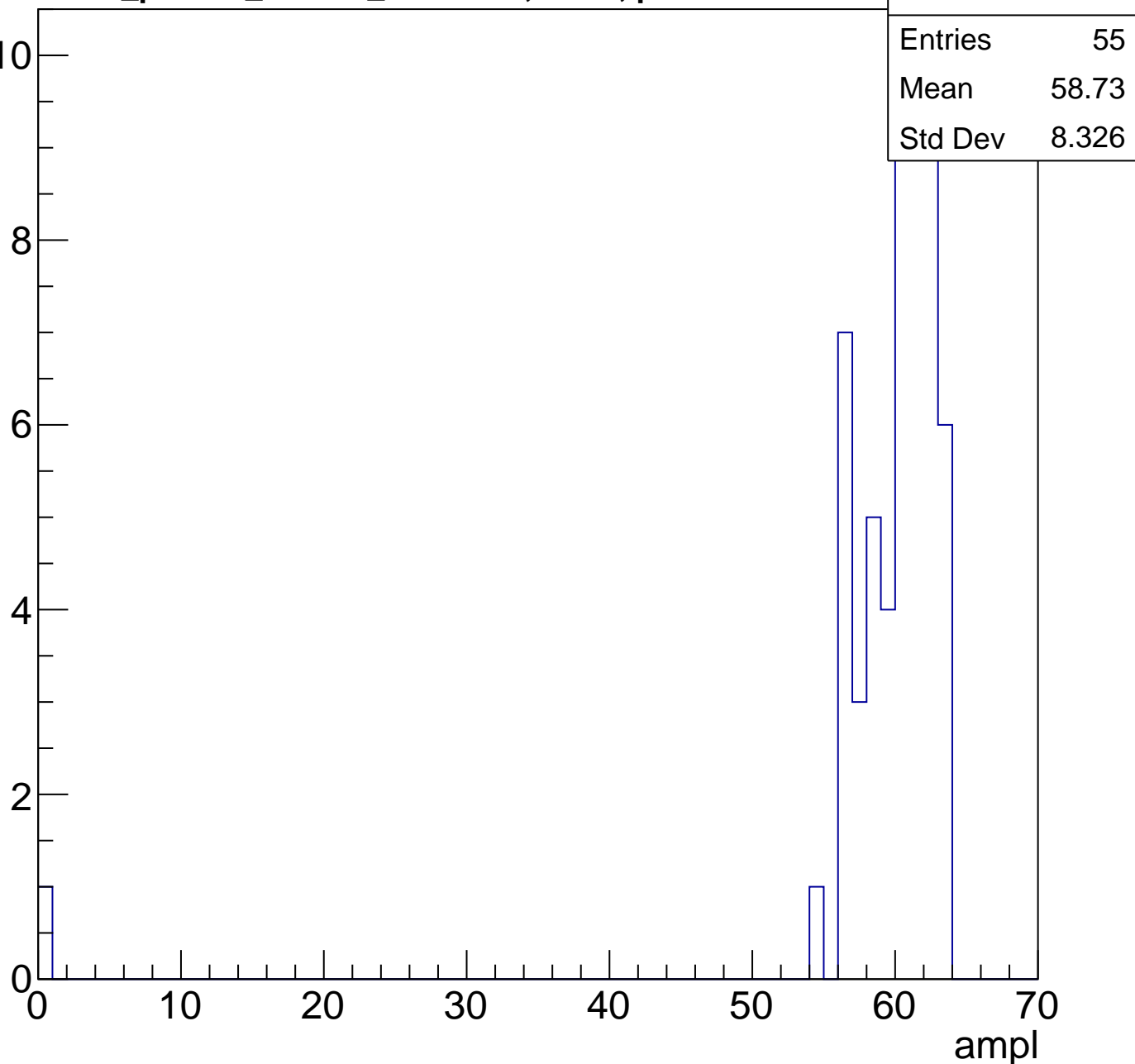
40

50

60

70

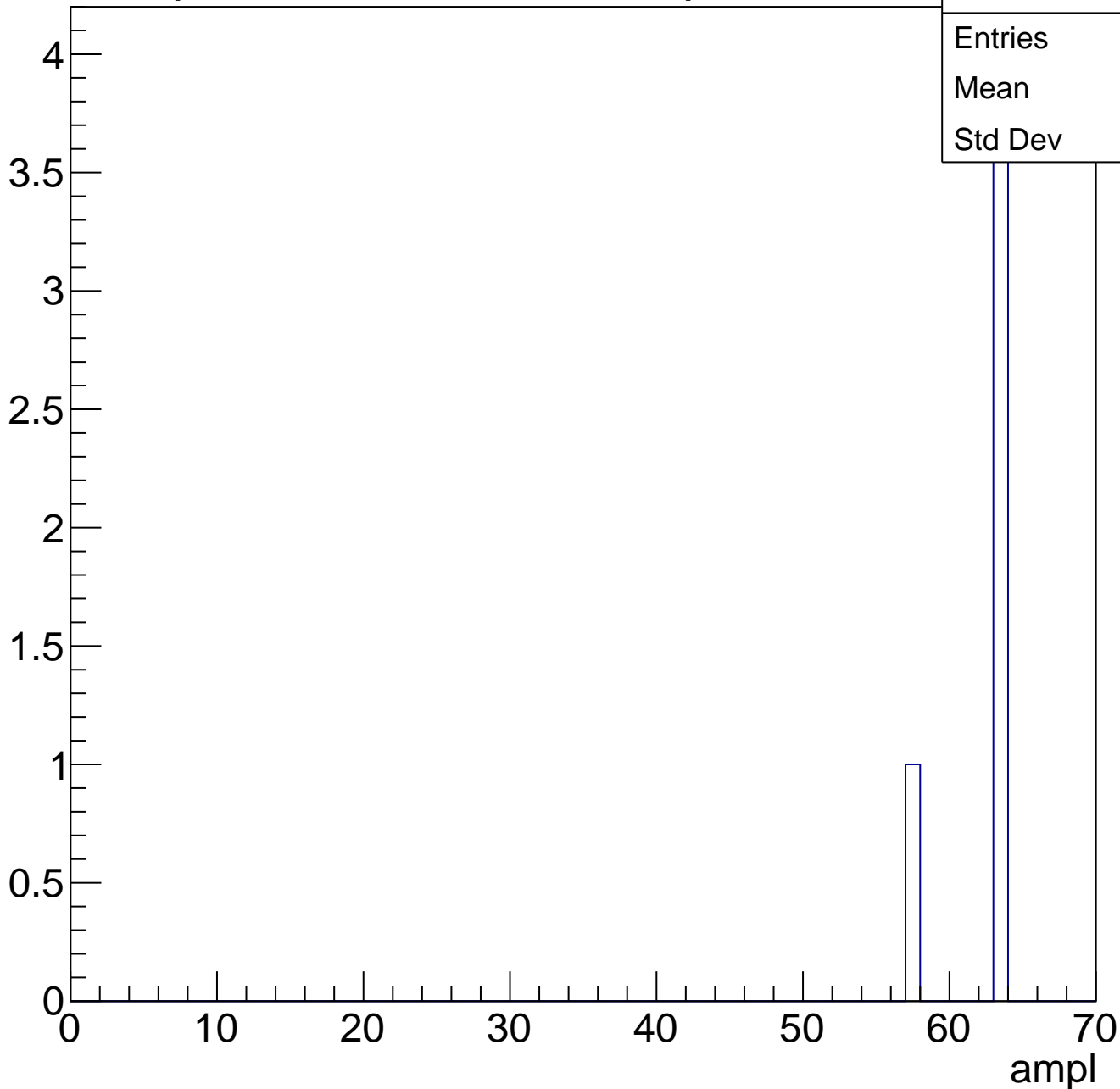
ampl



B0L001S, U19-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch17, adc0

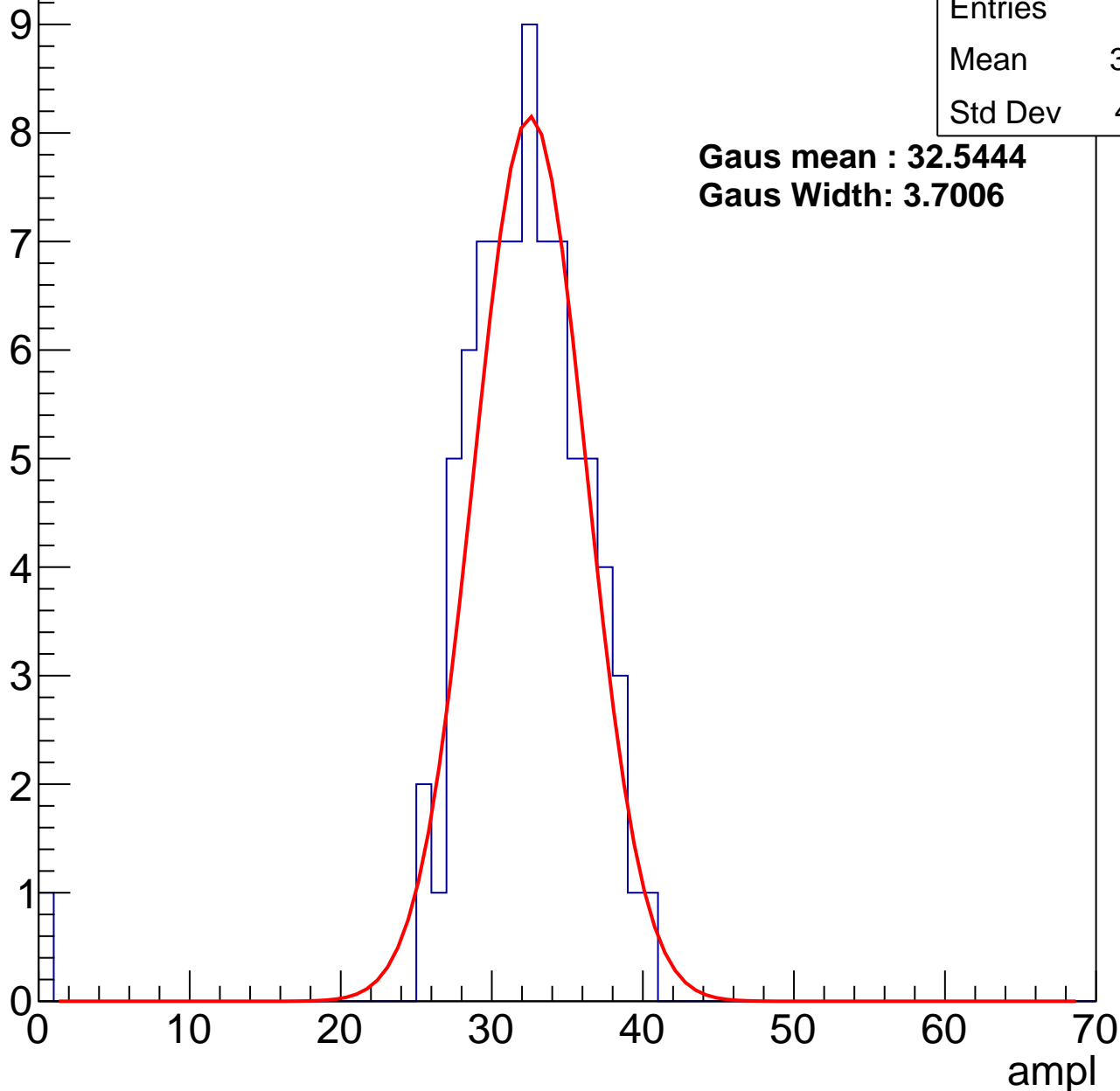
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	31.56
Std Dev	4.991

Gaus mean : 32.5444

Gaus Width: 3.7006



B0L001S, U19-ch17, adc1

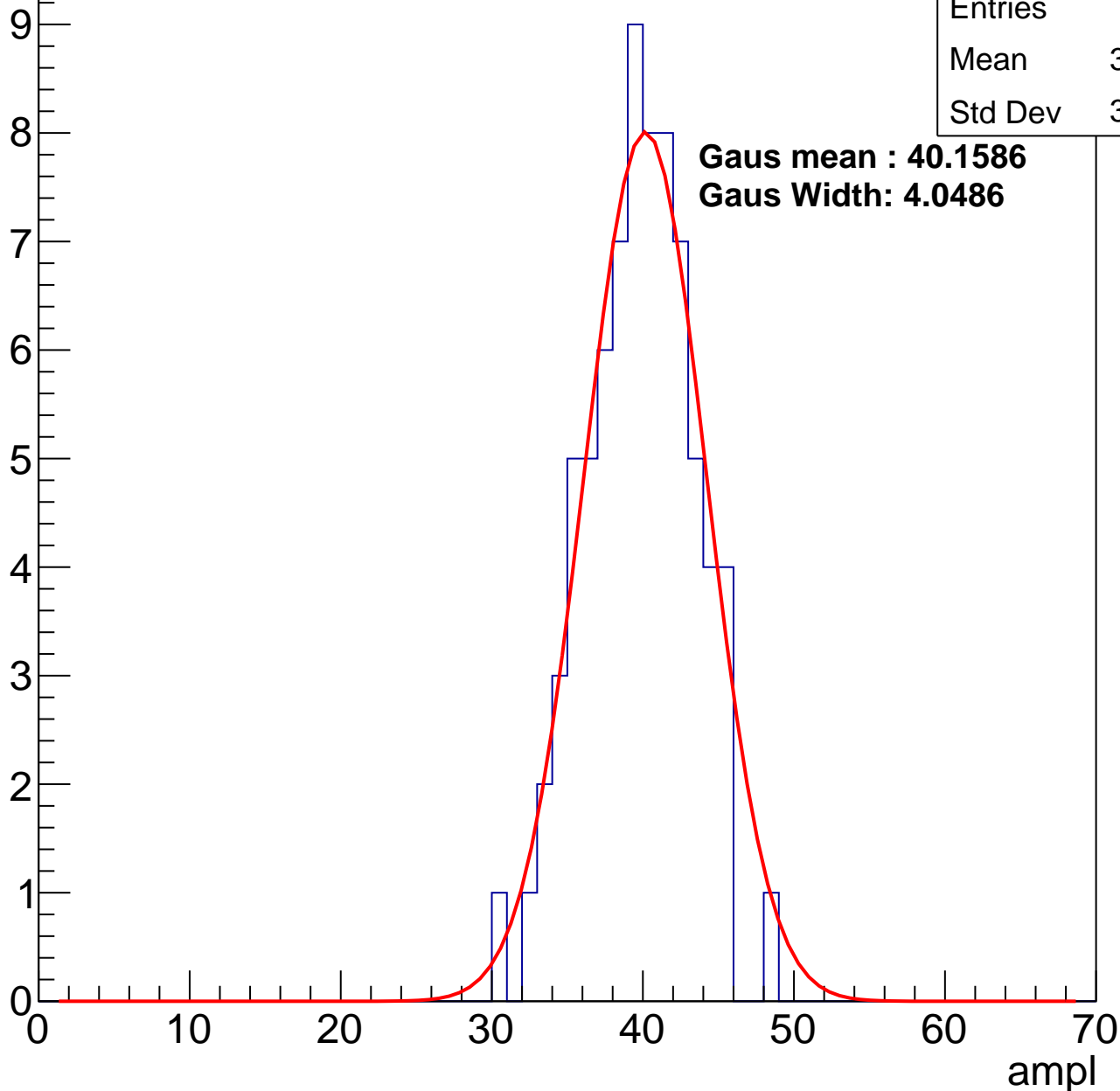
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	39.28
Std Dev	3.508

Gaus mean : 40.1586

Gaus Width: 4.0486



B0L001S, U19-ch17, adc2

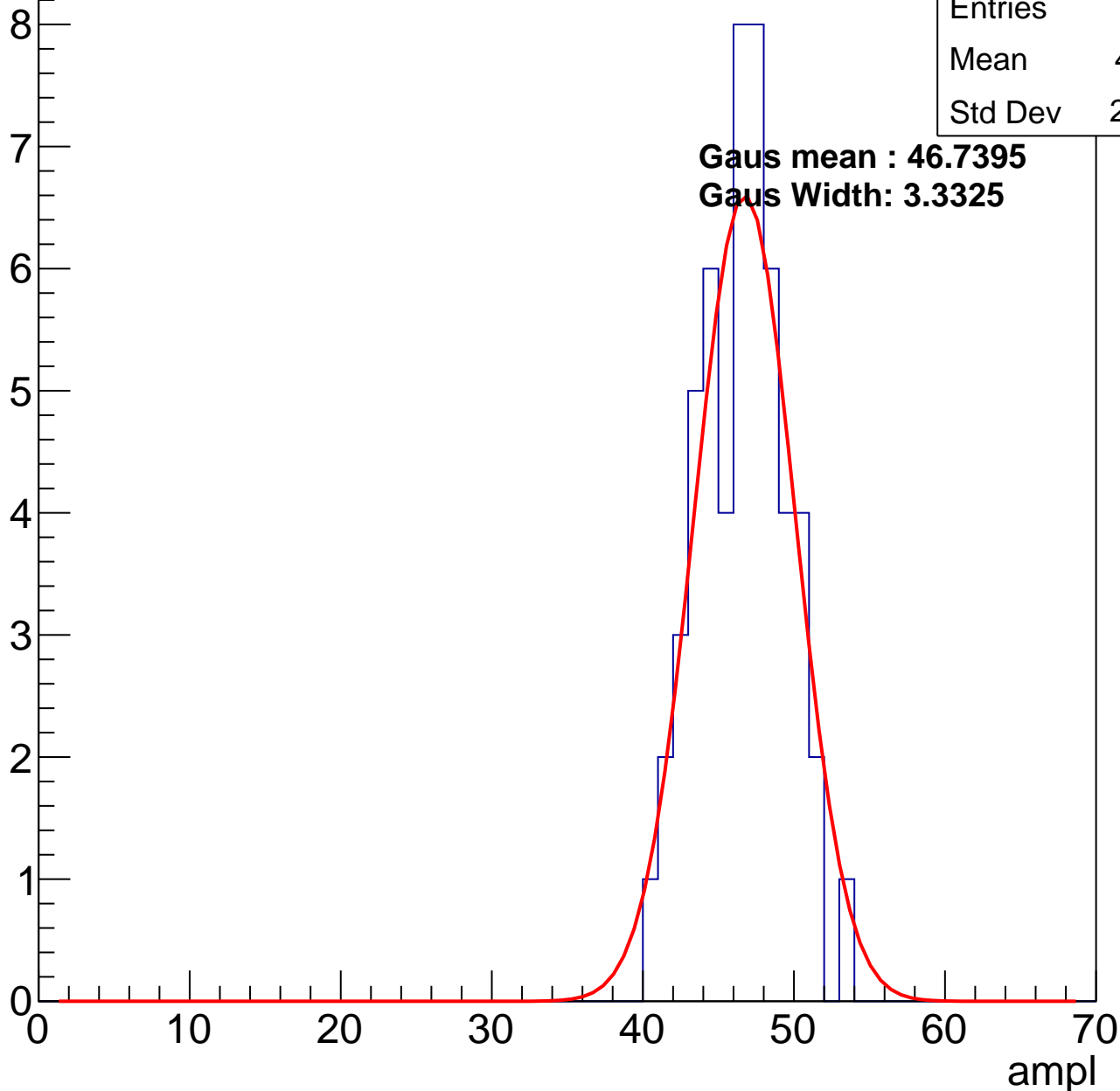
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	46.11
Std Dev	2.846

Gaus mean : 46.7395

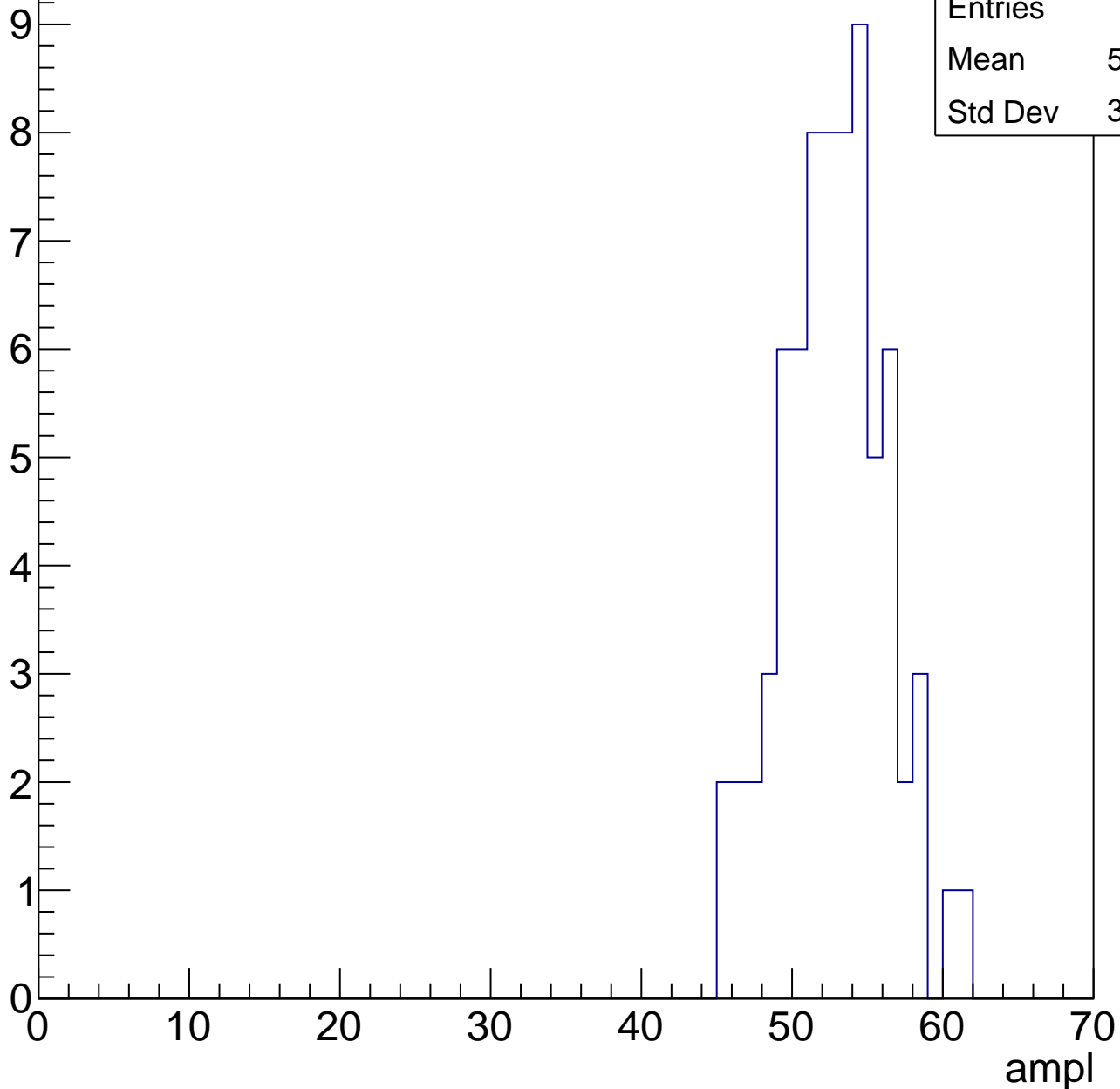
Gaus Width: 3.3325



B0L001S, U19-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

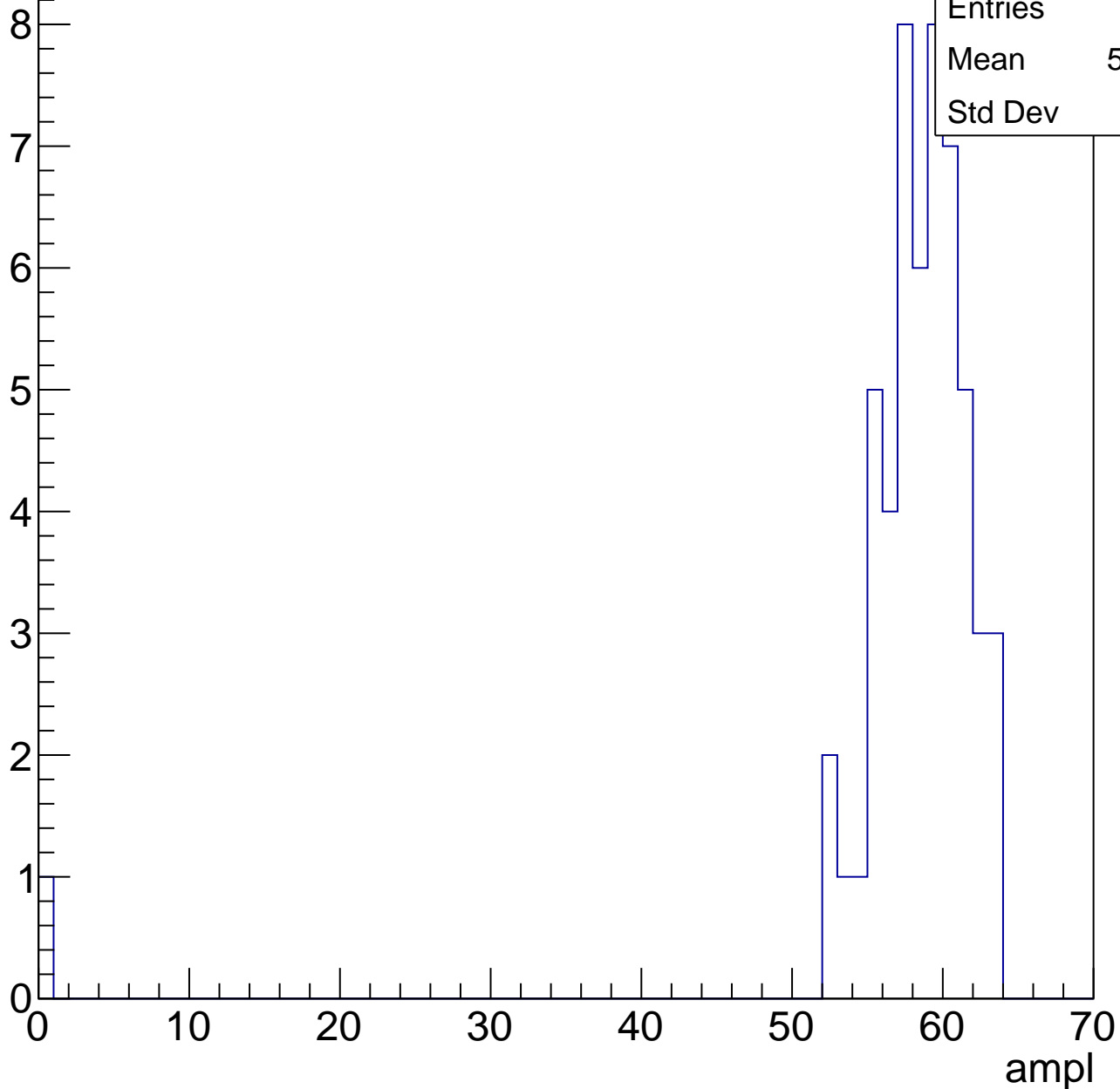


B0L001S, U19-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	57.15
Std Dev	8.29

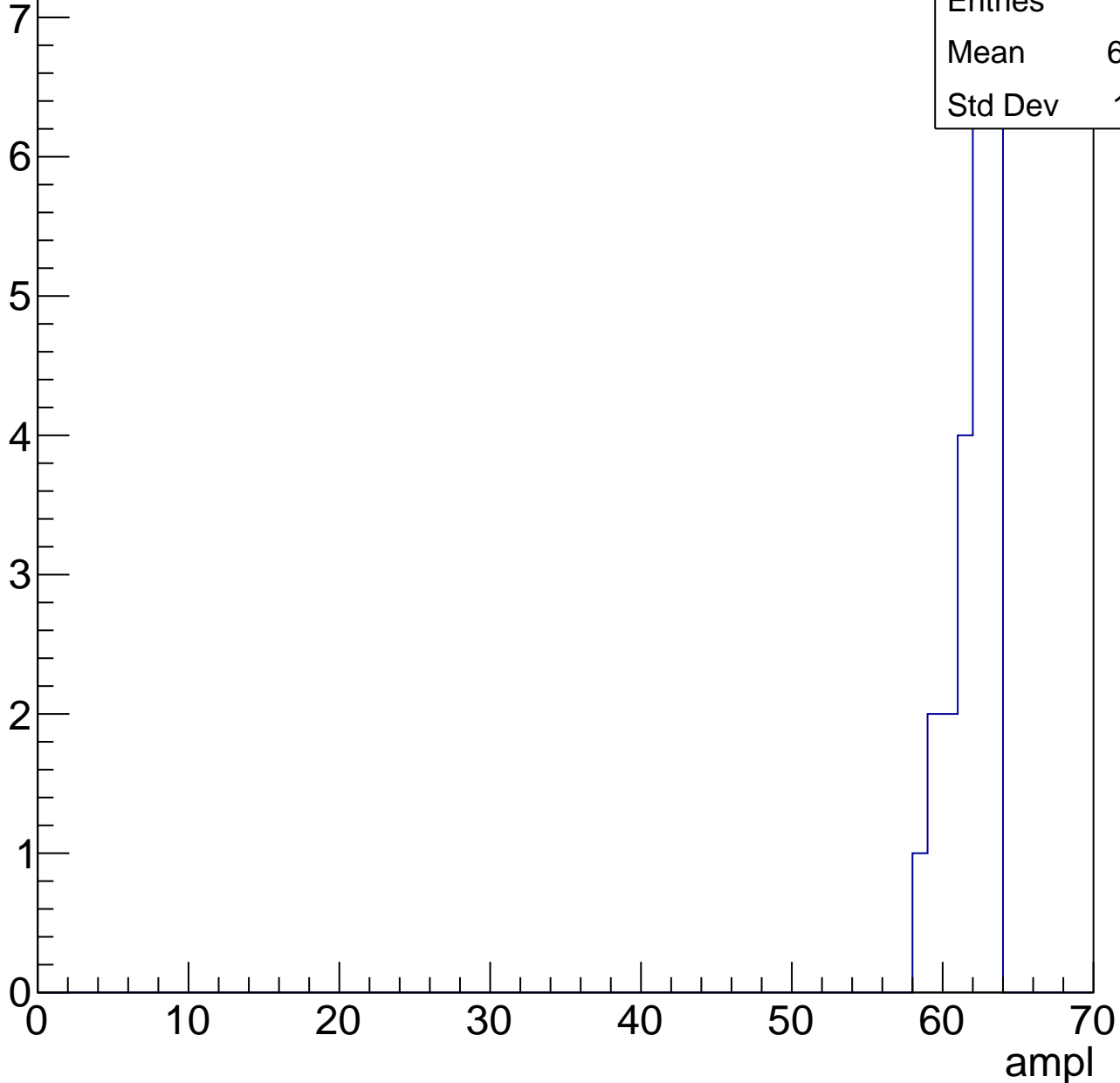


B0L001S, U19-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	61.52
Std Dev	1.441



B0L001S, U19-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch18, adc0

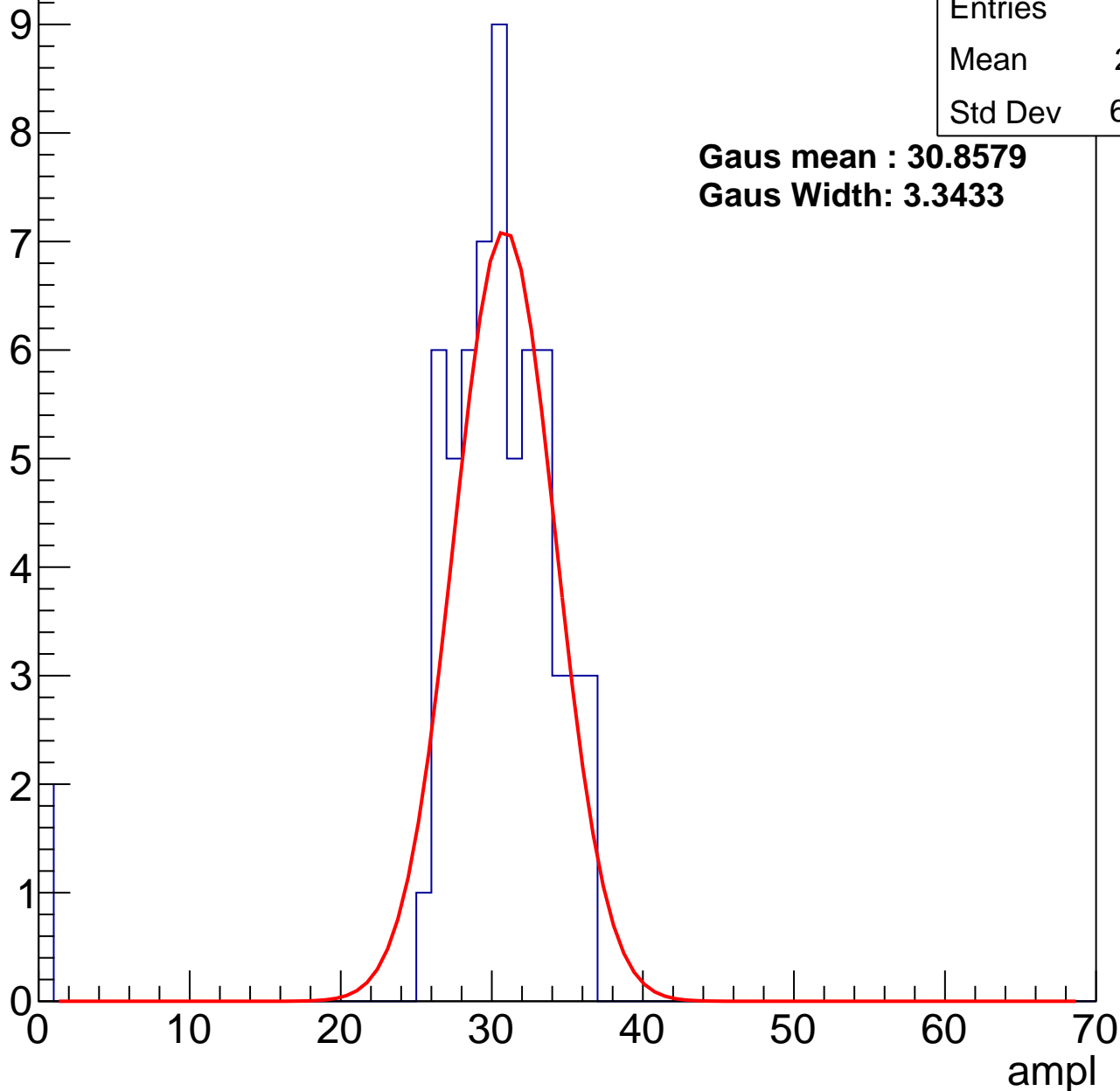
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.31
Std Dev	6.066

Gaus mean : 30.8579

Gaus Width: 3.3433



B0L001S, U19-ch18, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	37.23
Std Dev	3.451

Gaus mean : 37.3971

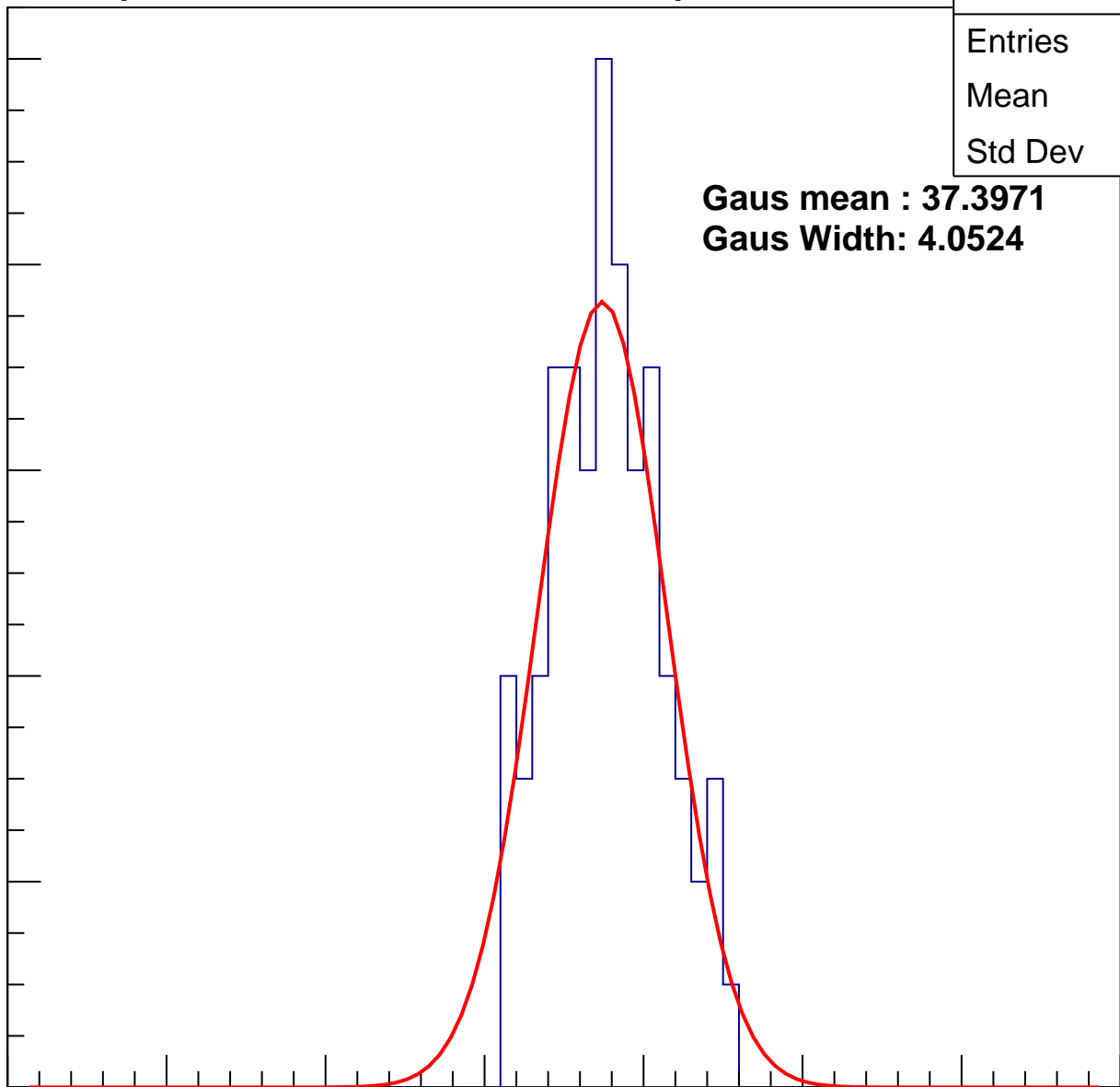
Gaus Width: 4.0524

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch18, adc2

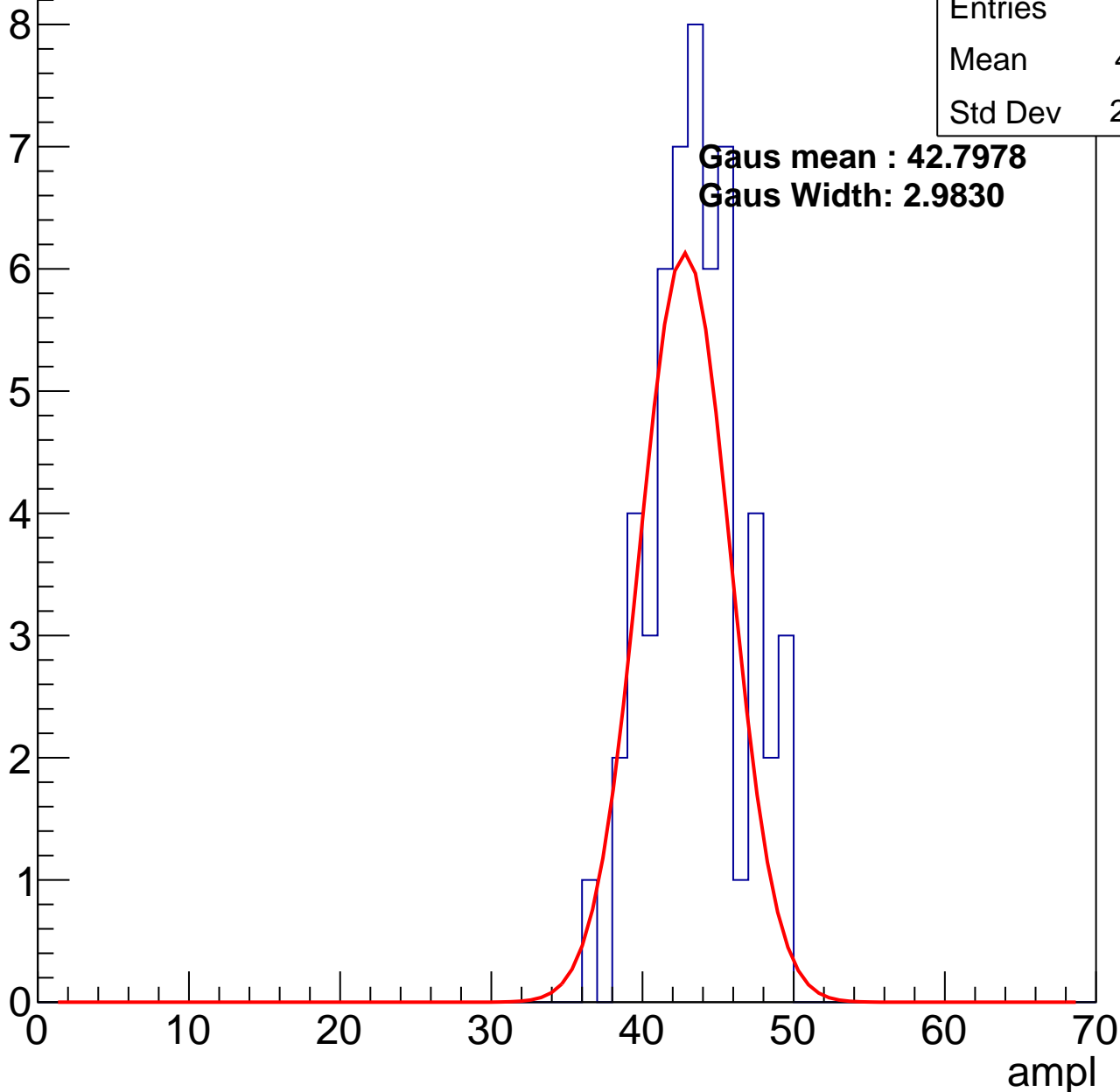
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	43.11
Std Dev	2.998

Gaus mean : 42.7978

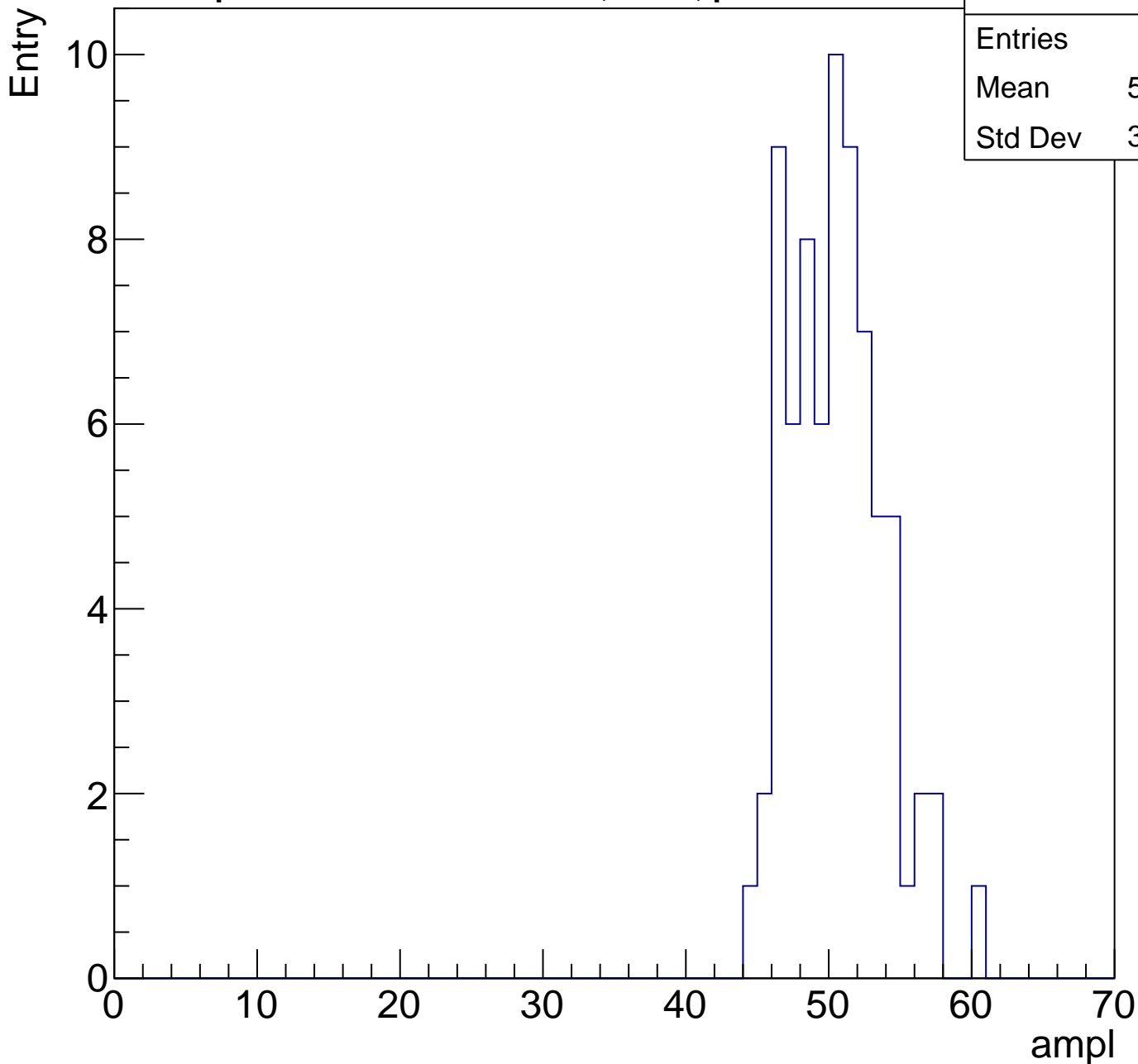
Gaus Width: 2.9830



B0L001S, U19-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	50.09
Std Dev	3.243

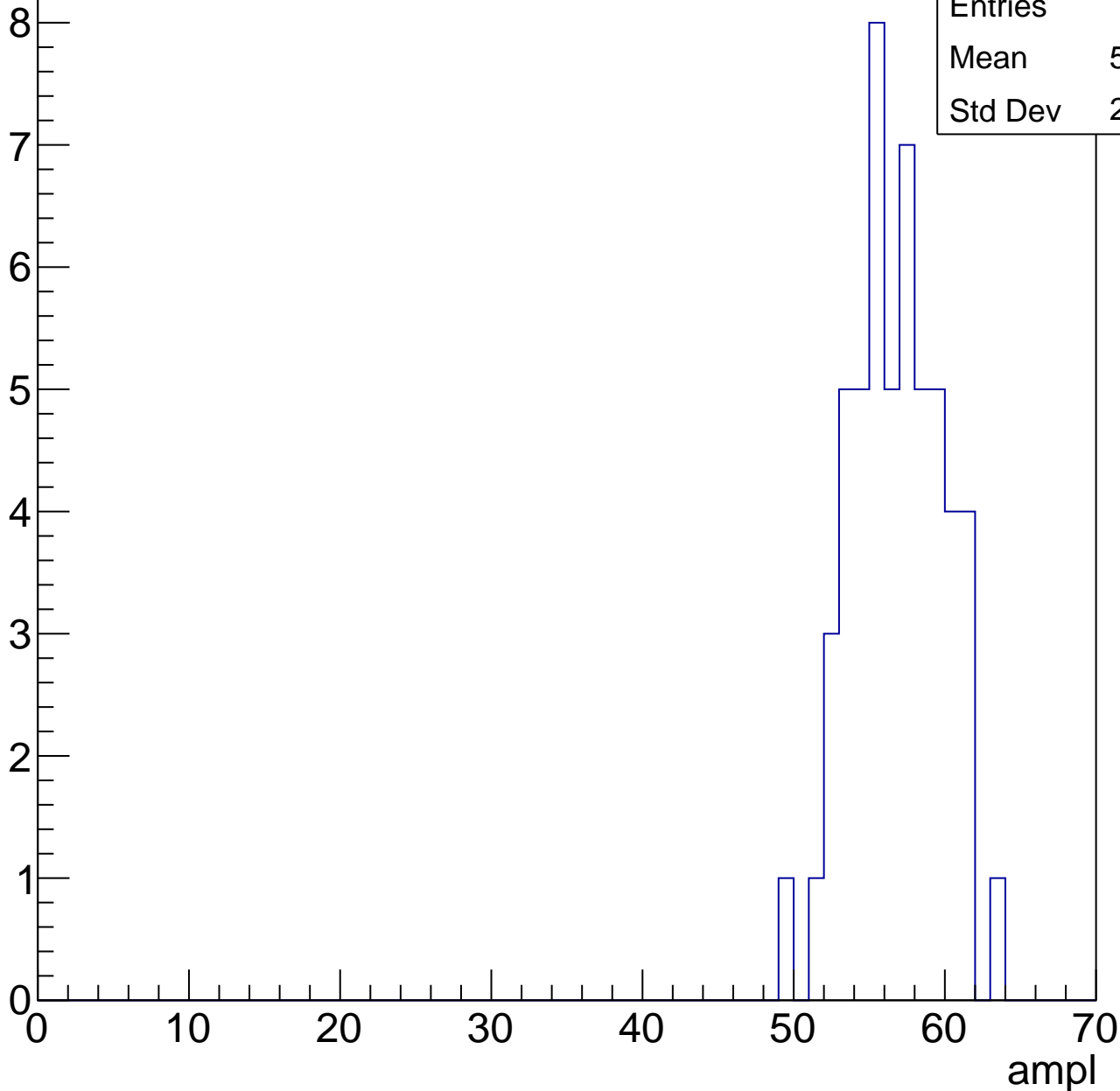


B0L001S, U19-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.33
Std Dev	2.963

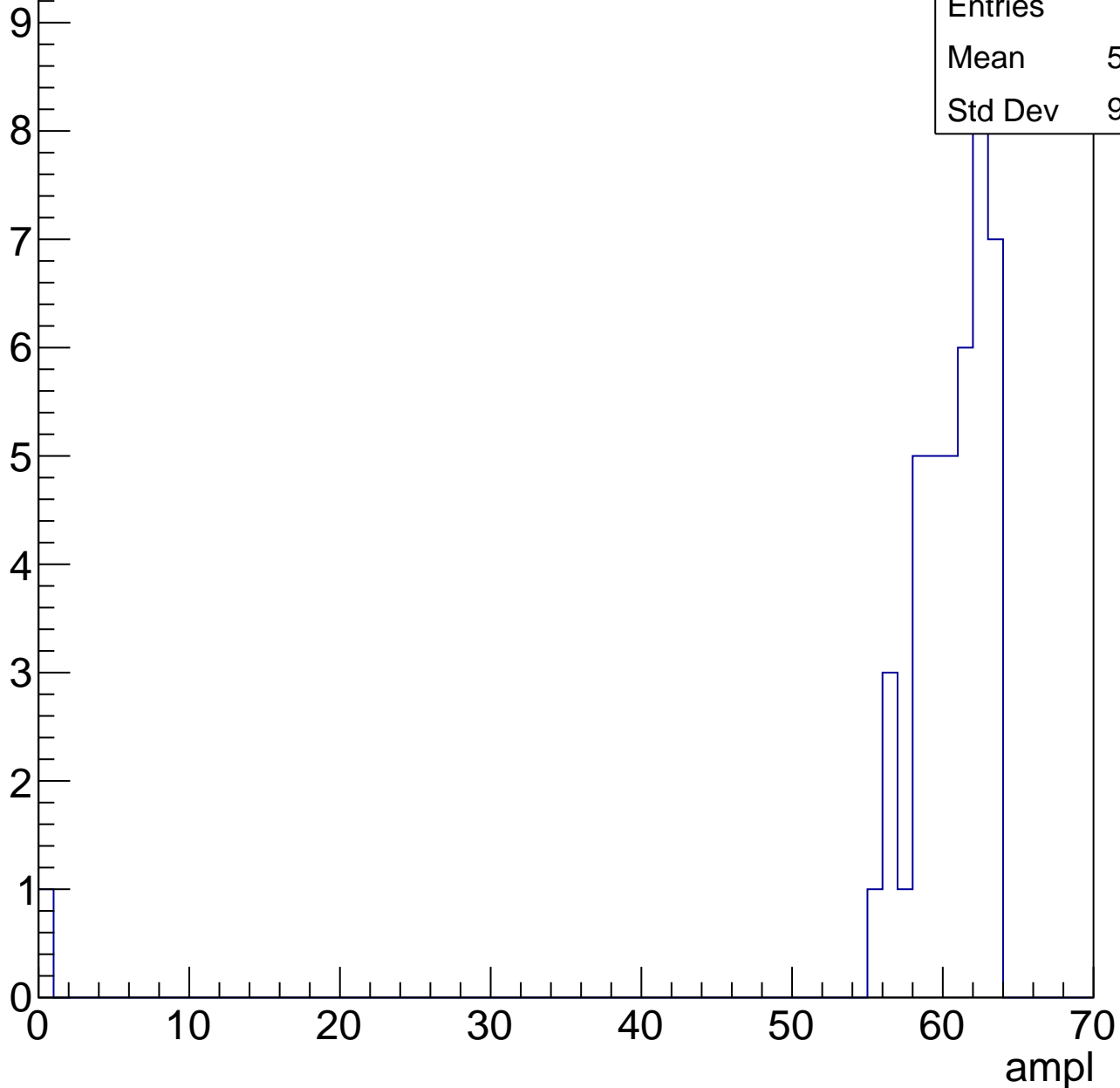


B0L001S, U19-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.84
Std Dev	9.343



B0L001S, U19-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U19-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch19, adc0

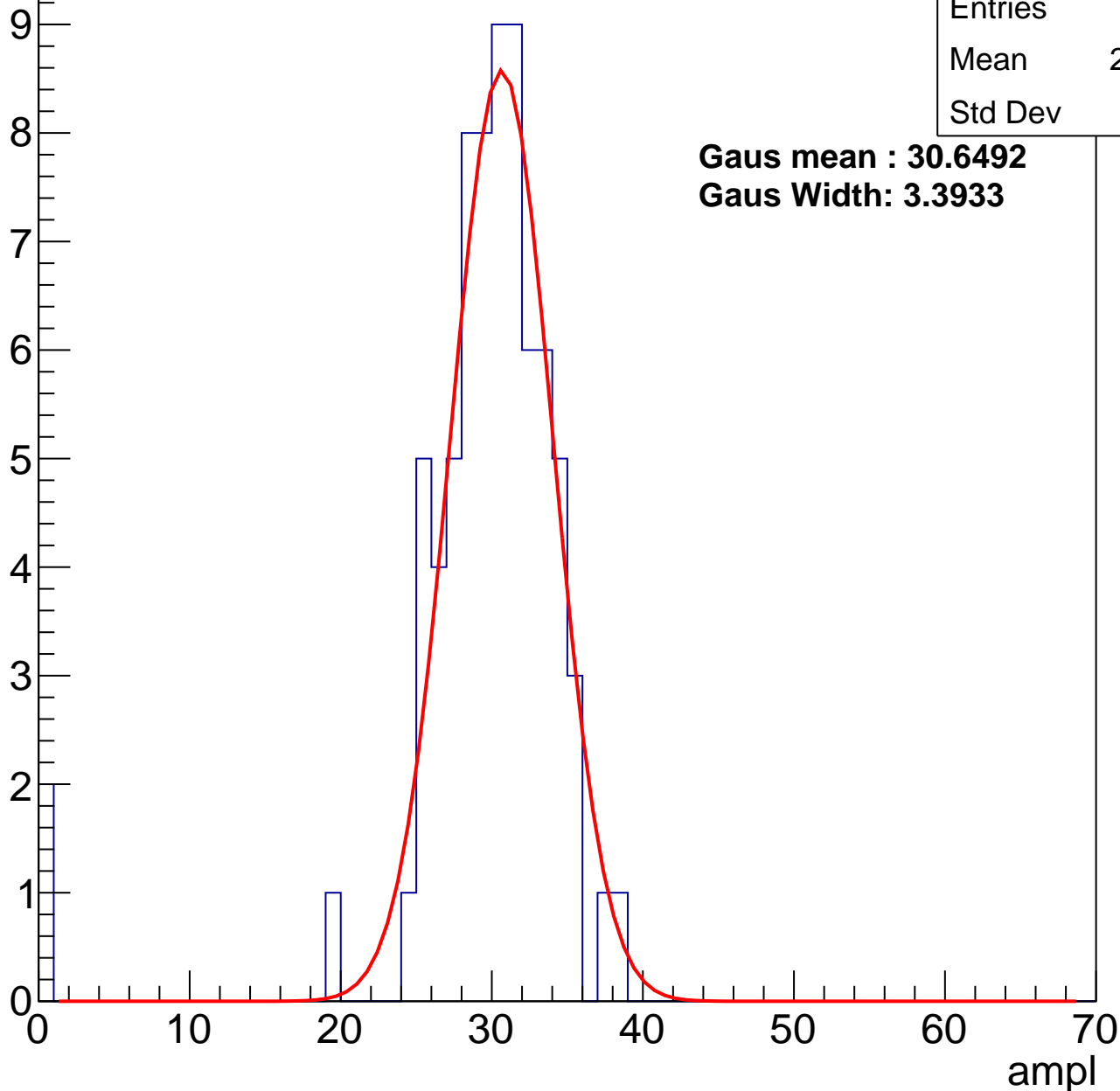
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	29.08
Std Dev	5.84

Gaus mean : 30.6492

Gaus Width: 3.3933



B0L001S, U19-ch19, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	59
Mean	36.54
Std Dev	2.895

Gaus mean : 37.0821

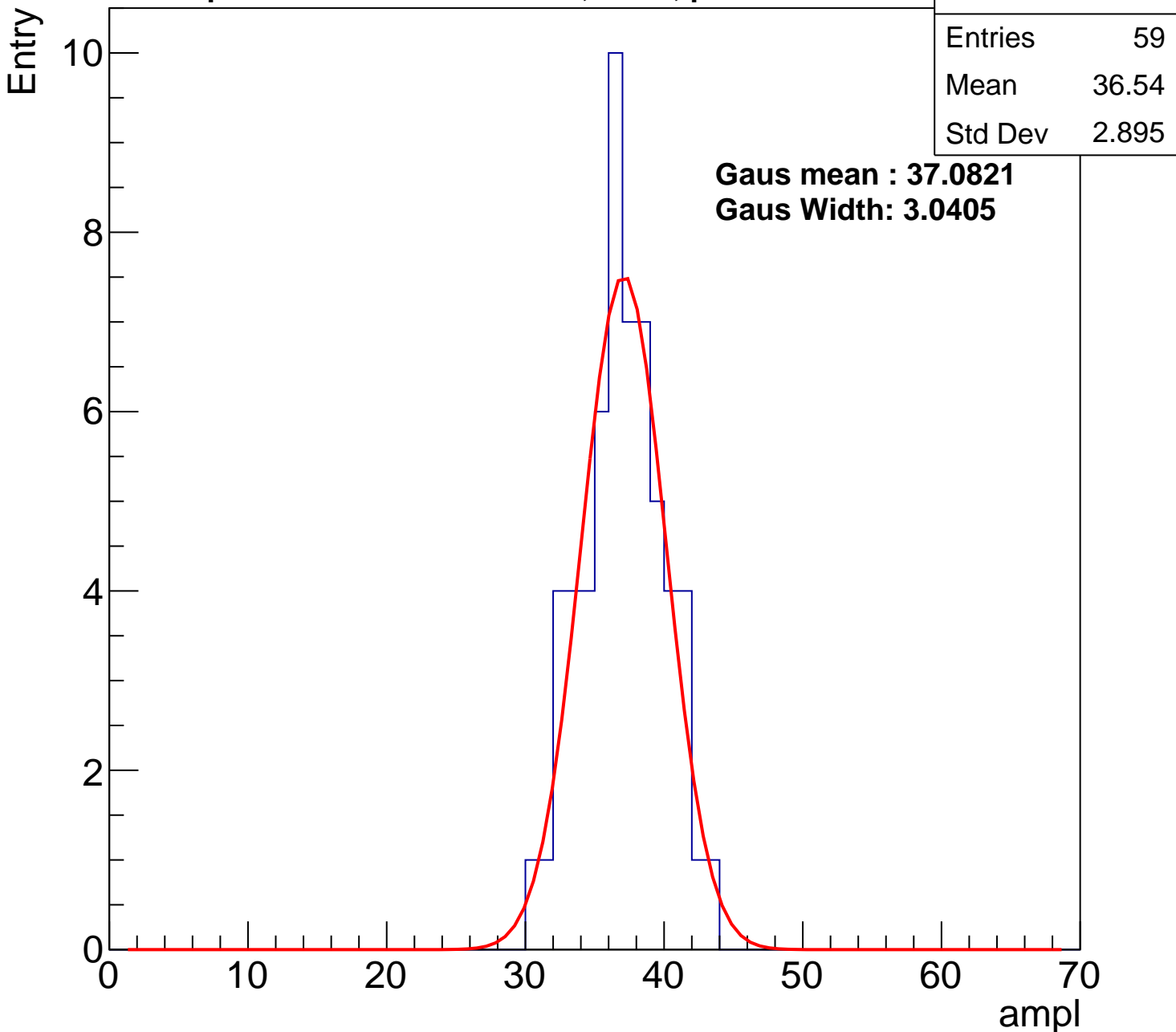
Gaus Width: 3.0405

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch19, adc2

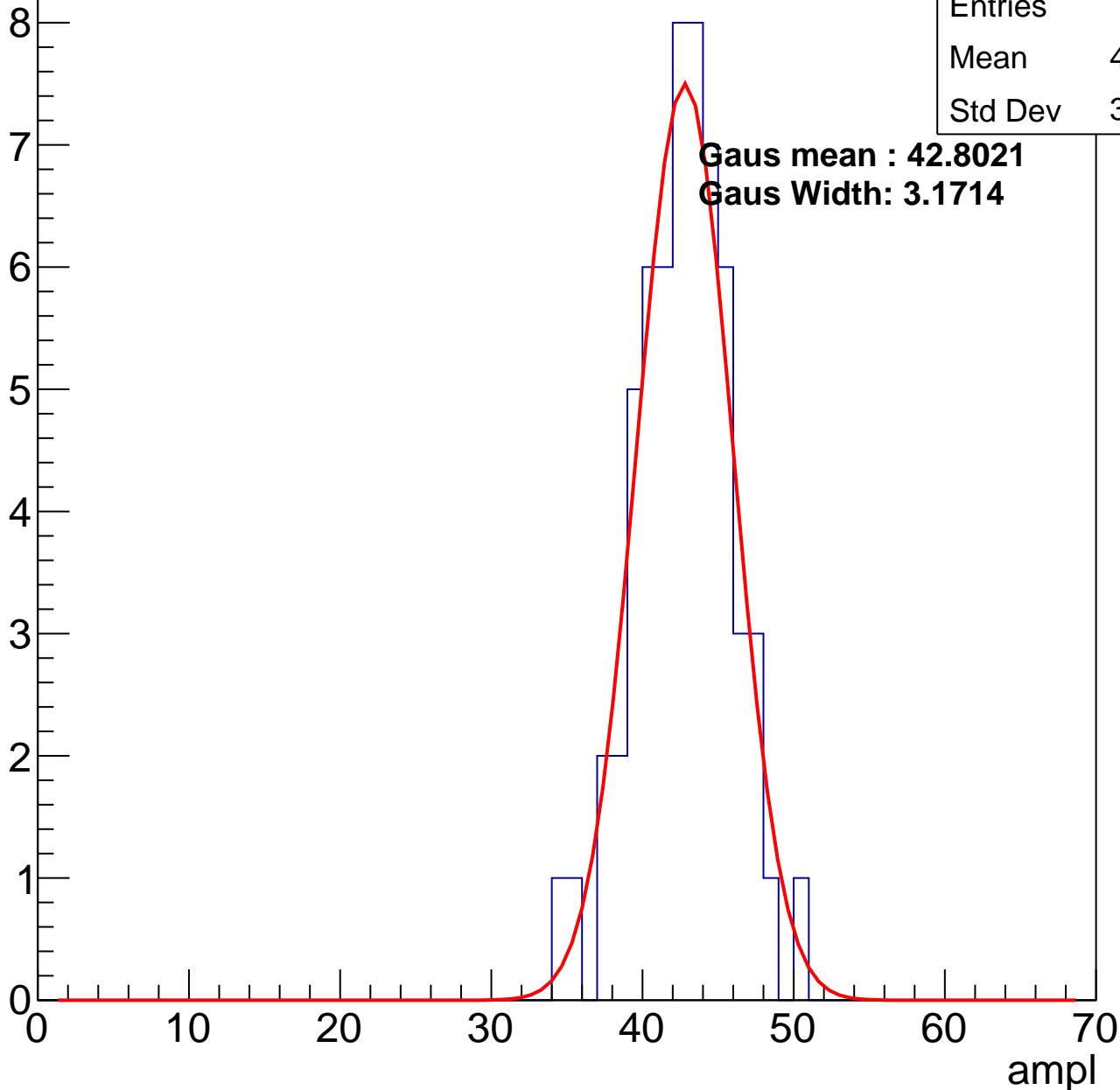
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	42.25
Std Dev	3.107

Gaus mean : 42.8021

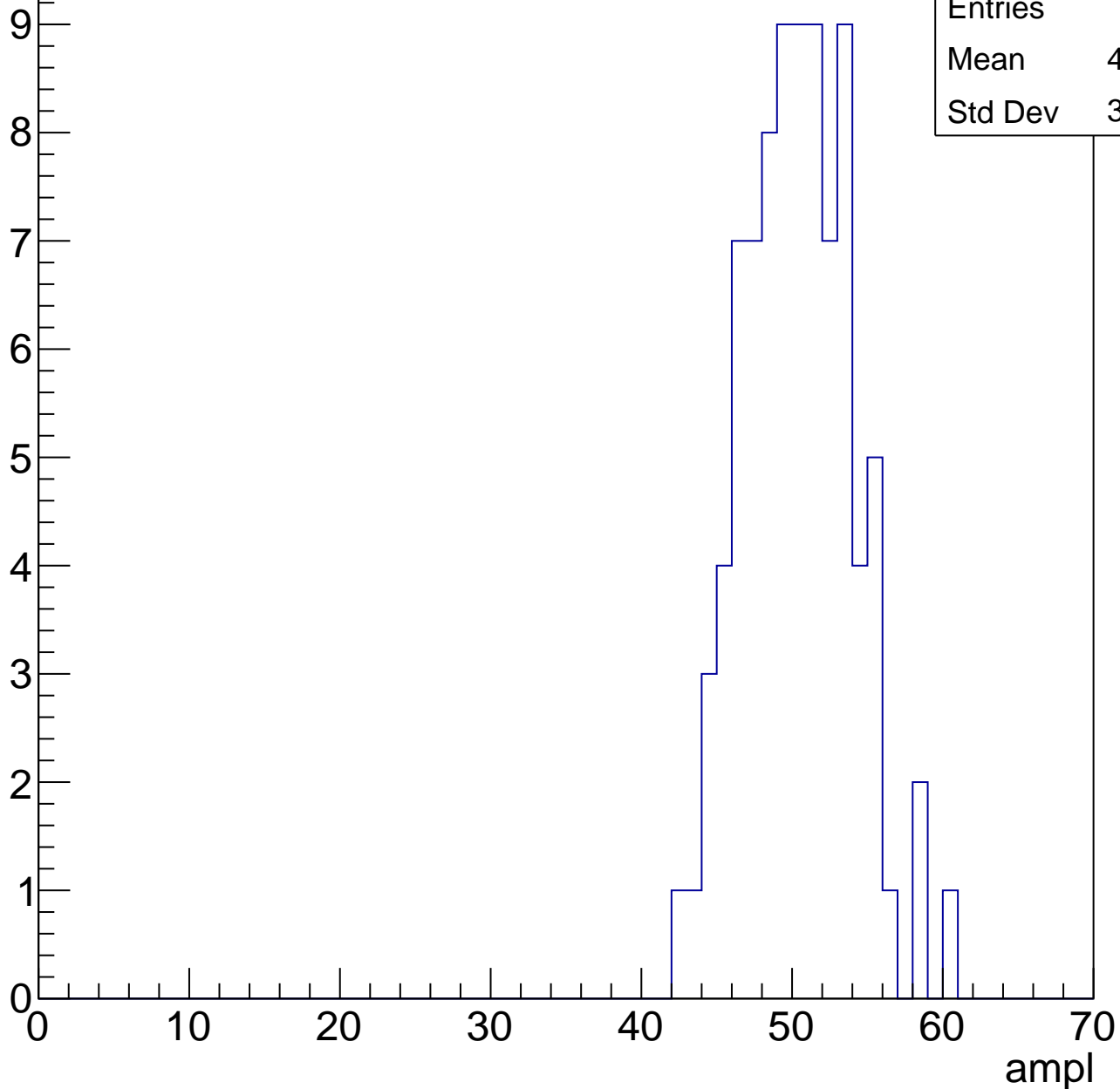
Gaus Width: 3.1714



B0L001S, U19-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

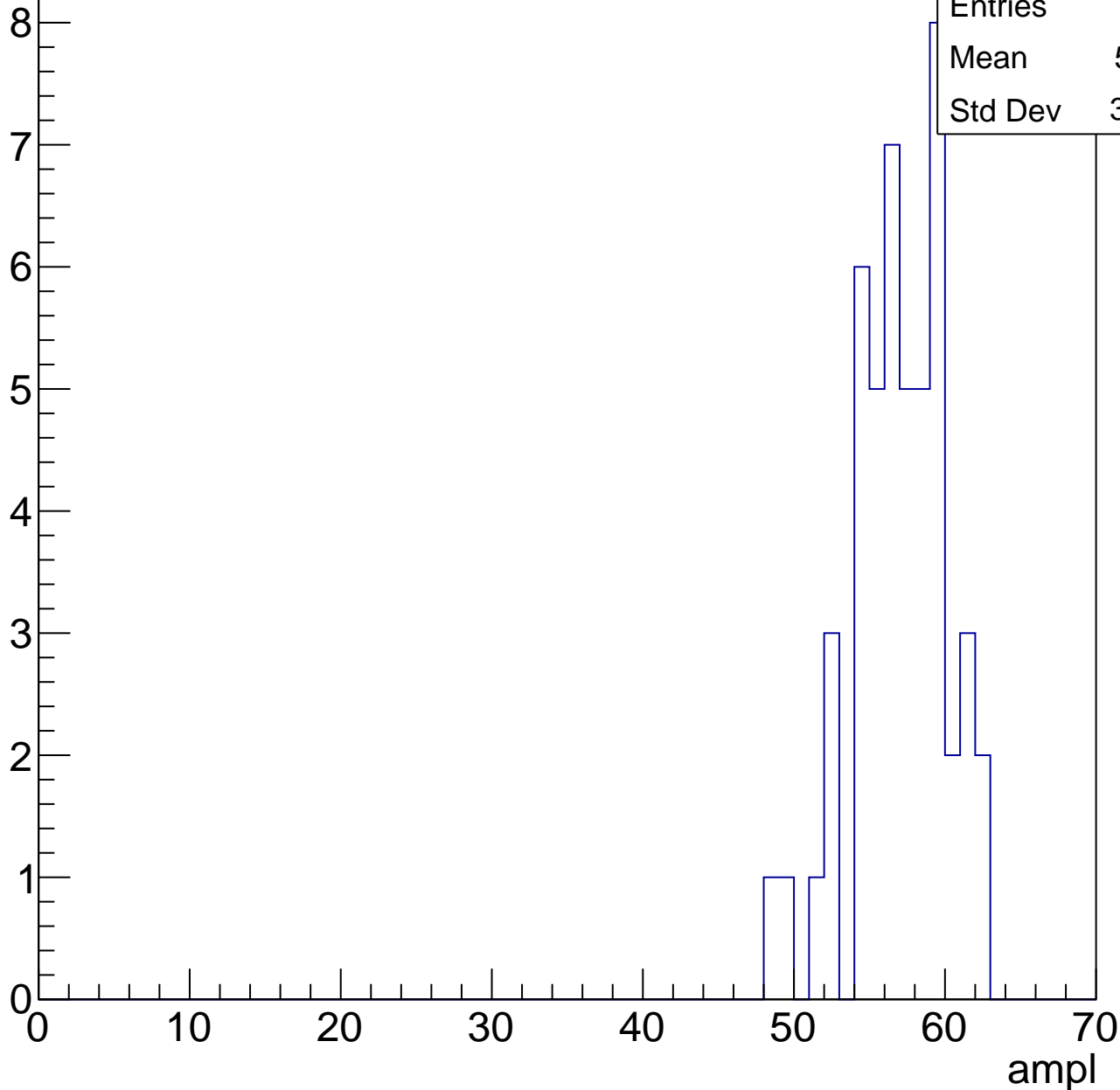


B0L001S, U19-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	56.51
Std Dev	3.124

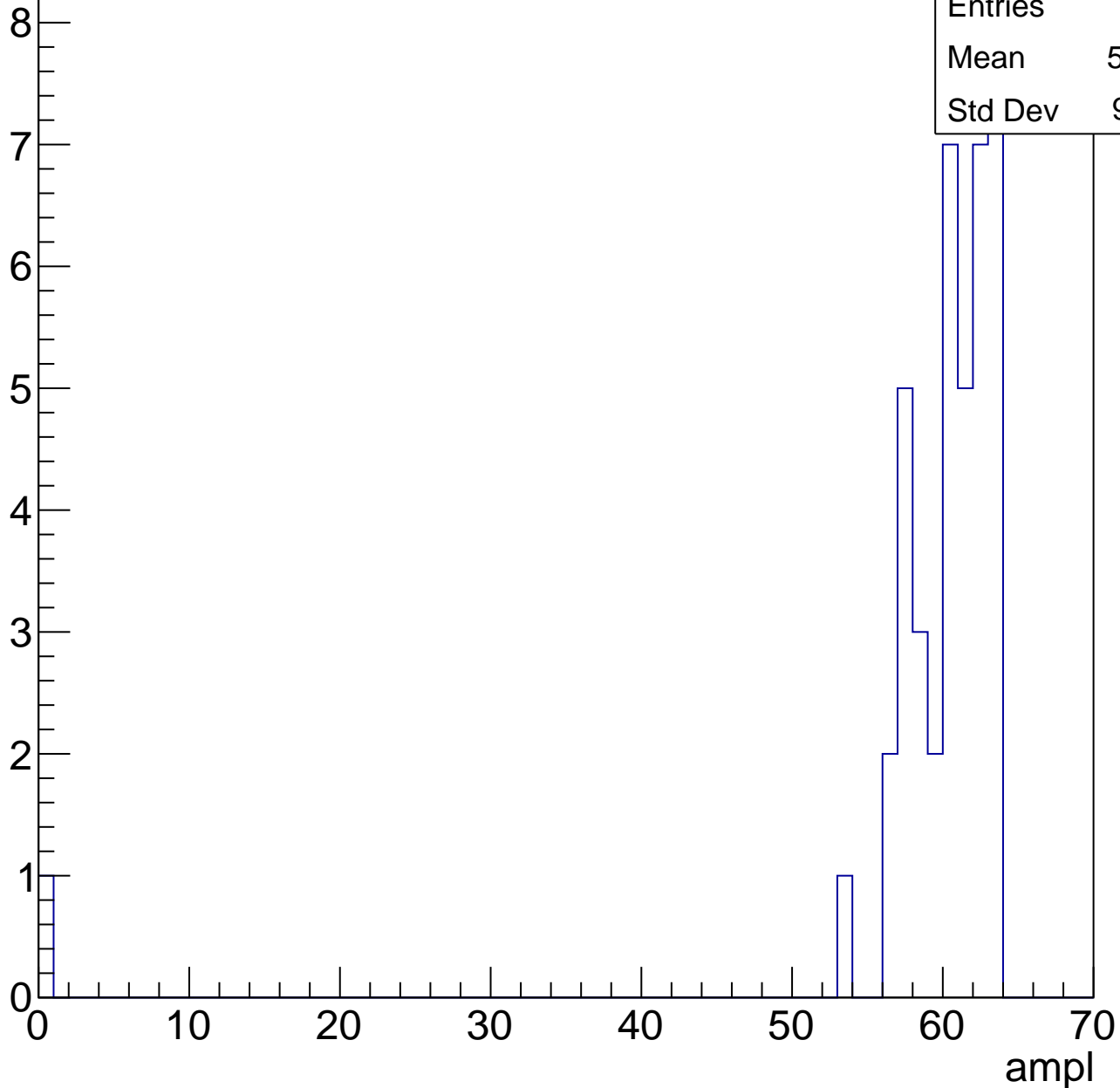


B0L001S, U19-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.66
Std Dev	9.591



B0L001S, U19-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

B0L001S, U19-ch19, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch20, adc0

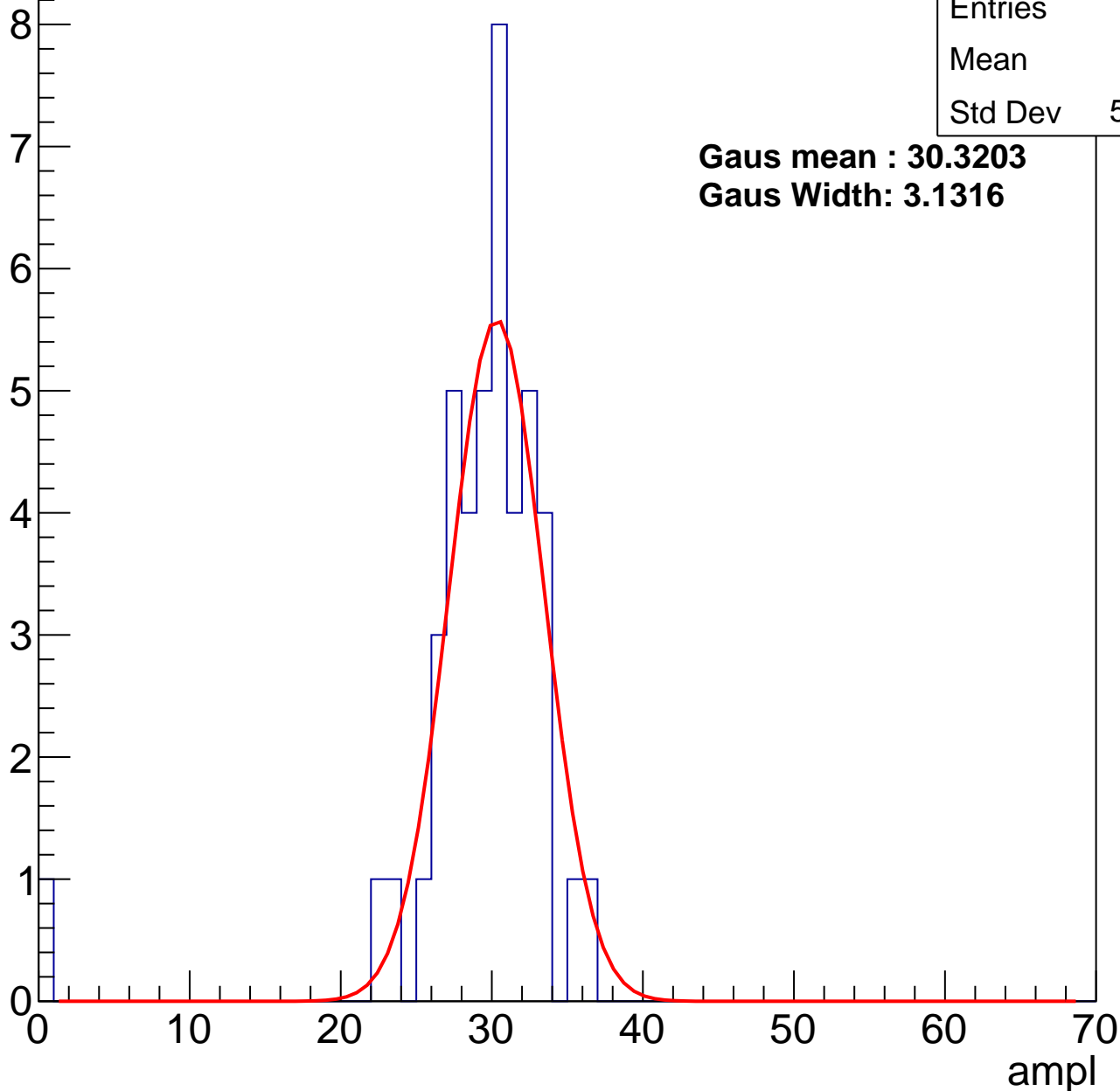
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	28.8
Std Dev	5.242

Gaus mean : 30.3203

Gaus Width: 3.1316



B0L001S, U19-ch20, adc1

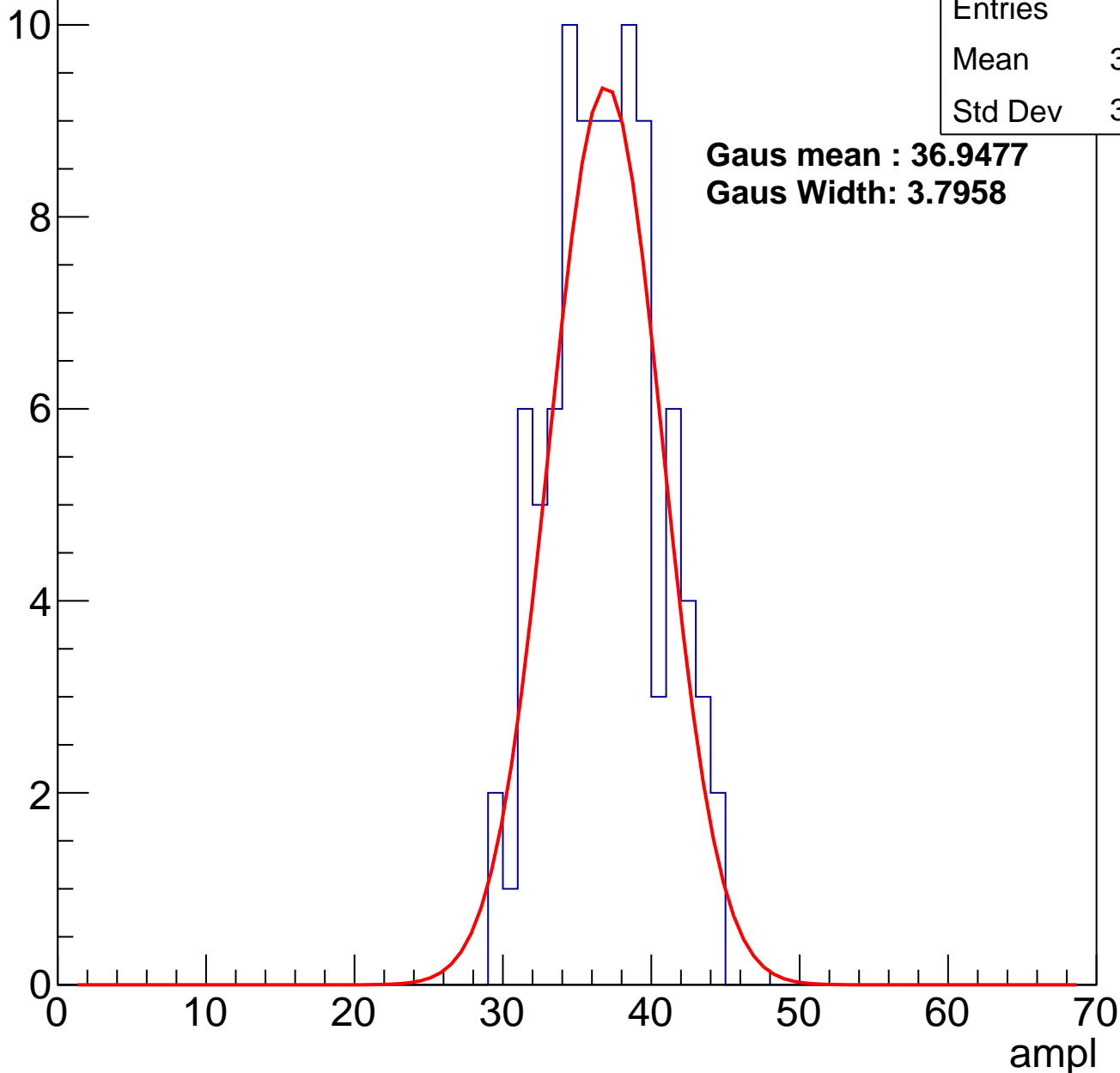
calib_packv5_042523_0143.root, FC#9, port A1

Entries	94
Mean	36.45
Std Dev	3.566

Gaus mean : 36.9477

Gaus Width: 3.7958

Entry



B0L001S, U19-ch20, adc2

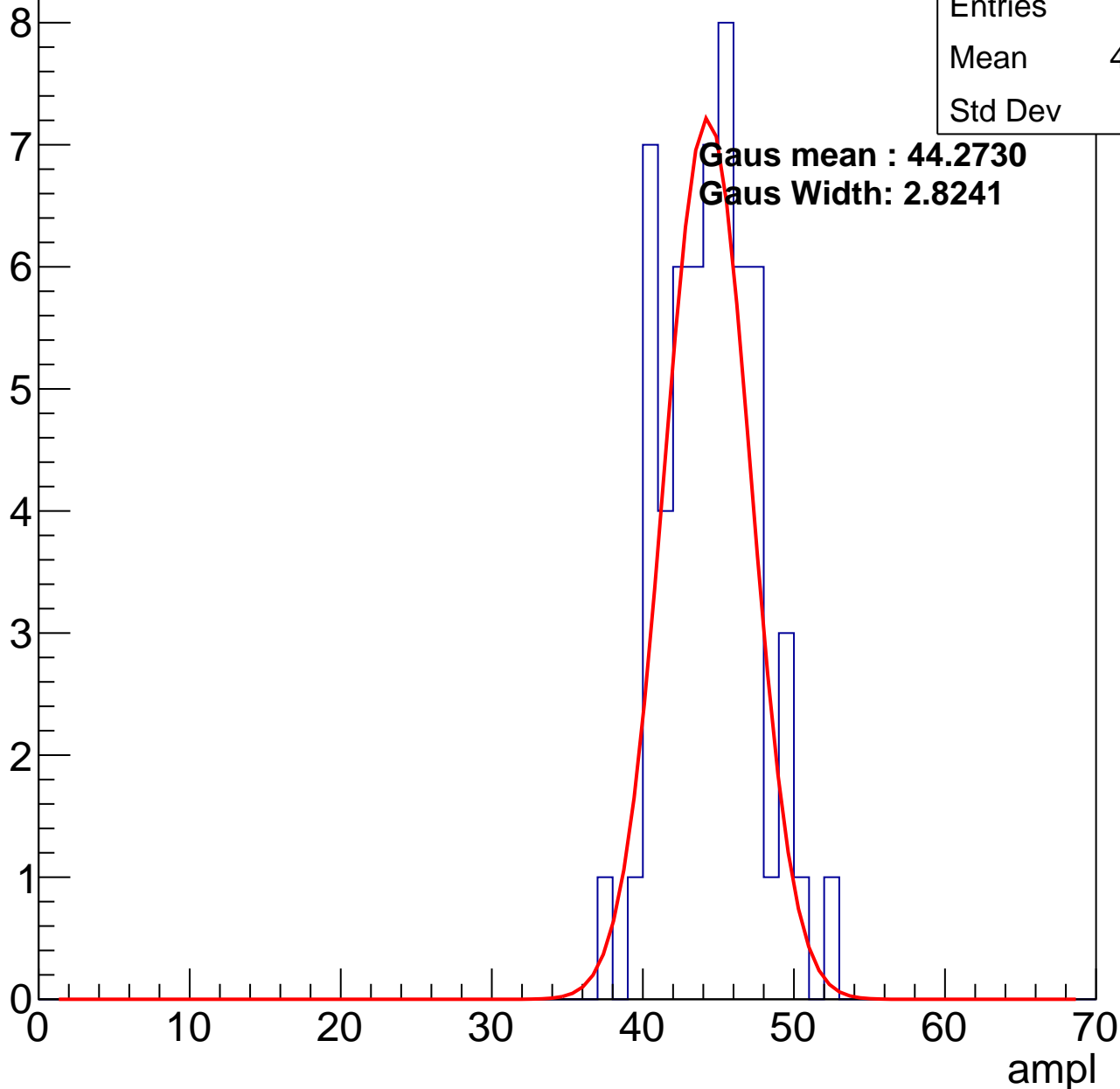
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.02
Std Dev	3.02

Gaus mean : 44.2730

Gaus Width: 2.8241



B0L001S, U19-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

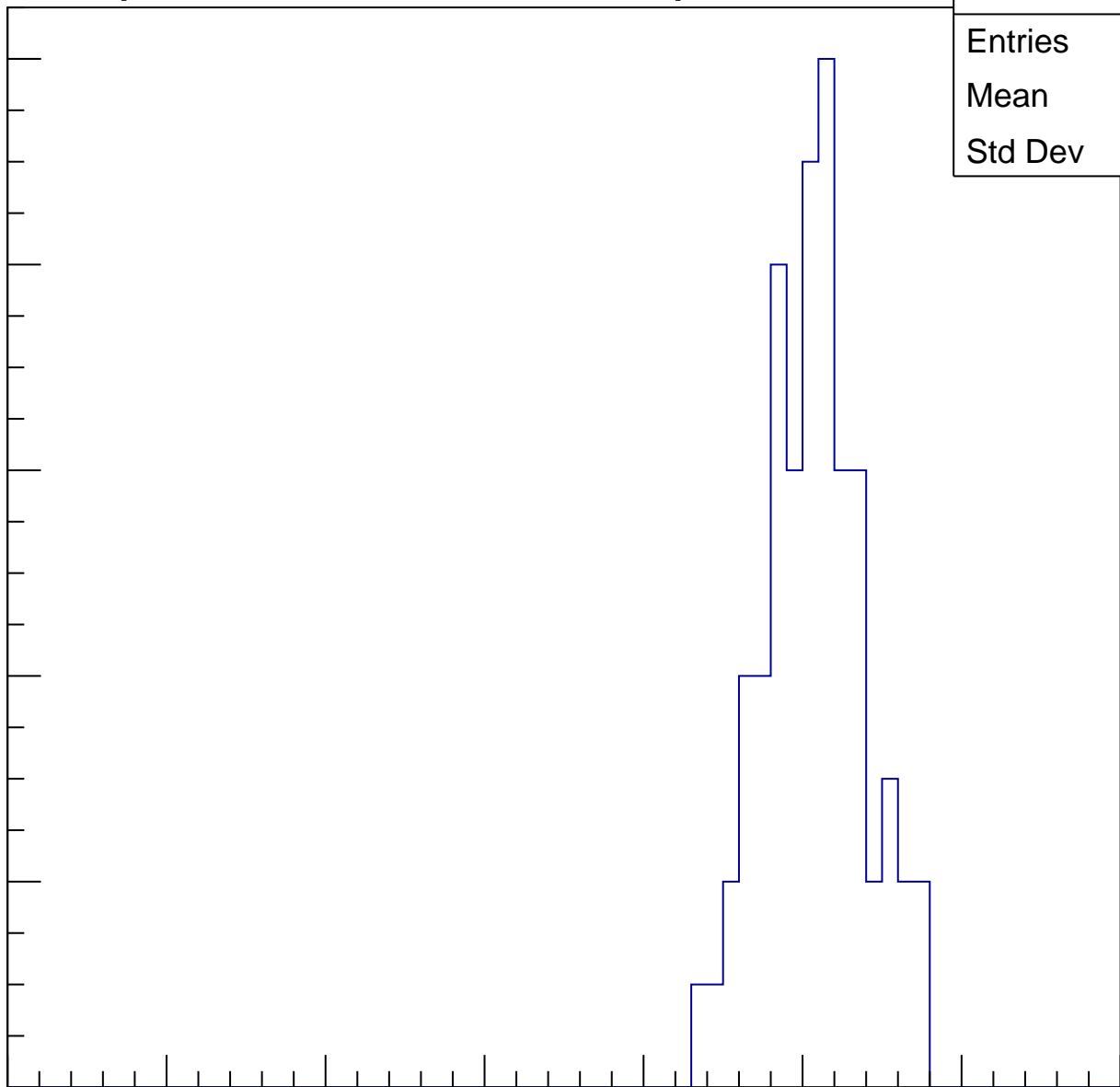
Entries	66
Mean	50.24
Std Dev	3.1

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

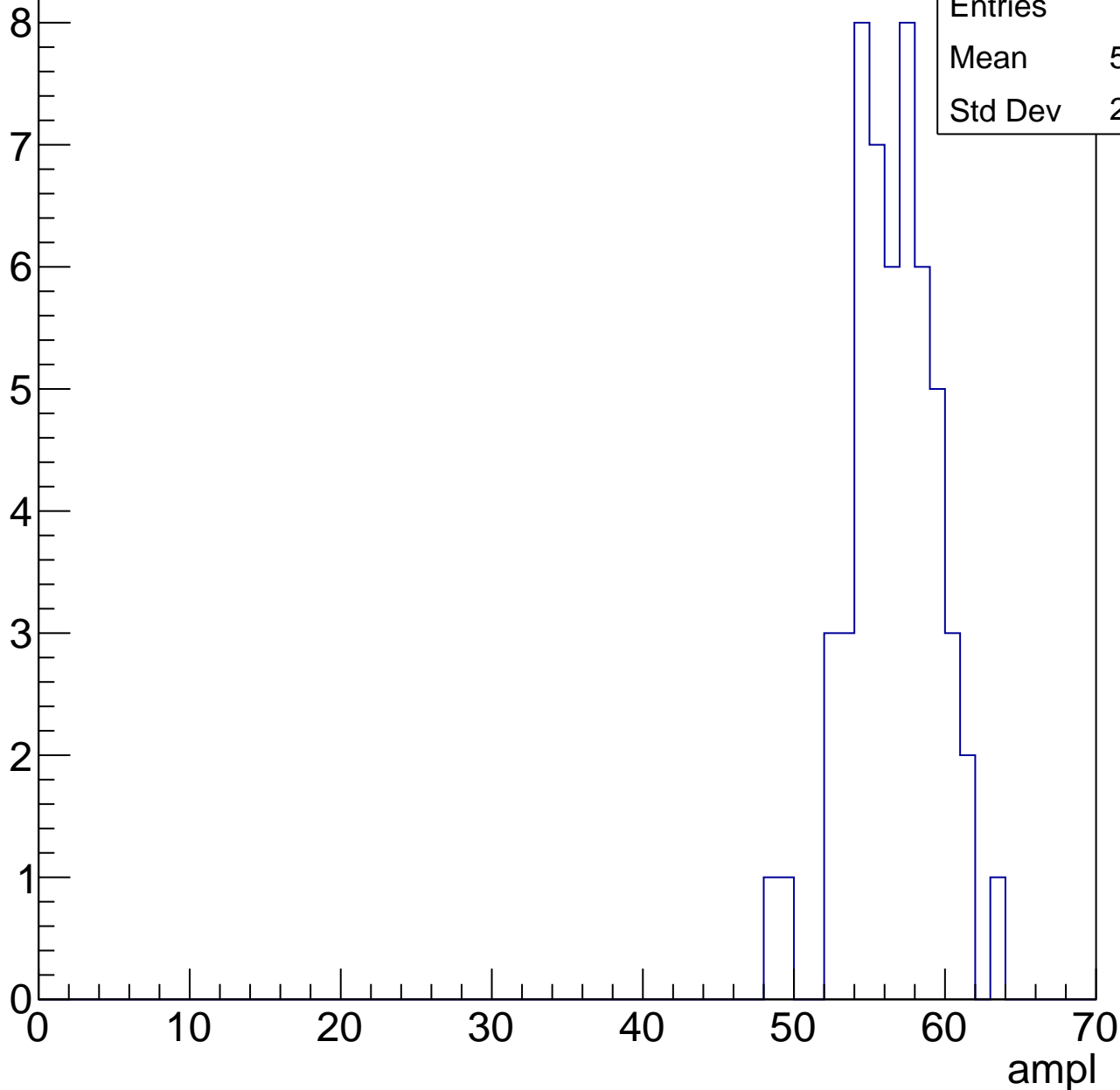


B0L001S, U19-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.09
Std Dev	2.888

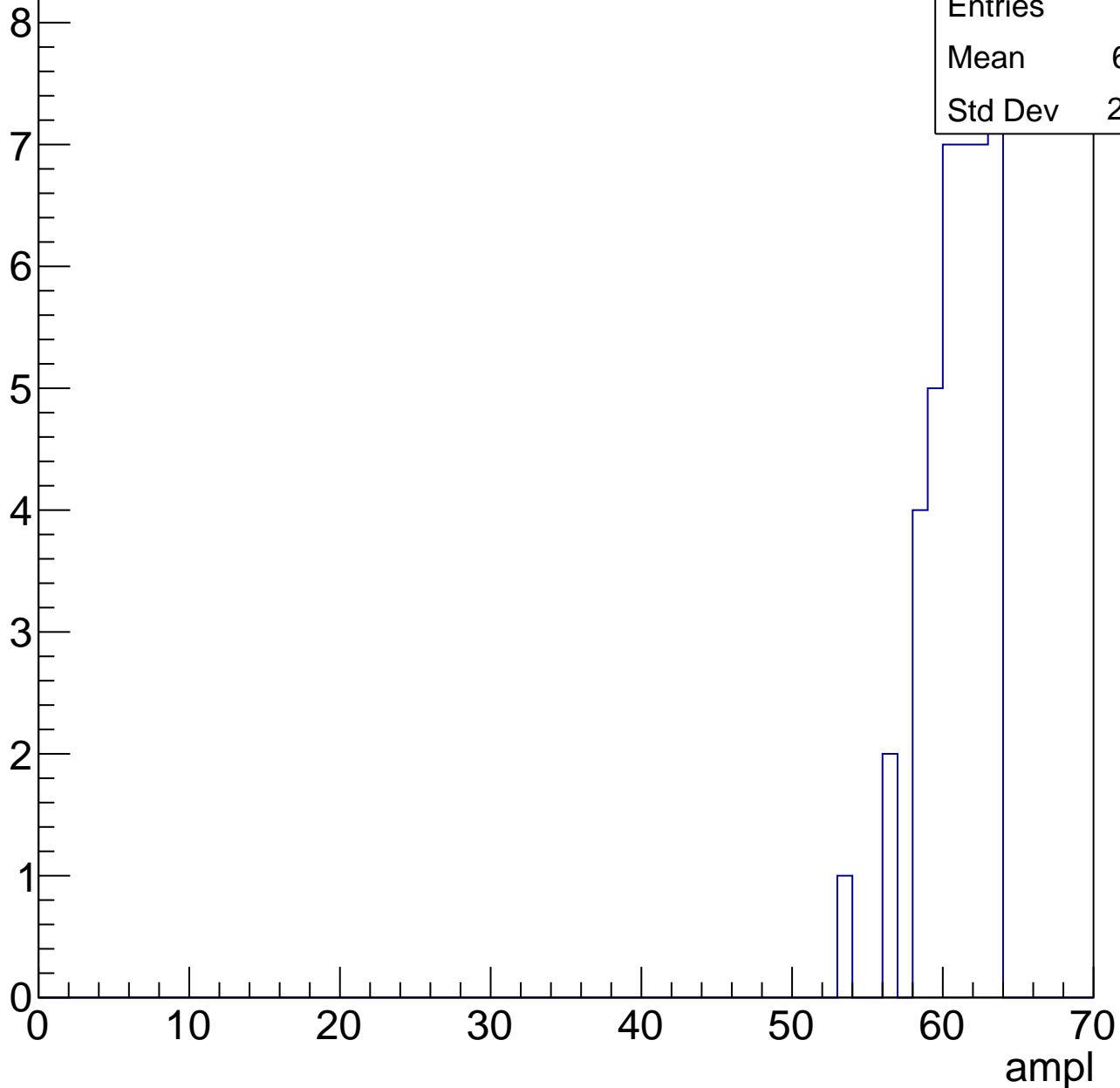


B0L001S, U19-ch20, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

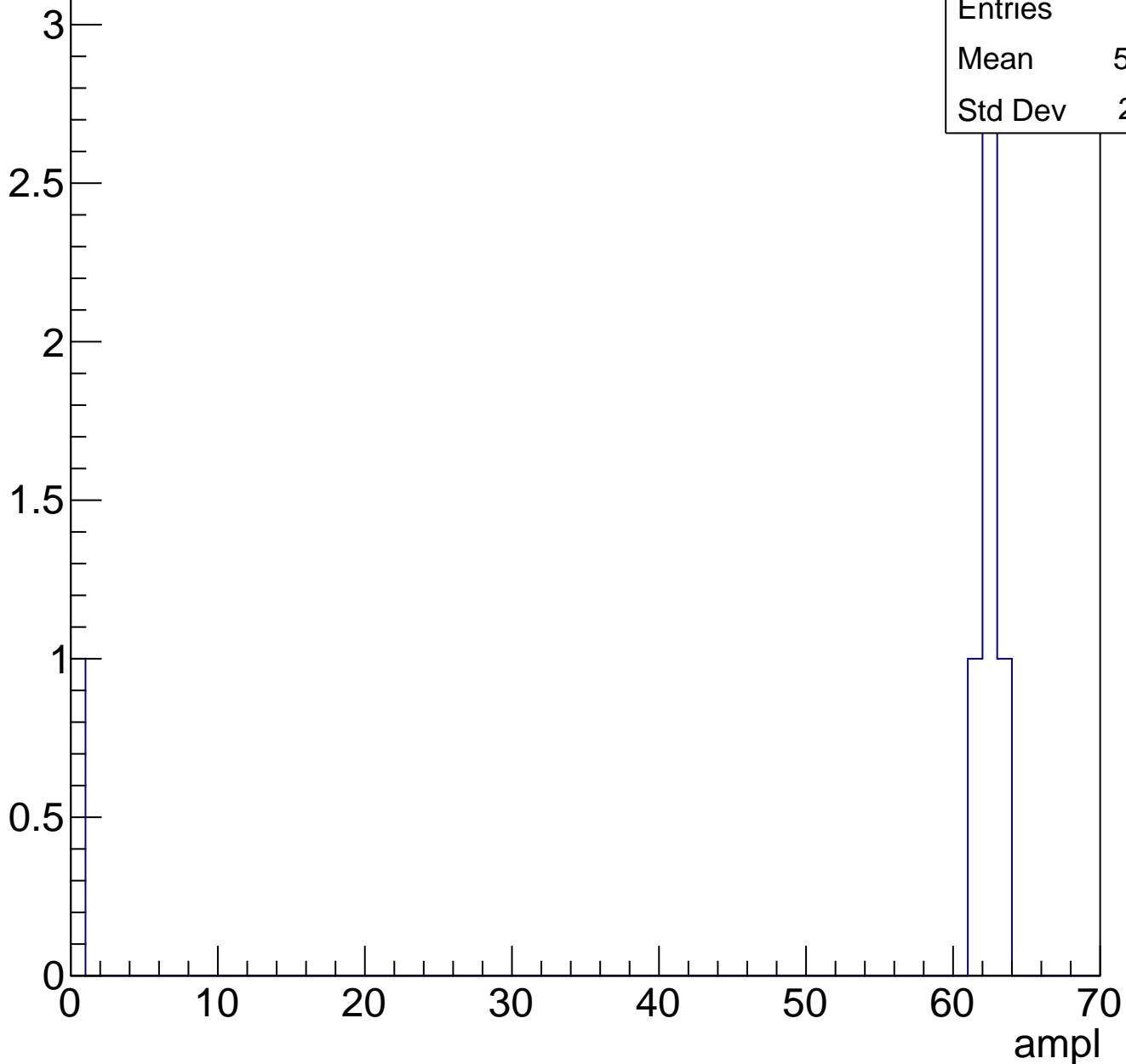
Entries	41
Mean	60.41
Std Dev	2.219



B0L001S, U19-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch21, adc0

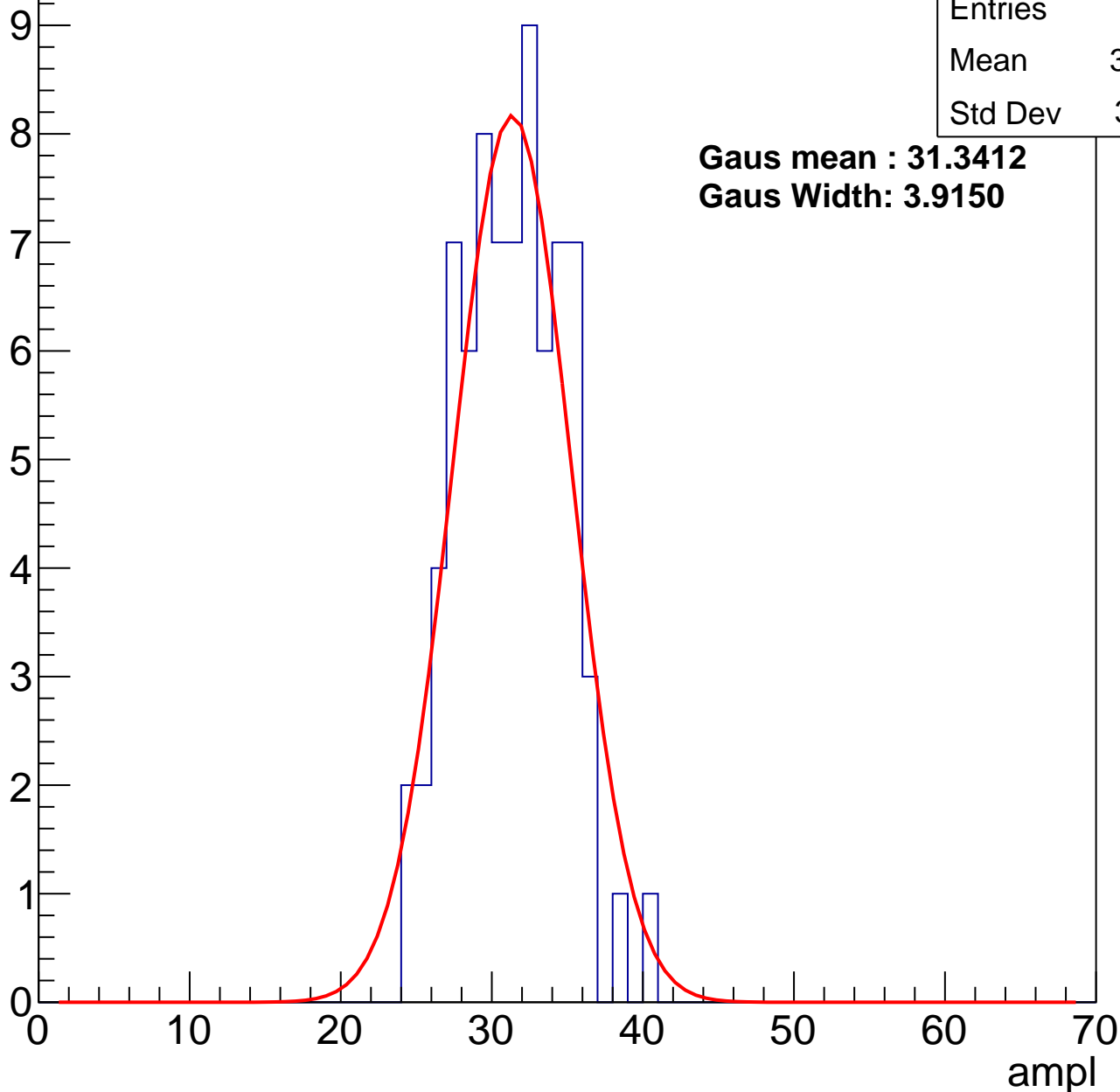
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	30.82
Std Dev	3.391

Gaus mean : 31.3412

Gaus Width: 3.9150



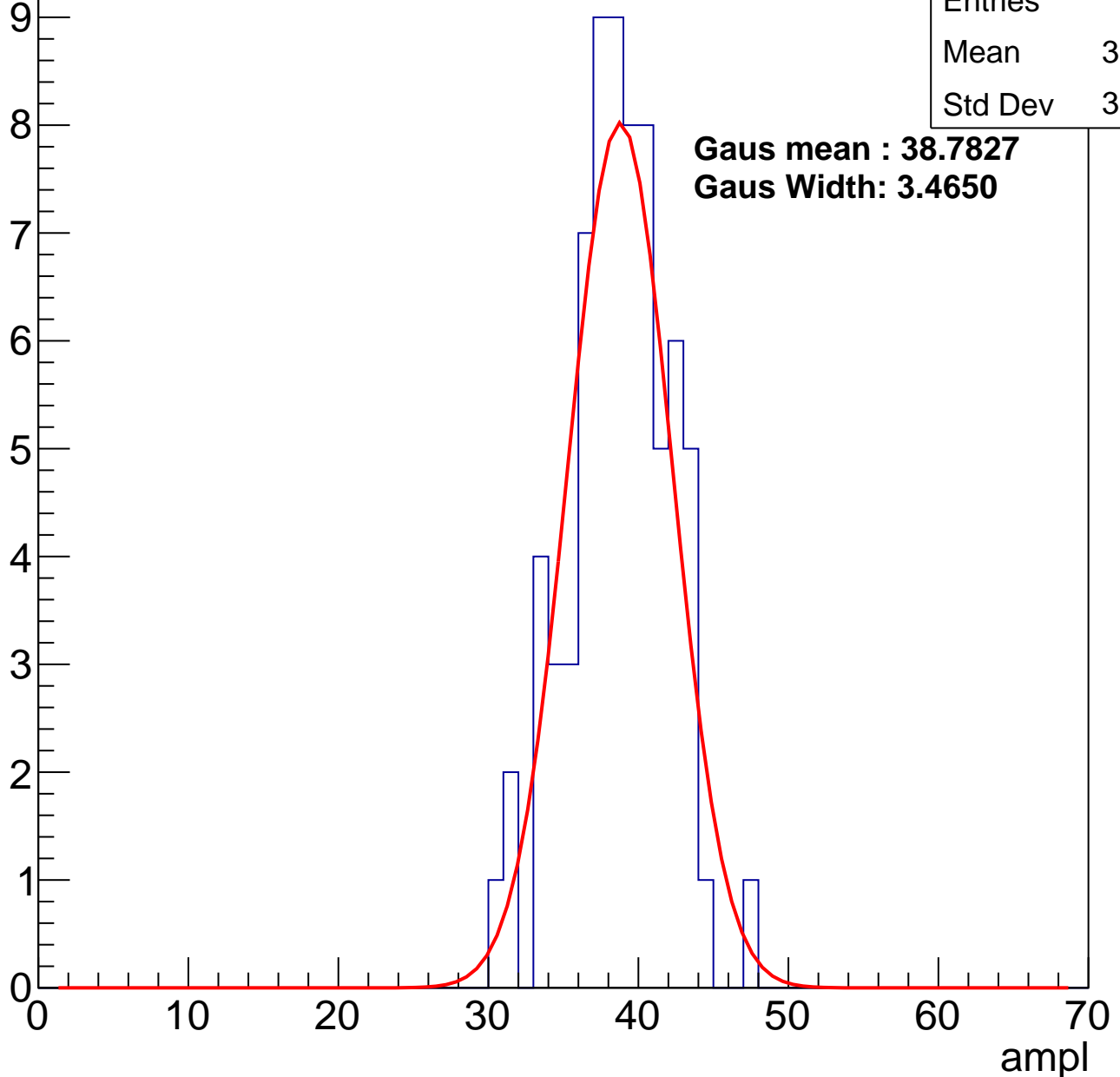
B0L001S, U19-ch21, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.24
Std Dev	3.319

Gaus mean : 38.7827
Gaus Width: 3.4650



B0L001S, U19-ch21, adc2

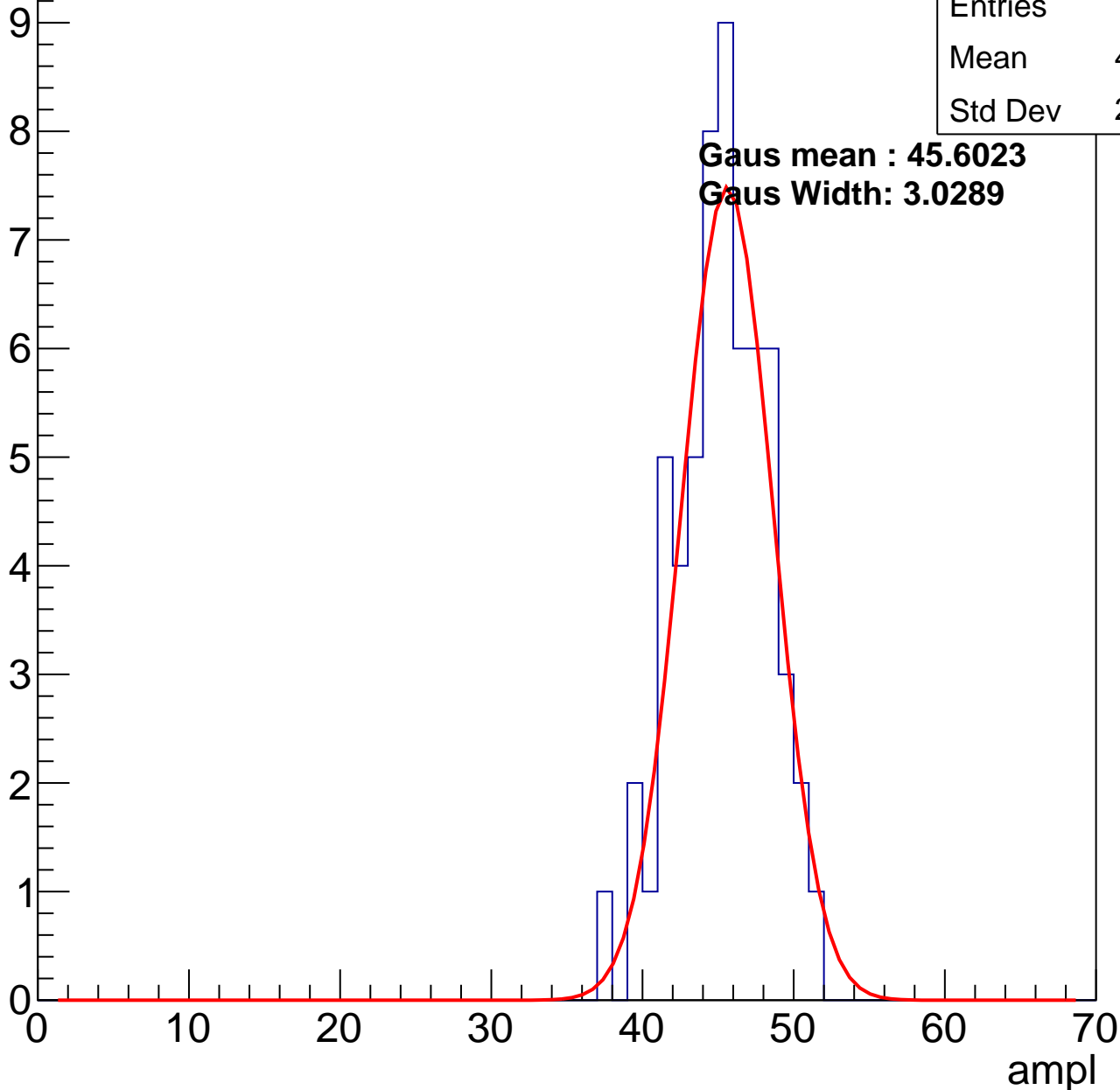
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.81
Std Dev	2.971

Gaus mean : 45.6023

Gaus Width: 3.0289

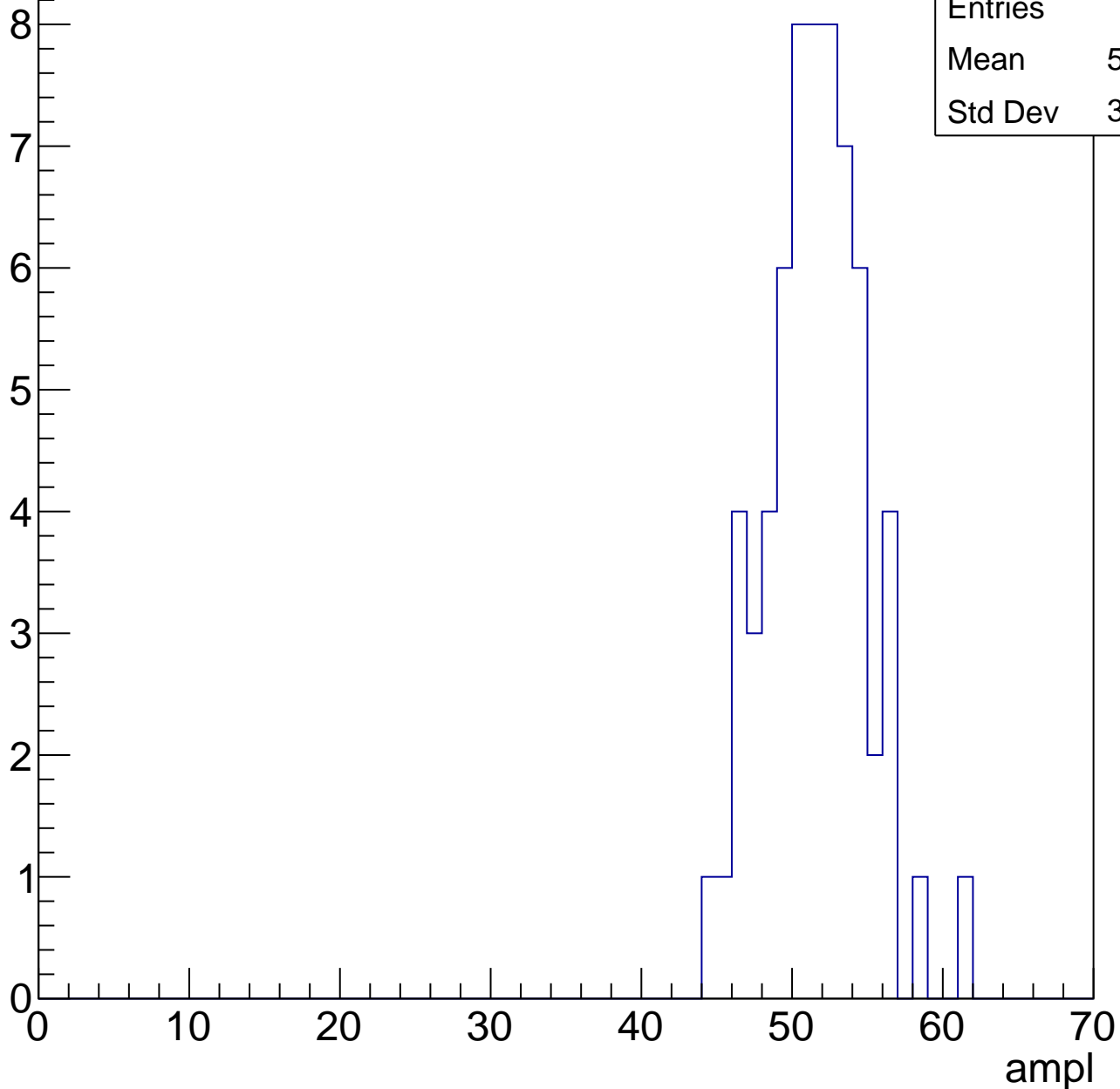


B0L001S, U19-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

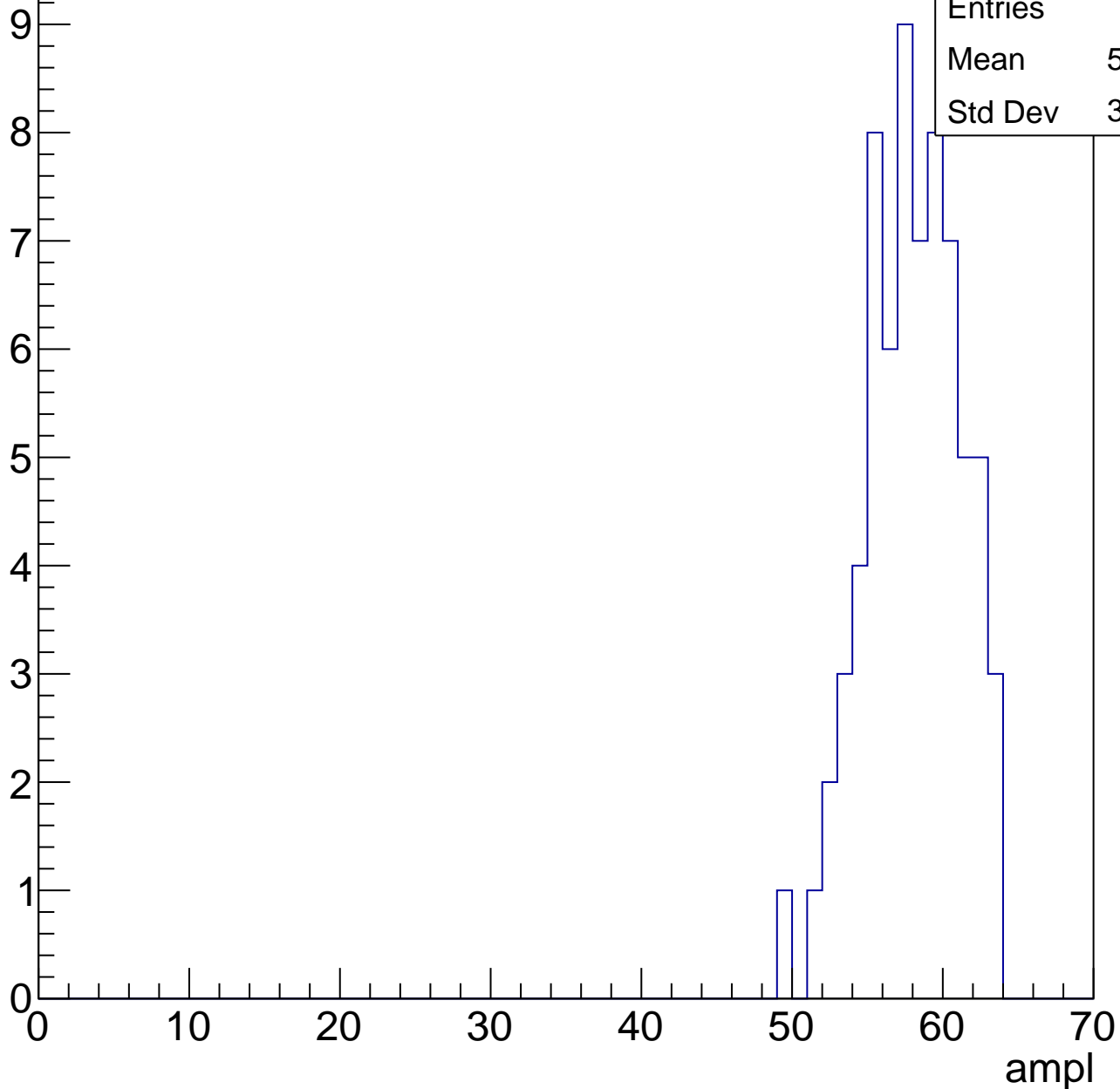
Entries	64
Mean	51.12
Std Dev	3.238



B0L001S, U19-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



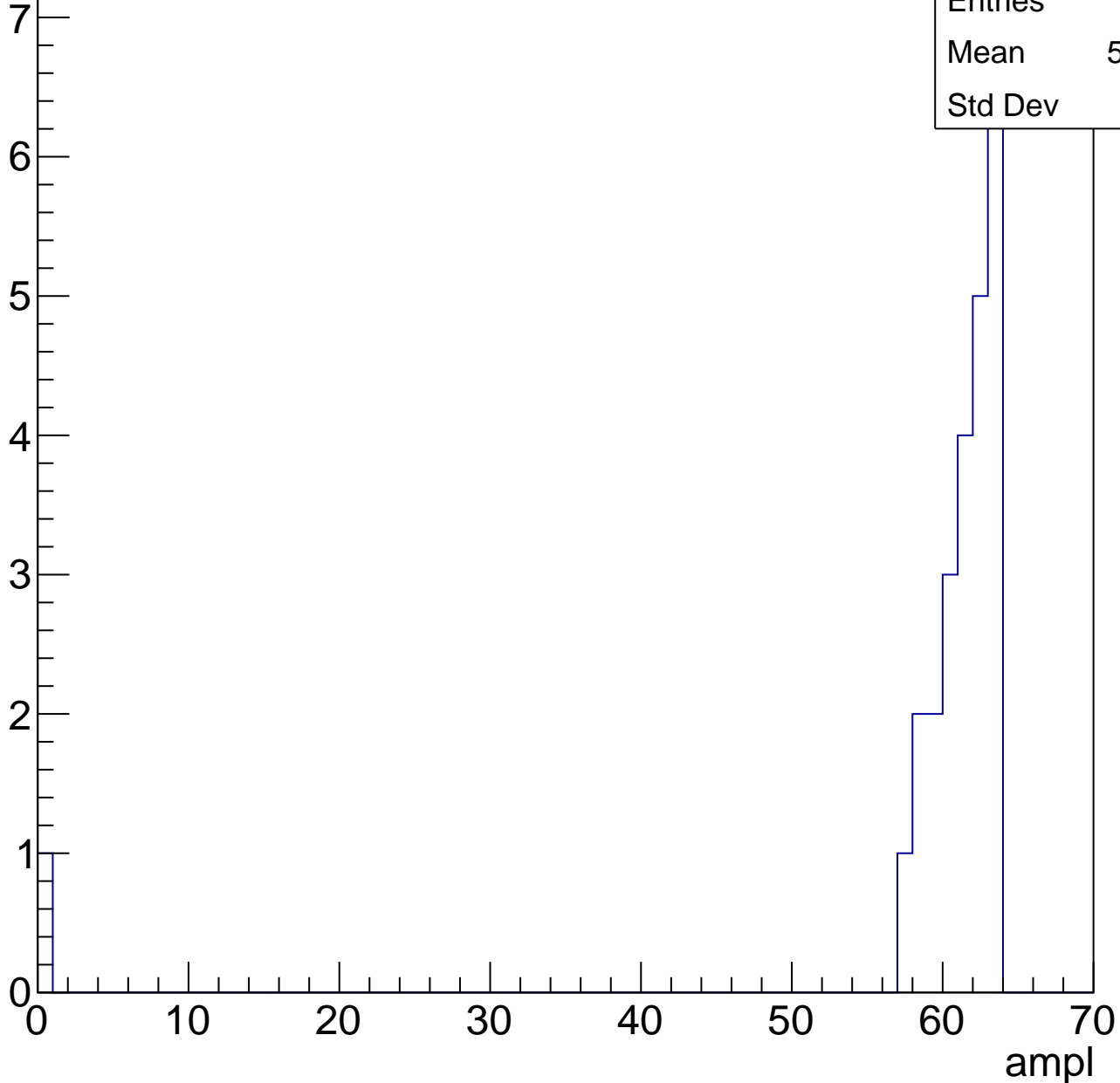
Entries	69
Mean	57.54
Std Dev	3.109

B0L001S, U19-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.64
Std Dev	12.1



B0L001S, U19-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch22, adc0

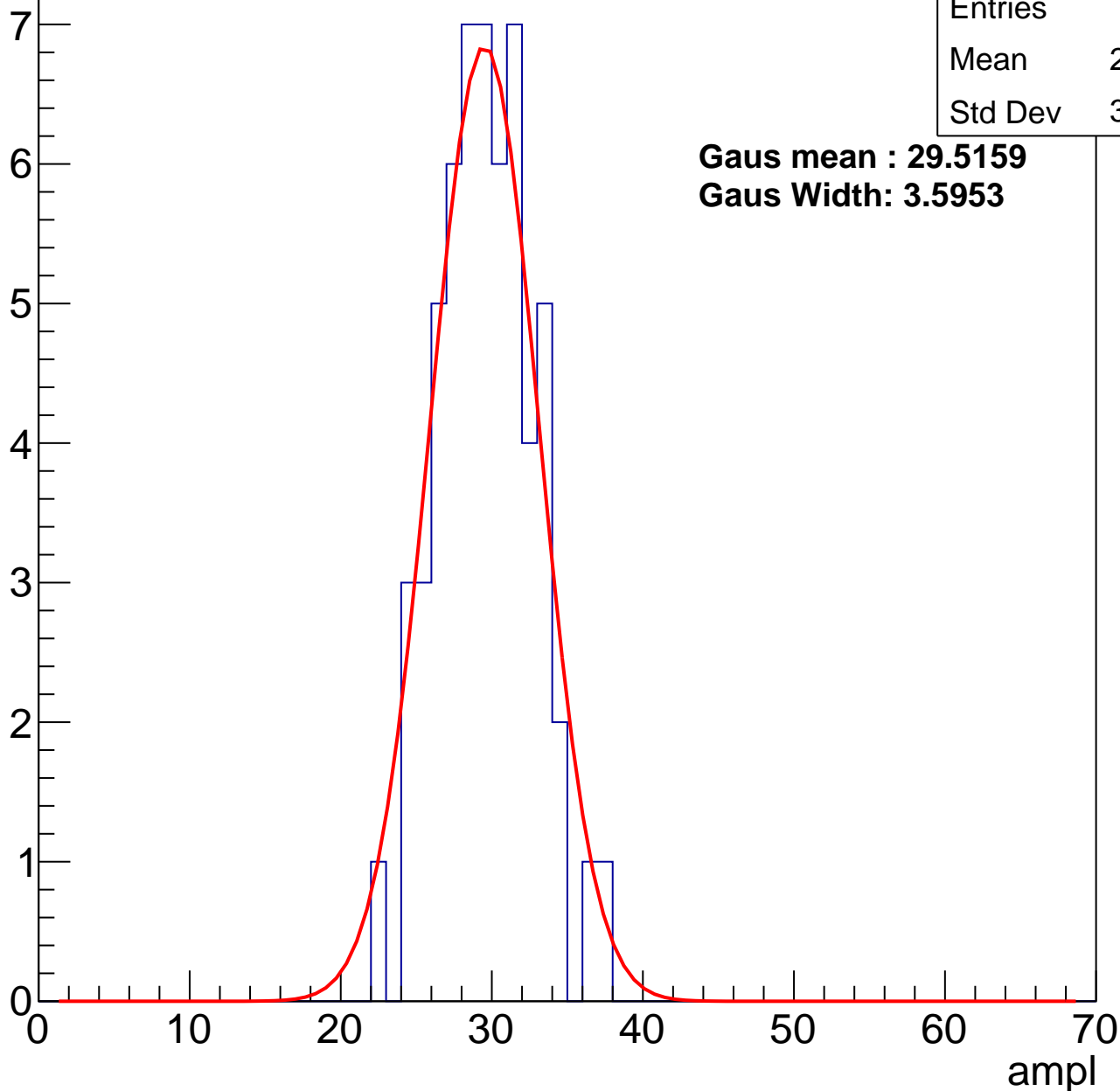
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	29.16
Std Dev	3.106

Gaus mean : 29.5159

Gaus Width: 3.5953



B0L001S, U19-ch22, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	35.81
Std Dev	3.463

Gaus mean : 36.3371

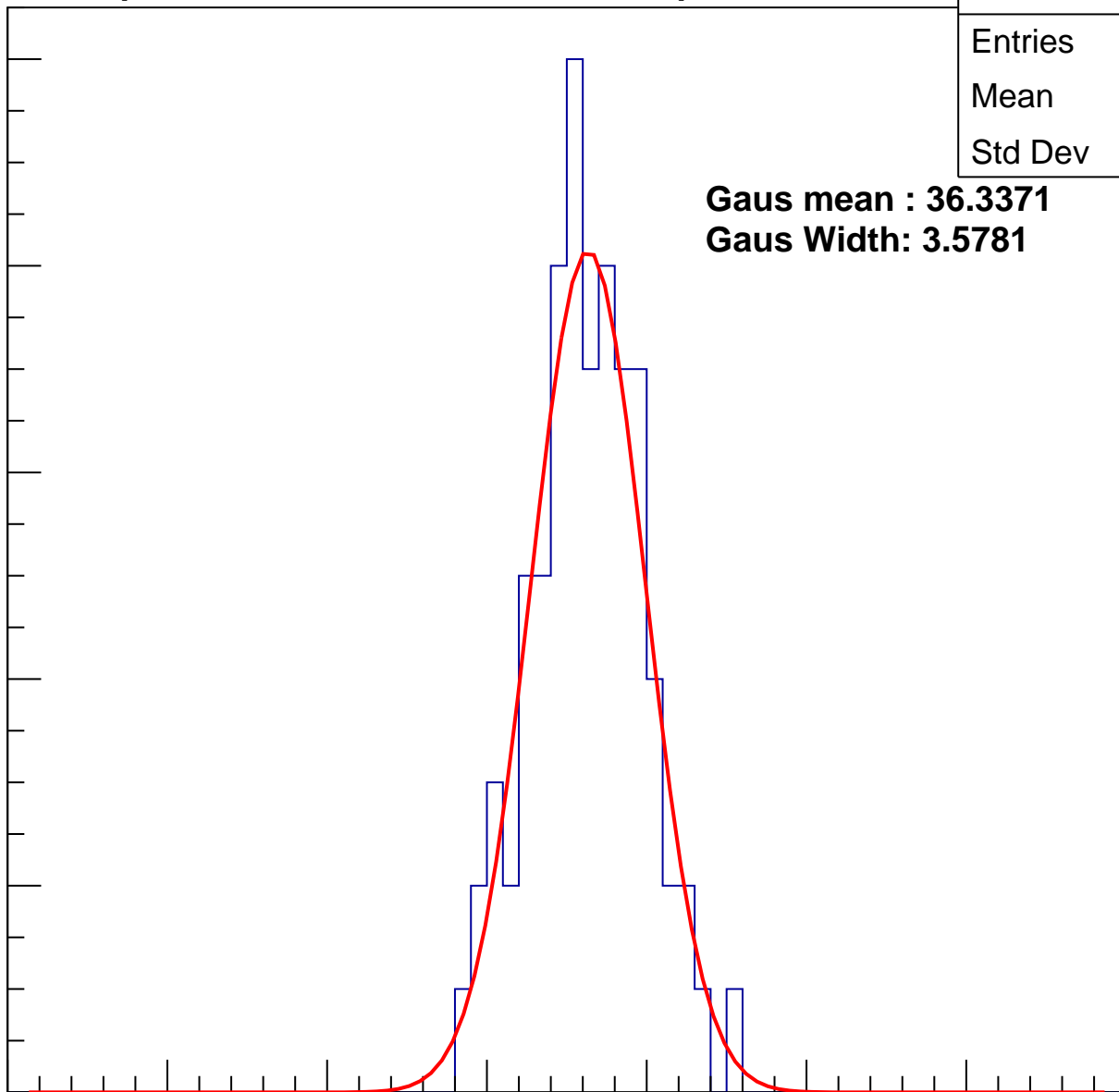
Gaus Width: 3.5781

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch22, adc2

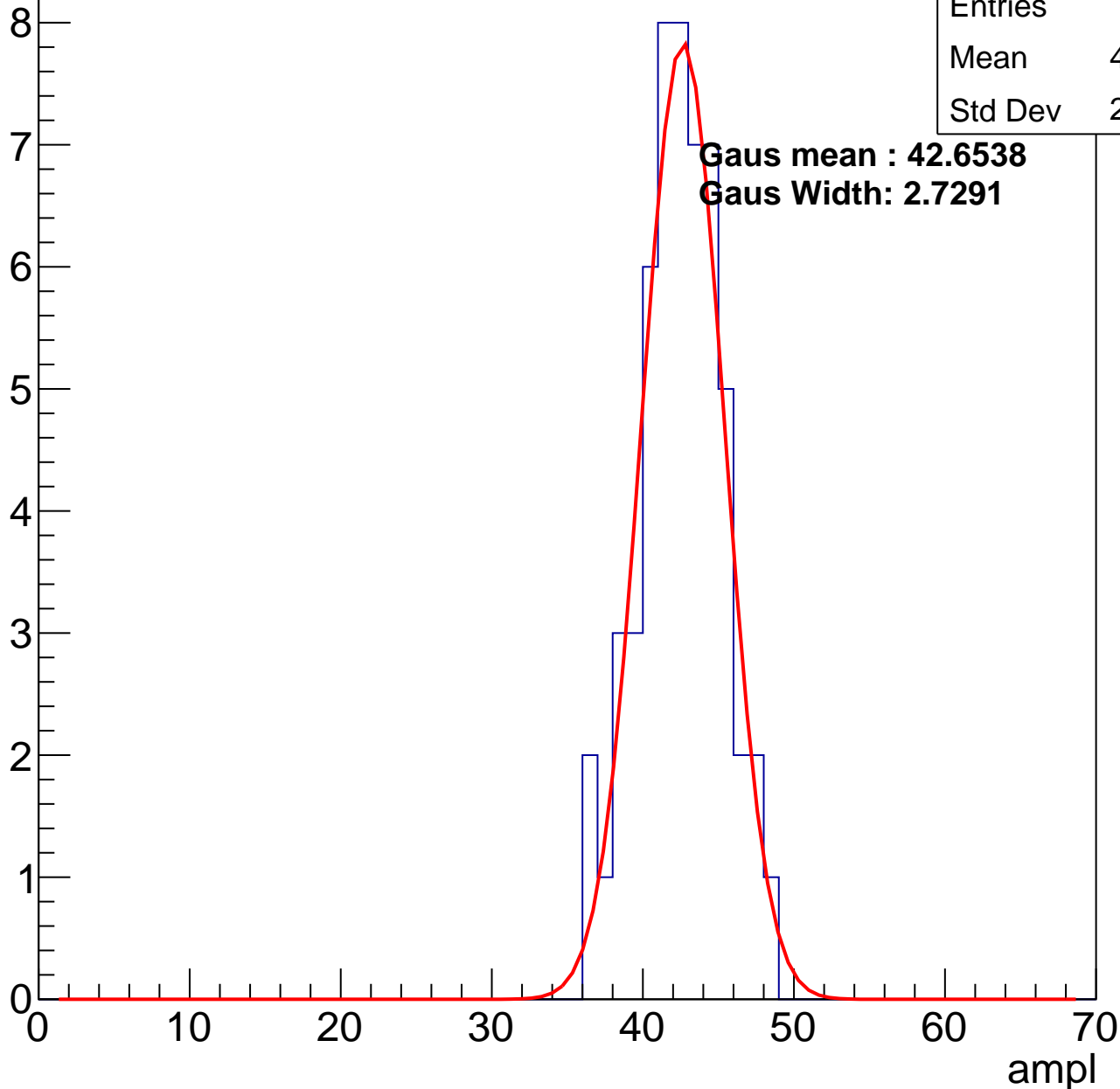
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	42.04
Std Dev	2.703

Gaus mean : 42.6538

Gaus Width: 2.7291

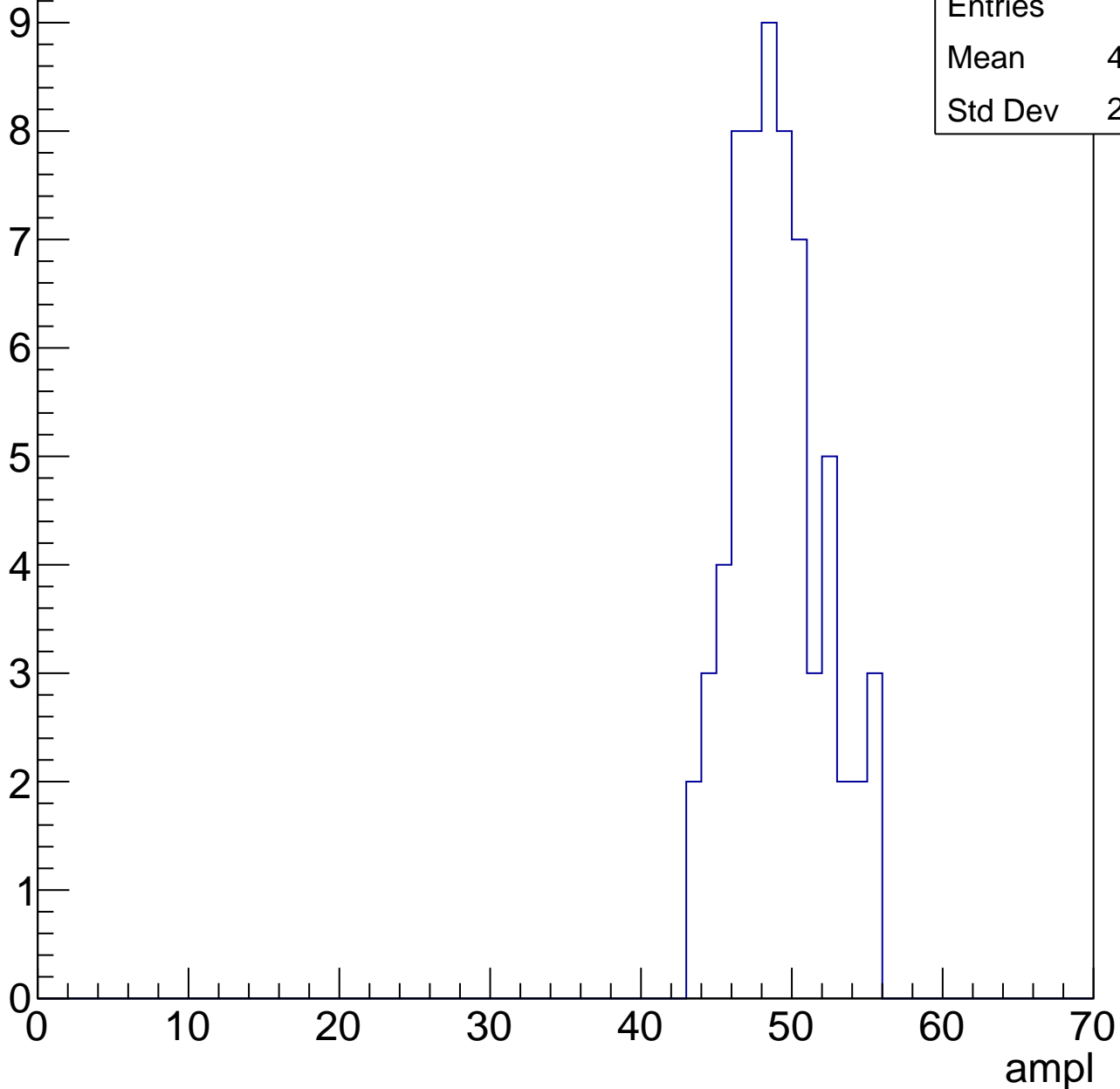


B0L001S, U19-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

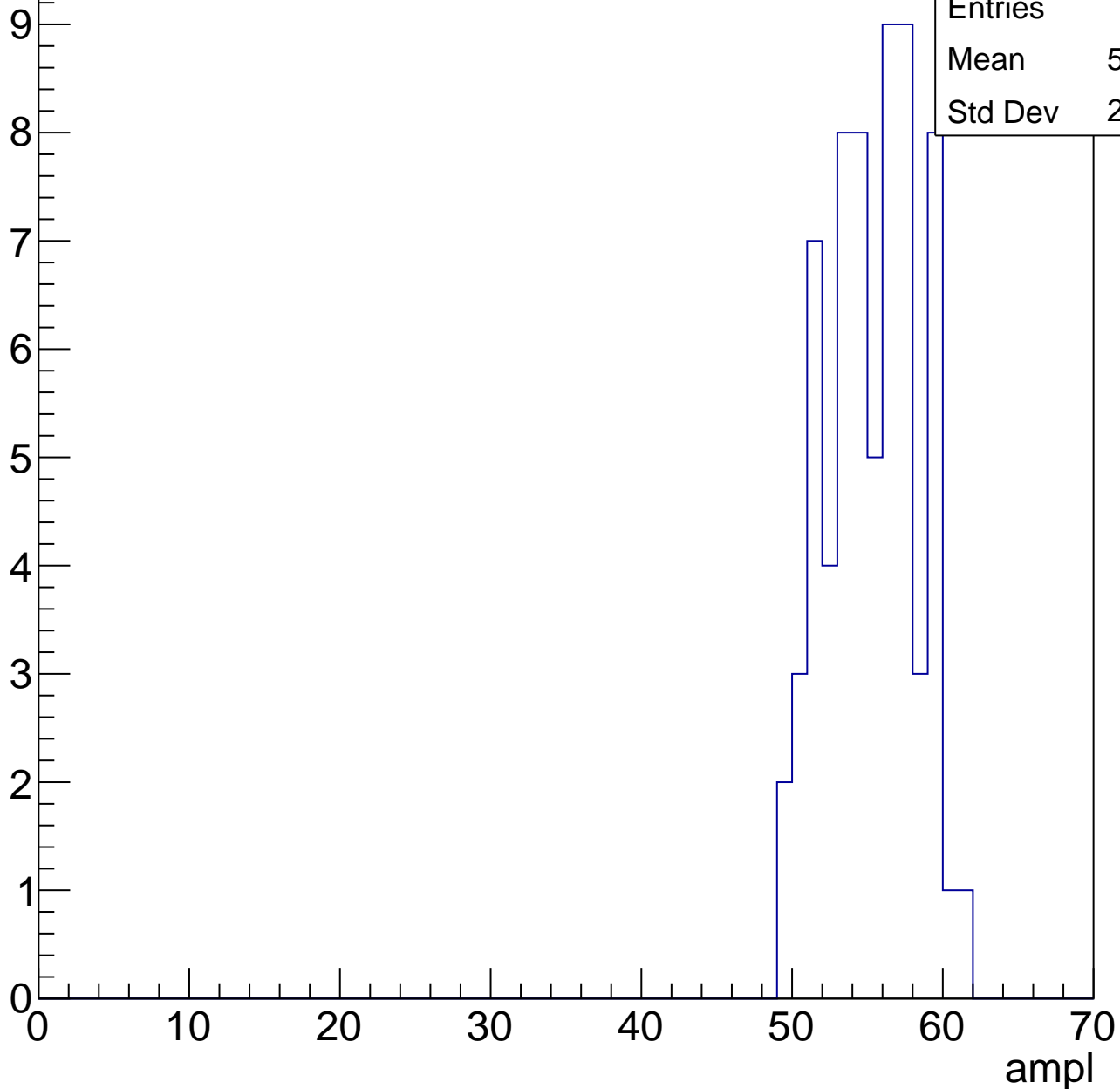
Entries	64
Mean	48.56
Std Dev	2.973



B0L001S, U19-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch22, adc5

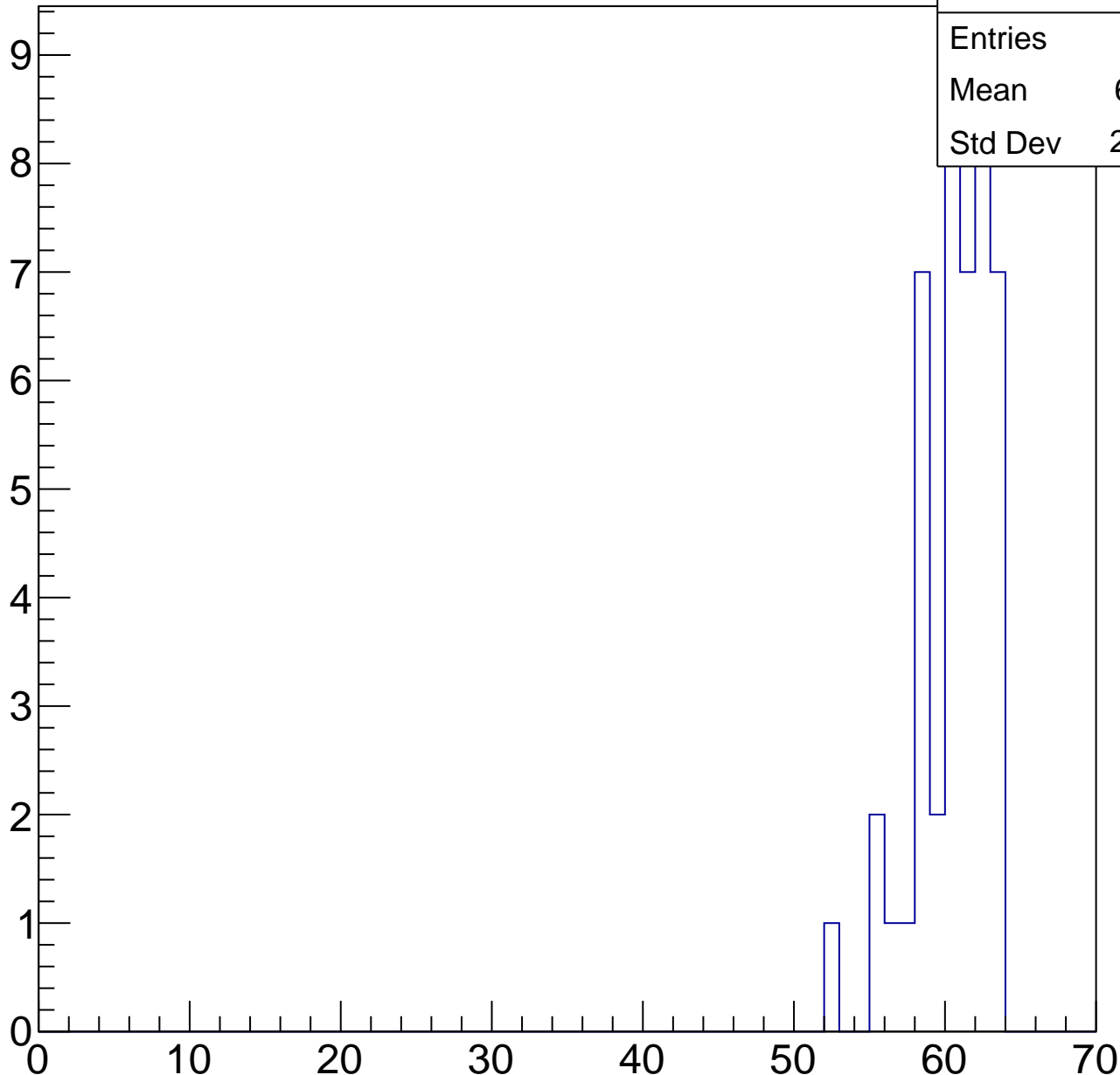
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	60.11
Std Dev	2.443

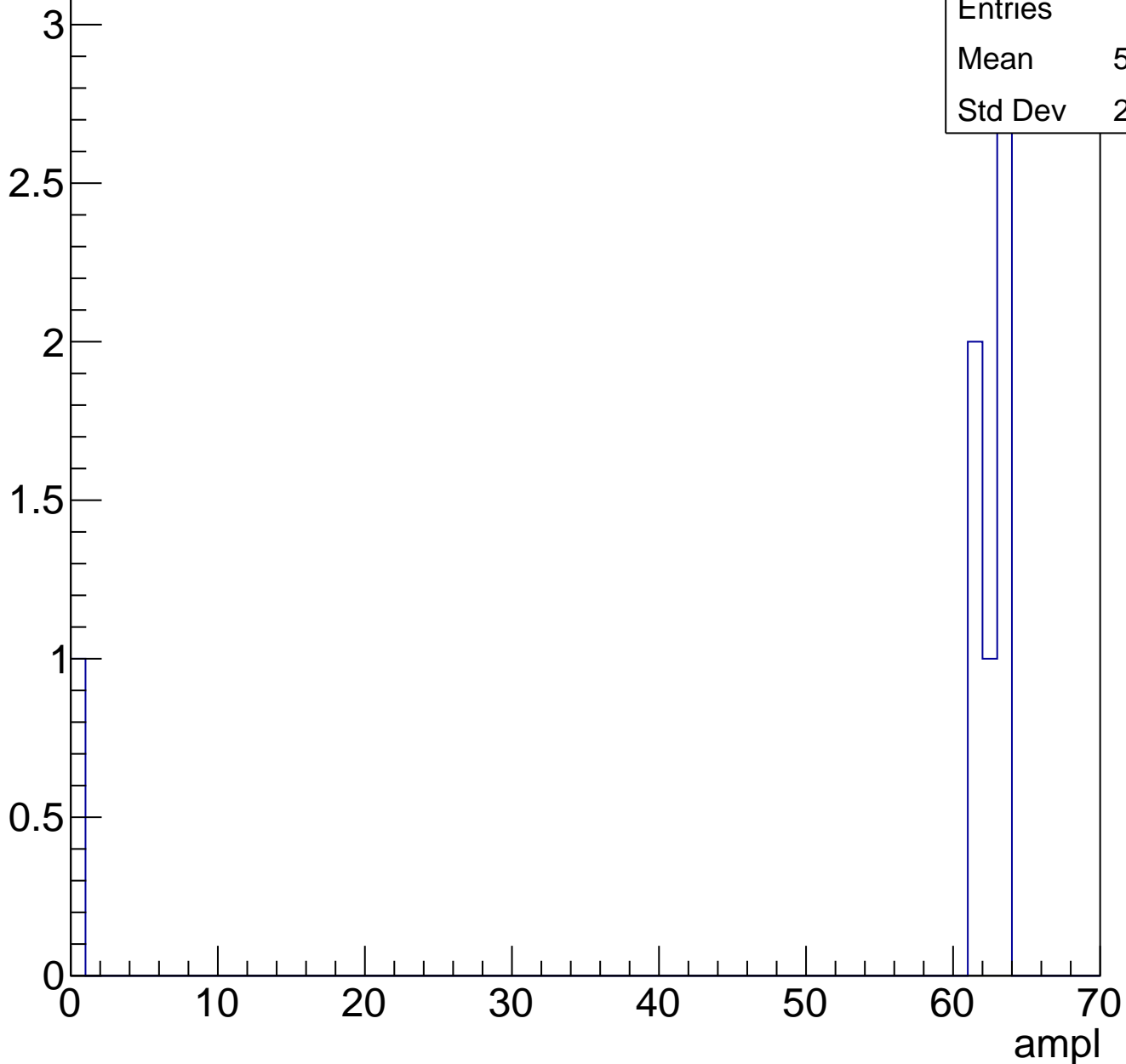
ampl



B0L001S, U19-ch22, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch23, adc0

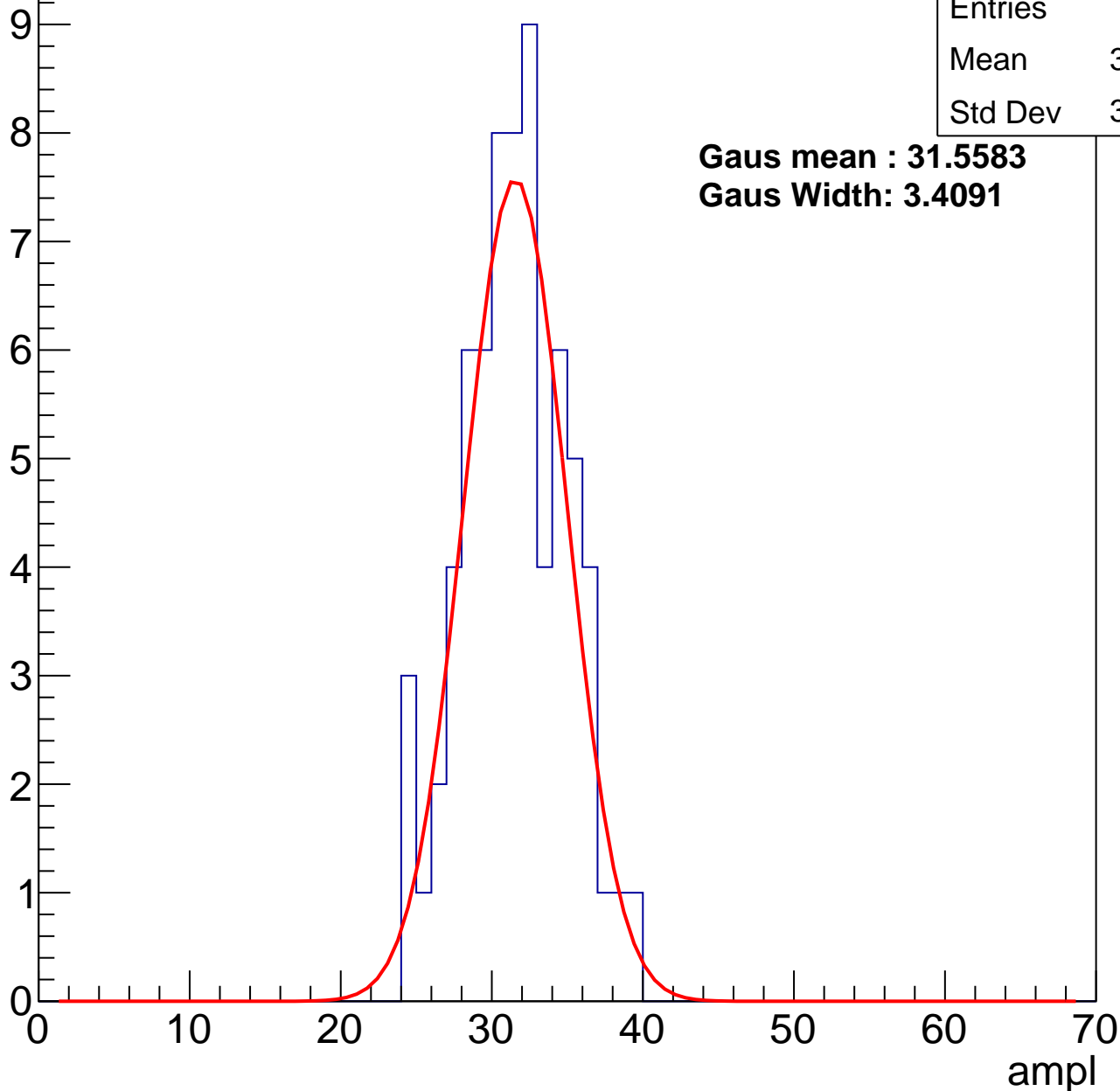
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	31.07
Std Dev	3.385

Gaus mean : 31.5583

Gaus Width: 3.4091



B0L001S, U19-ch23, adc1

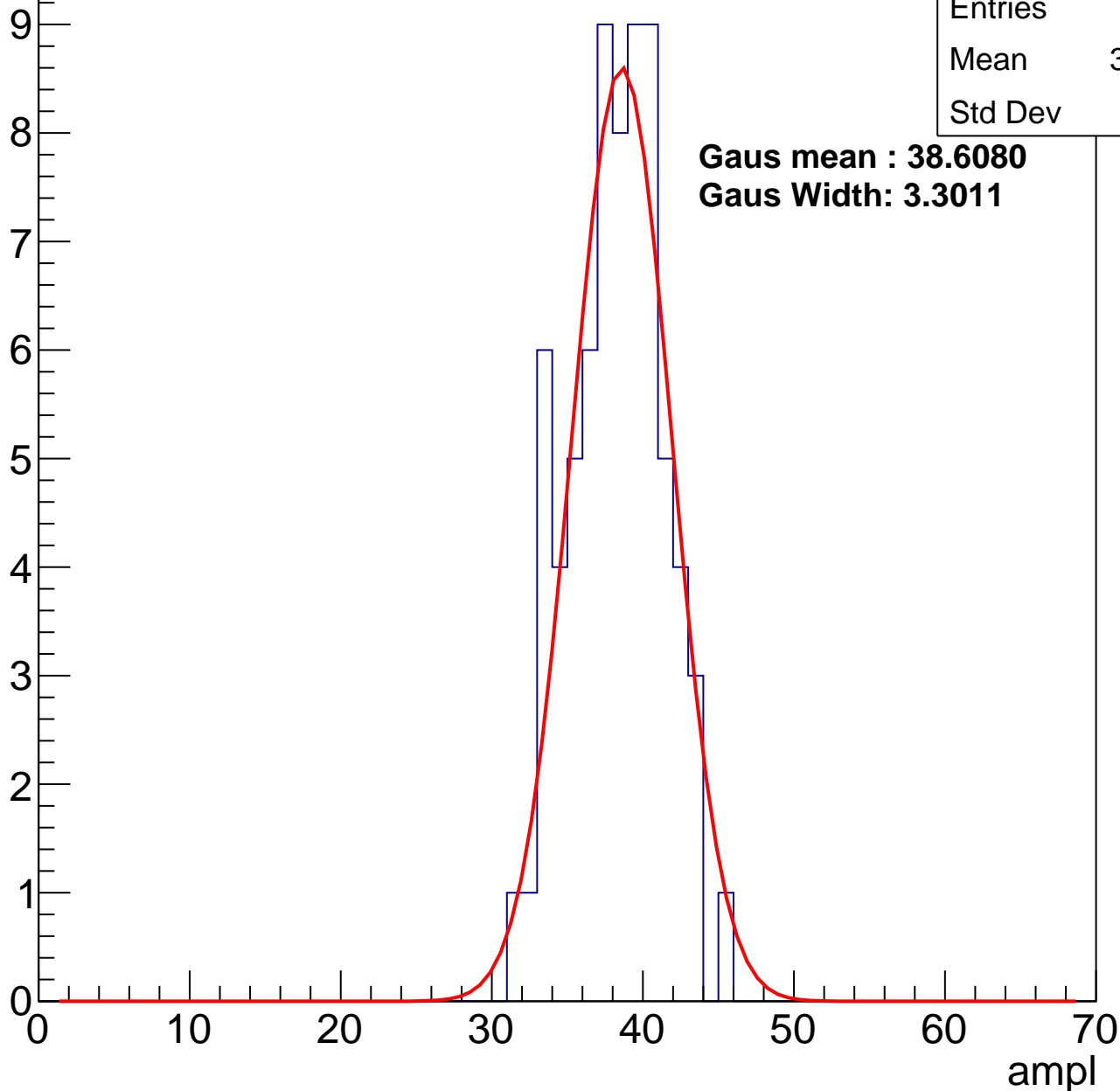
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.79
Std Dev	3.03

Gaus mean : 38.6080

Gaus Width: 3.3011



B0L001S, U19-ch23, adc2

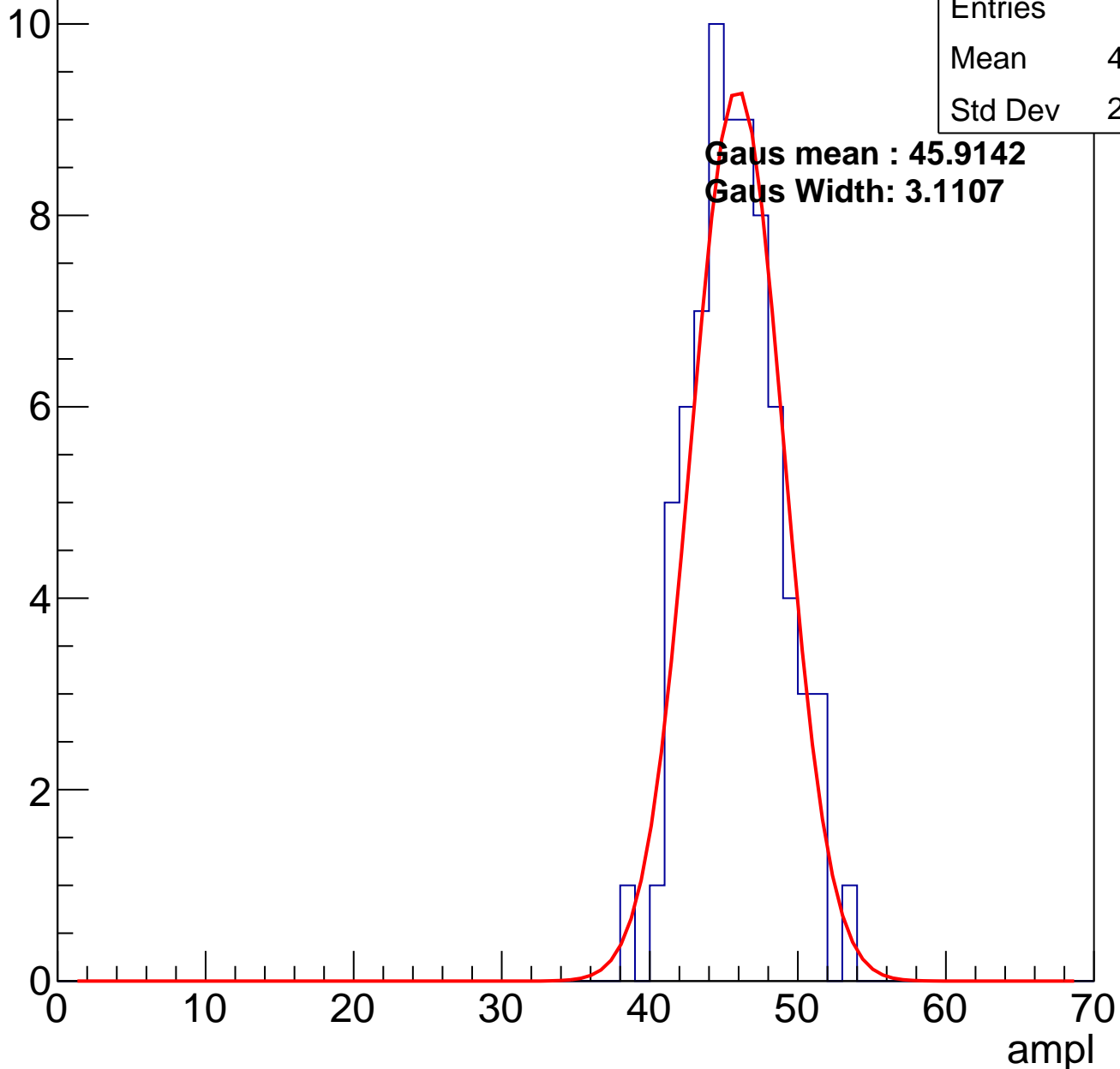
calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	45.36
Std Dev	2.967

Gaus mean : 45.9142

Gaus Width: 3.1107

Entry

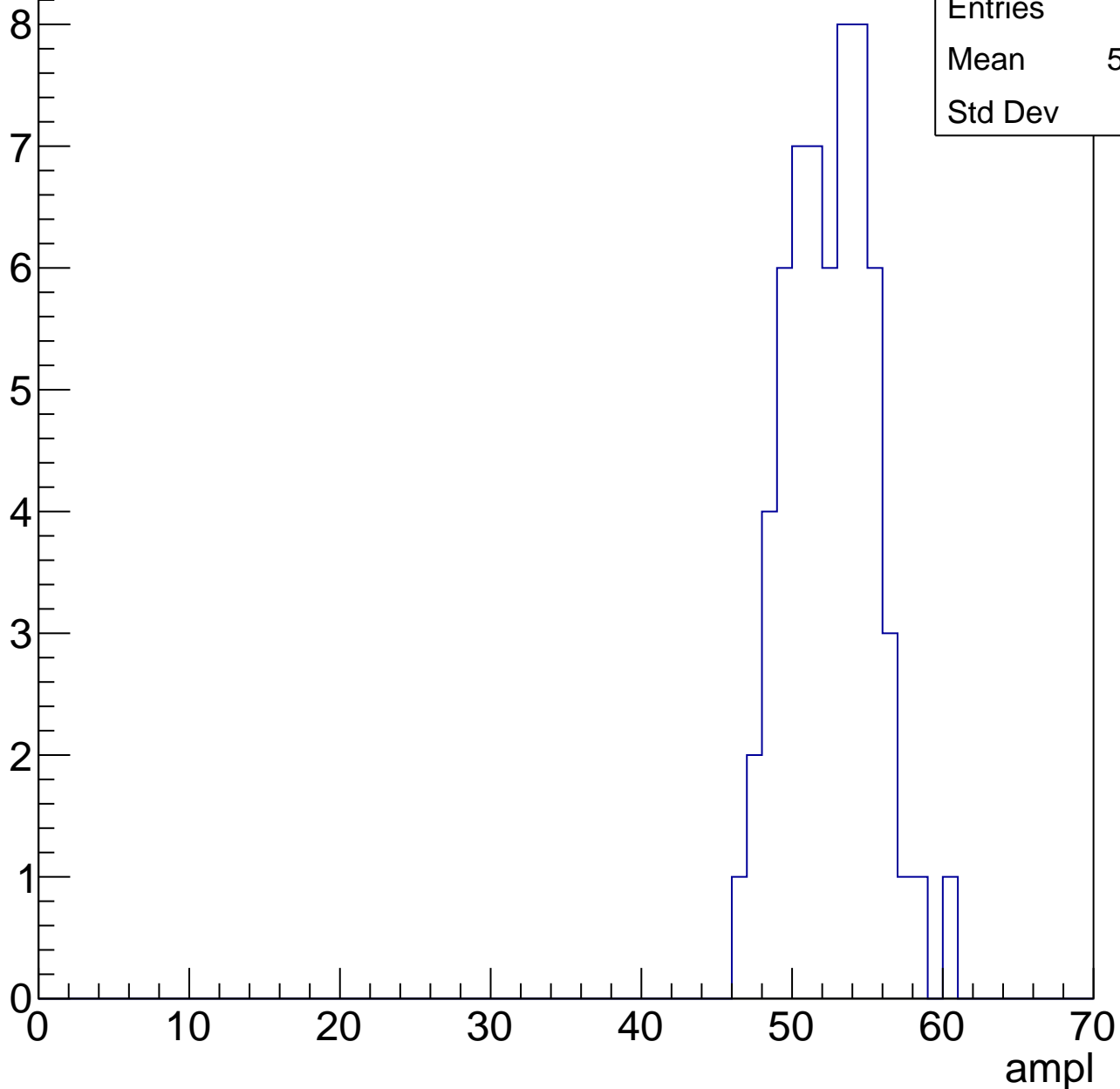


B0L001S, U19-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

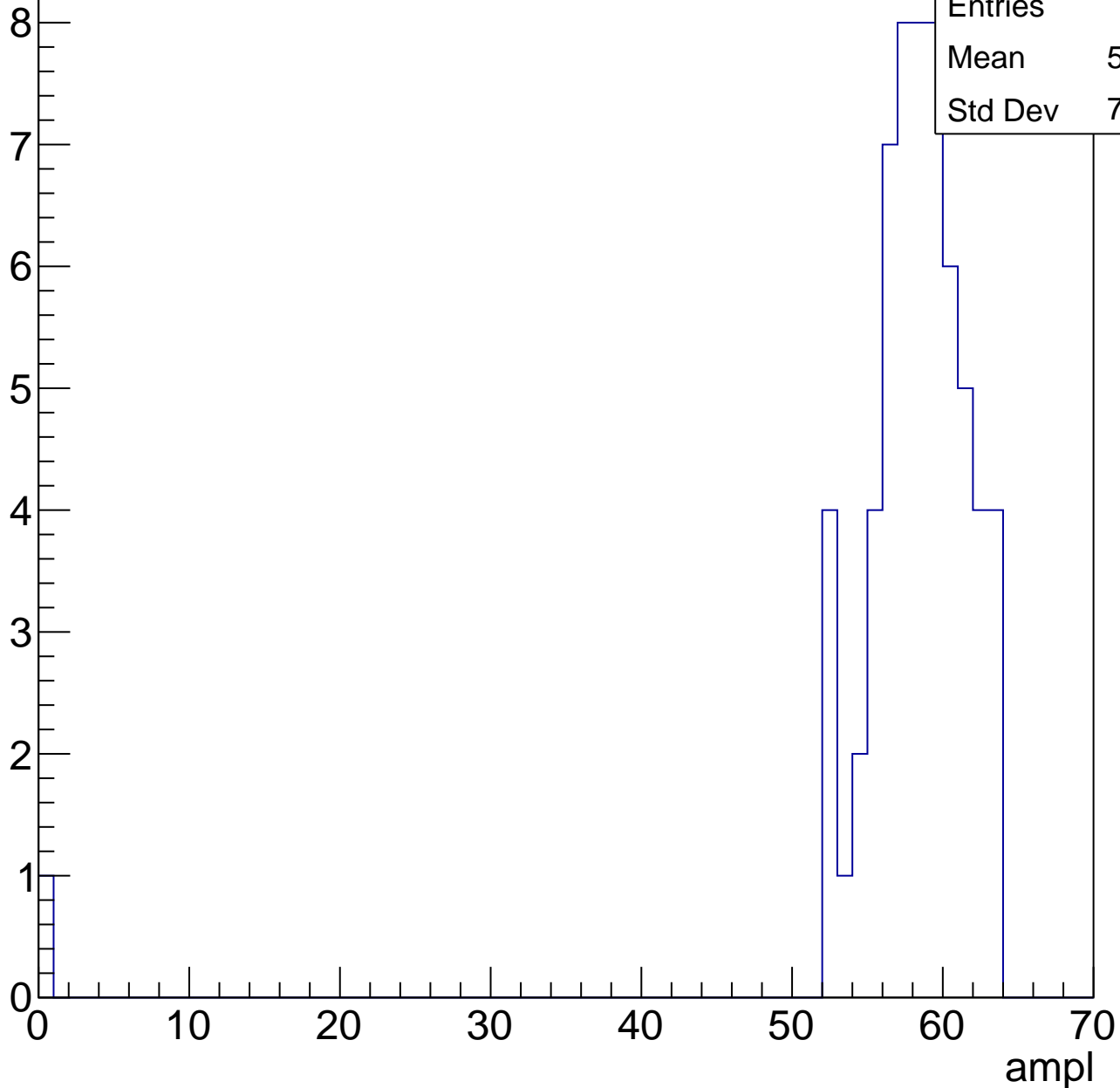
Entries	61
Mean	52.03
Std Dev	2.88



B0L001S, U19-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch24, adc0

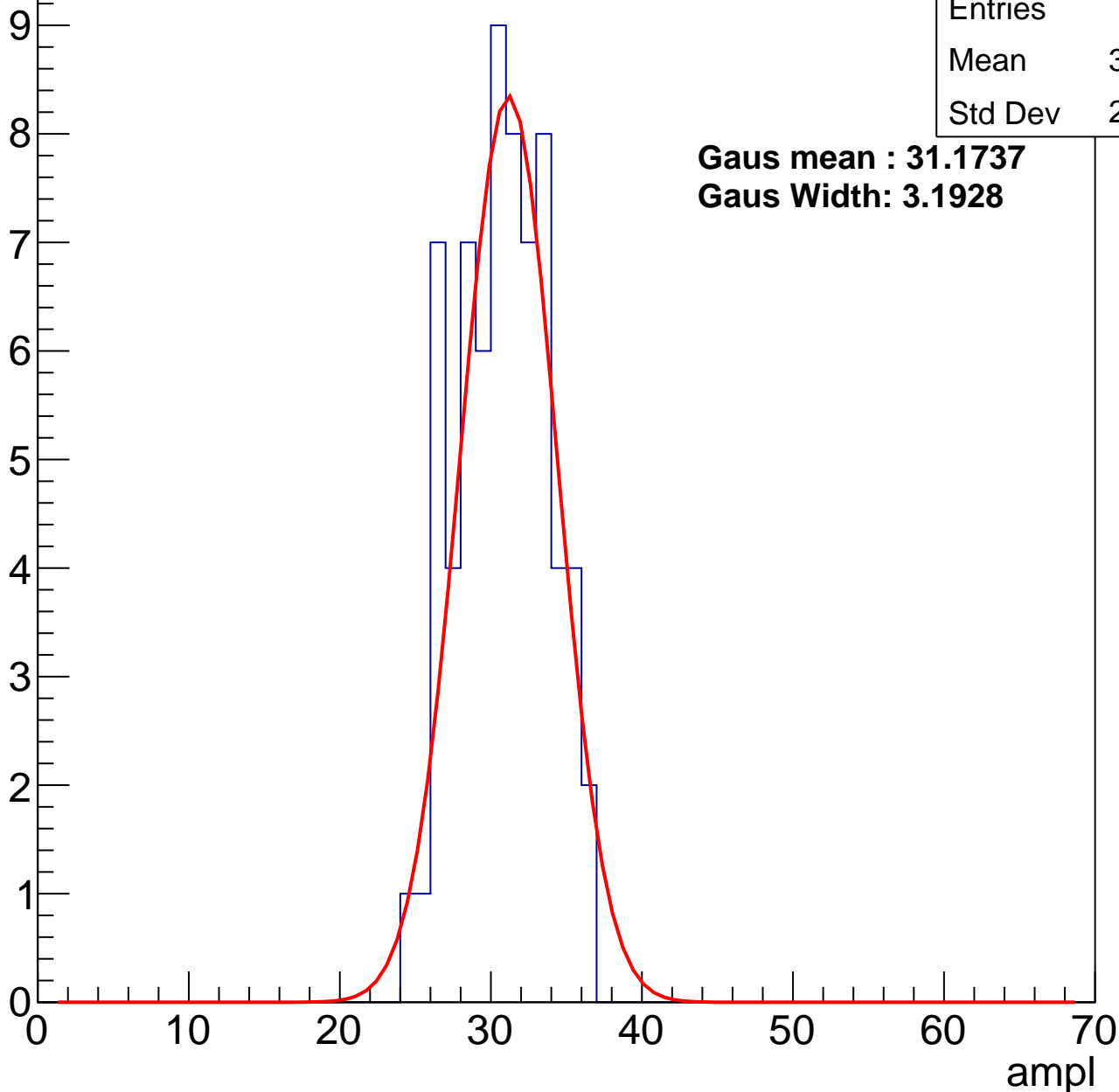
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	30.34
Std Dev	2.919

Gaus mean : 31.1737

Gaus Width: 3.1928



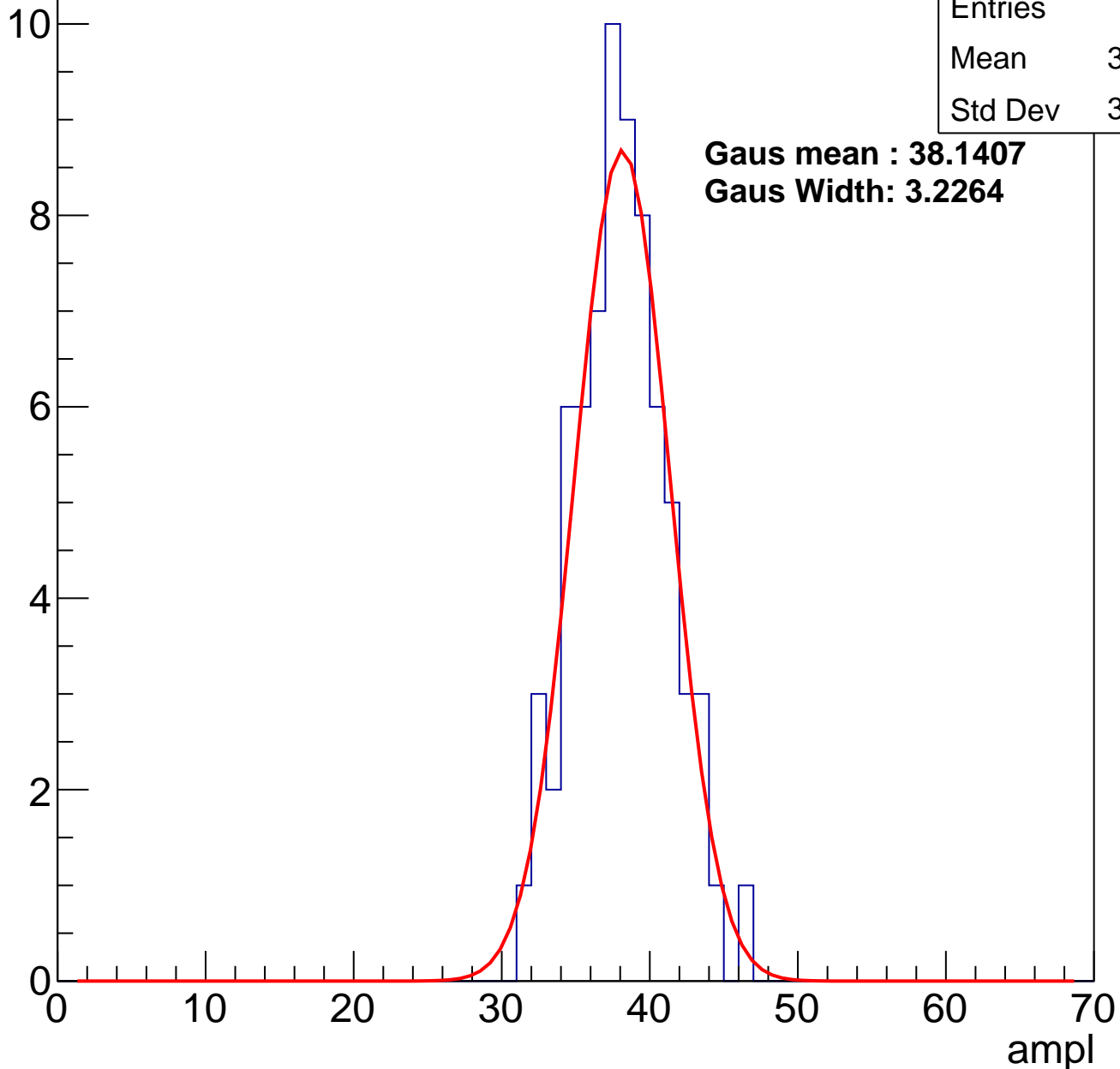
B0L001S, U19-ch24, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	37.65
Std Dev	3.109

Gaus mean : 38.1407
Gaus Width: 3.2264

Entry



B0L001S, U19-ch24, adc2

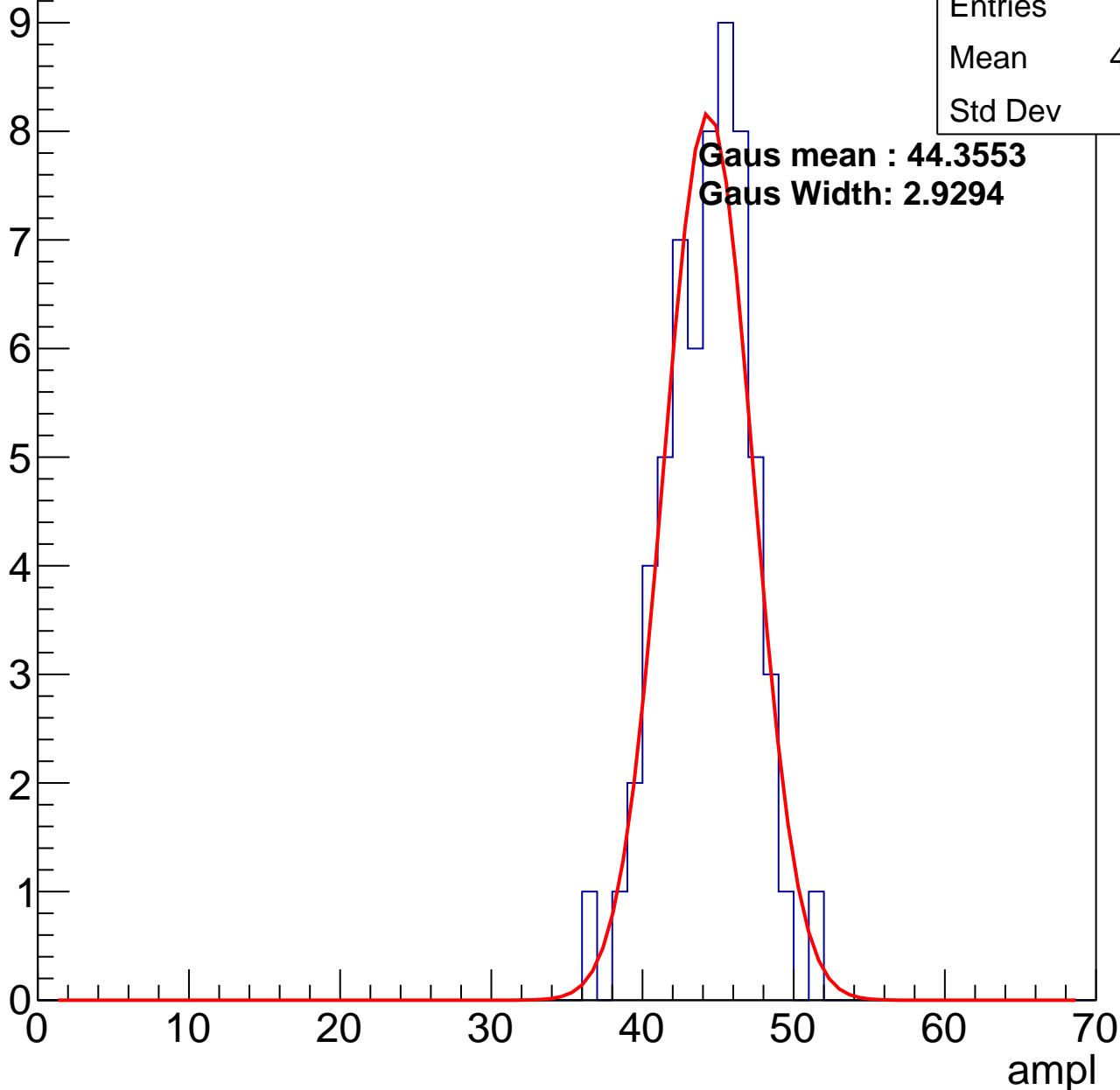
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.82
Std Dev	2.86

Gaus mean : 44.3553

Gaus Width: 2.9294



B0L001S, U19-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	50.77
Std Dev	3.131

Entry

10

8

6

4

2

0

0

10

20

30

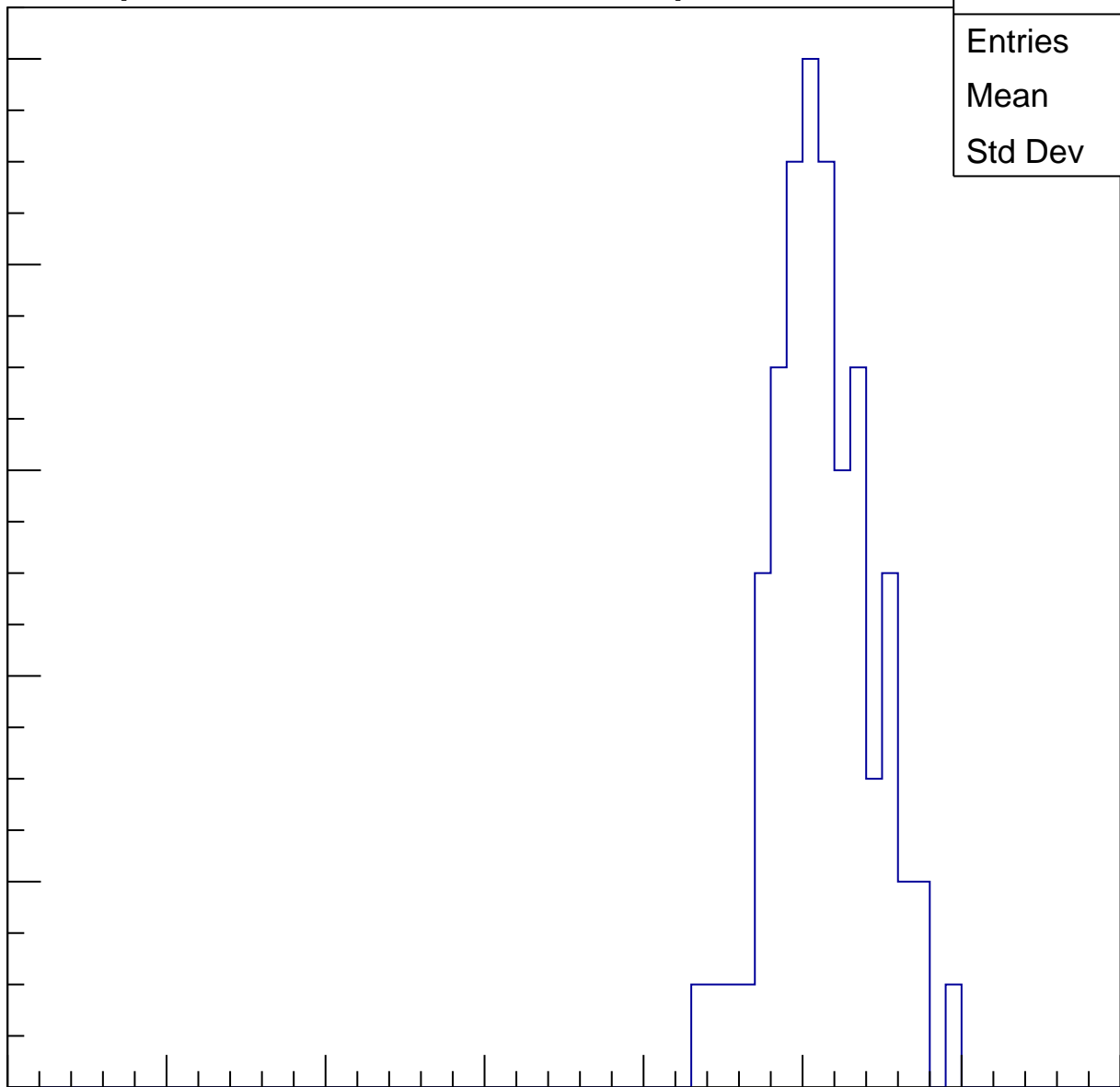
40

50

60

ampl

70

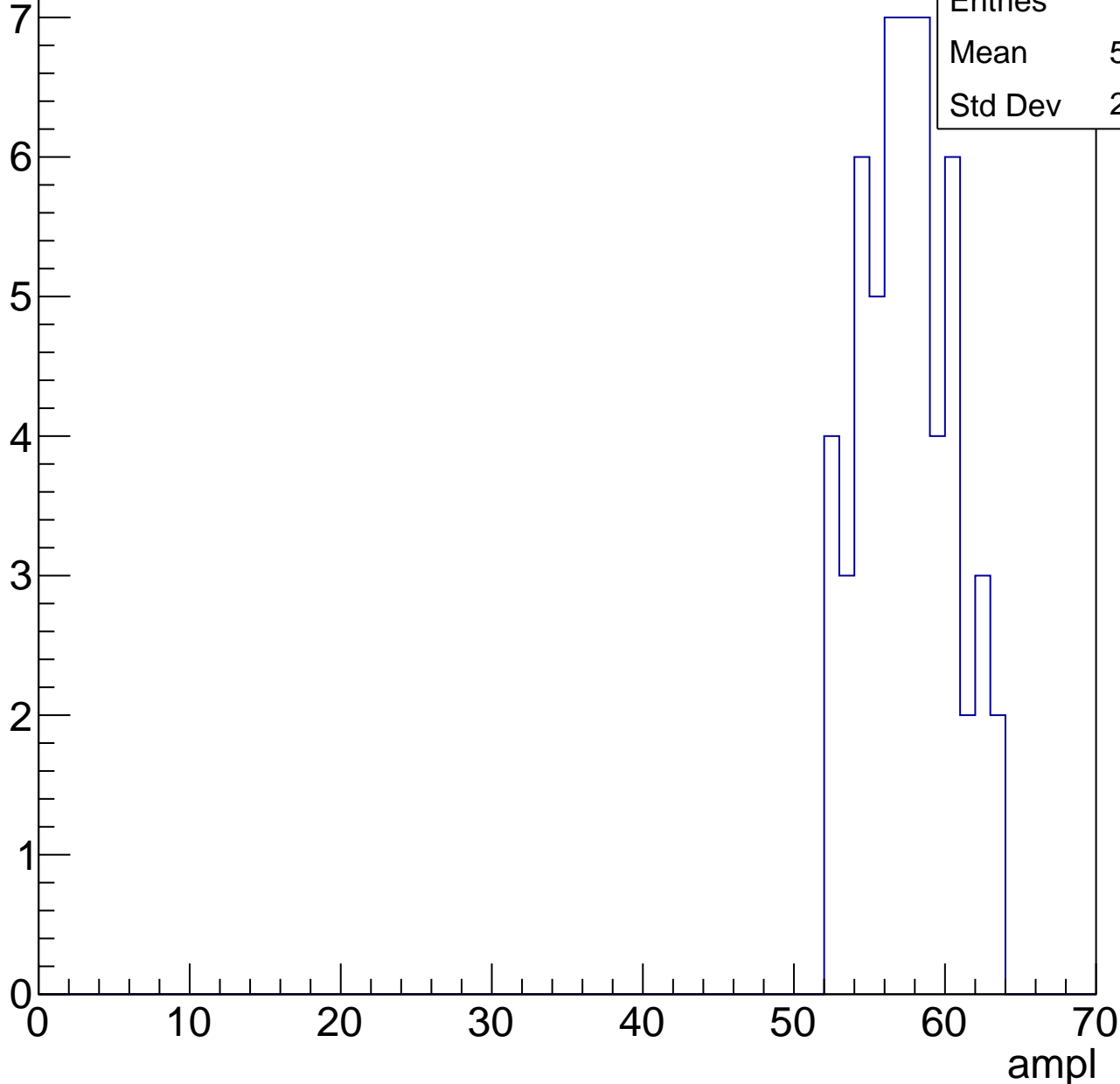


B0L001S, U19-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	57.02
Std Dev	2.943

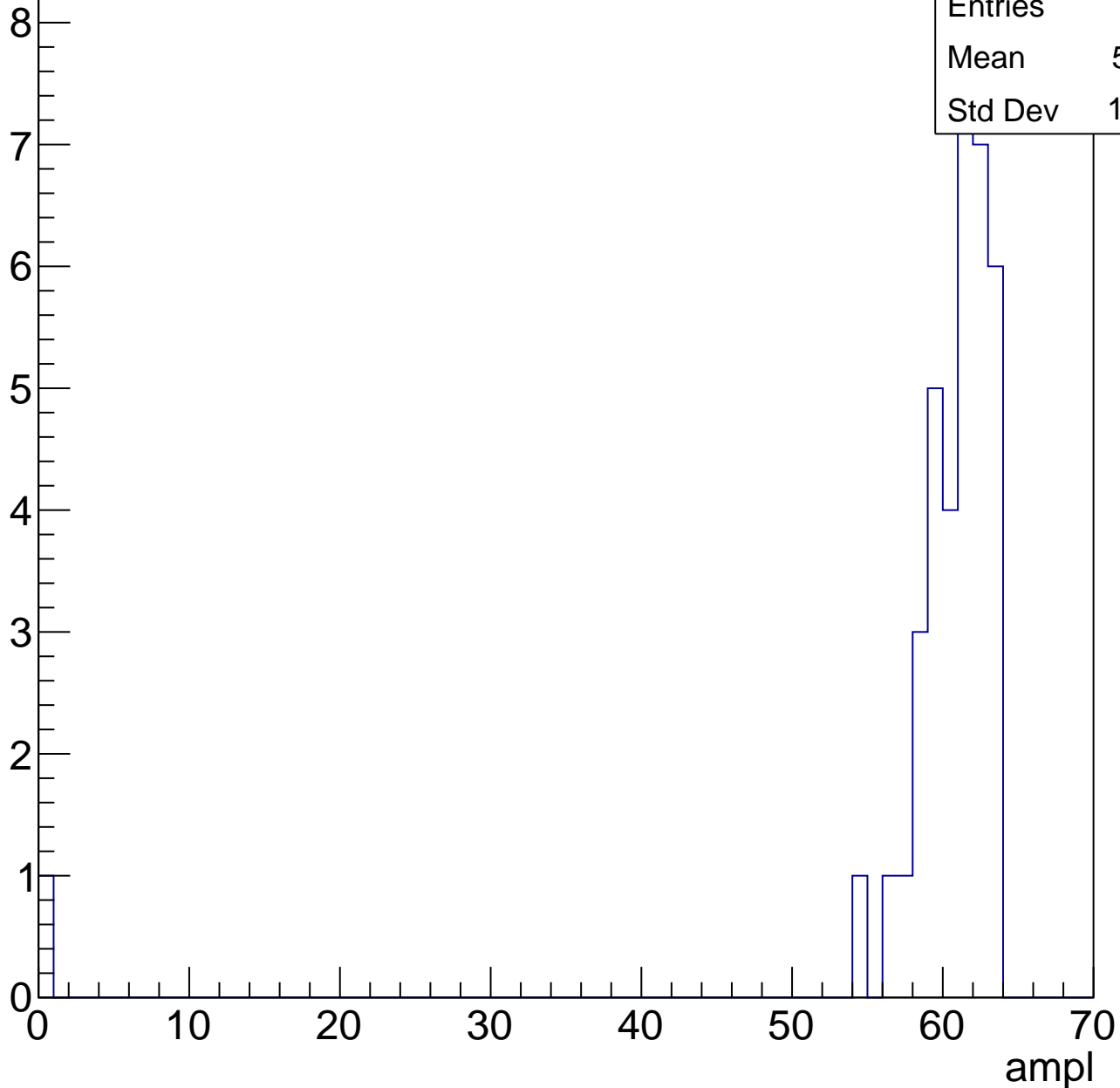


B0L001S, U19-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.81
Std Dev	10.02



B0L001S, U19-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L001S, U19-ch25, adc0

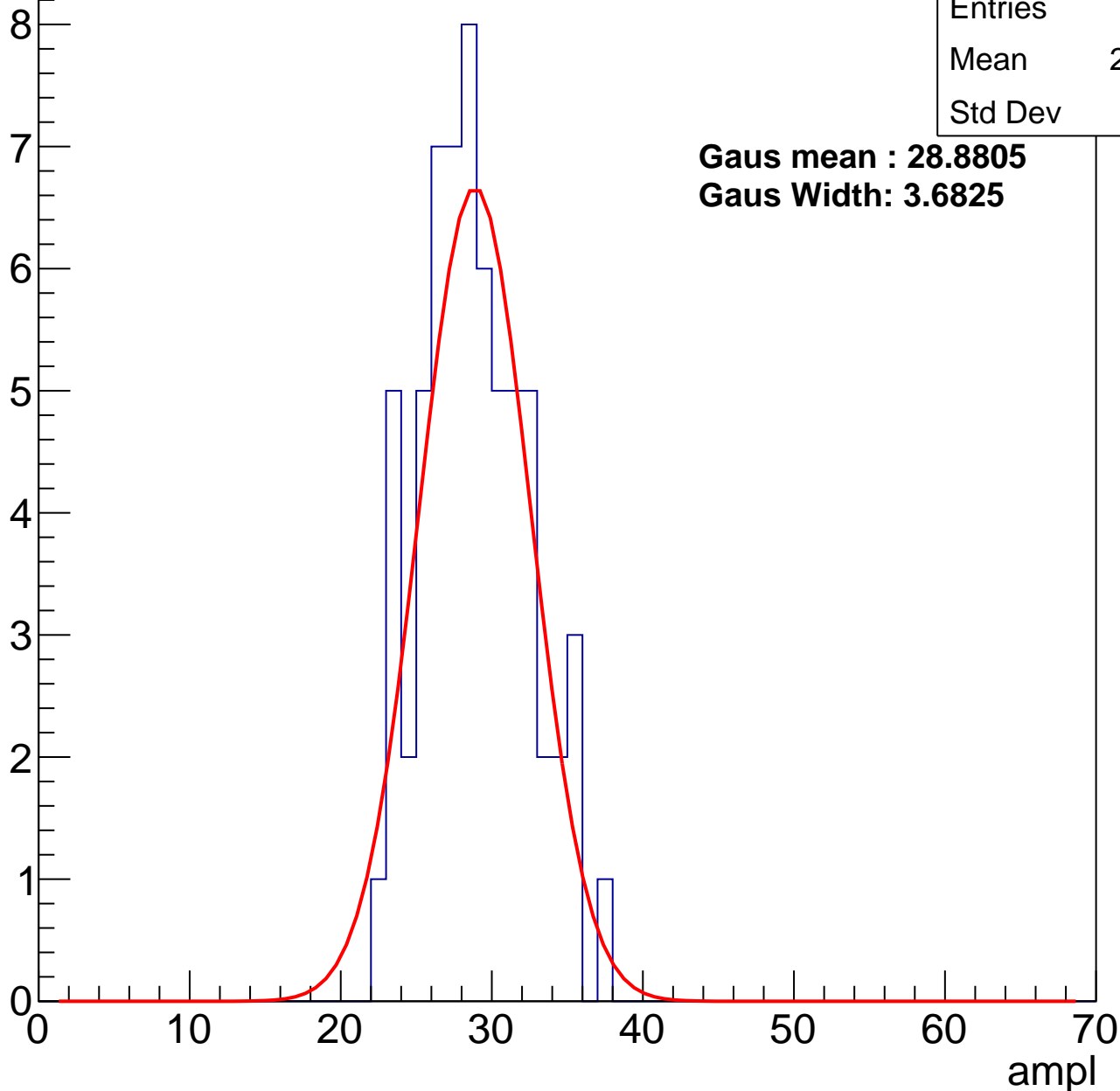
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	28.44
Std Dev	3.45

Gaus mean : 28.8805

Gaus Width: 3.6825



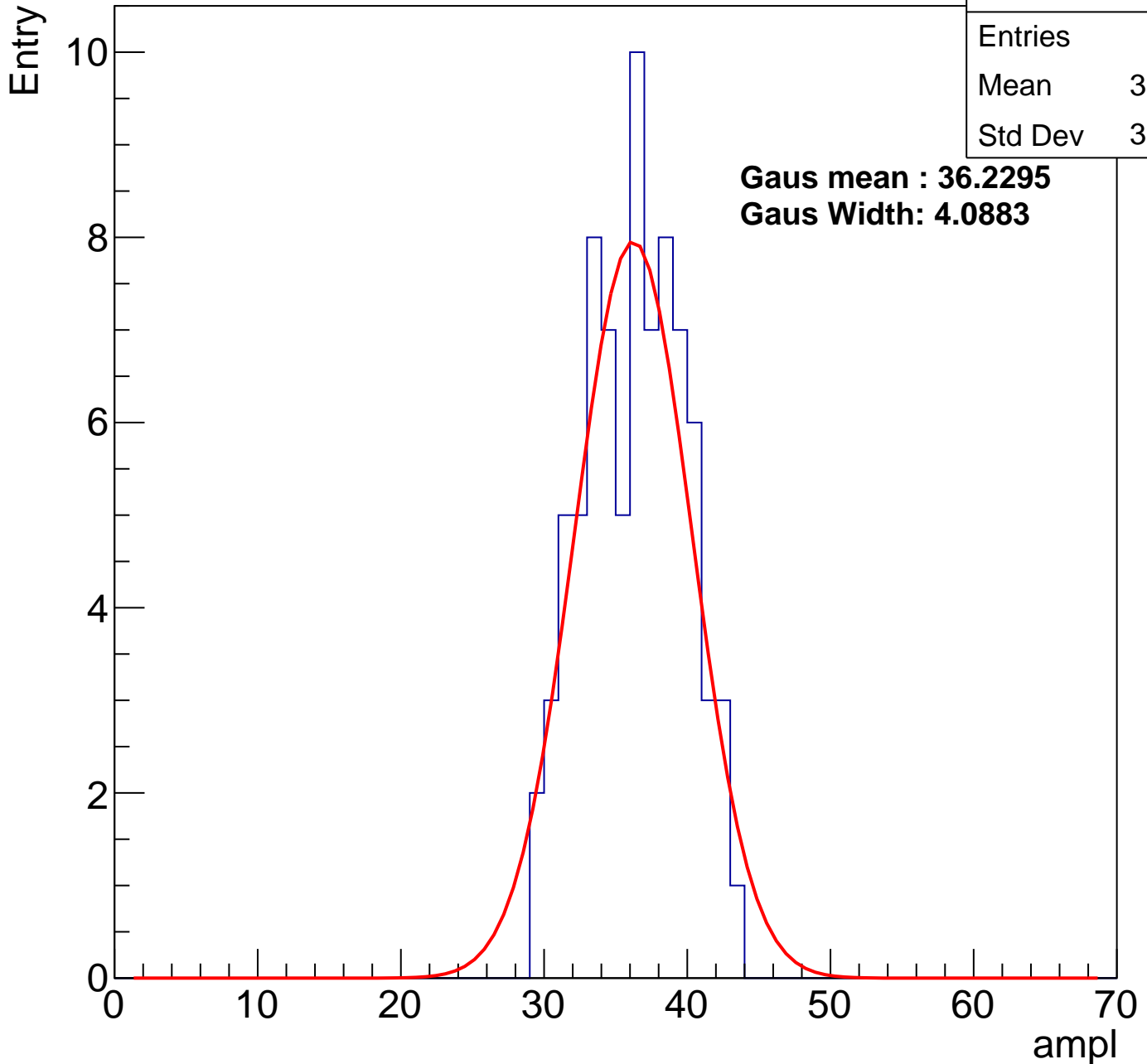
B0L001S, U19-ch25, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	35.85
Std Dev	3.435

Gaus mean : 36.2295

Gaus Width: 4.0883



B0L001S, U19-ch25, adc2

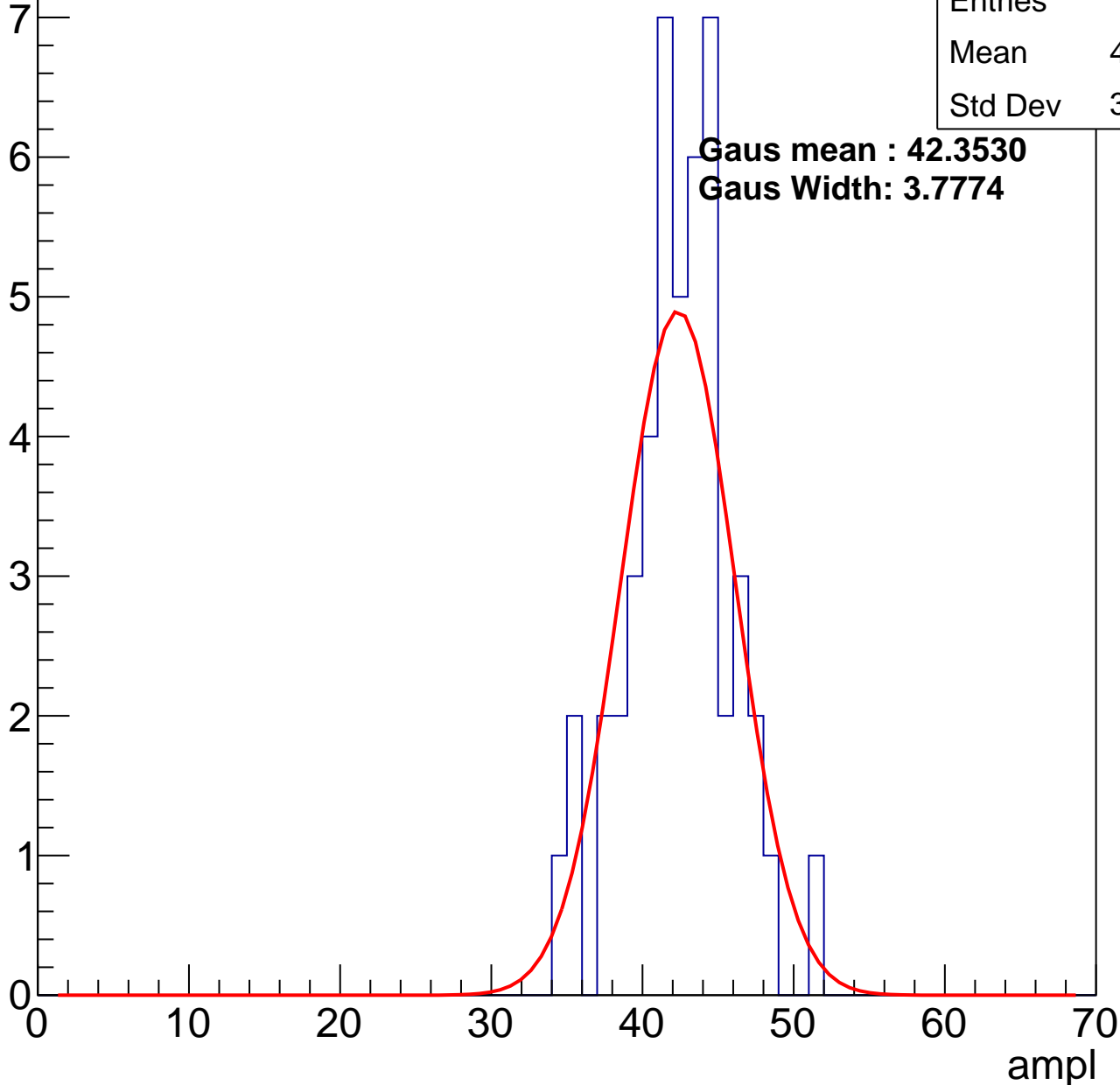
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	41.98
Std Dev	3.419

Gaus mean : 42.3530

Gaus Width: 3.7774



B0L001S, U19-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	48.8
Std Dev	3.414

Entry

10

8

6

4

2

0

0

10

20

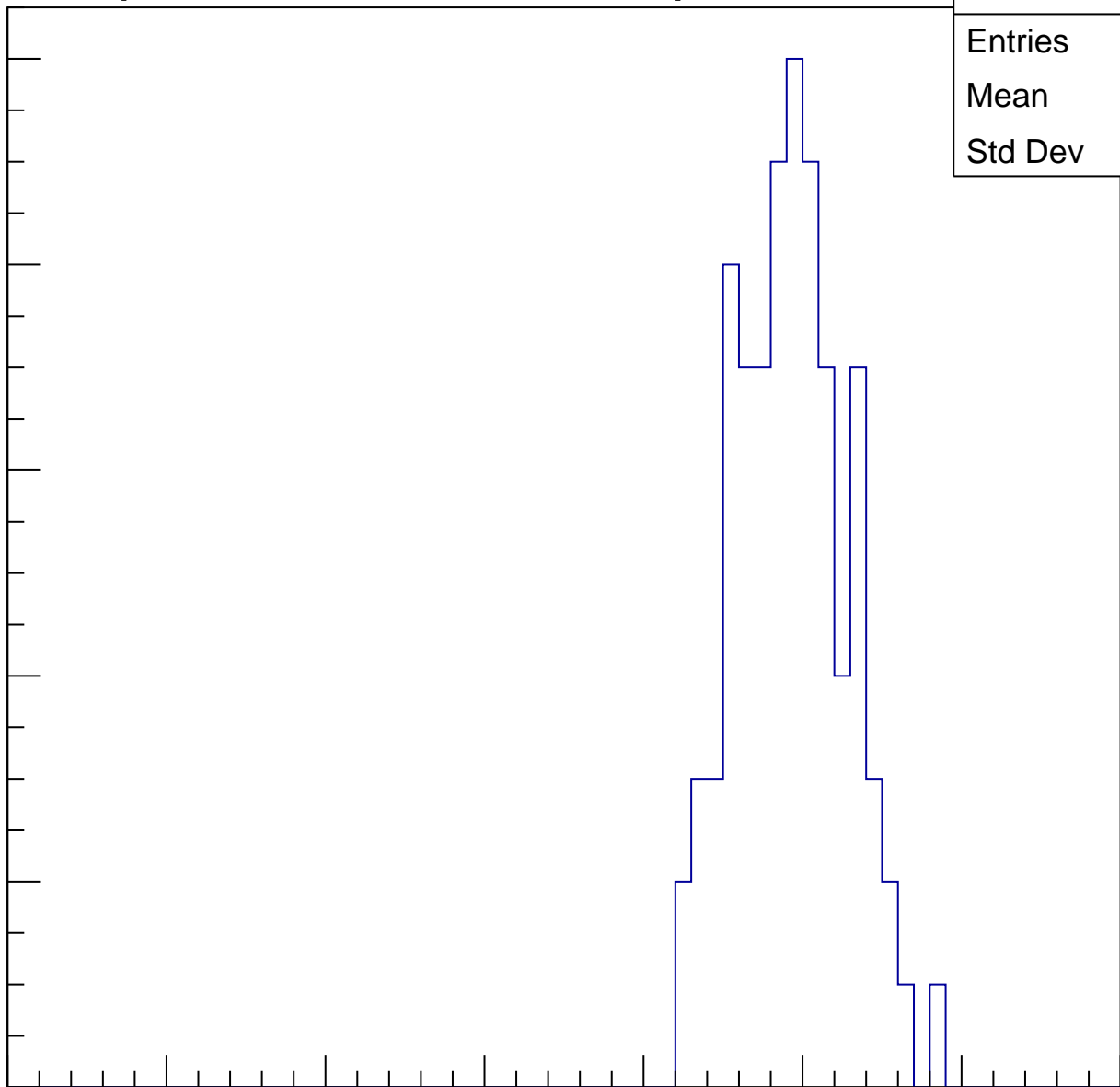
30

40

50

60

ampl

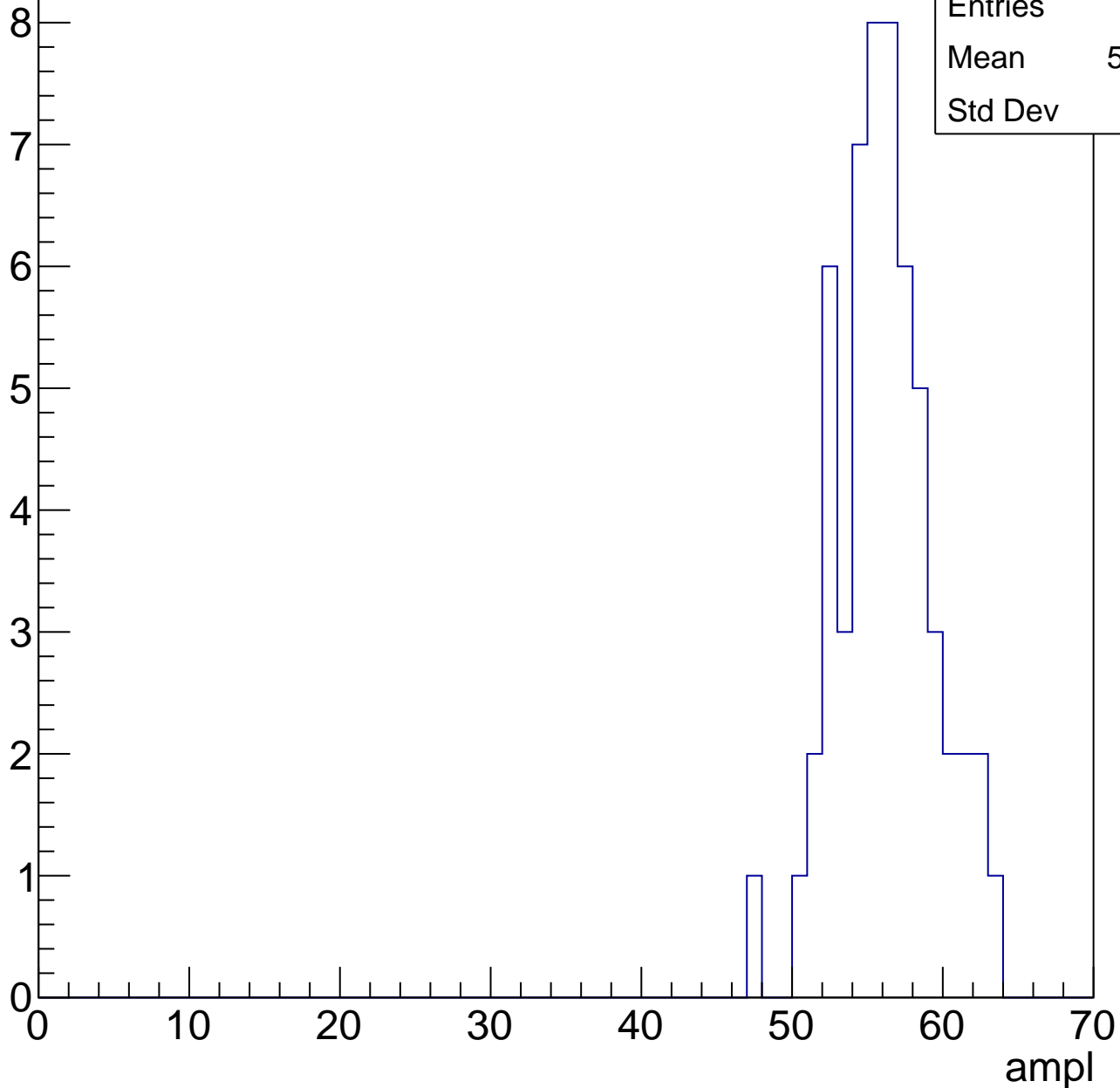


B0L001S, U19-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	55.68
Std Dev	3.18



B0L001S, U19-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

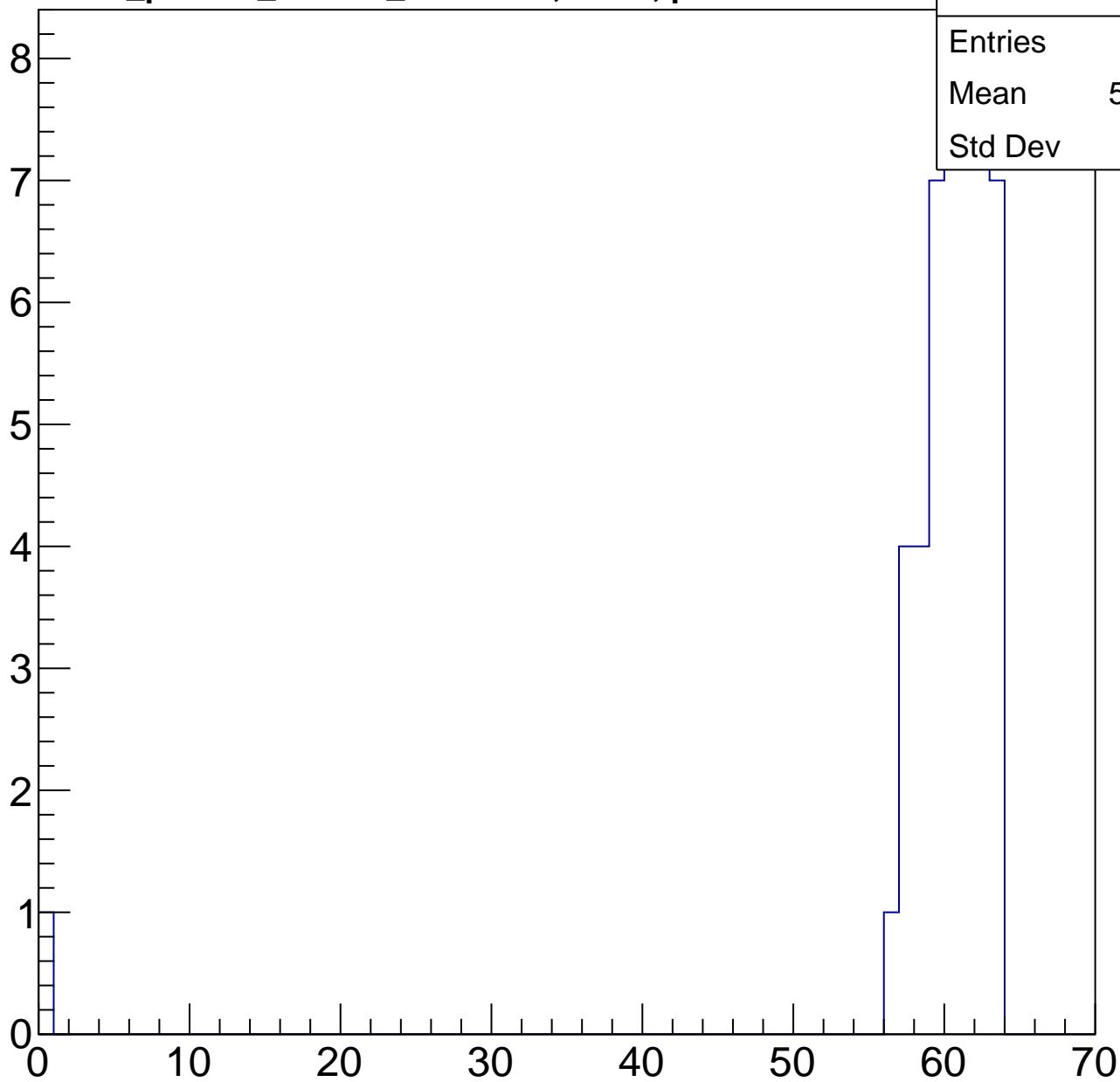
Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.04
Std Dev	8.82

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U19-ch26, adc0

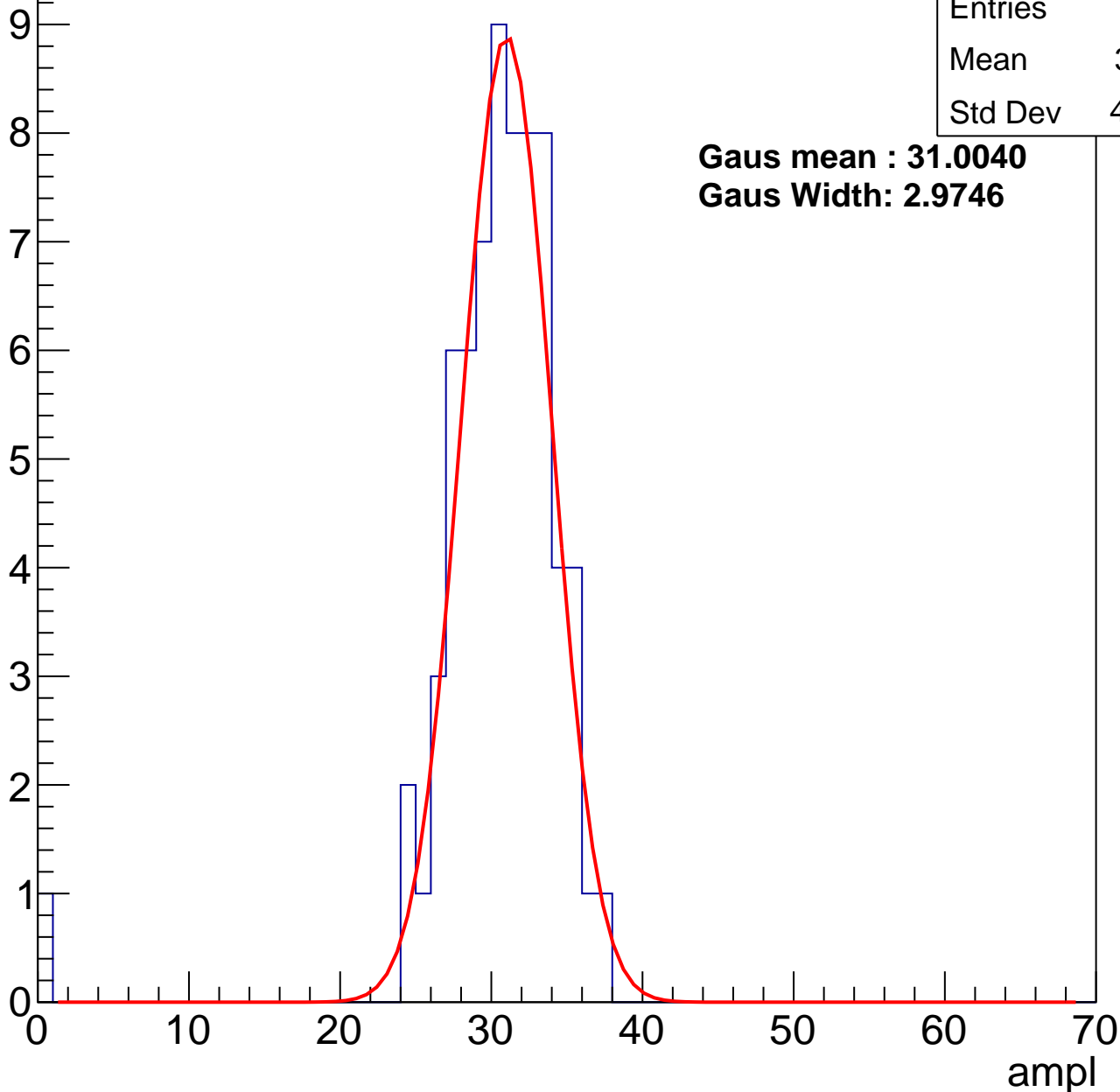
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.01
Std Dev	4.648

Gaus mean : 31.0040

Gaus Width: 2.9746



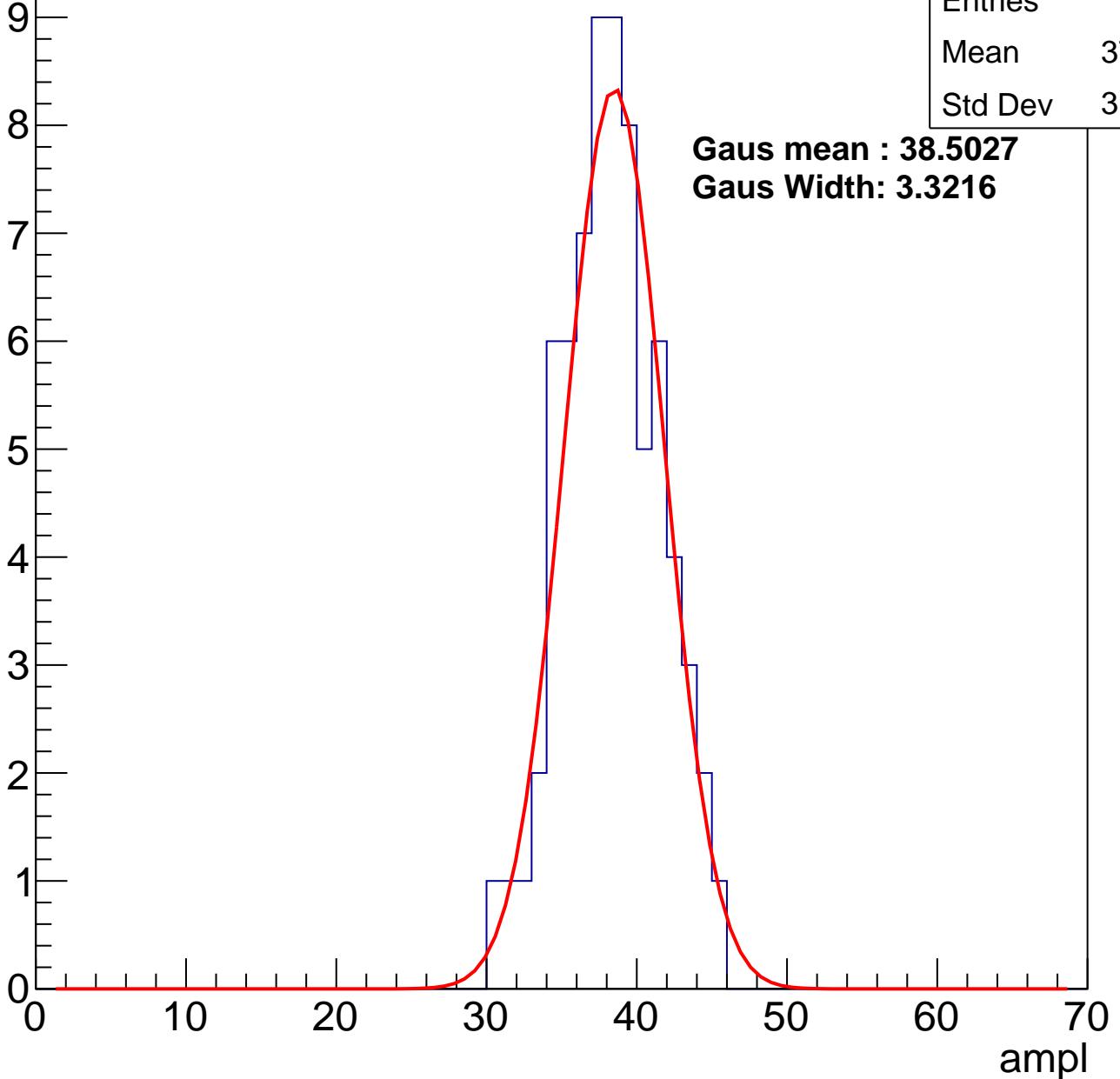
B0L001S, U19-ch26, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.86
Std Dev	3.199

Gaus mean : 38.5027
Gaus Width: 3.3216



B0L001S, U19-ch26, adc2

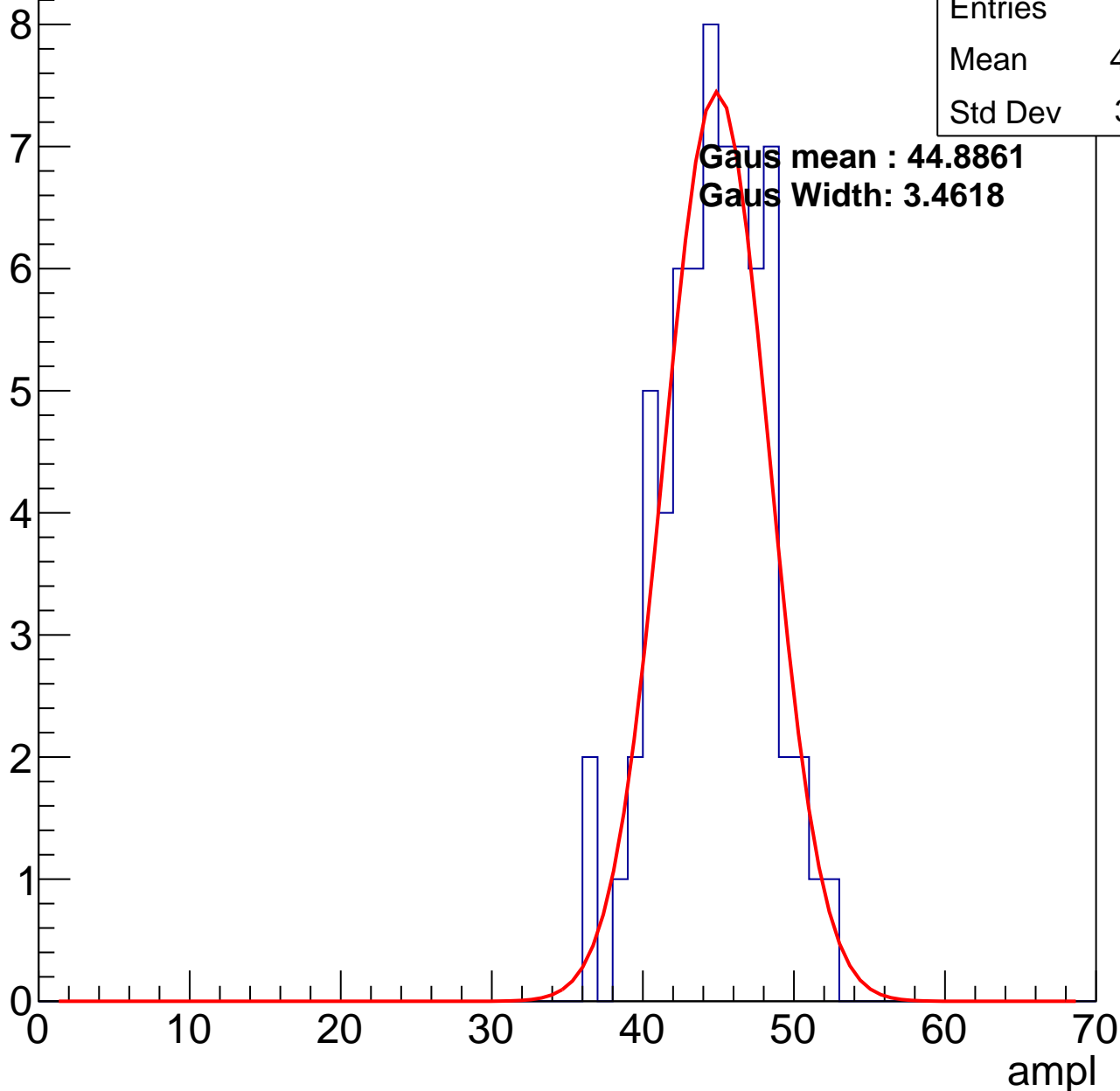
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.33
Std Dev	3.431

Gaus mean : 44.8861

Gaus Width: 3.4618

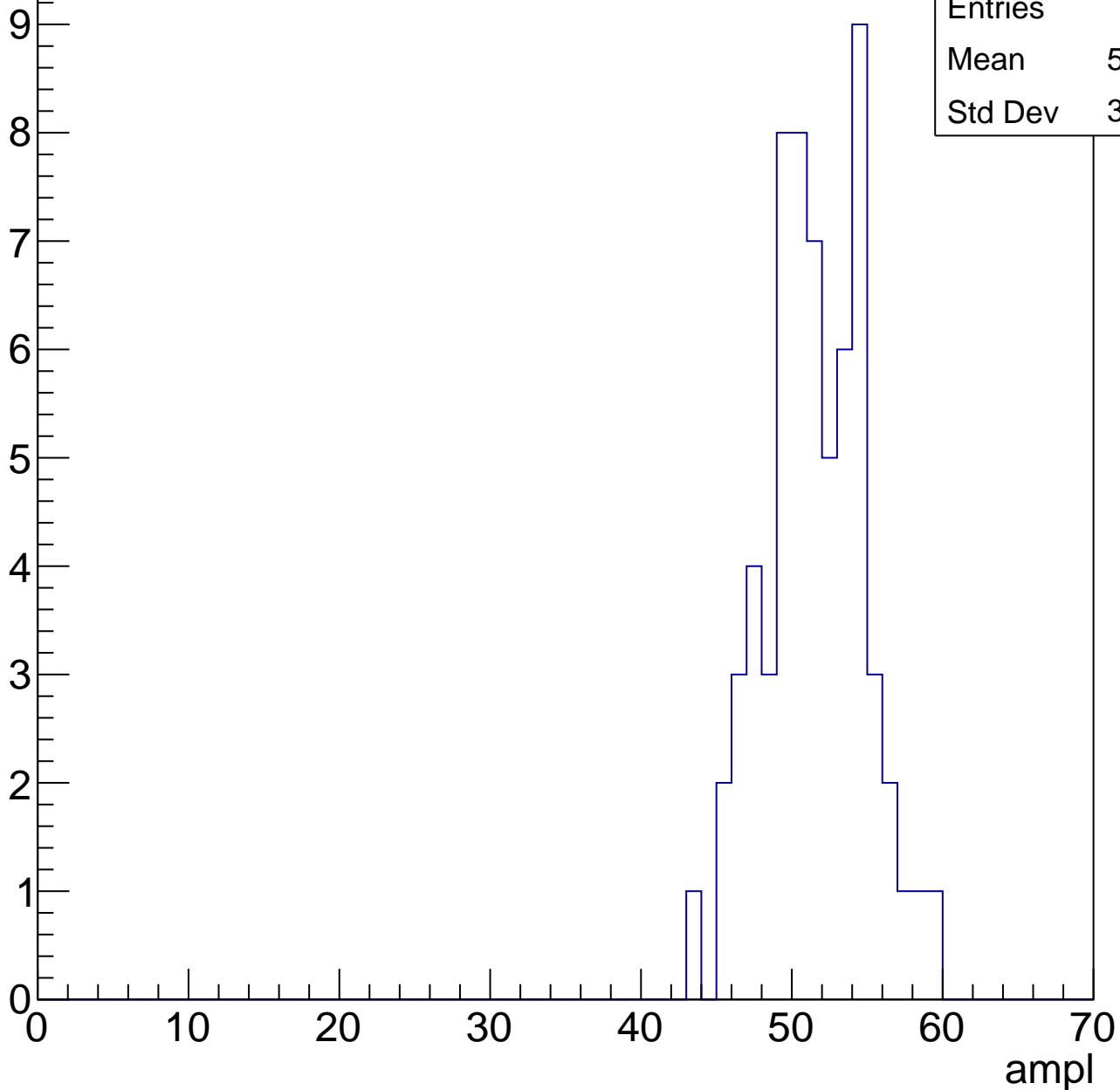


B0L001S, U19-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

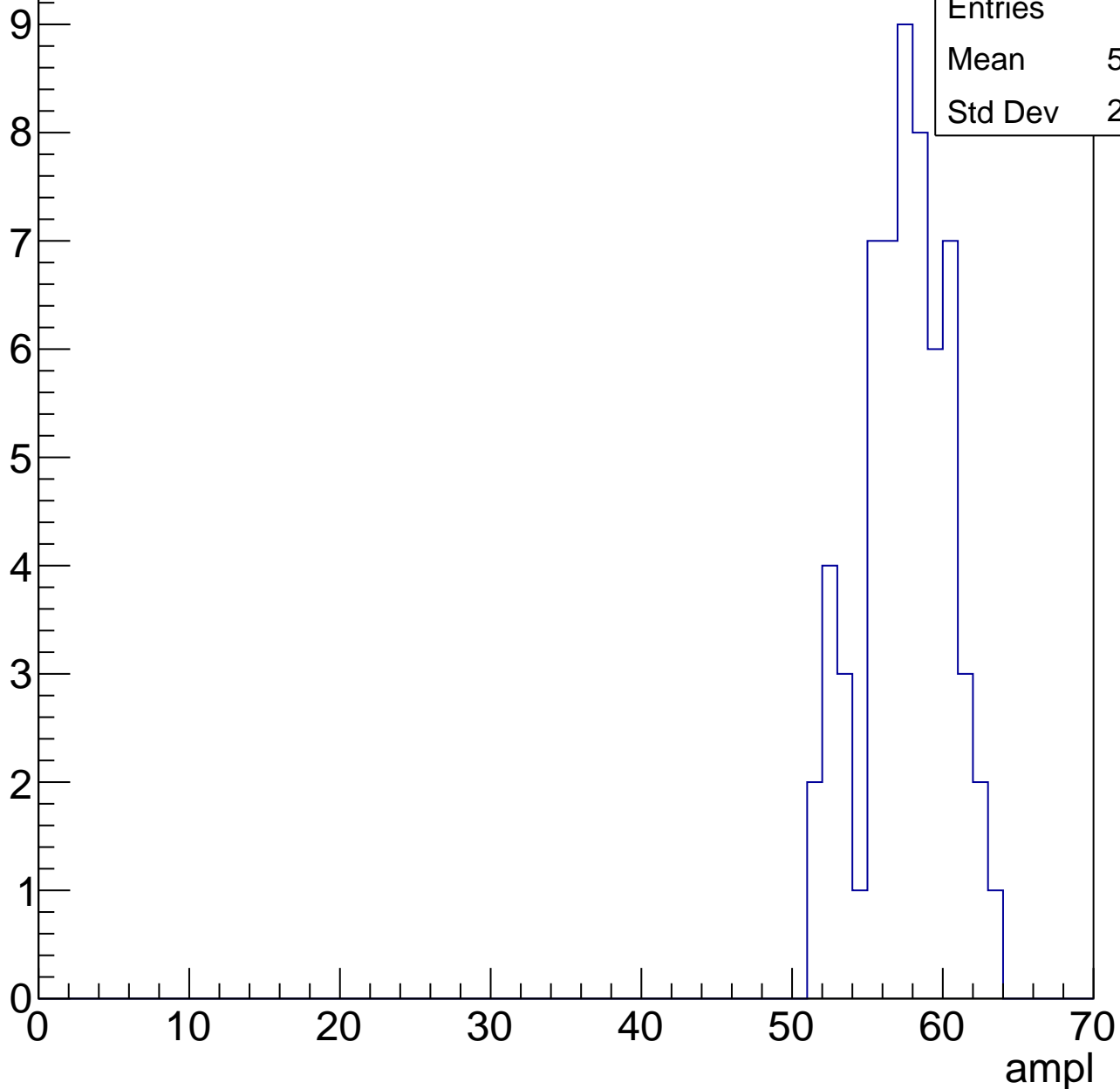
Entries	64
Mean	51.05
Std Dev	3.304



B0L001S, U19-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



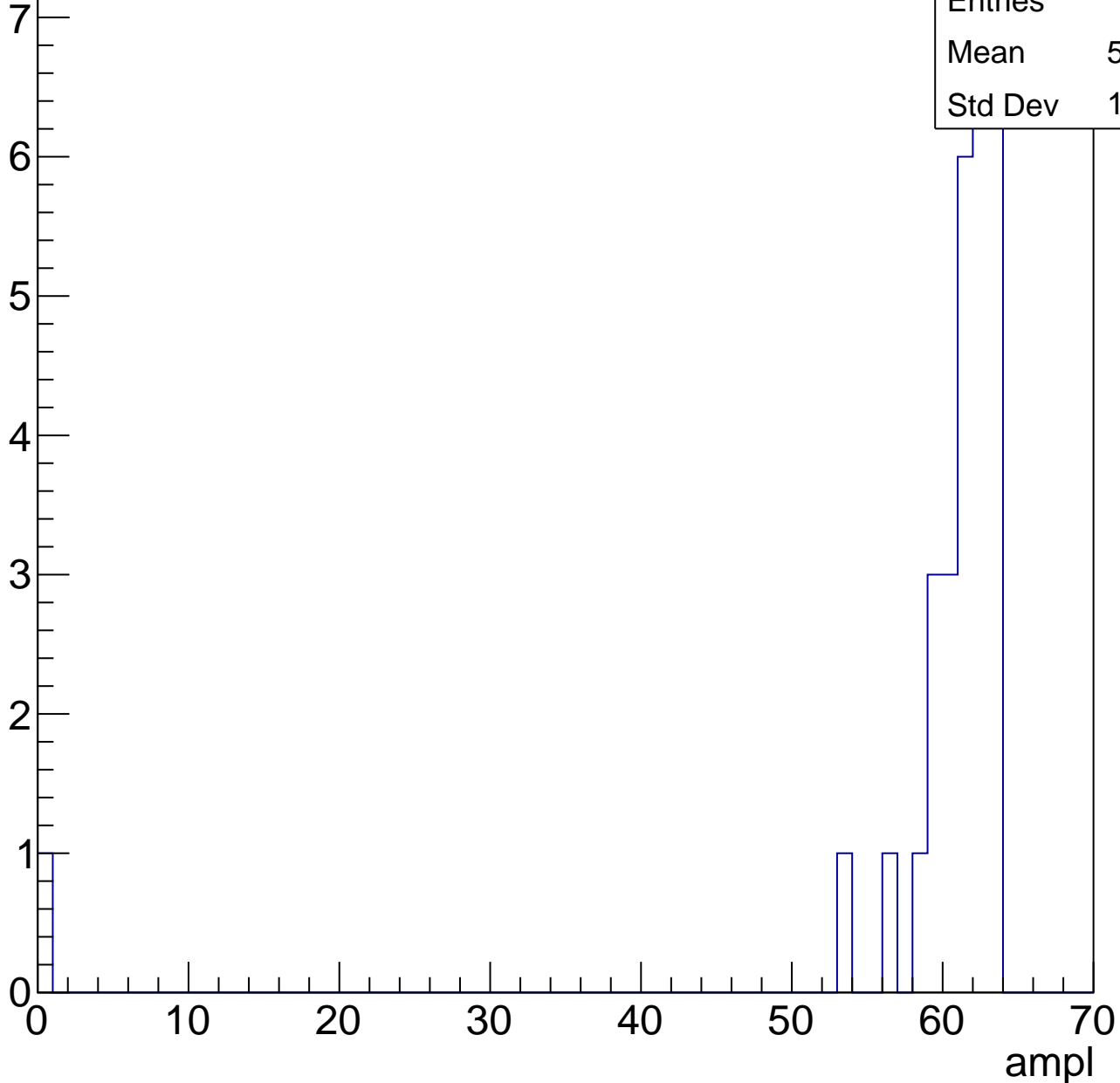
Entries	60
Mean	57.02
Std Dev	2.866

B0L001S, U19-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.83
Std Dev	11.15



B0L001S, U19-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch26, adc7

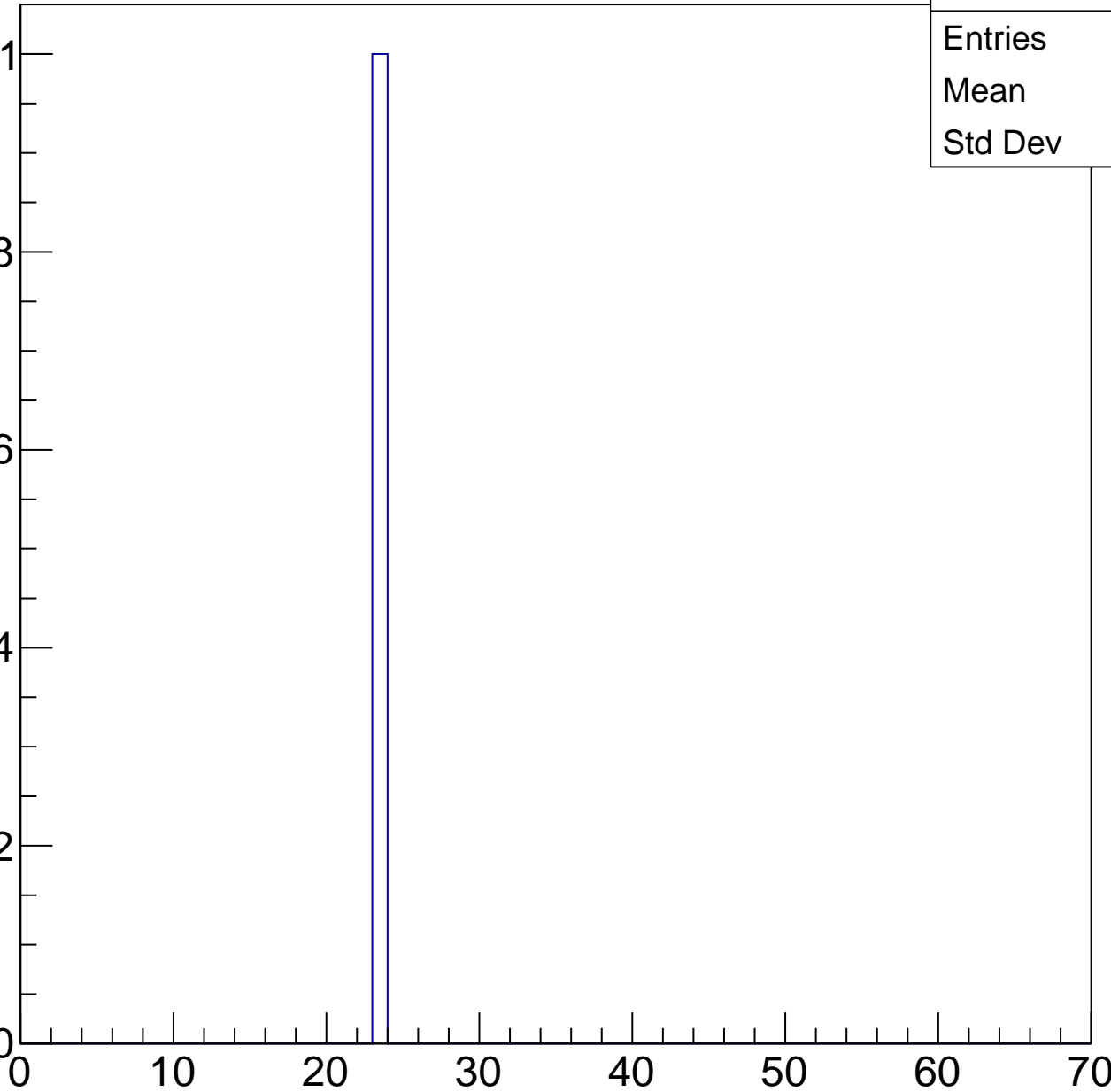
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B0L001S, U19-ch27, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	30.52
Std Dev	3.276

Gaus mean : 30.8865

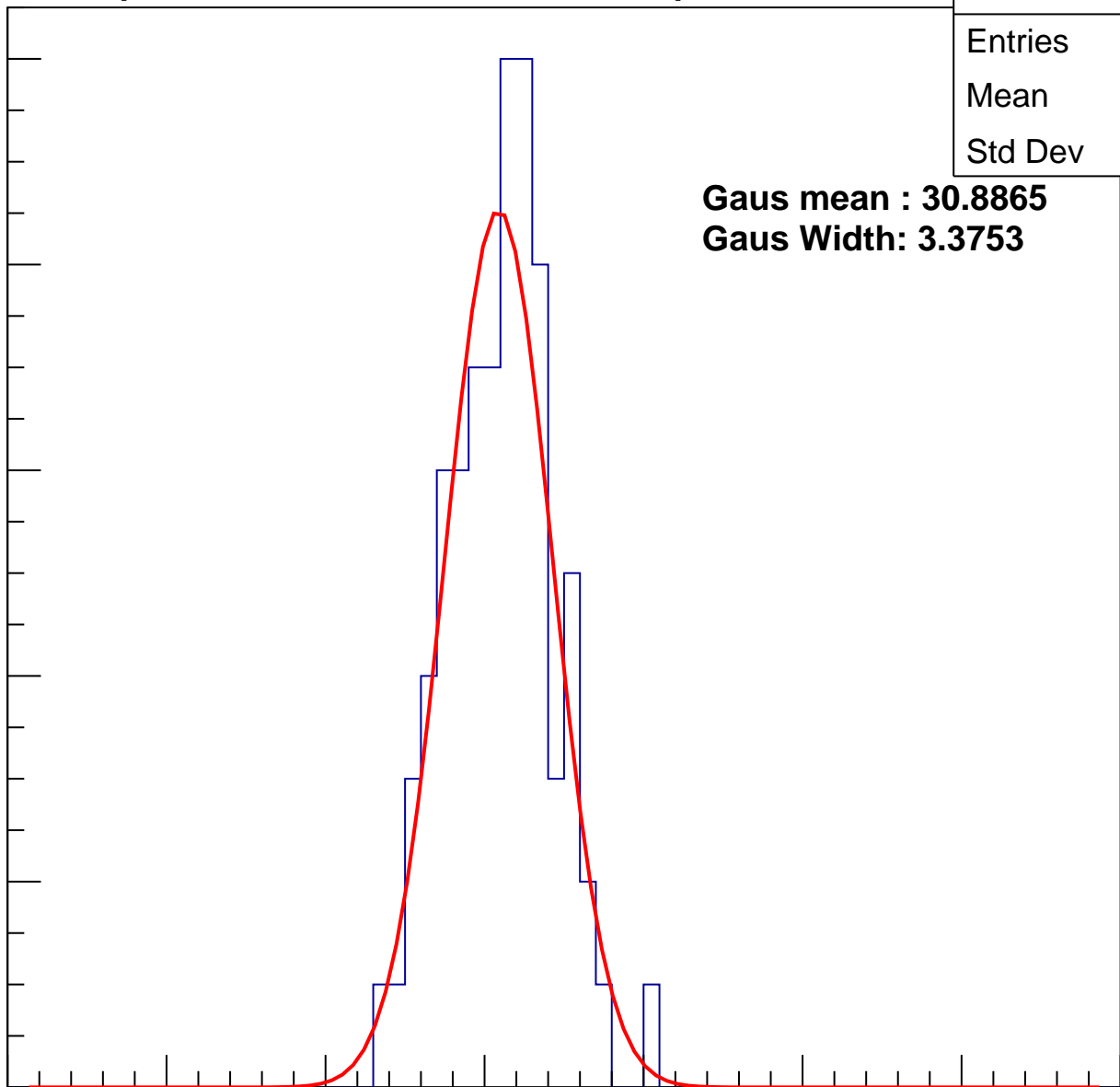
Gaus Width: 3.3753

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch27, adc1

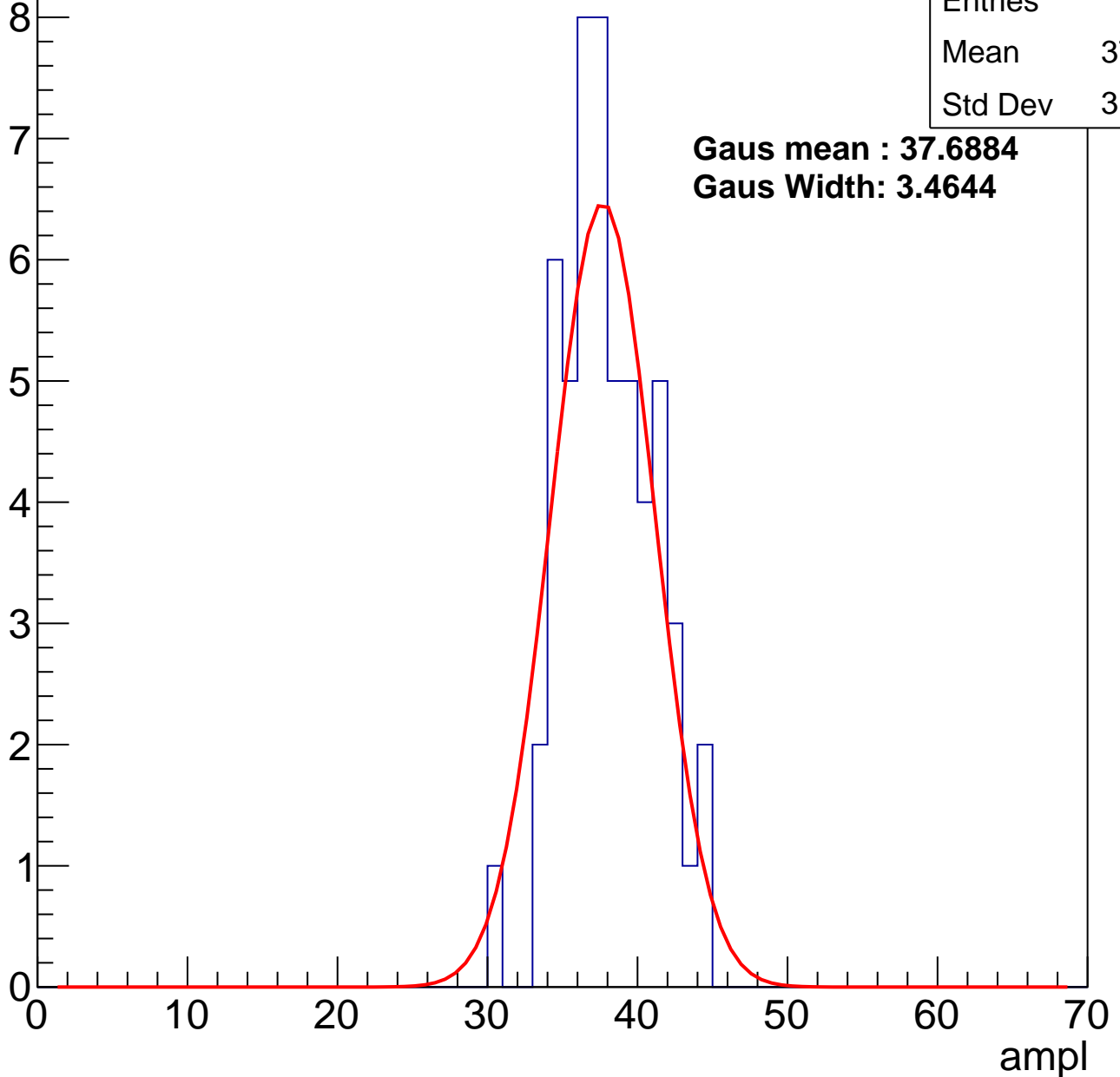
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	37.56
Std Dev	3.002

Gaus mean : 37.6884

Gaus Width: 3.4644



B0L001S, U19-ch27, adc2

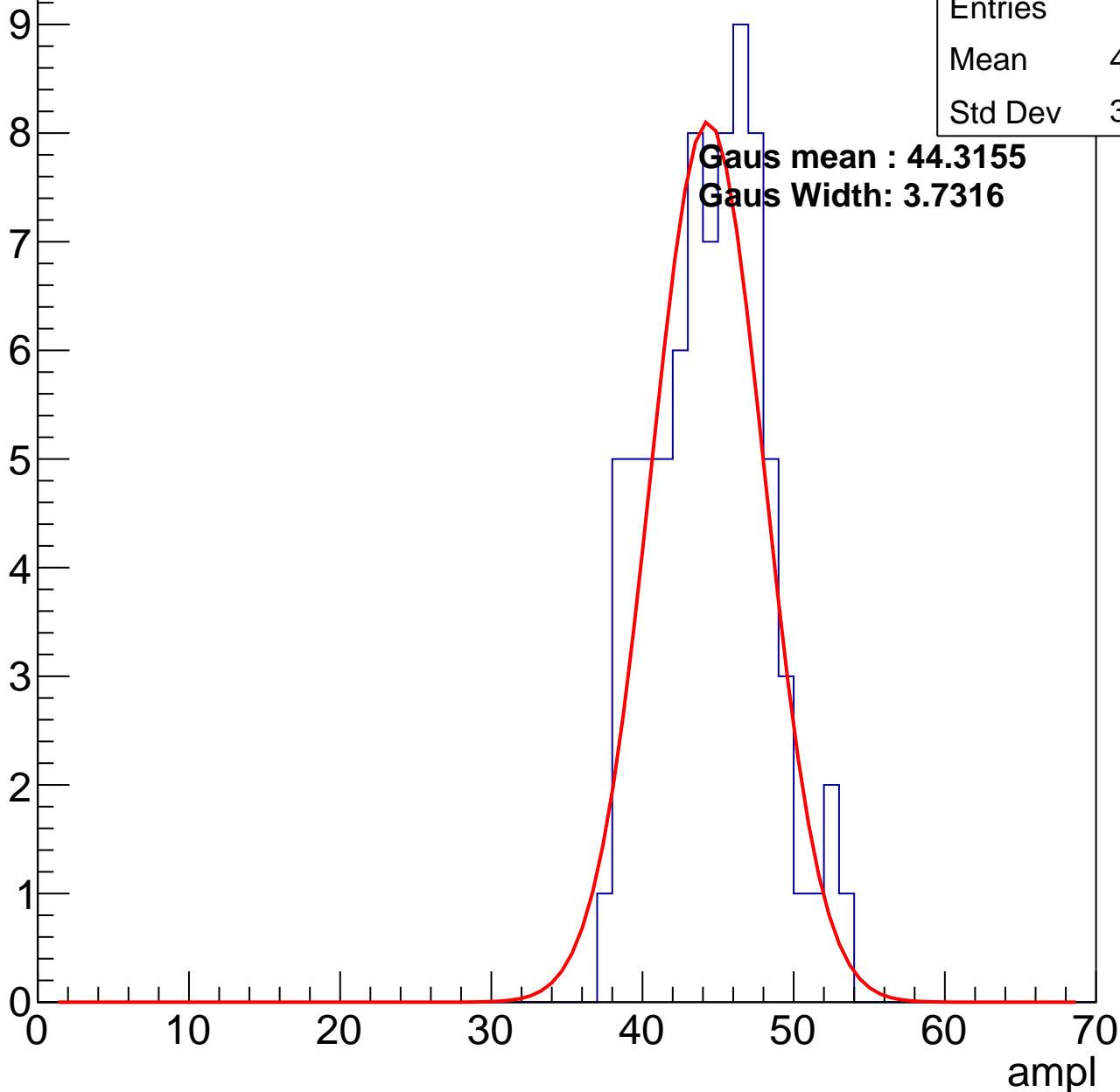
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	44.08
Std Dev	3.667

Gaus mean : 44.3155

Gaus Width: 3.7316

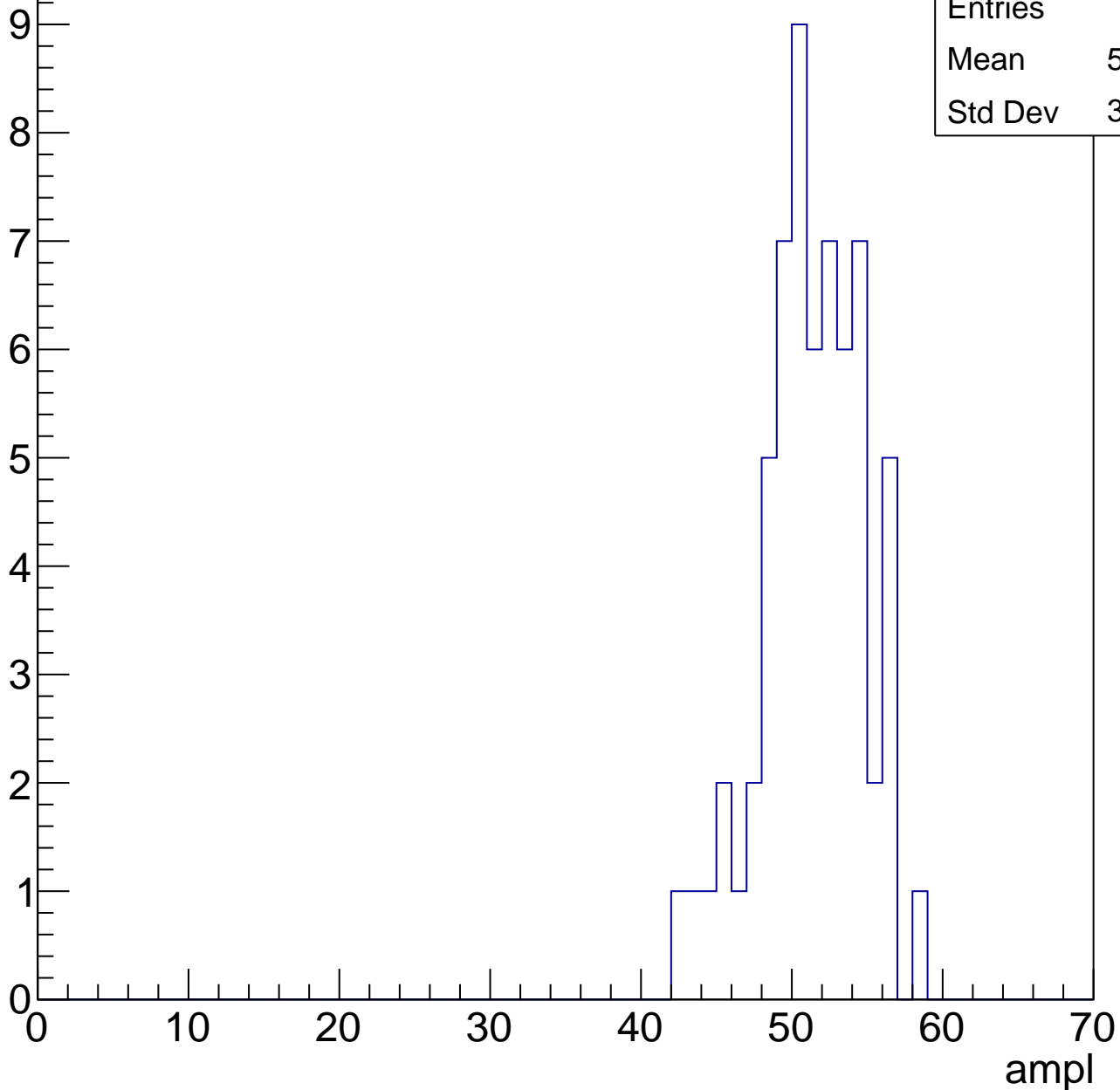


B0L001S, U19-ch27, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

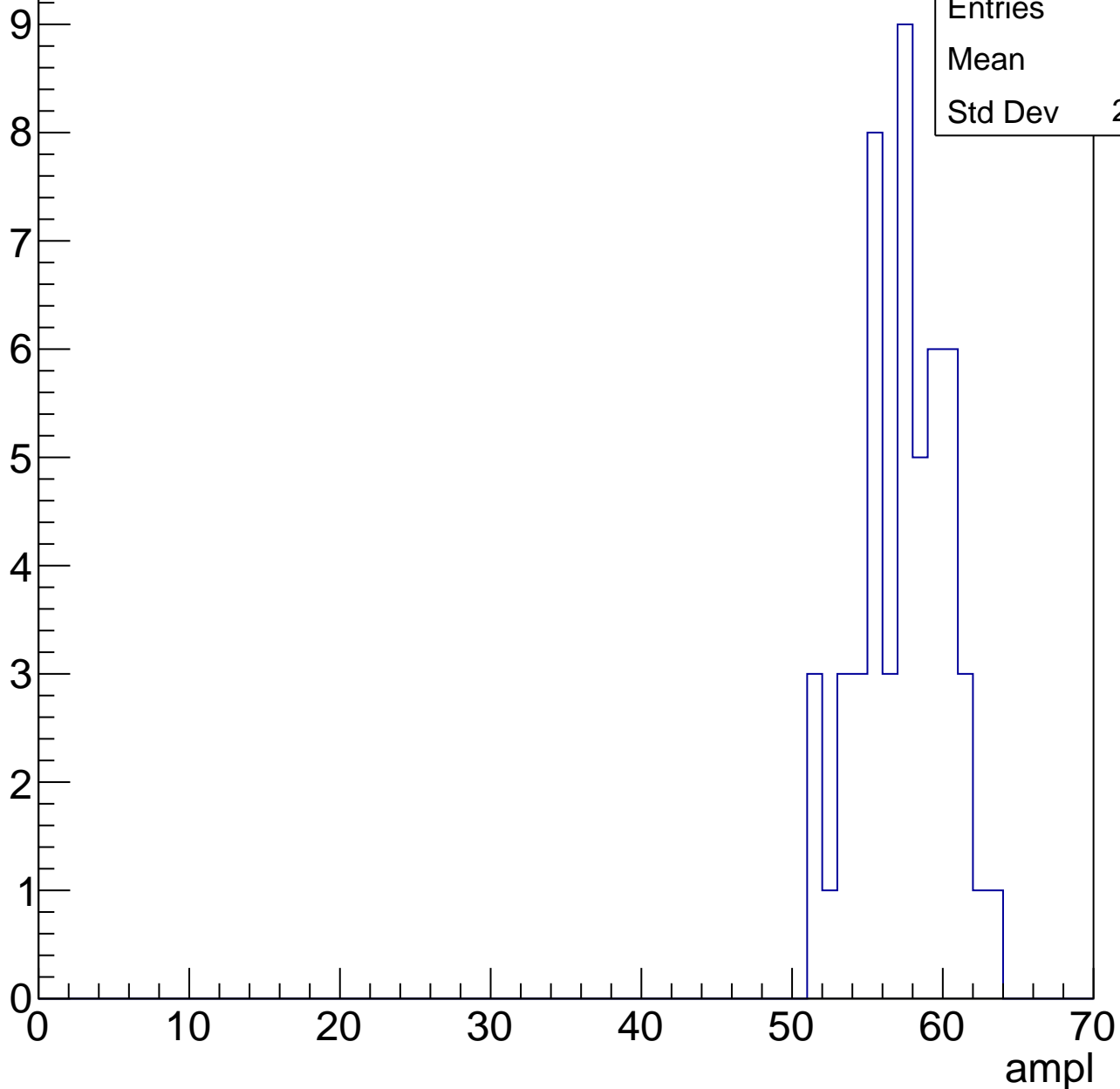
Entries	63
Mean	50.89
Std Dev	3.343



B0L001S, U19-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

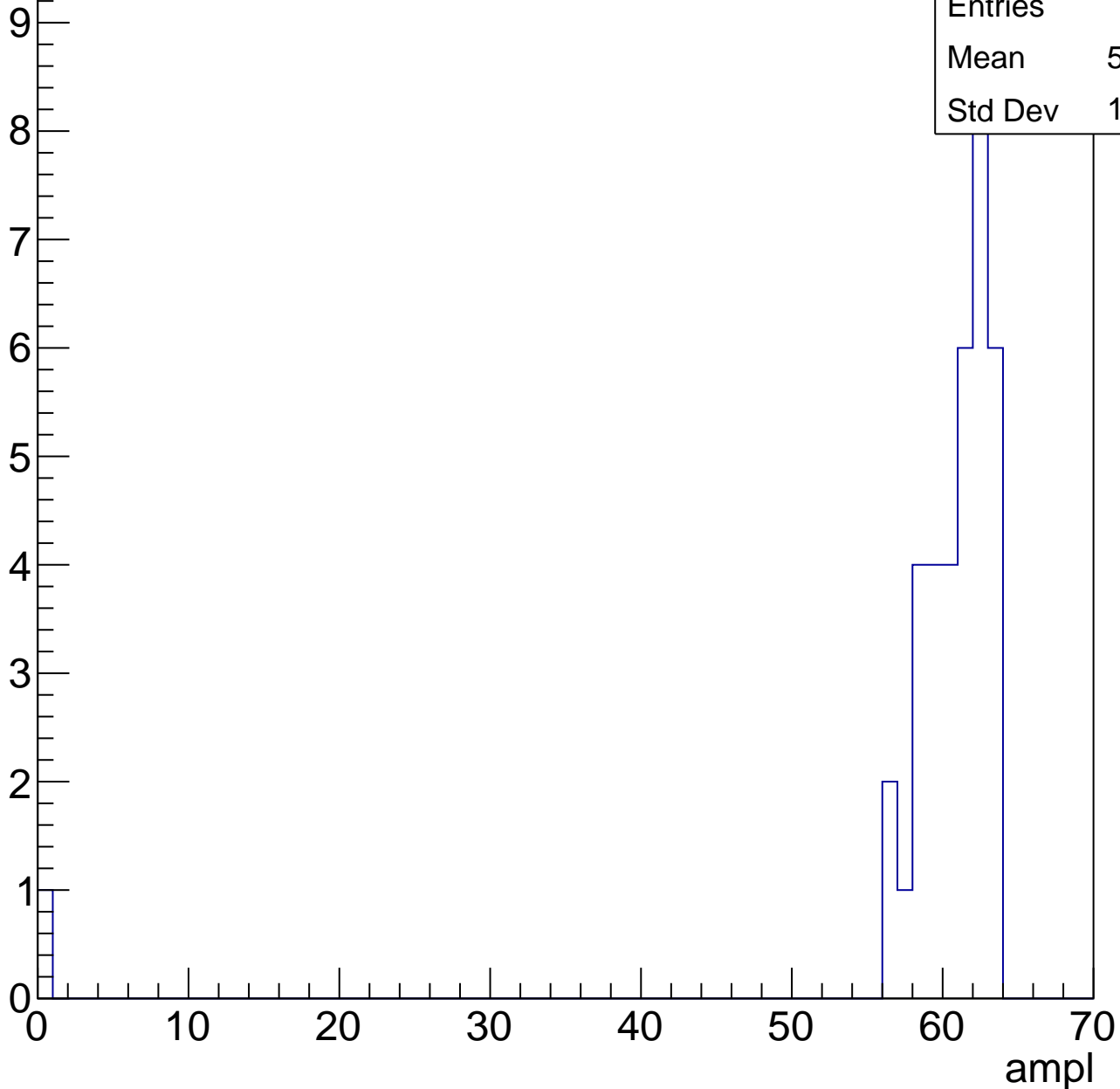


B0L001S, U19-ch27, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.89
Std Dev	10.02



B0L001S, U19-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch27, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch28, adc0

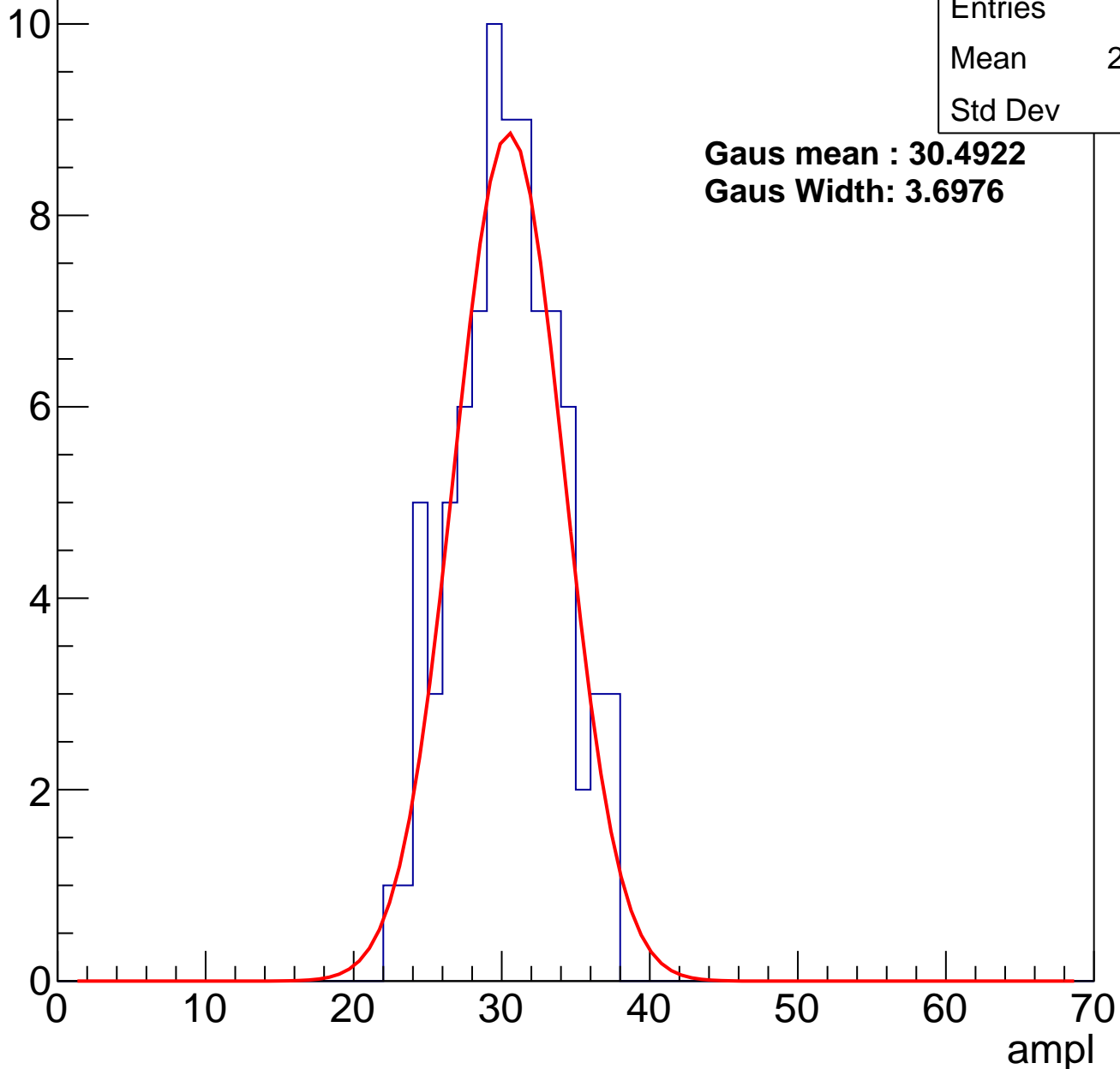
calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	29.94
Std Dev	3.52

Gaus mean : 30.4922

Gaus Width: 3.6976

Entry



B0L001S, U19-ch28, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	37.53
Std Dev	2.993

Gaus mean : 37.9367

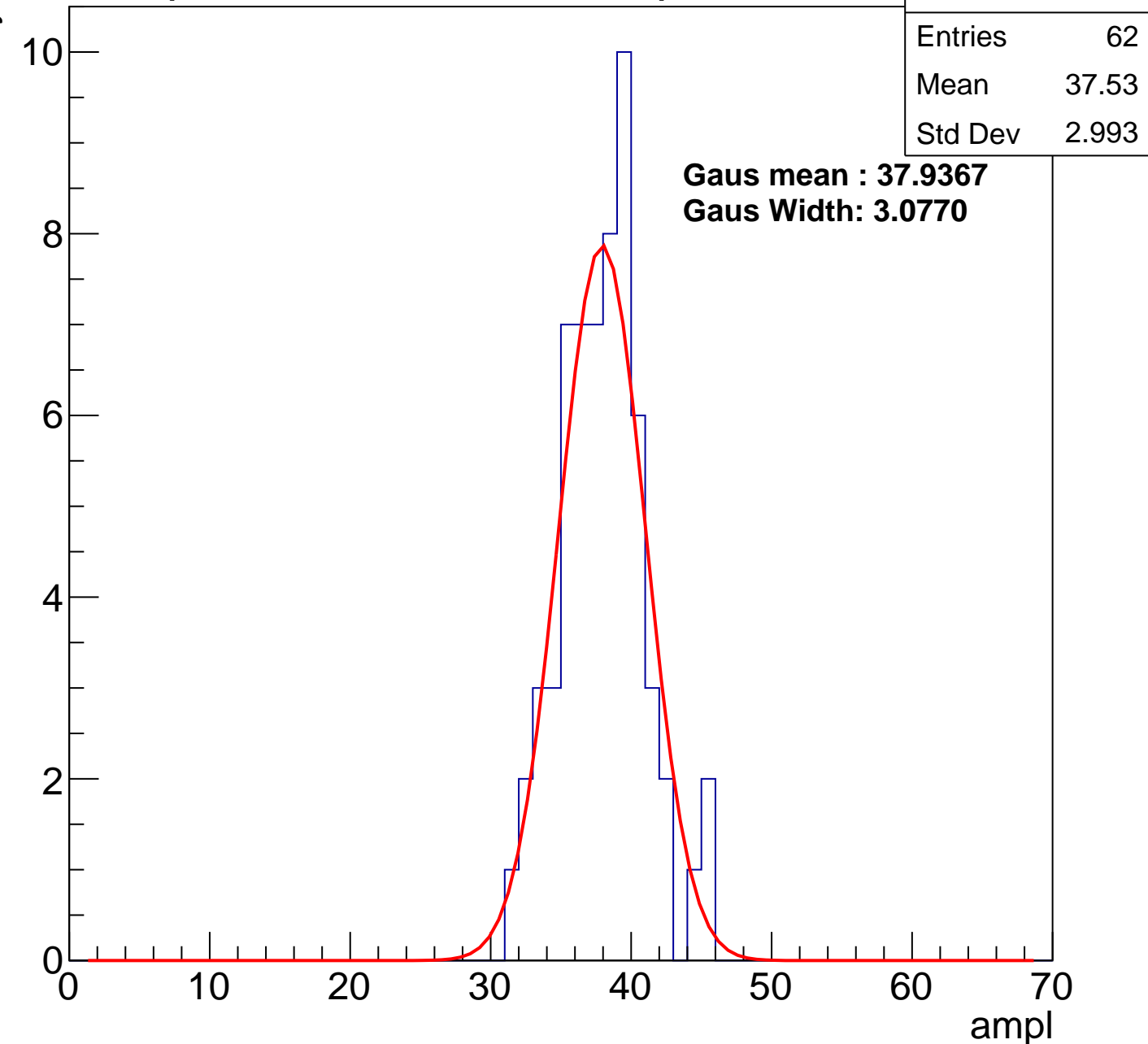
Gaus Width: 3.0770

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch28, adc2

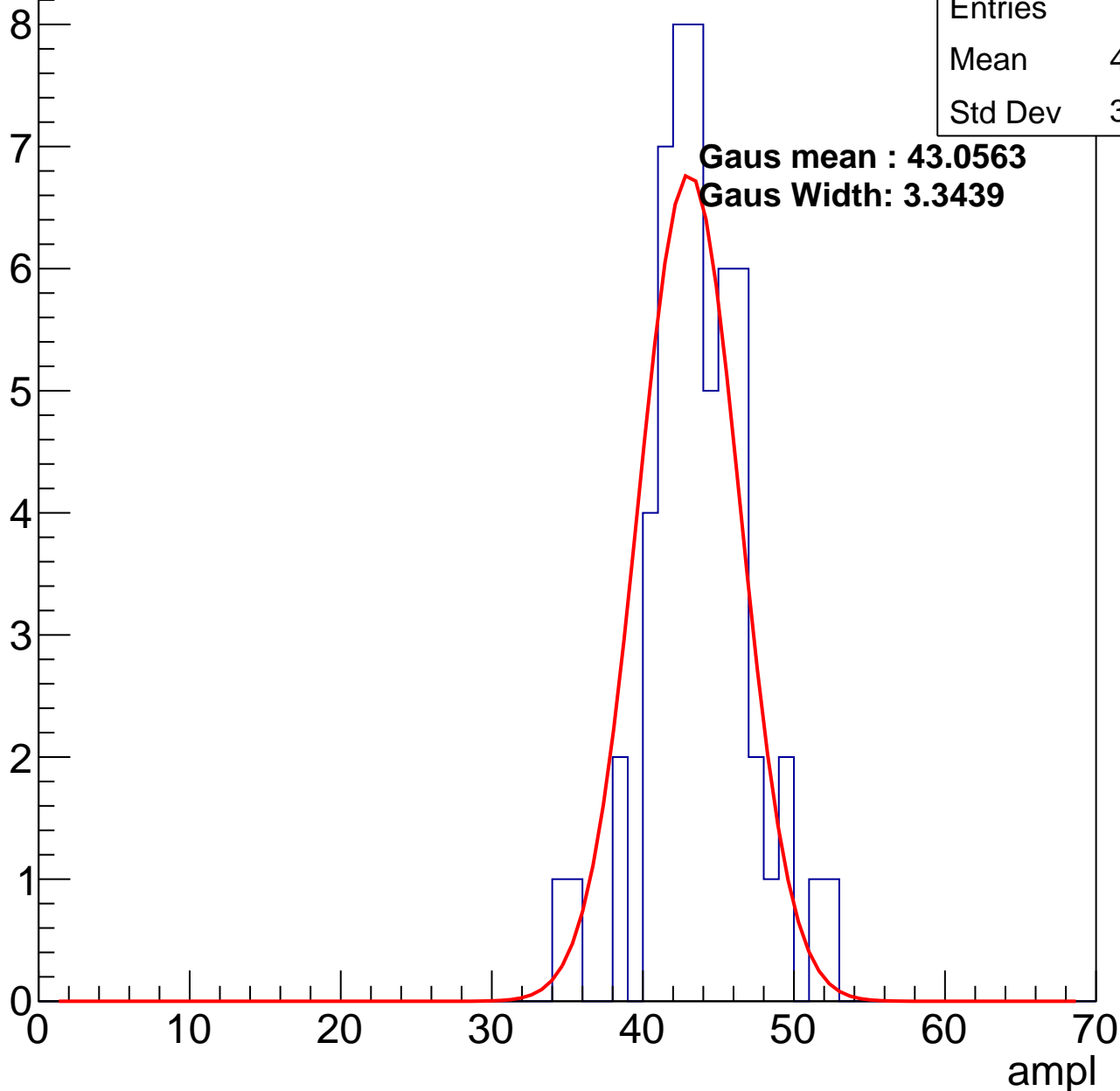
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.29
Std Dev	3.356

Gaus mean : 43.0563

Gaus Width: 3.3439

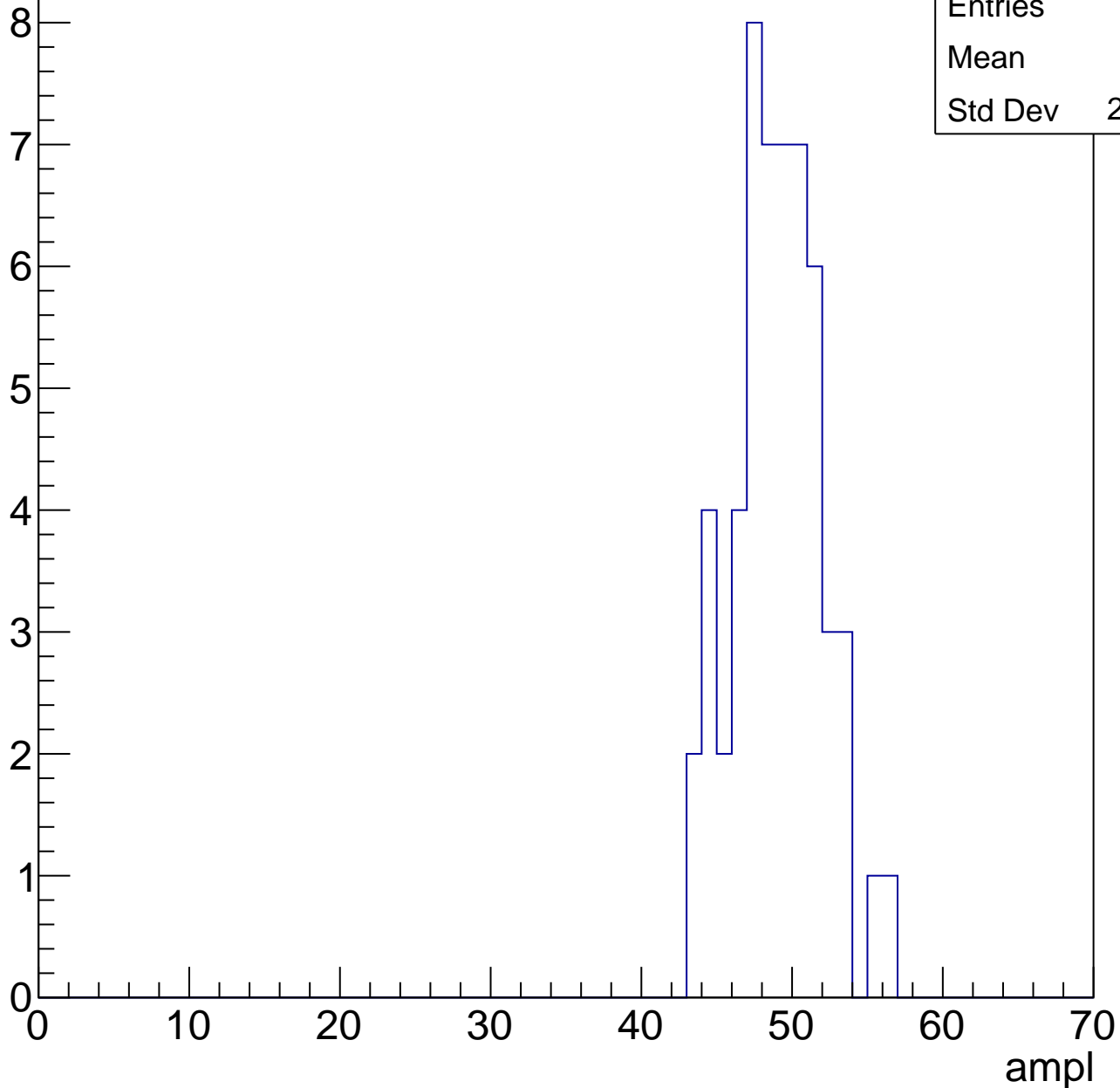


B0L001S, U19-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	48.6
Std Dev	2.896



B0L001S, U19-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	55.6
Std Dev	3.418

Entry

10

8

6

4

2

0

0

10

20

30

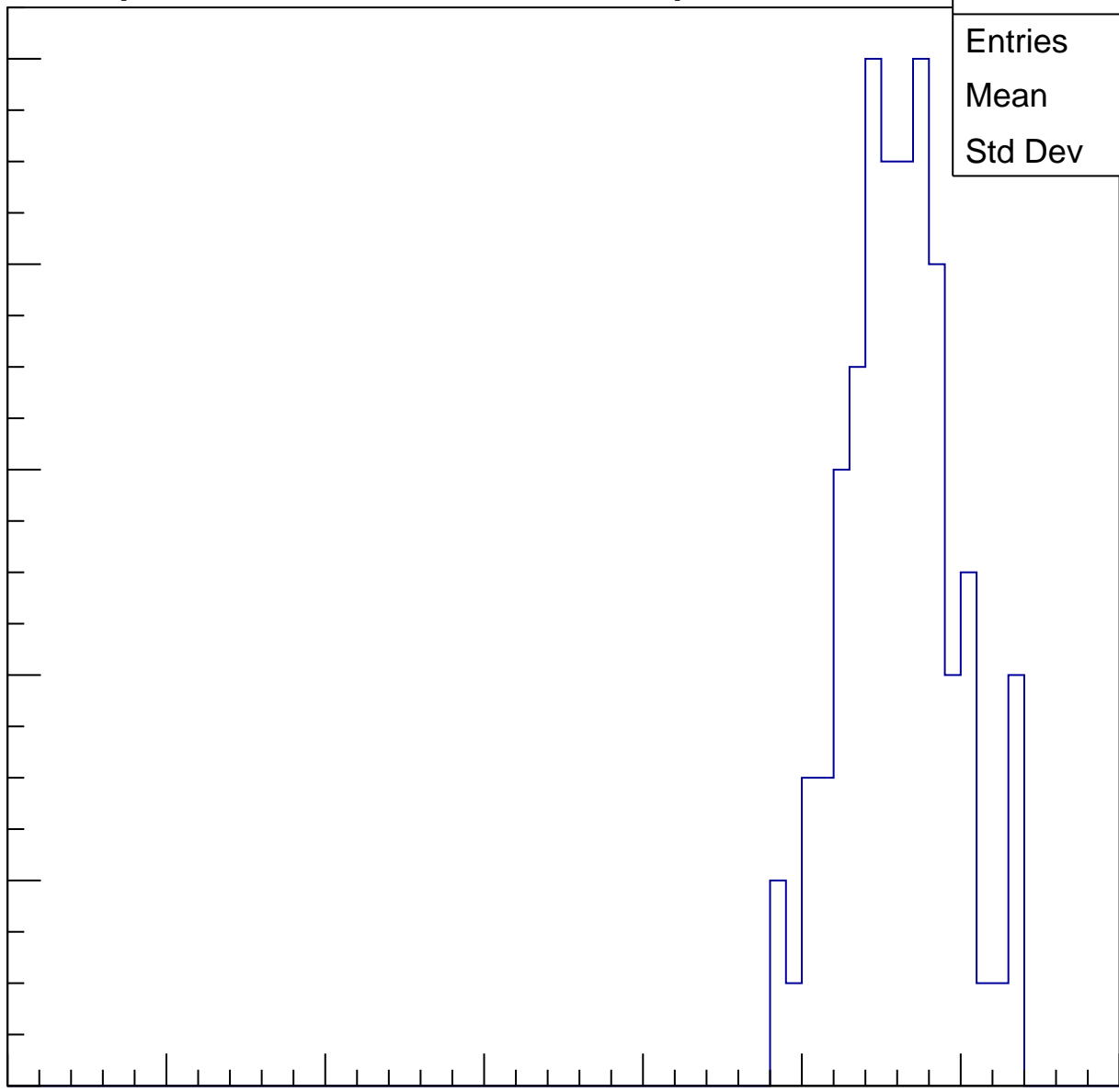
40

50

60

ampl

70

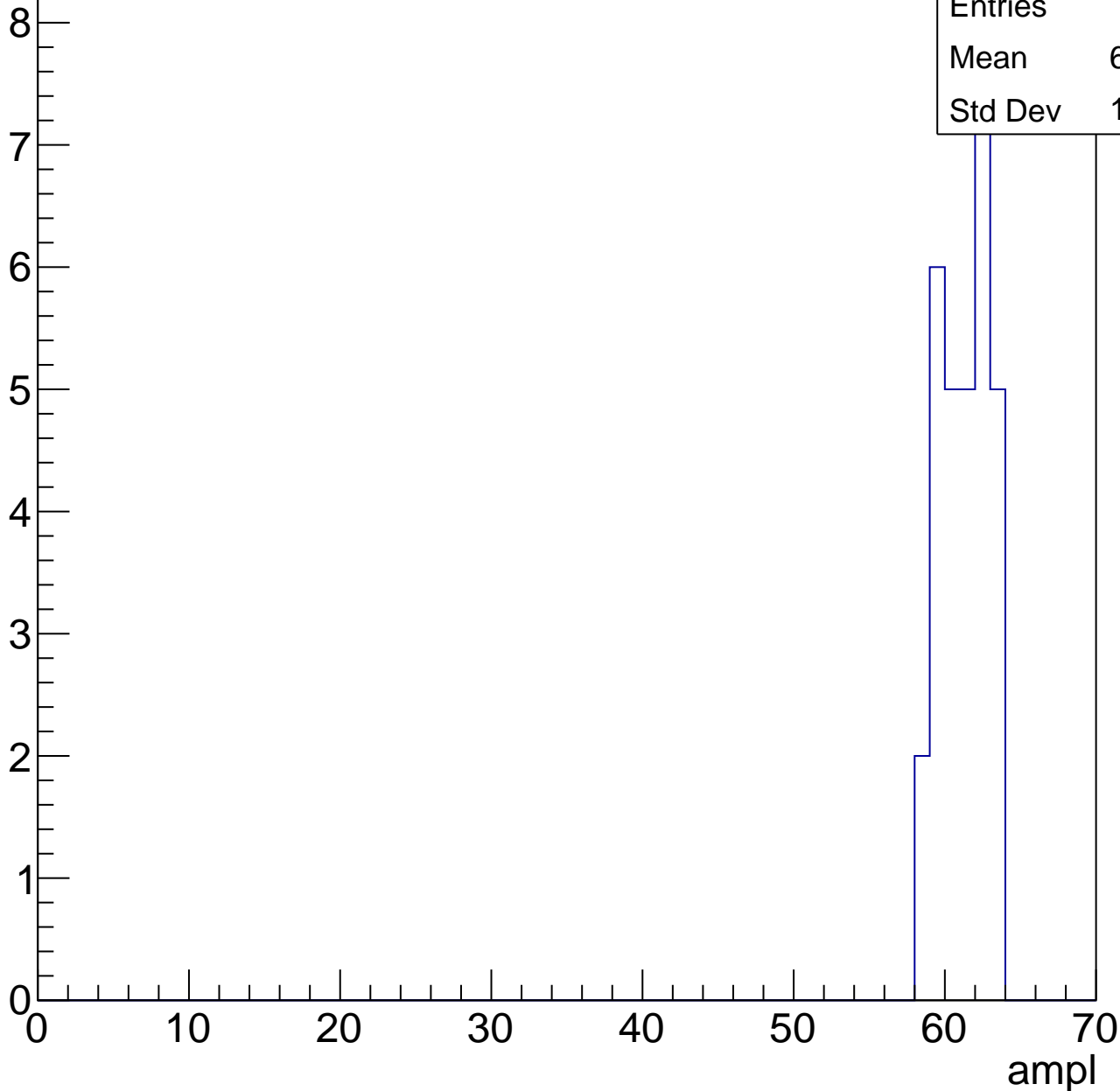


B0L001S, U19-ch28, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

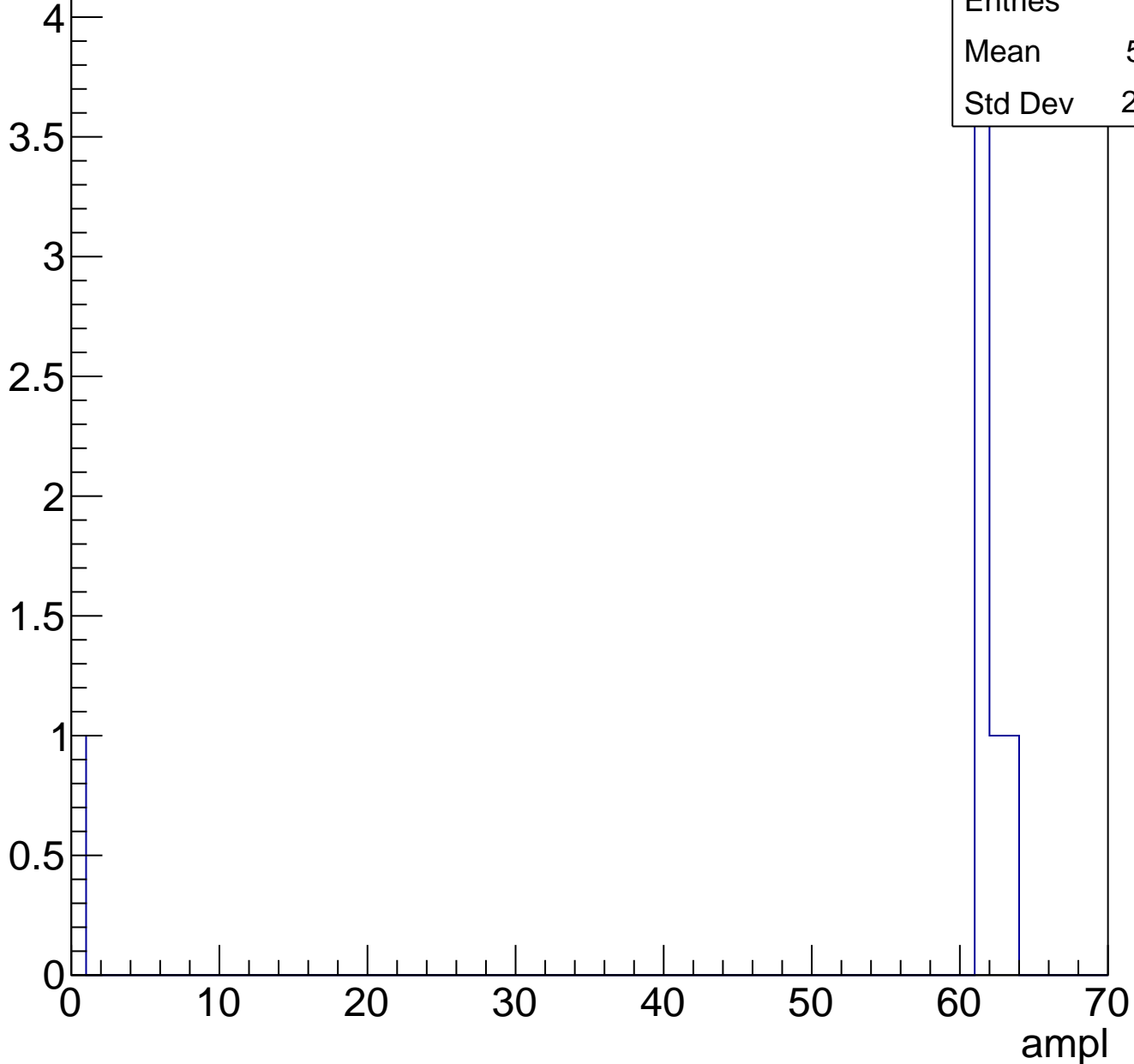
Entries	31
Mean	60.84
Std Dev	1.547



B0L001S, U19-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch29, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	32.29
Std Dev	2.865

Gaus mean : 32.6439

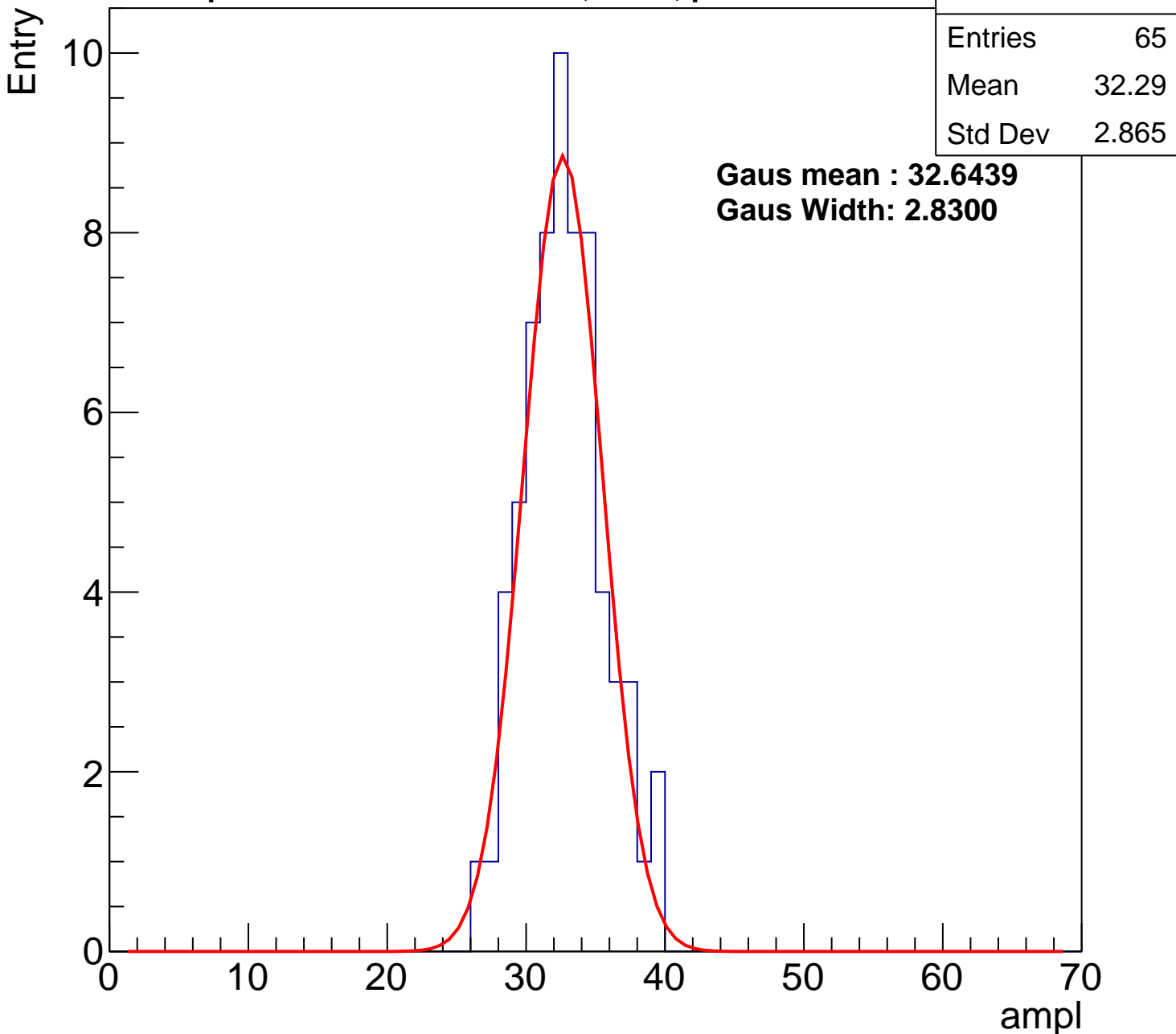
Gaus Width: 2.8300

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch29, adc1

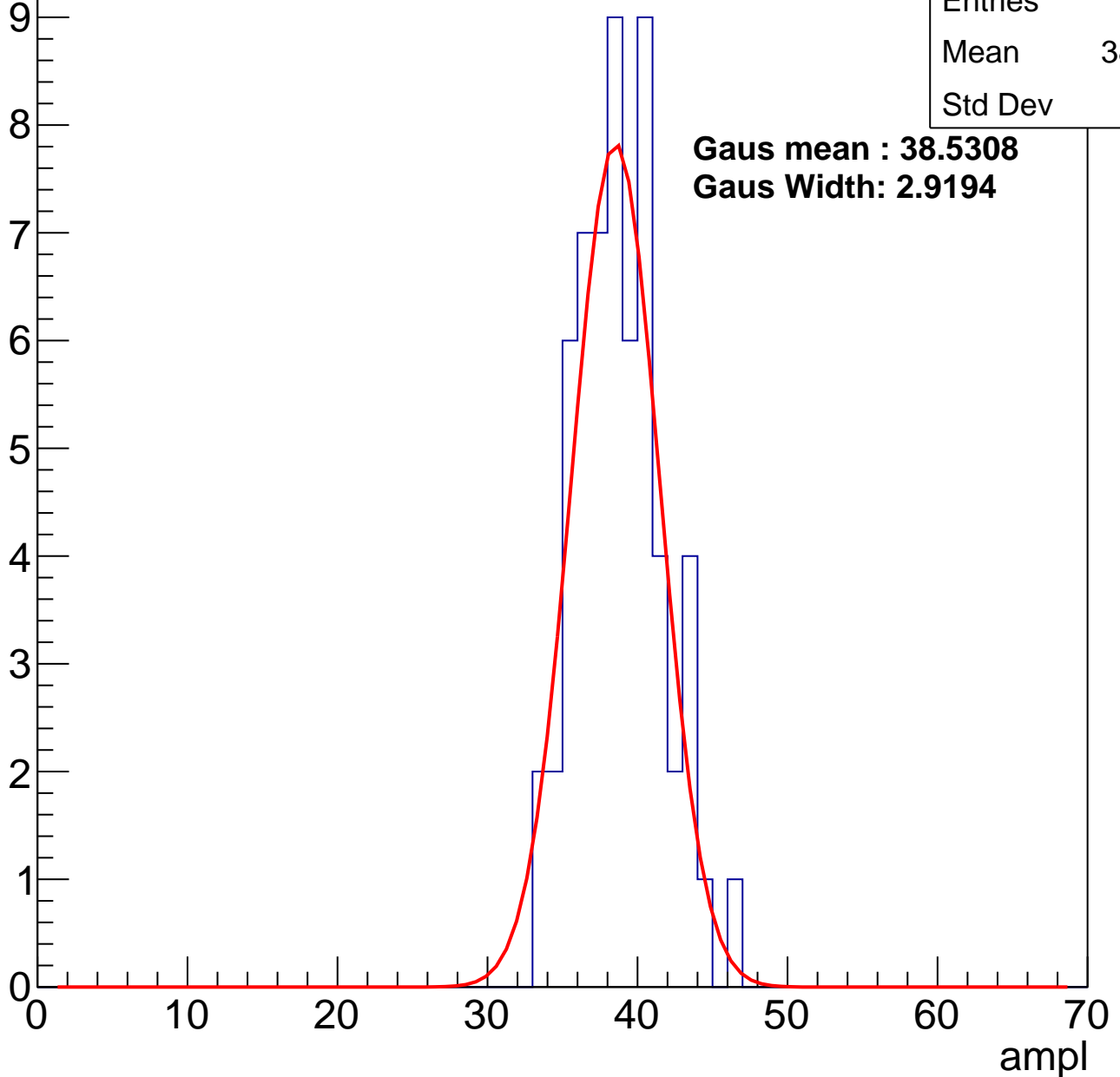
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.35
Std Dev	2.81

Gaus mean : 38.5308

Gaus Width: 2.9194



B0L001S, U19-ch29, adc2

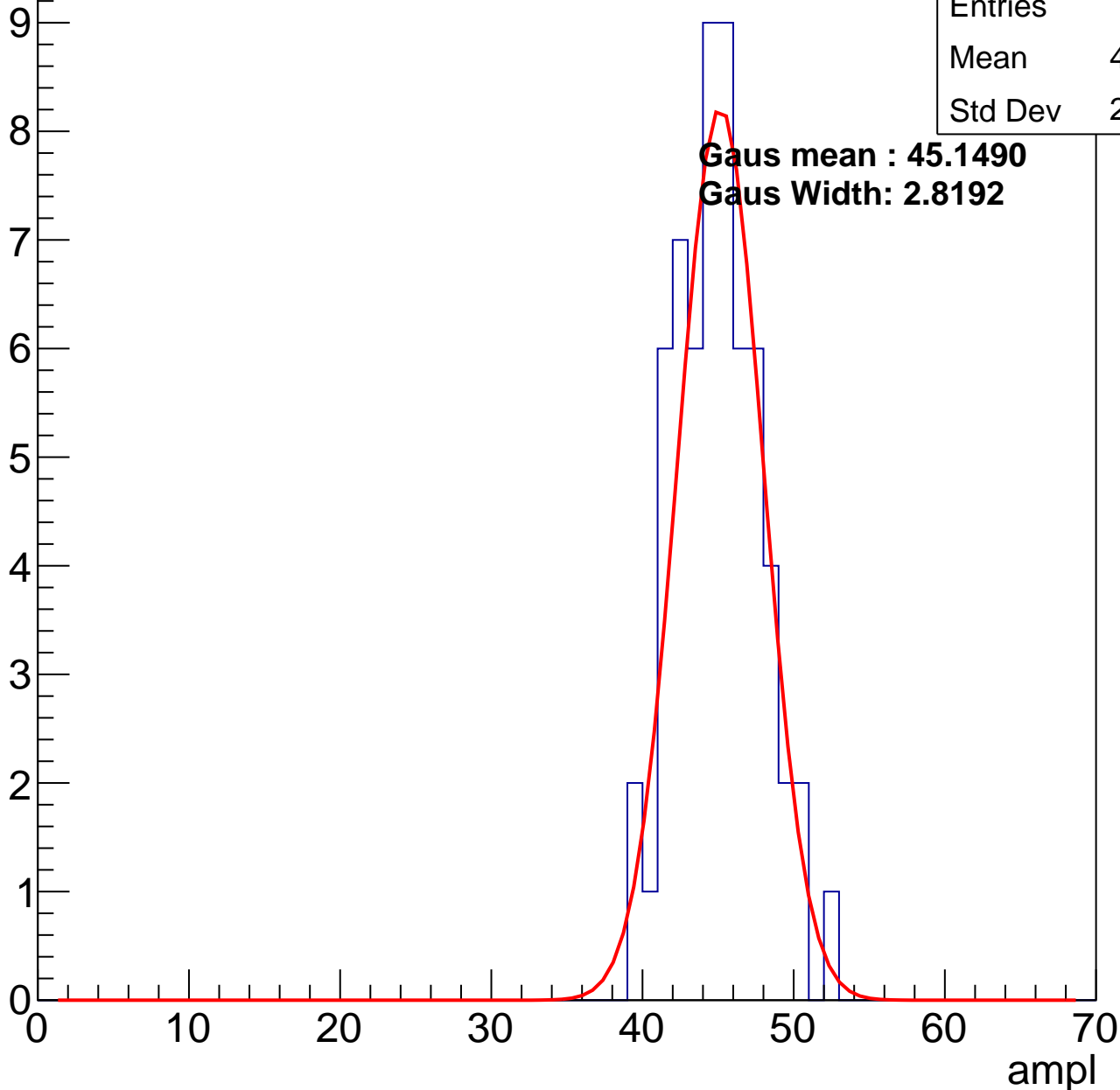
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.54
Std Dev	2.785

Gaus mean : 45.1490

Gaus Width: 2.8192



B0L001S, U19-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	51.19
Std Dev	3.344

Entry

10

8

6

4

2

0

0

10

20

30

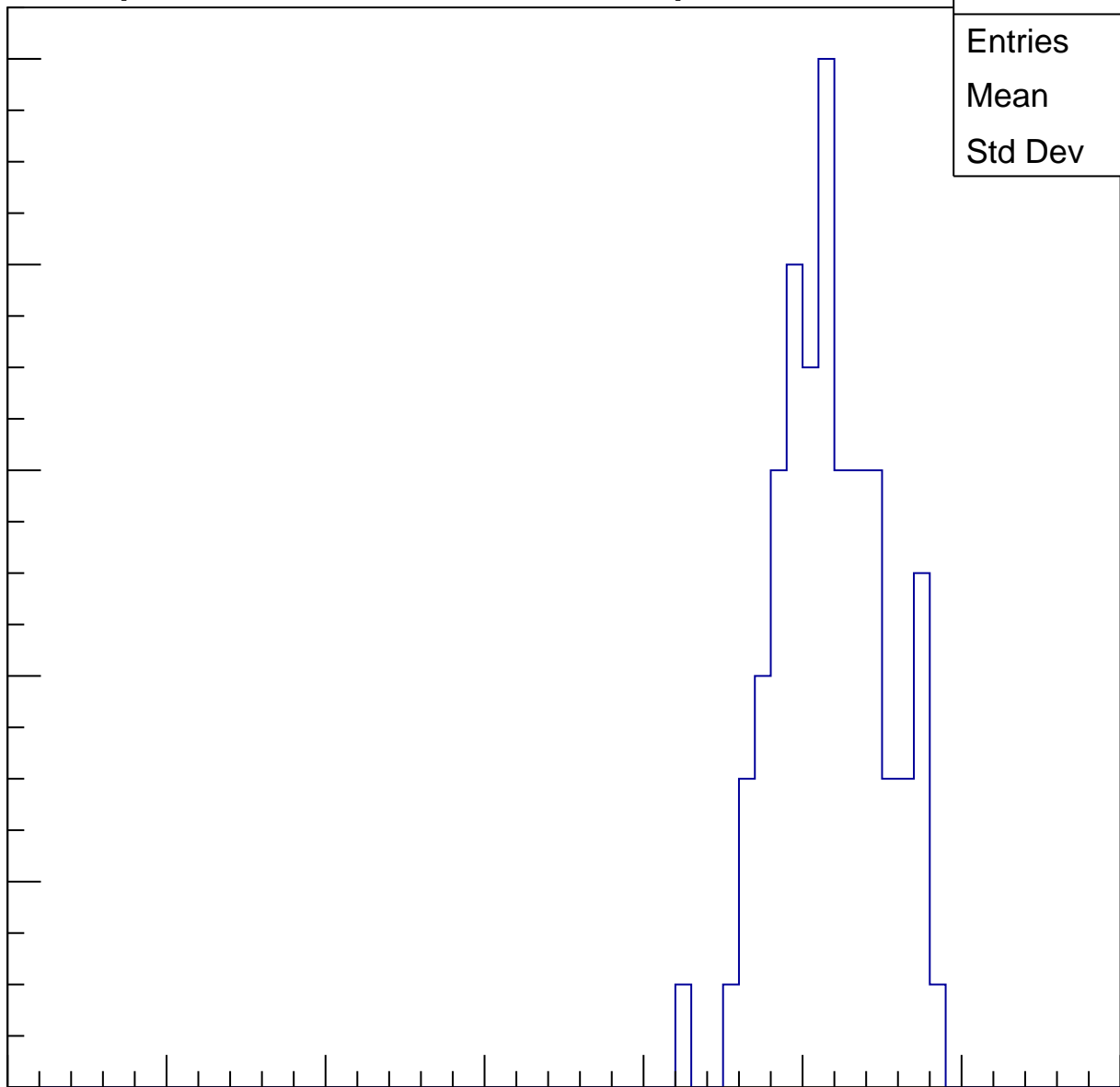
40

50

60

70

ampl

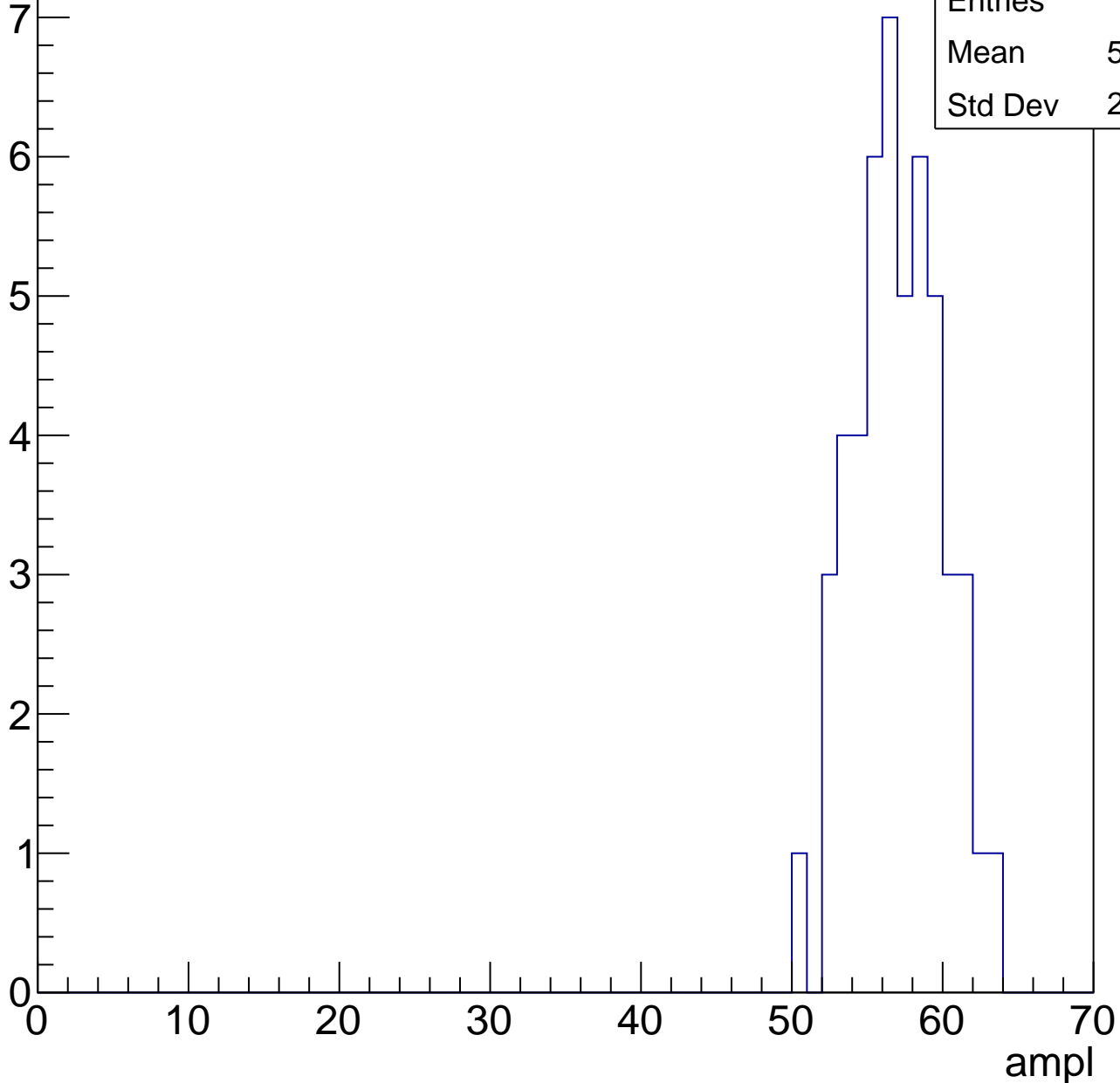


B0L001S, U19-ch29, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	56.57
Std Dev	2.886

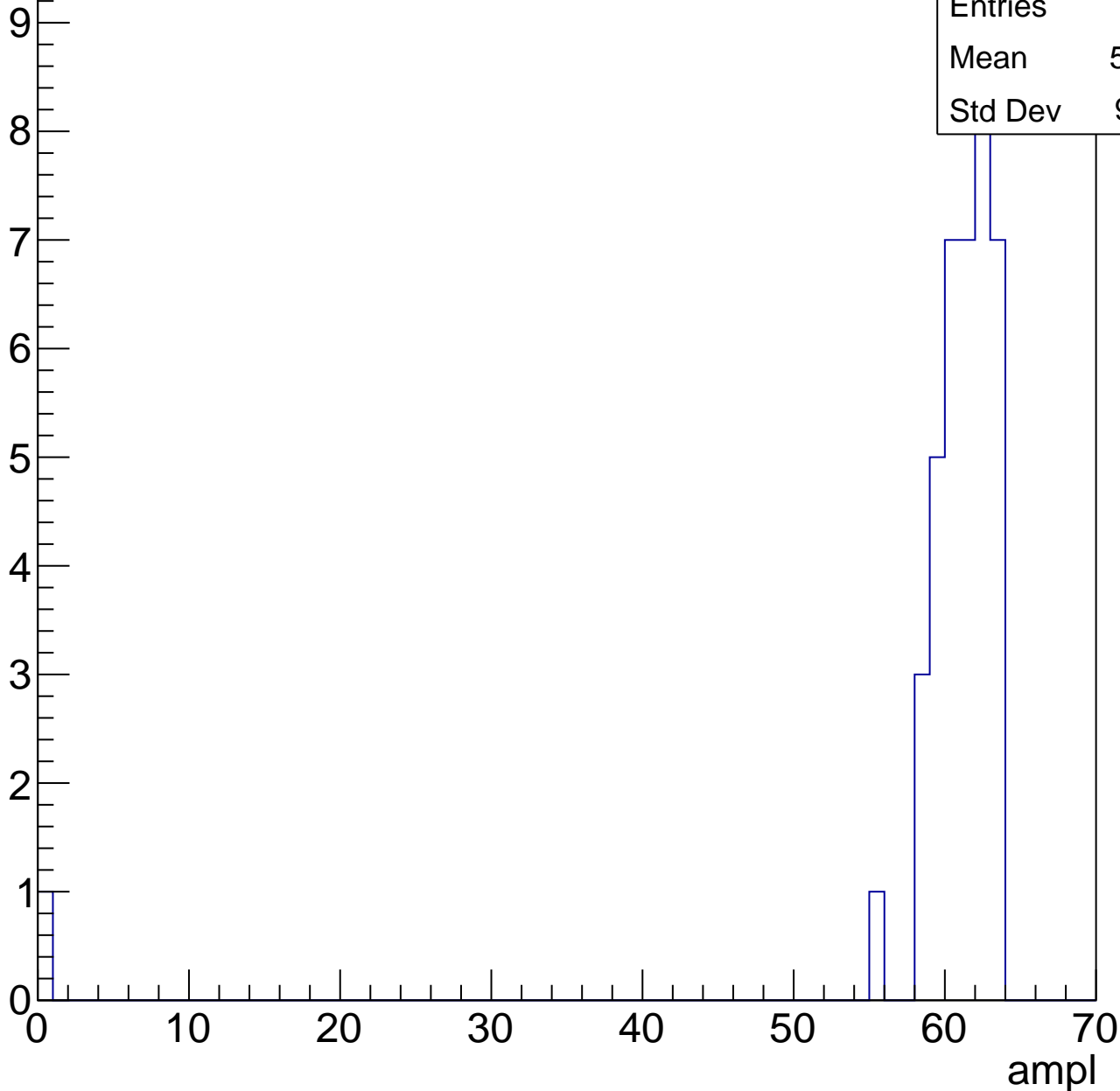


B0L001S, U19-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	59.25
Std Dev	9.651



B0L001S, U19-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L001S, U19-ch29, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch30, adc0

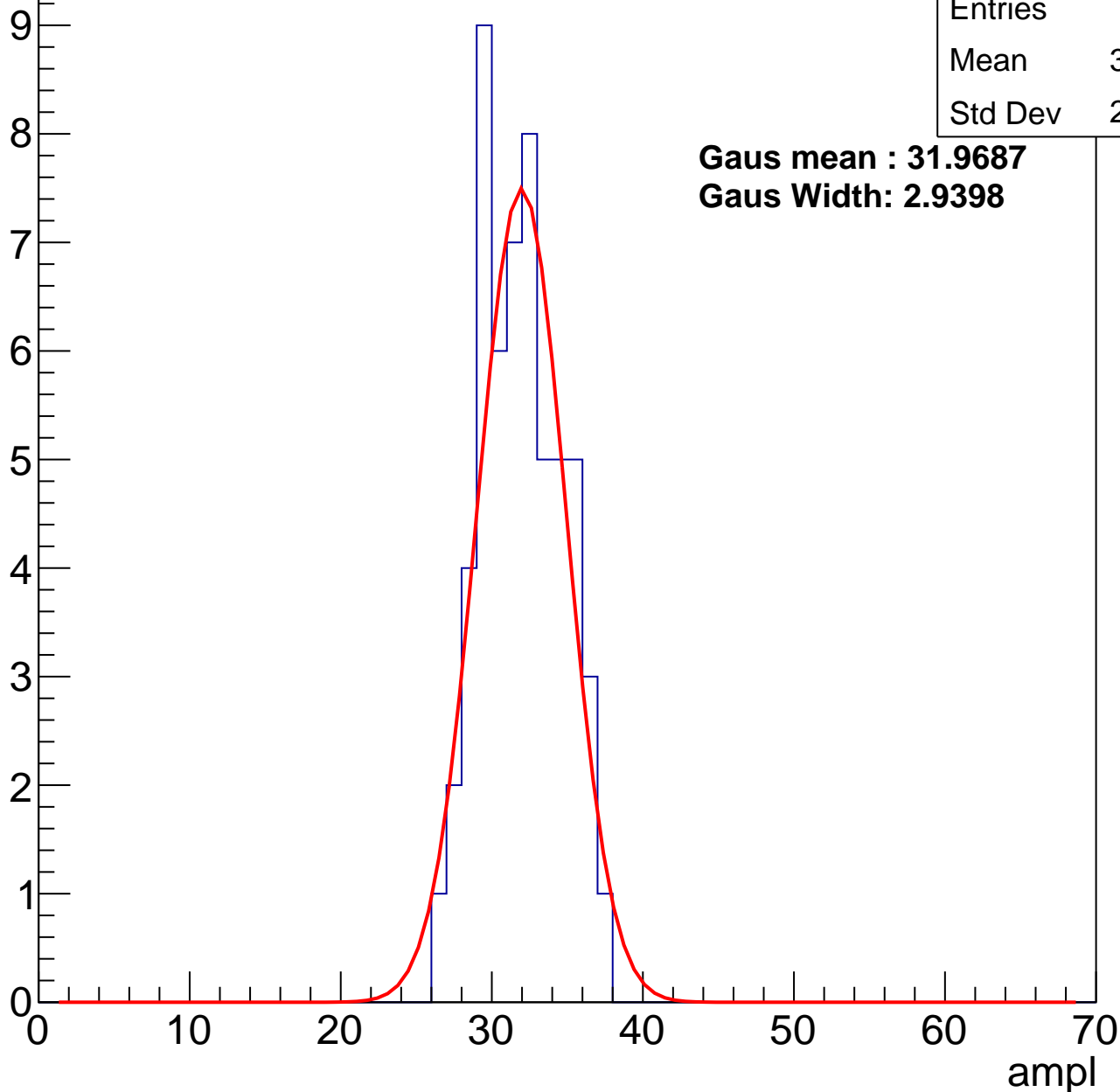
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	31.45
Std Dev	2.632

Gaus mean : 31.9687

Gaus Width: 2.9398



B0L001S, U19-ch30, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	37.51
Std Dev	2.914

Gaus mean : 38.0321

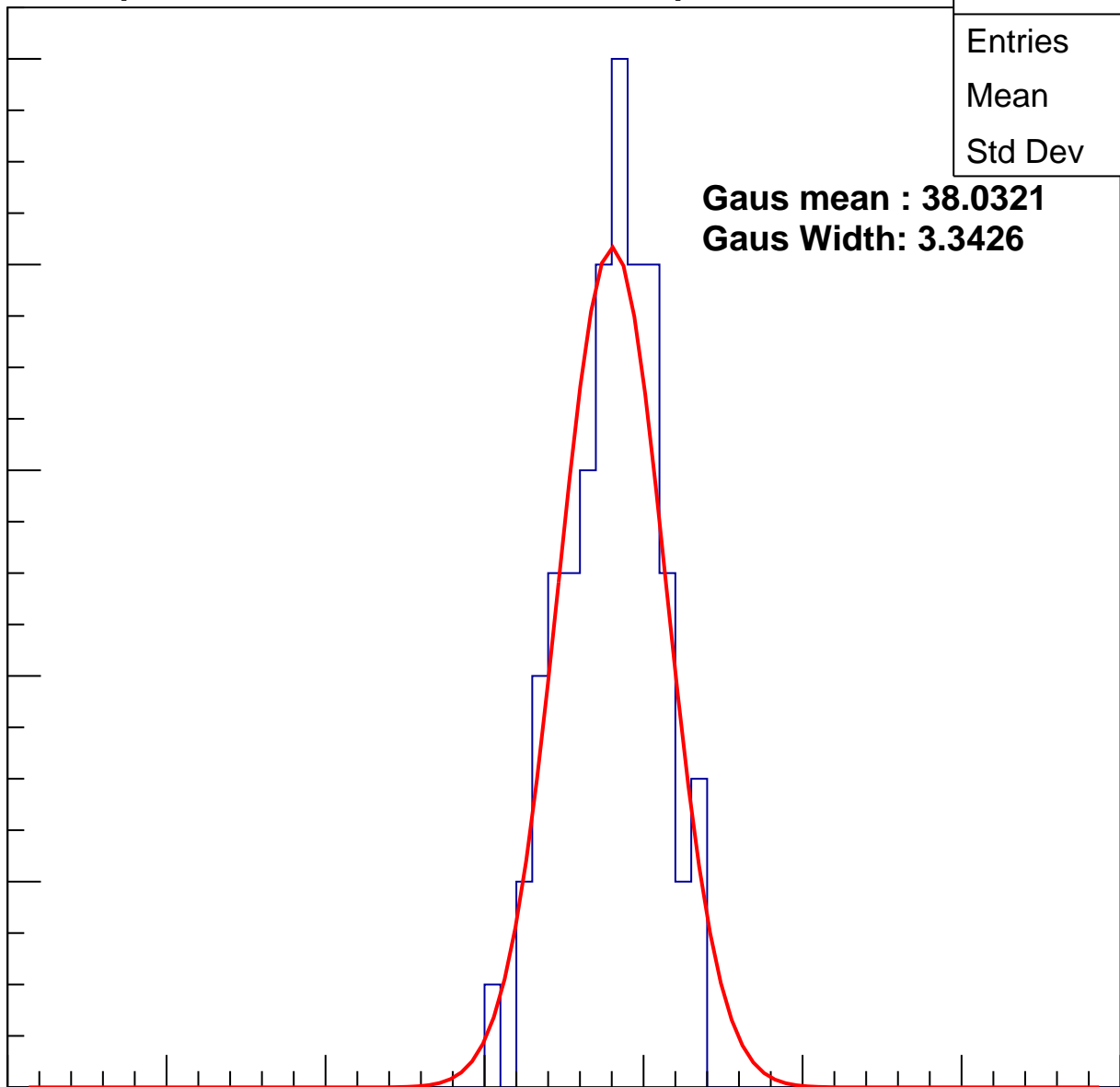
Gaus Width: 3.3426

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch30, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	44.82
Std Dev	3.181

Gaus mean : 45.4630

Gaus Width: 3.3695

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

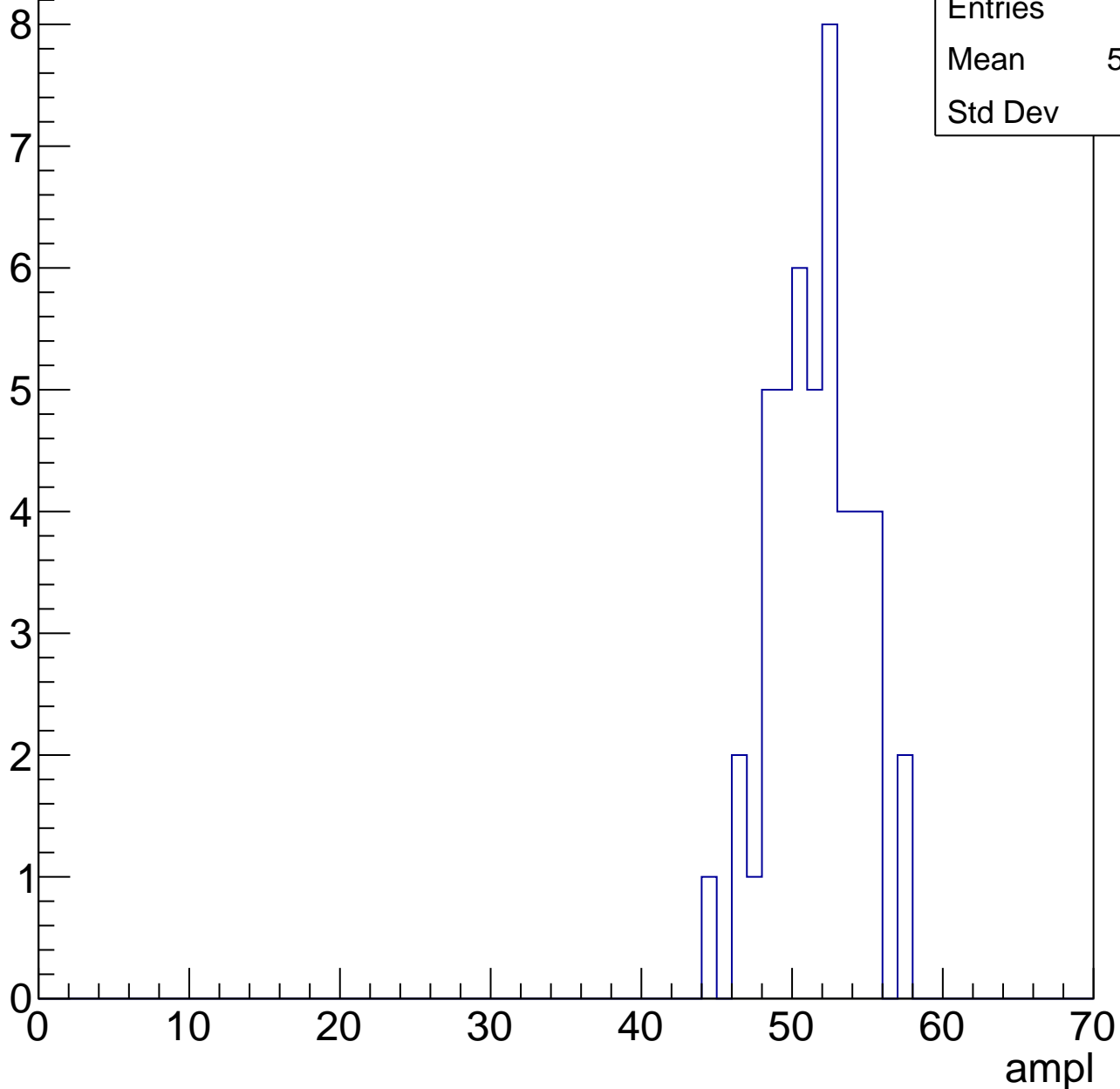
10

B0L001S, U19-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	51.09
Std Dev	2.85

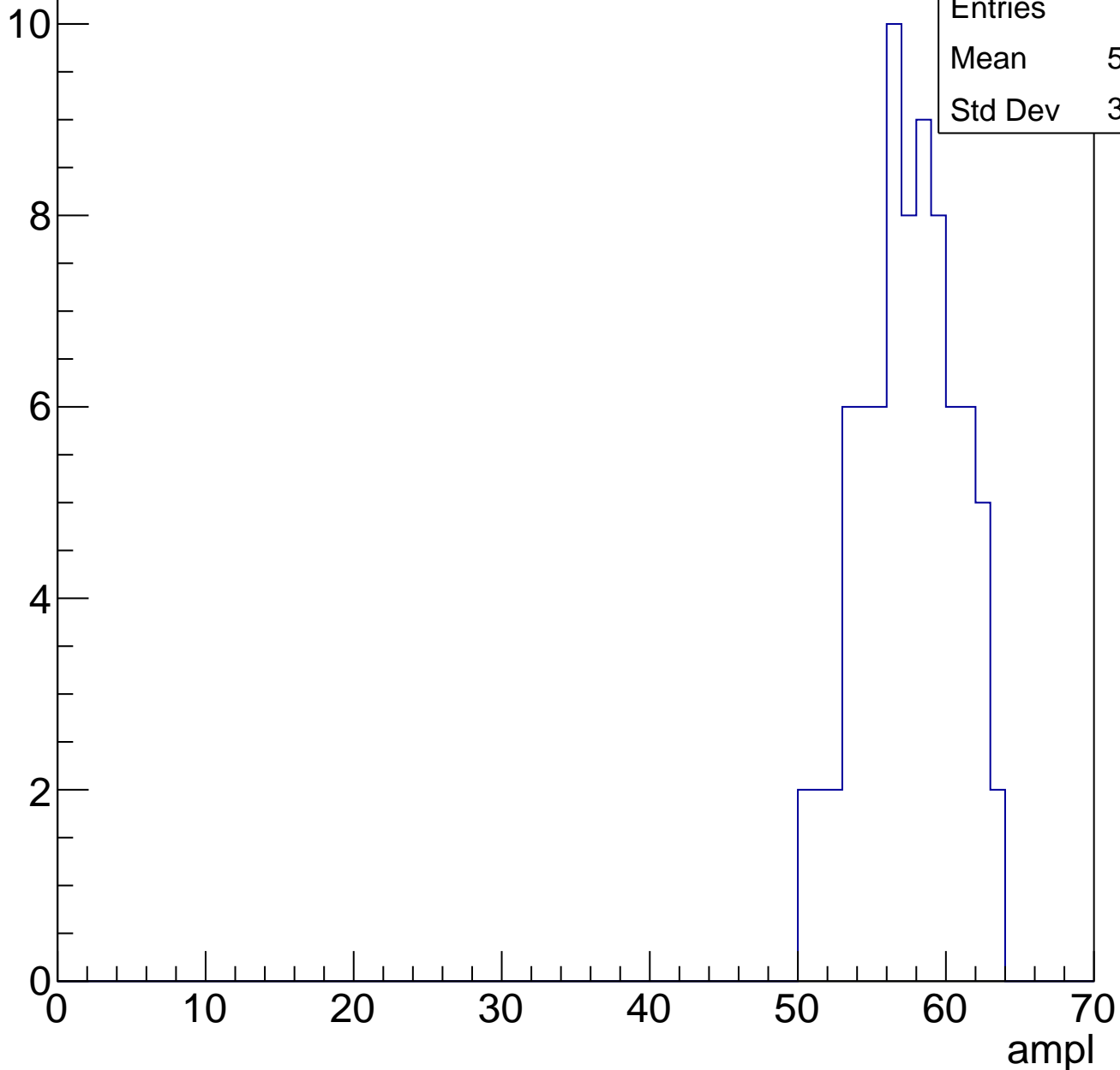


B0L001S, U19-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	57.05
Std Dev	3.186

Entry

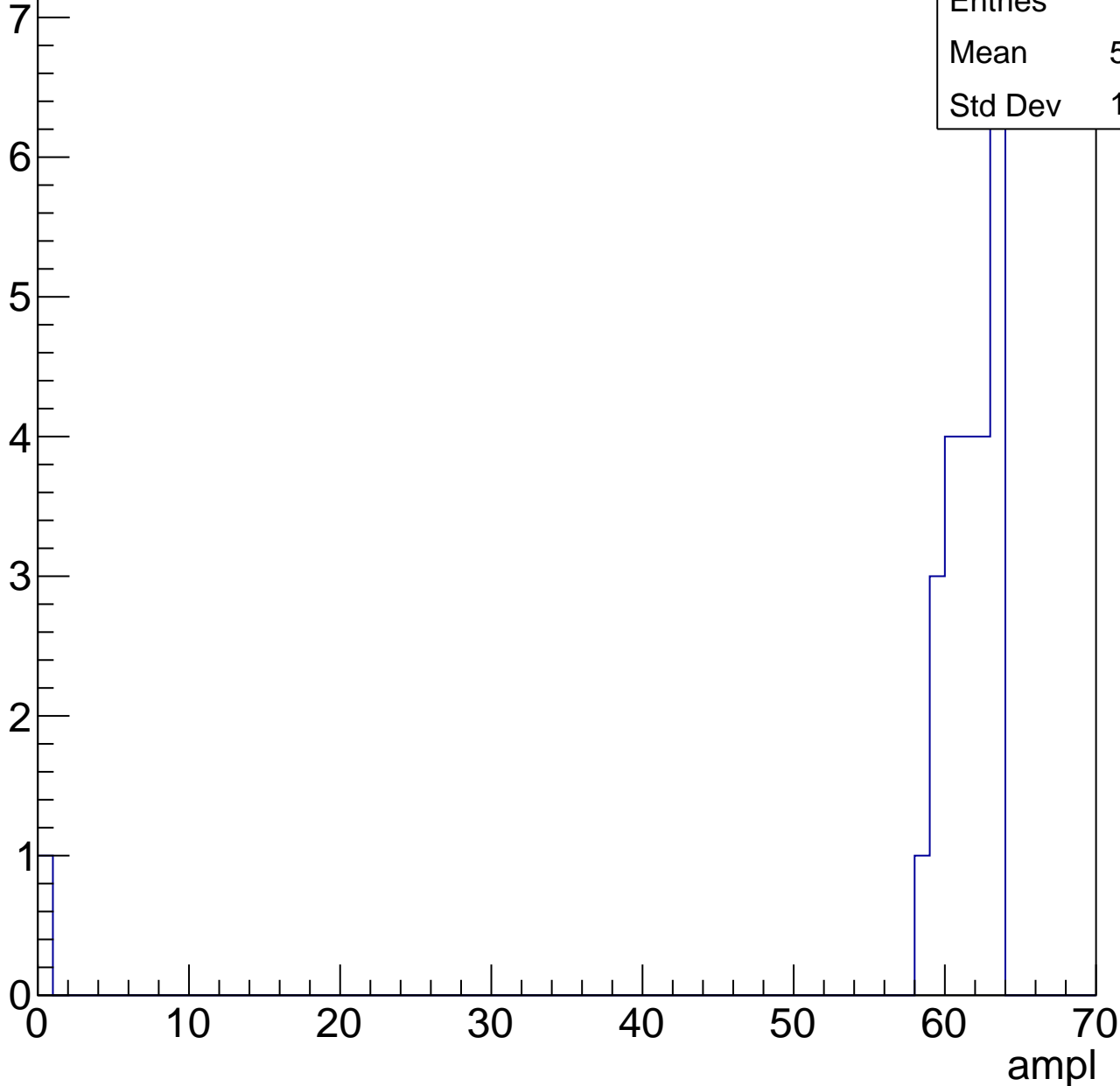


B0L001S, U19-ch30, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	58.67
Std Dev	12.33



B0L001S, U19-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

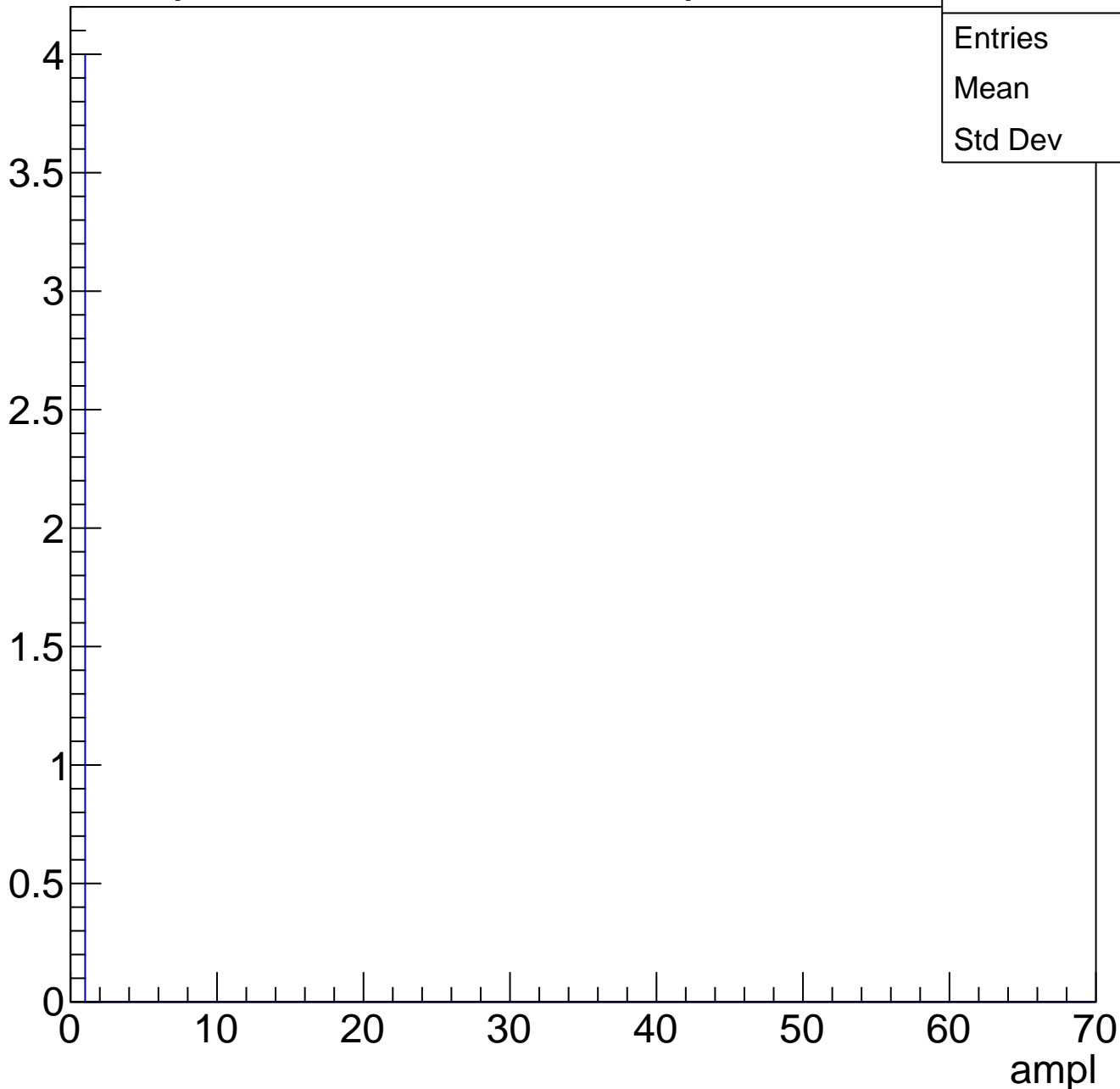
Entry



B0L001S, U19-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	0
Std Dev	0

B0L001S, U19-ch31, adc0

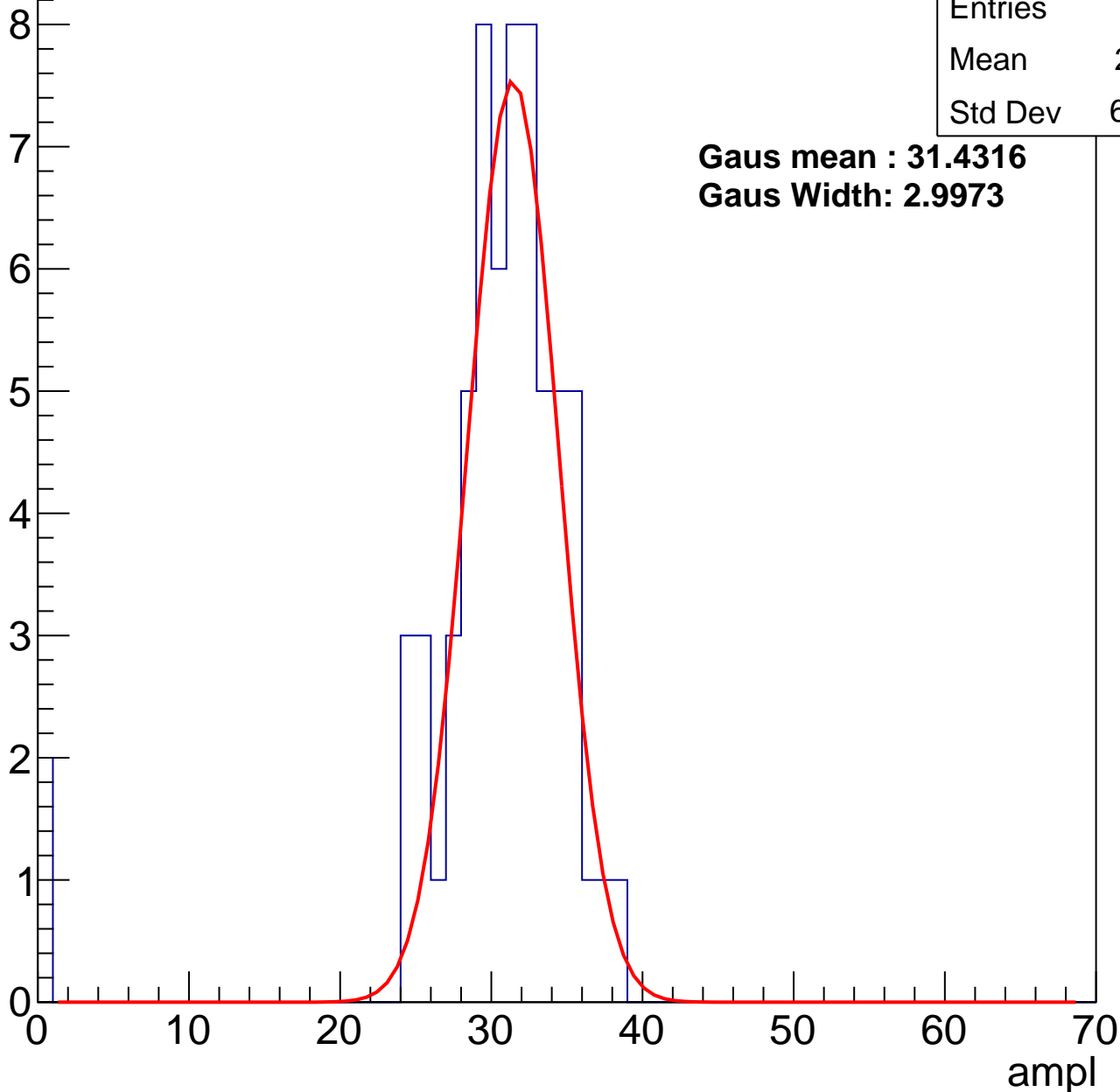
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.71
Std Dev	6.186

Gaus mean : 31.4316

Gaus Width: 2.9973



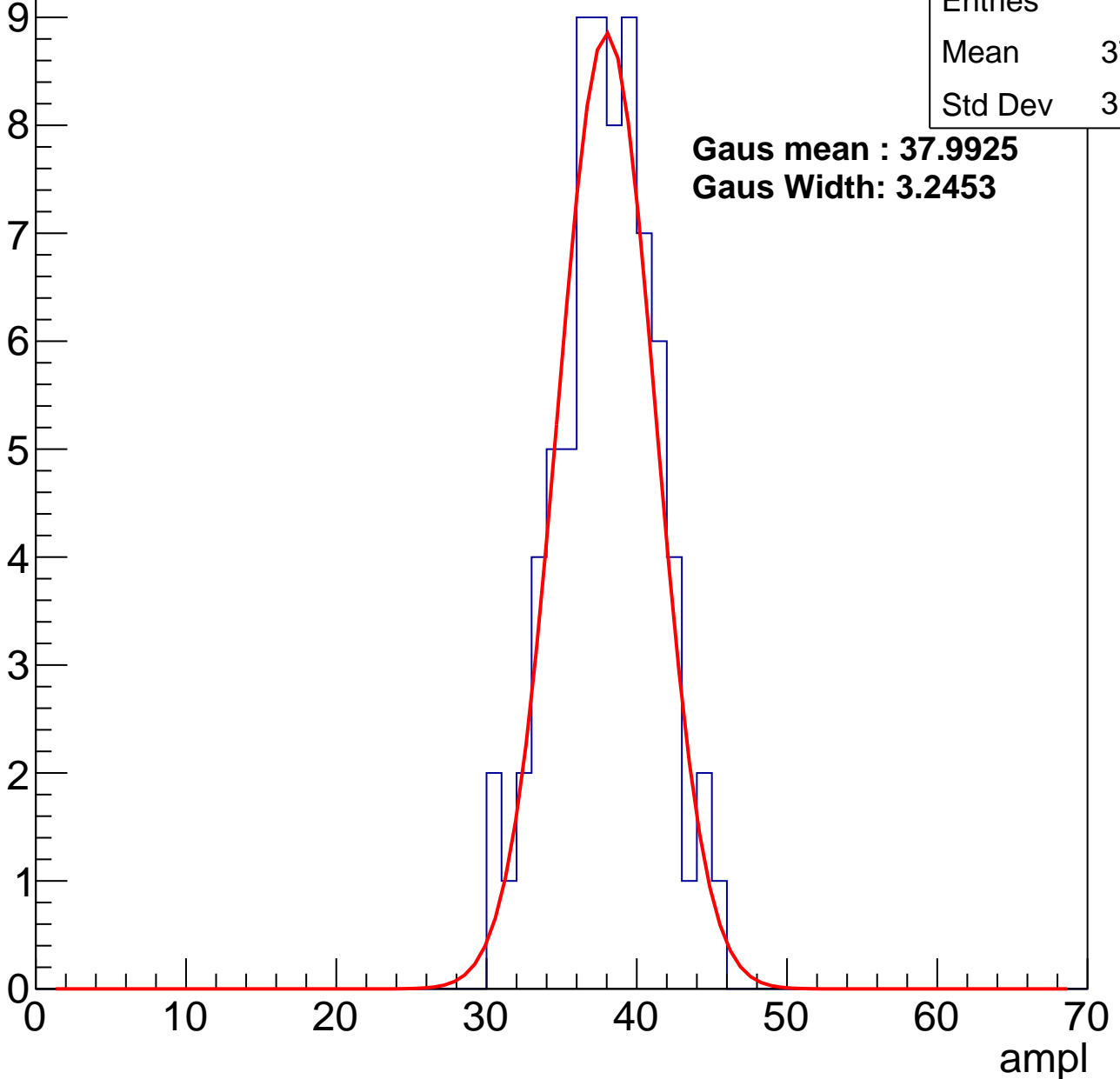
B0L001S, U19-ch31, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	37.52
Std Dev	3.272

Gaus mean : 37.9925
Gaus Width: 3.2453



B0L001S, U19-ch31, adc2

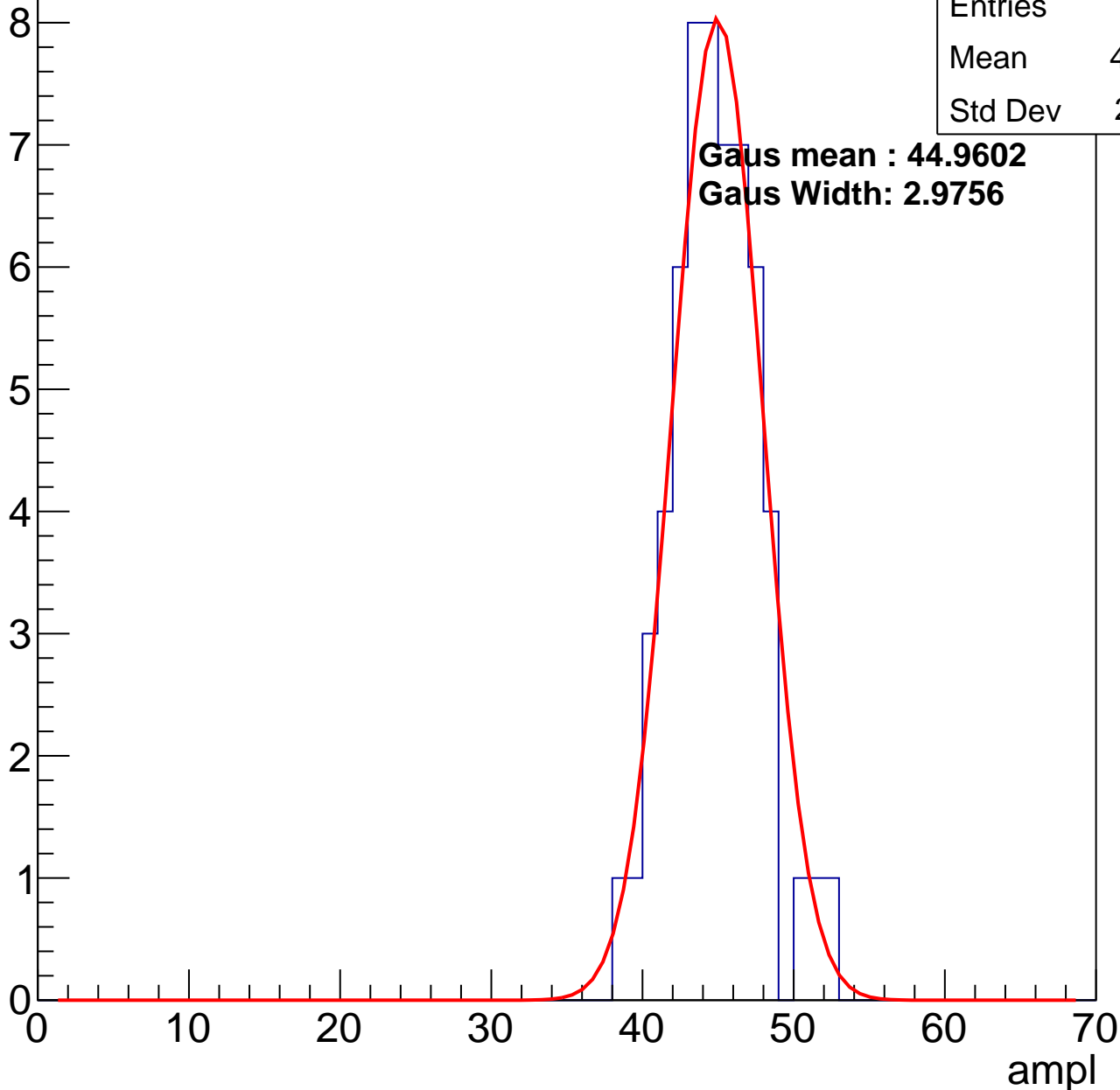
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.36
Std Dev	2.851

Gaus mean : 44.9602

Gaus Width: 2.9756



B0L001S, U19-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	50.92
Std Dev	3.23

Entry

10

8

6

4

2

0

0

10

20

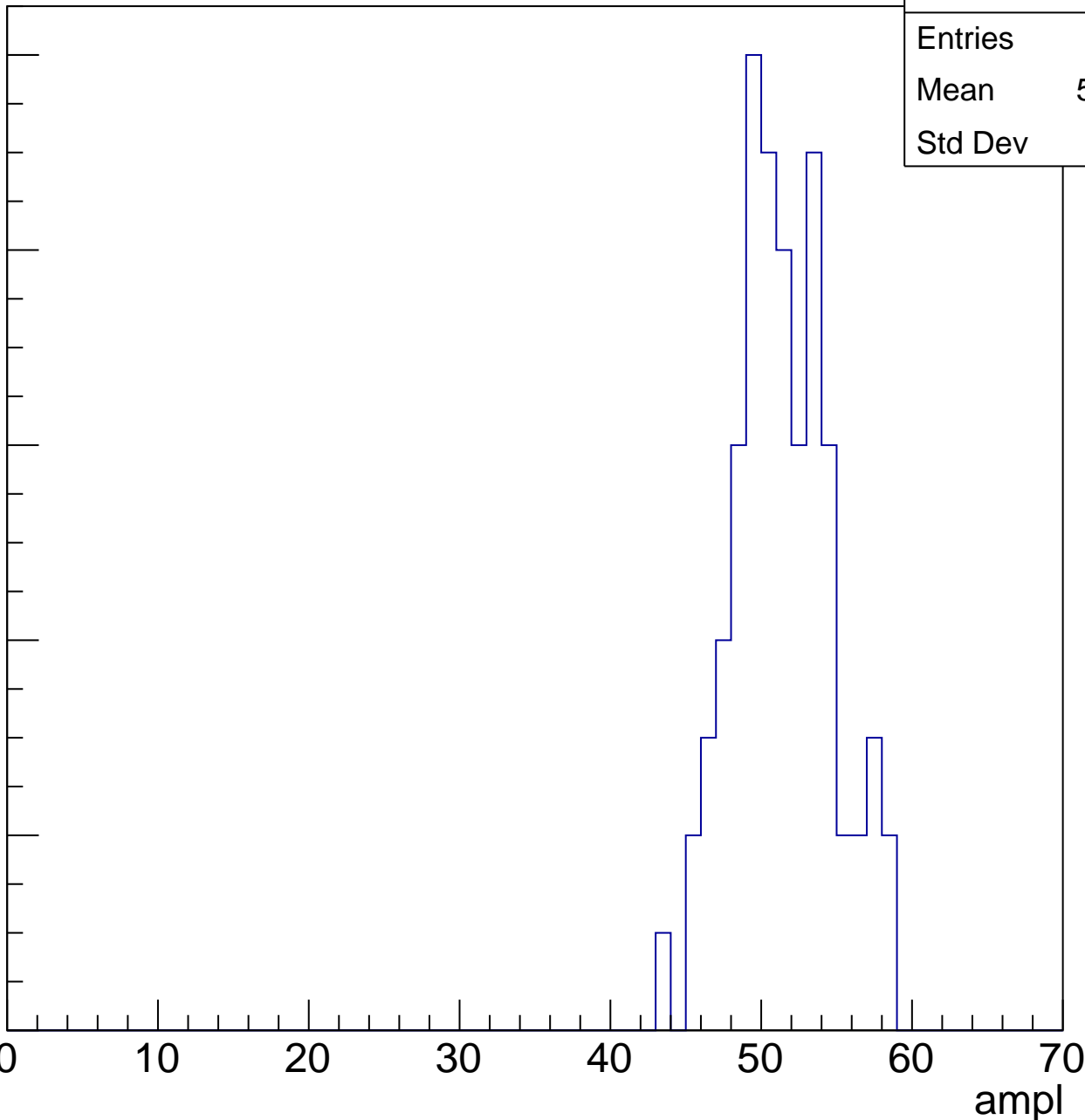
30

40

50

60

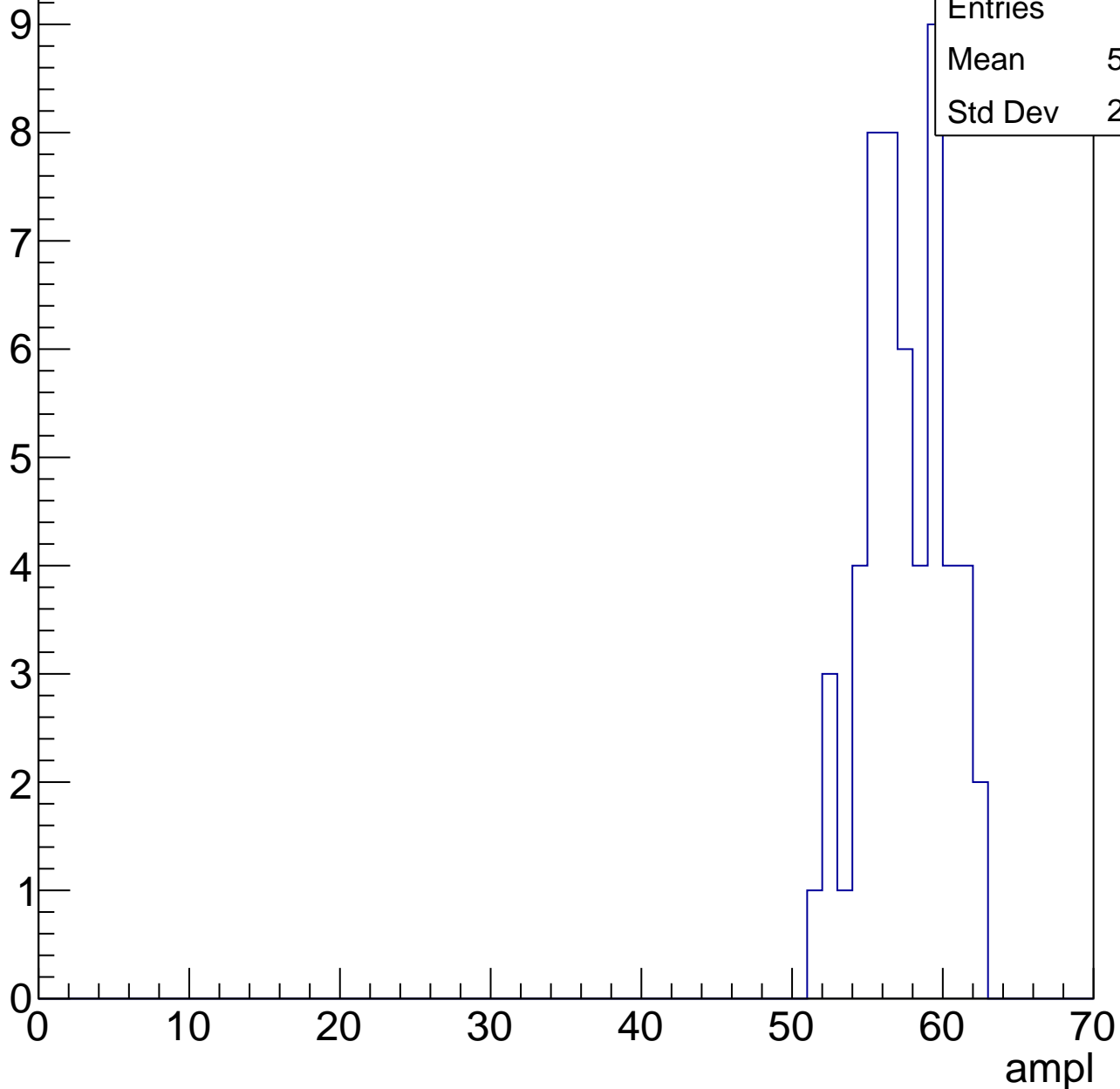
ampl



B0L001S, U19-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

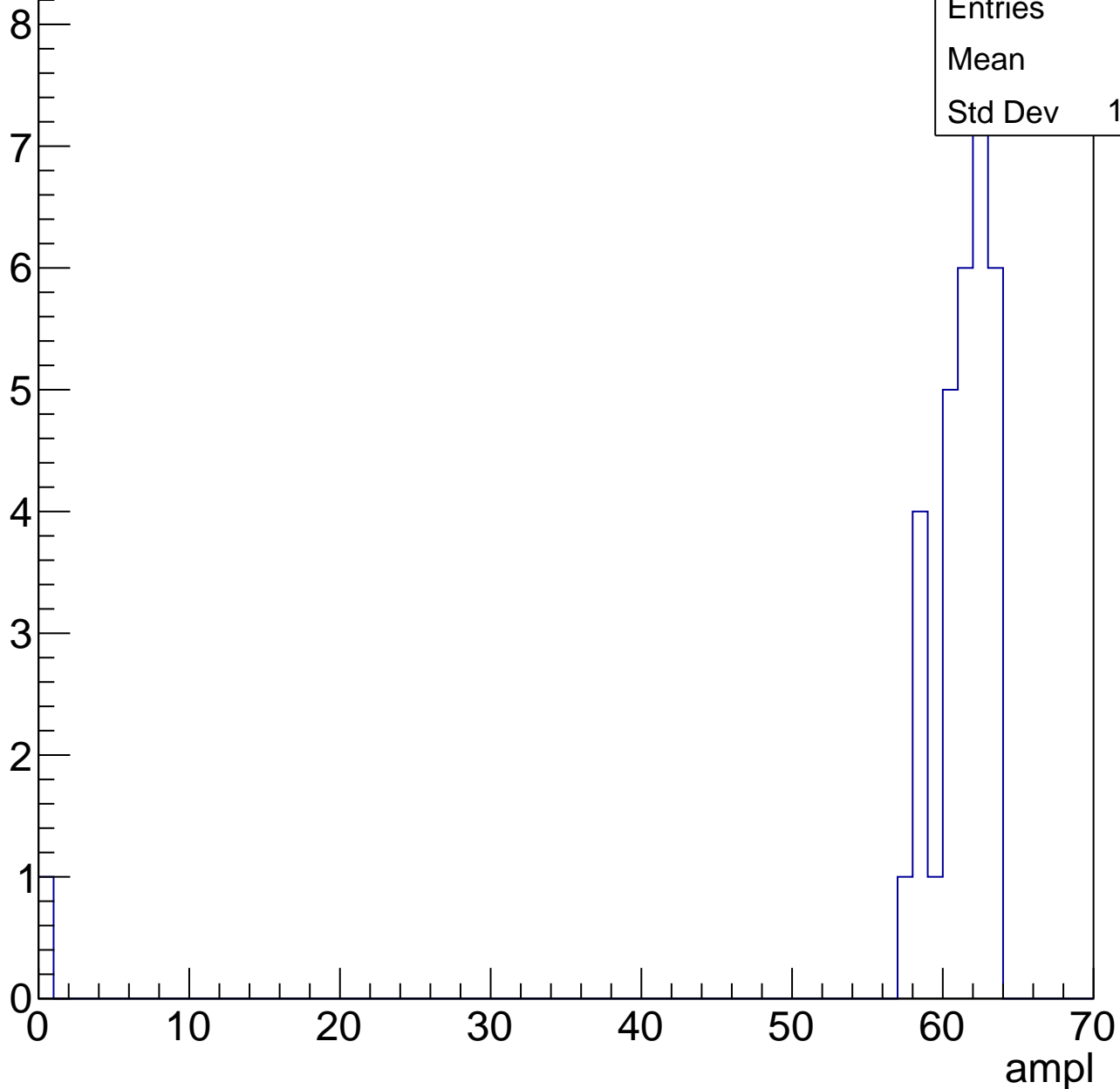


B0L001S, U19-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

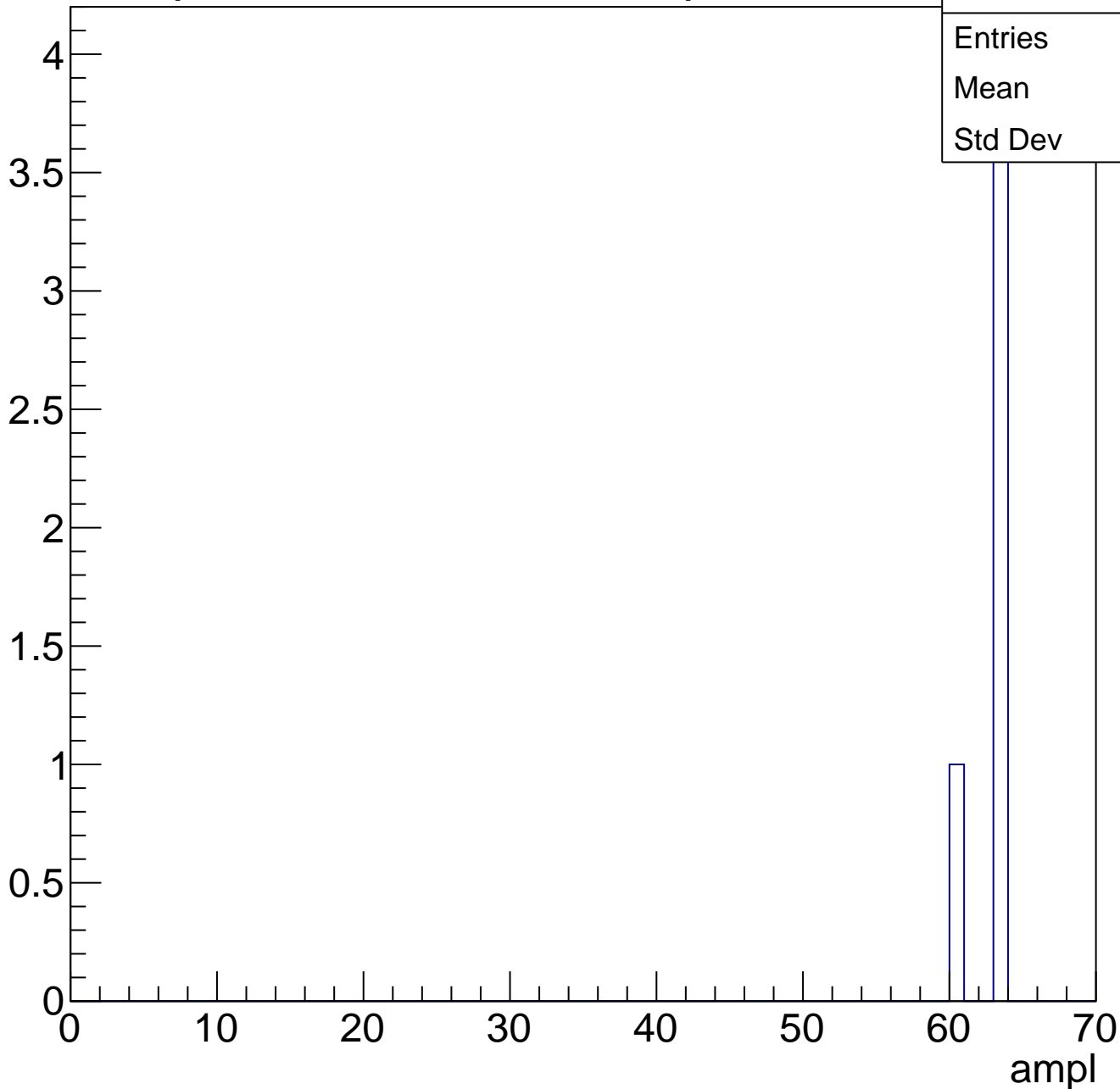
Entries	32
Mean	59
Std Dev	10.73



B0L001S, U19-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch32, adc0

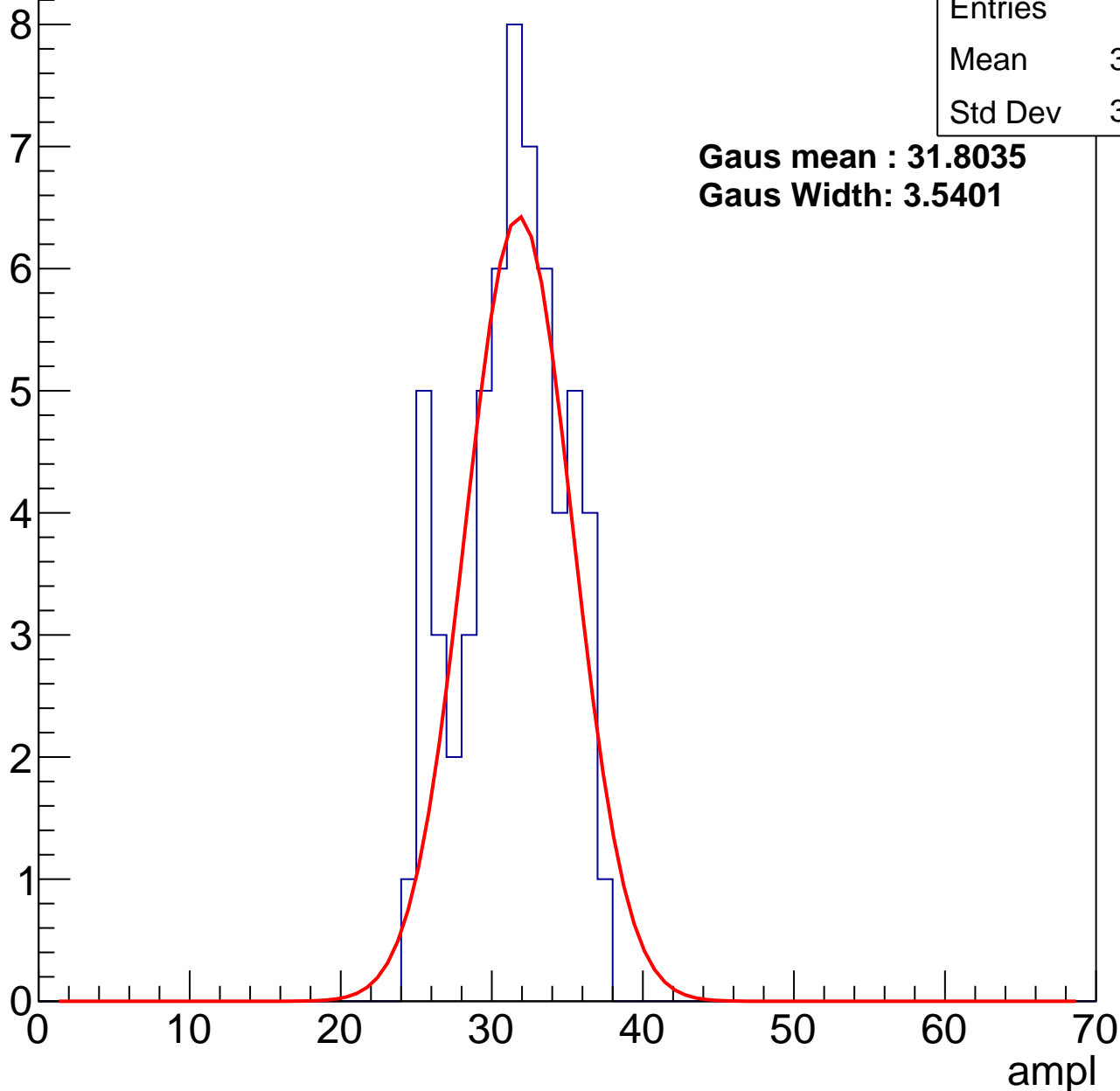
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.87
Std Dev	3.344

Gaus mean : 31.8035

Gaus Width: 3.5401



B0L001S, U19-ch32, adc1

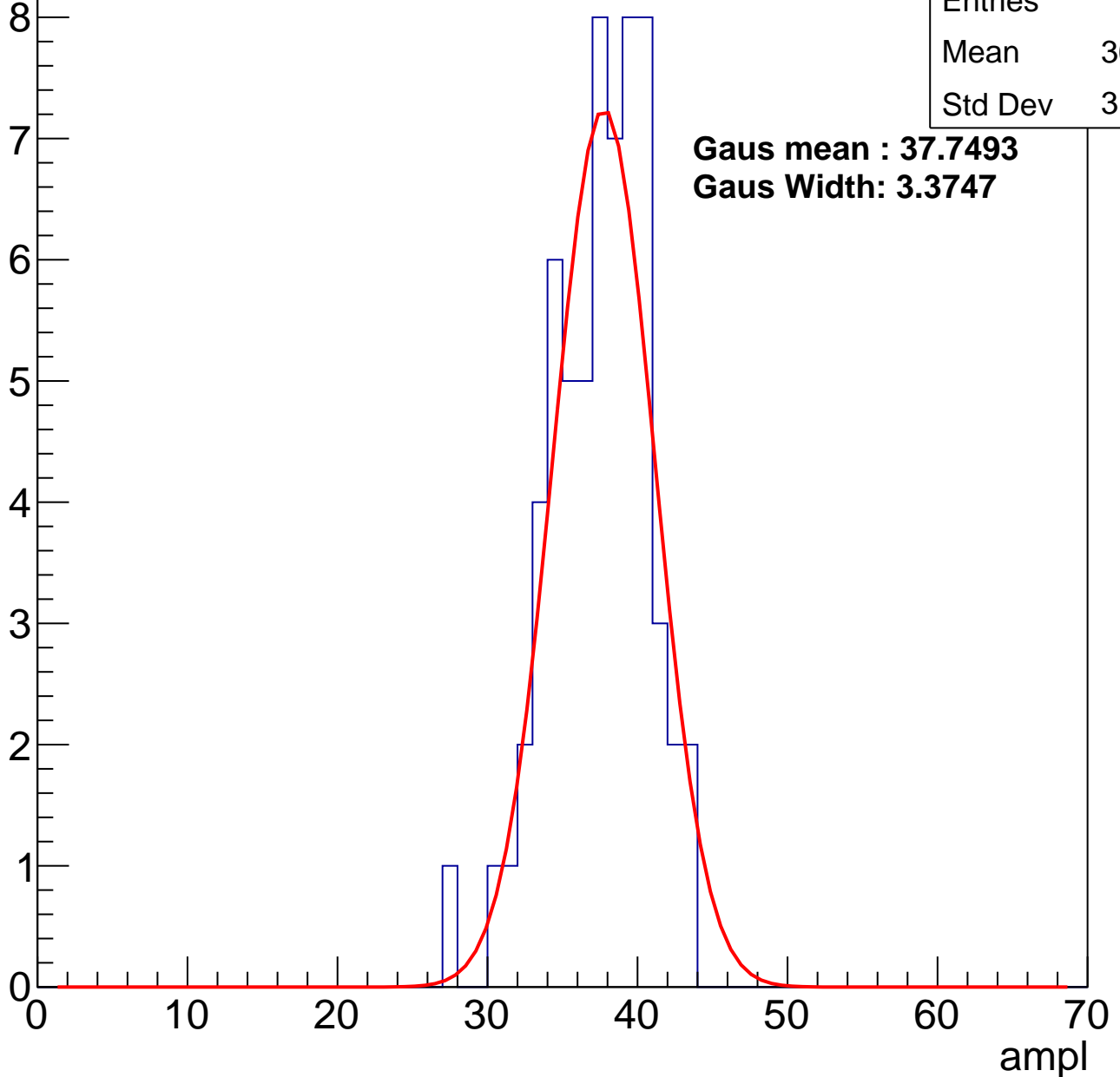
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	36.98
Std Dev	3.234

Gaus mean : 37.7493

Gaus Width: 3.3747



B0L001S, U19-ch32, adc2

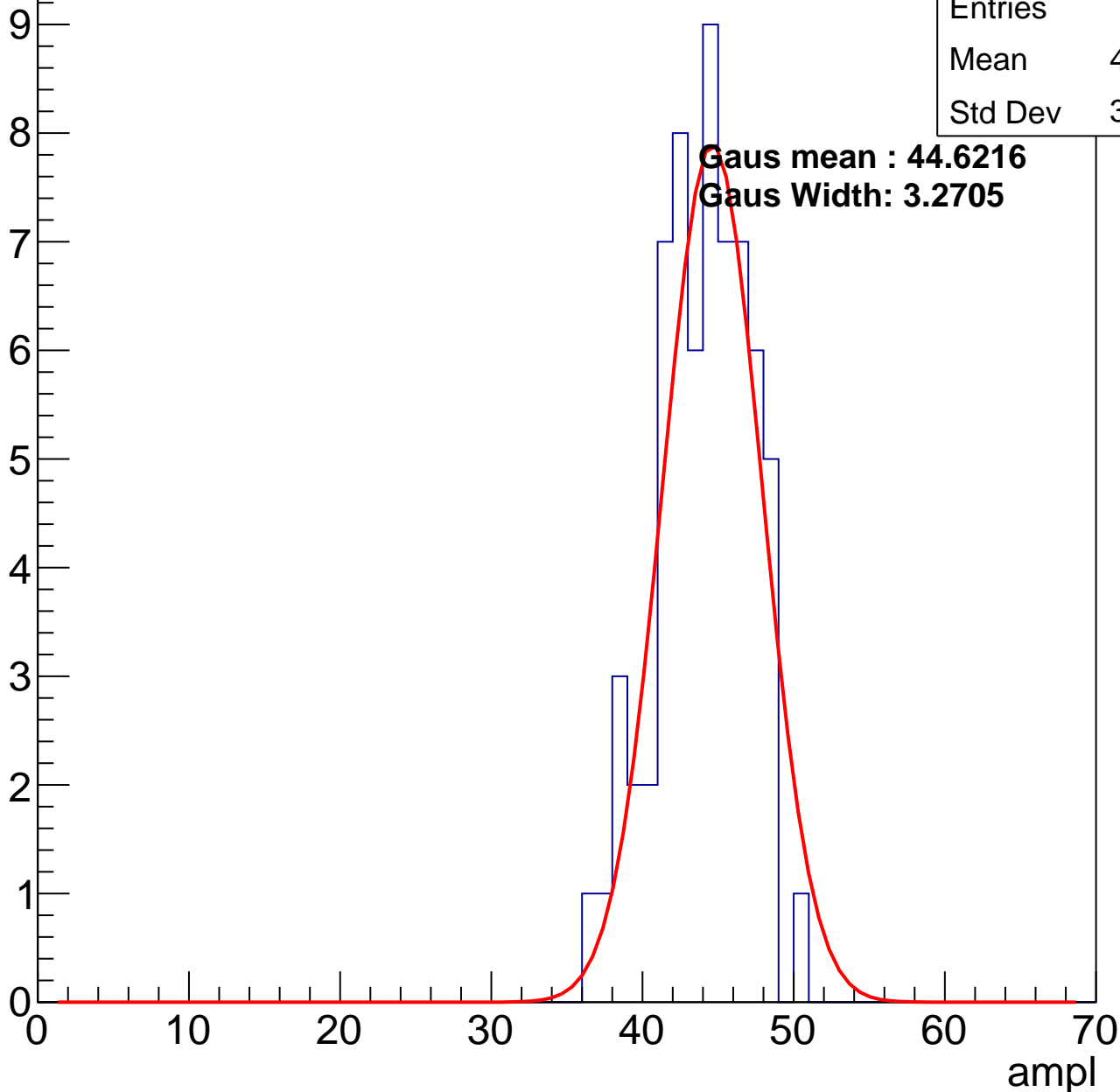
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.55
Std Dev	3.028

Gaus mean : 44.6216

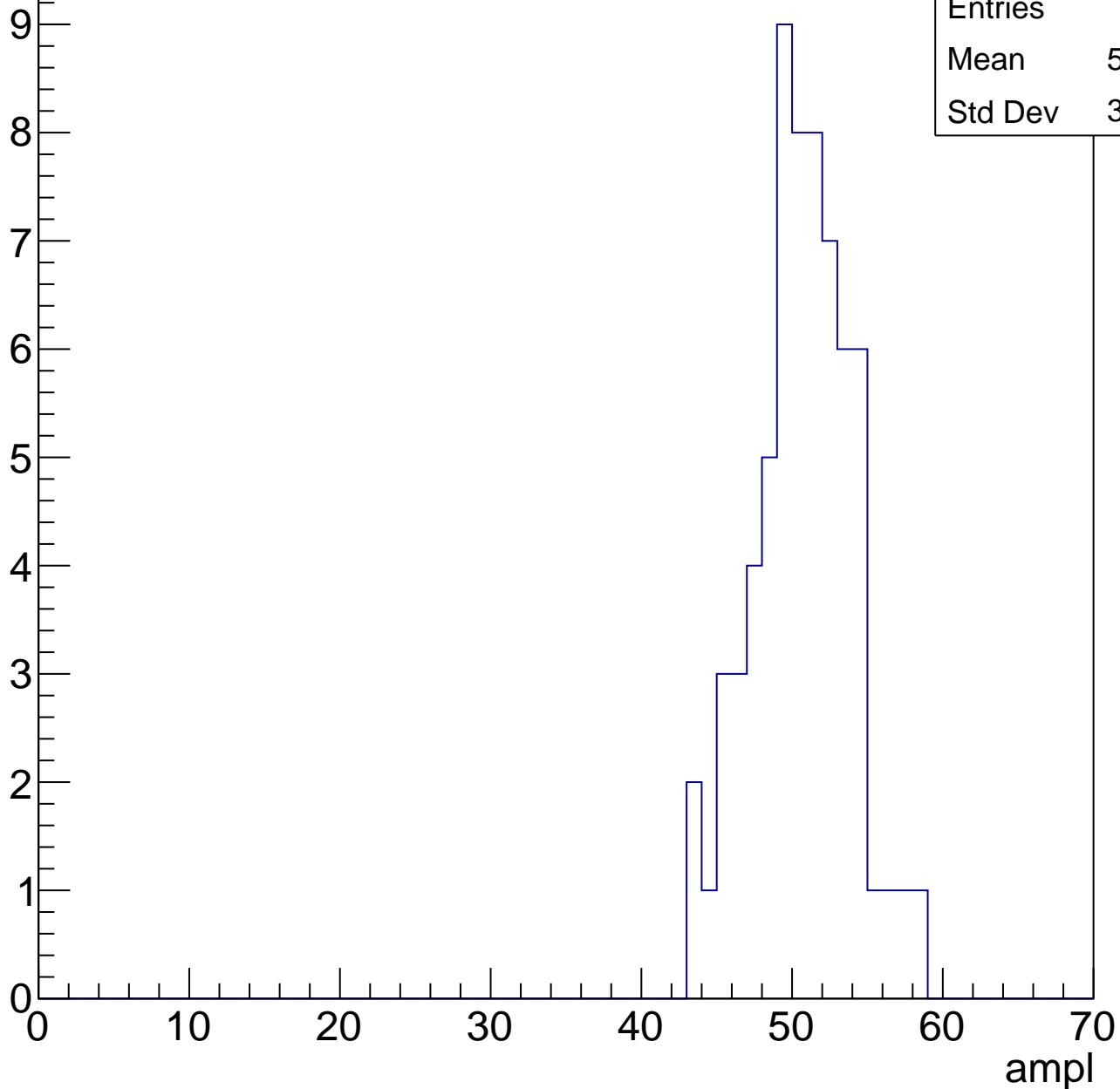
Gaus Width: 3.2705



B0L001S, U19-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

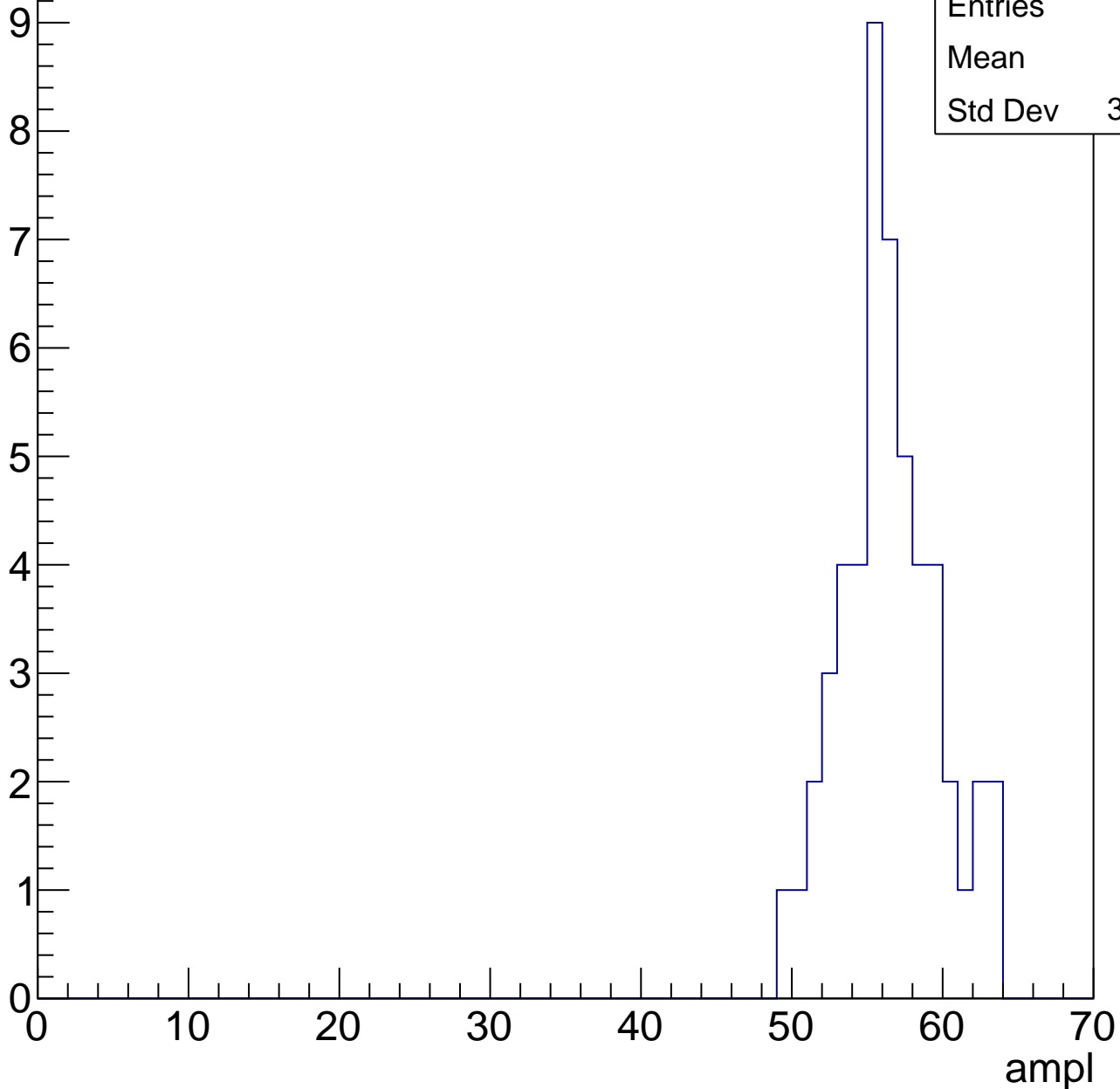


B0L001S, U19-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

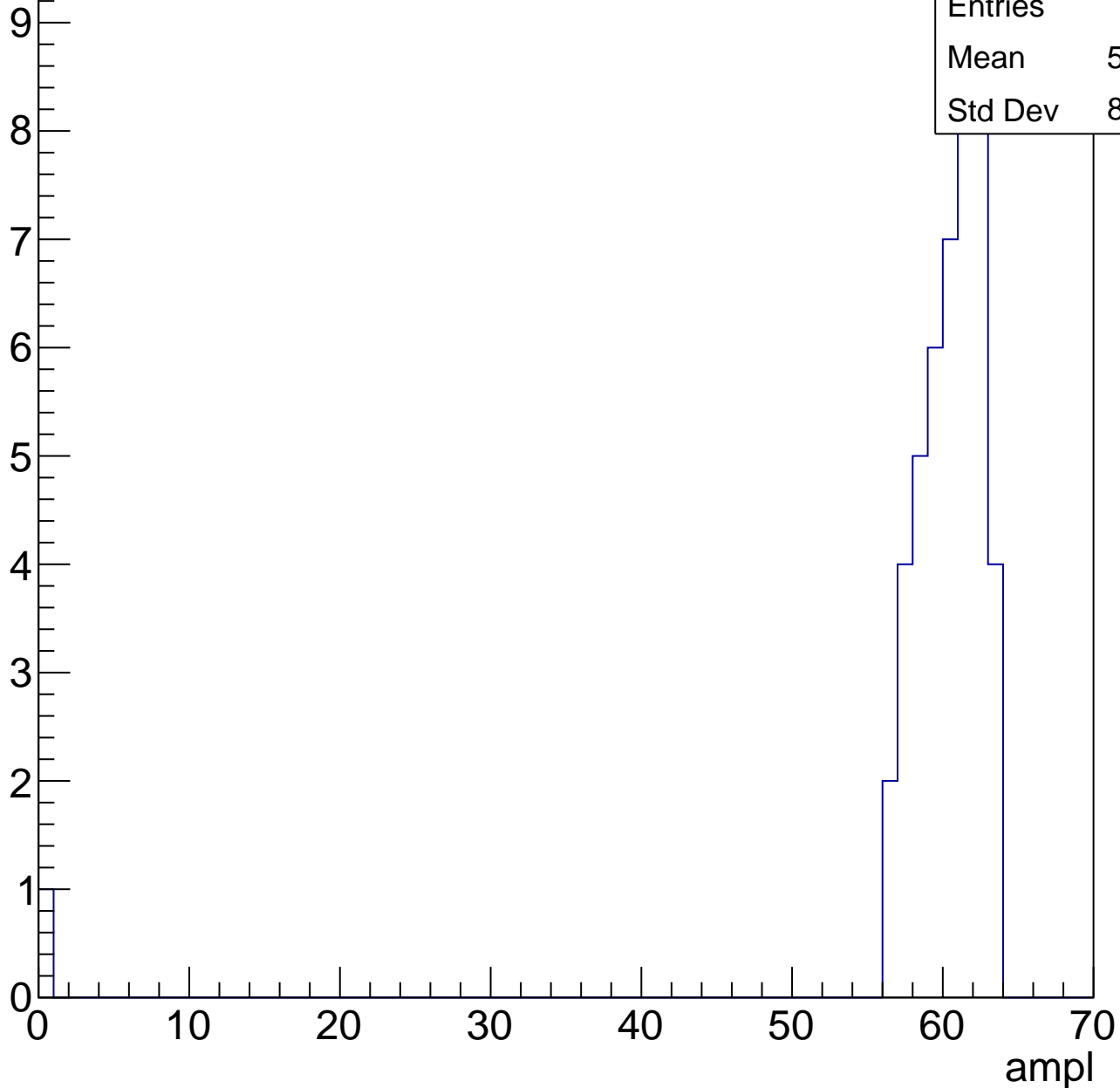
Entries	51
Mean	56
Std Dev	3.218



B0L001S, U19-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

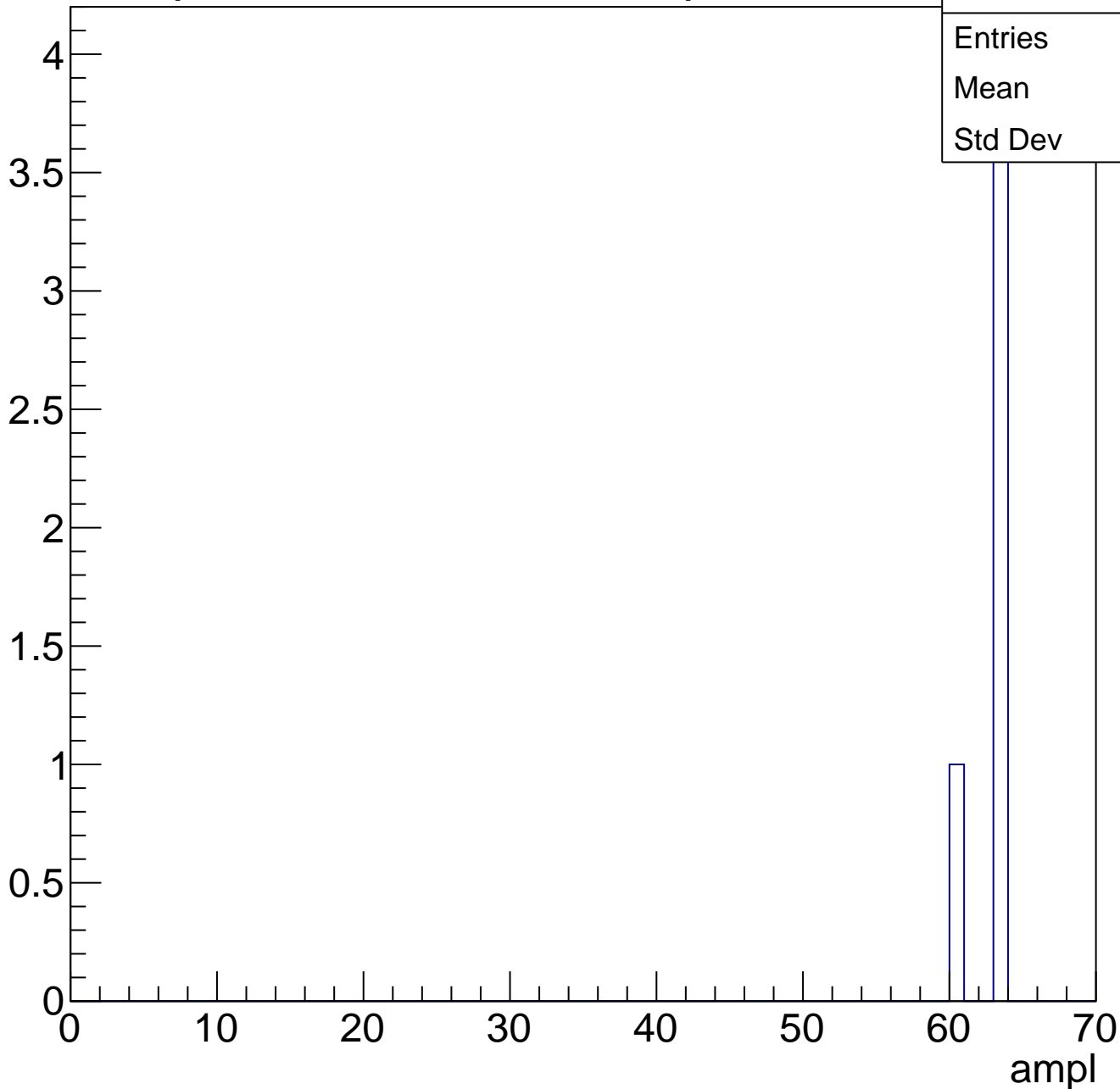
Entry



B0L001S, U19-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch33, adc0

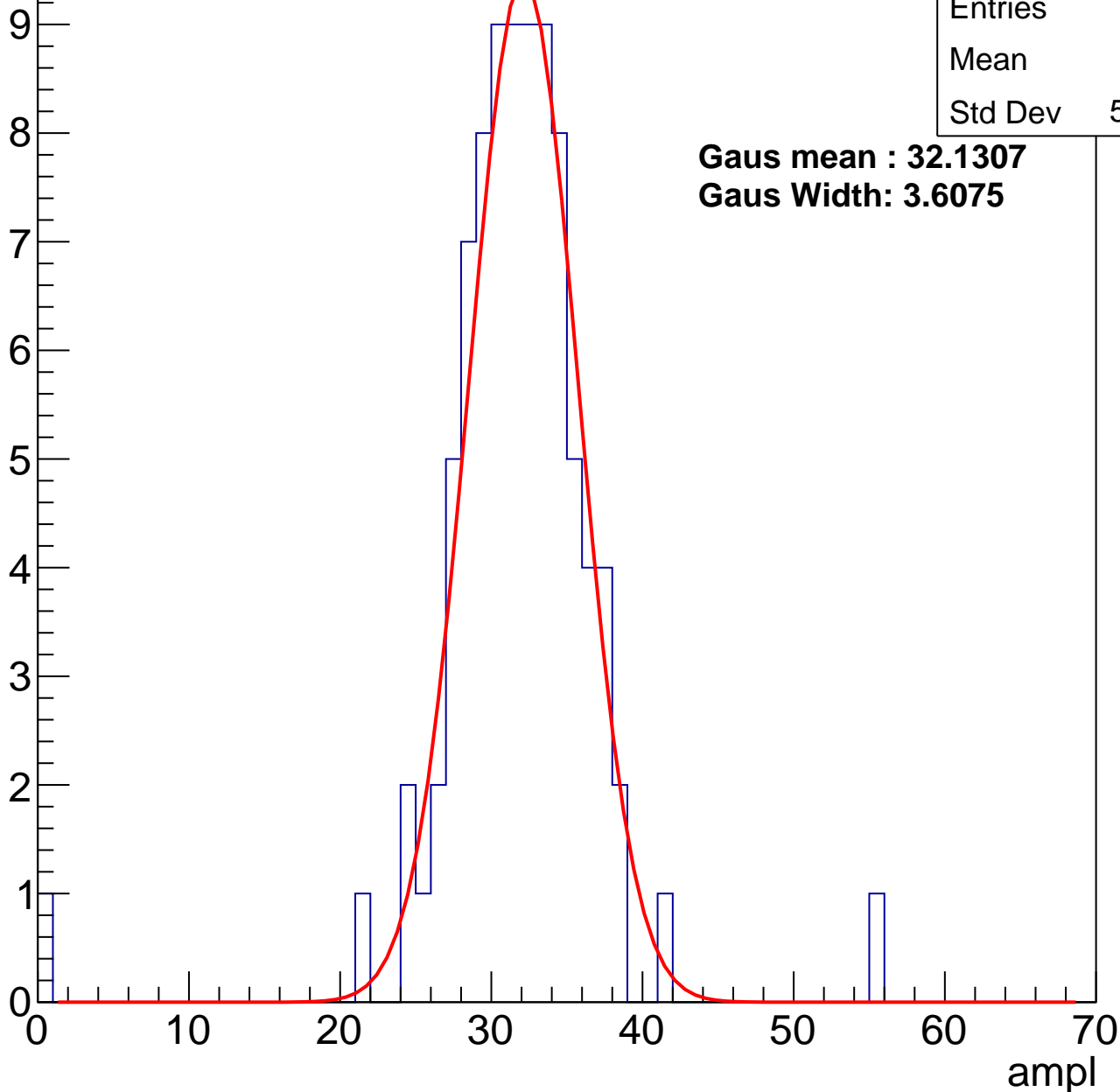
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	88
Mean	31.3
Std Dev	5.473

Gaus mean : 32.1307

Gaus Width: 3.6075



B0L001S, U19-ch33, adc1

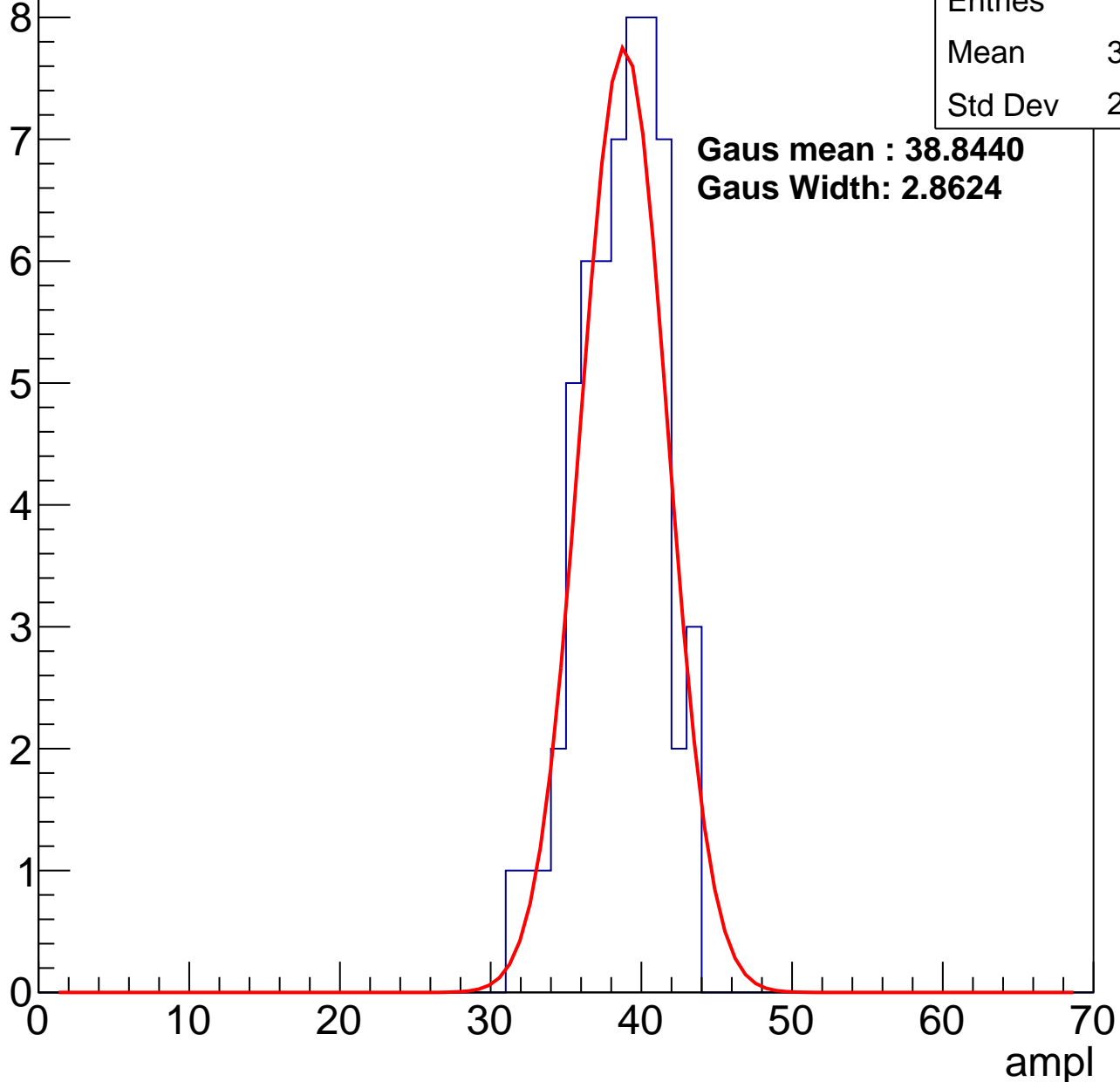
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	38.16
Std Dev	2.732

Gaus mean : 38.8440

Gaus Width: 2.8624



B0L001S, U19-ch33, adc2

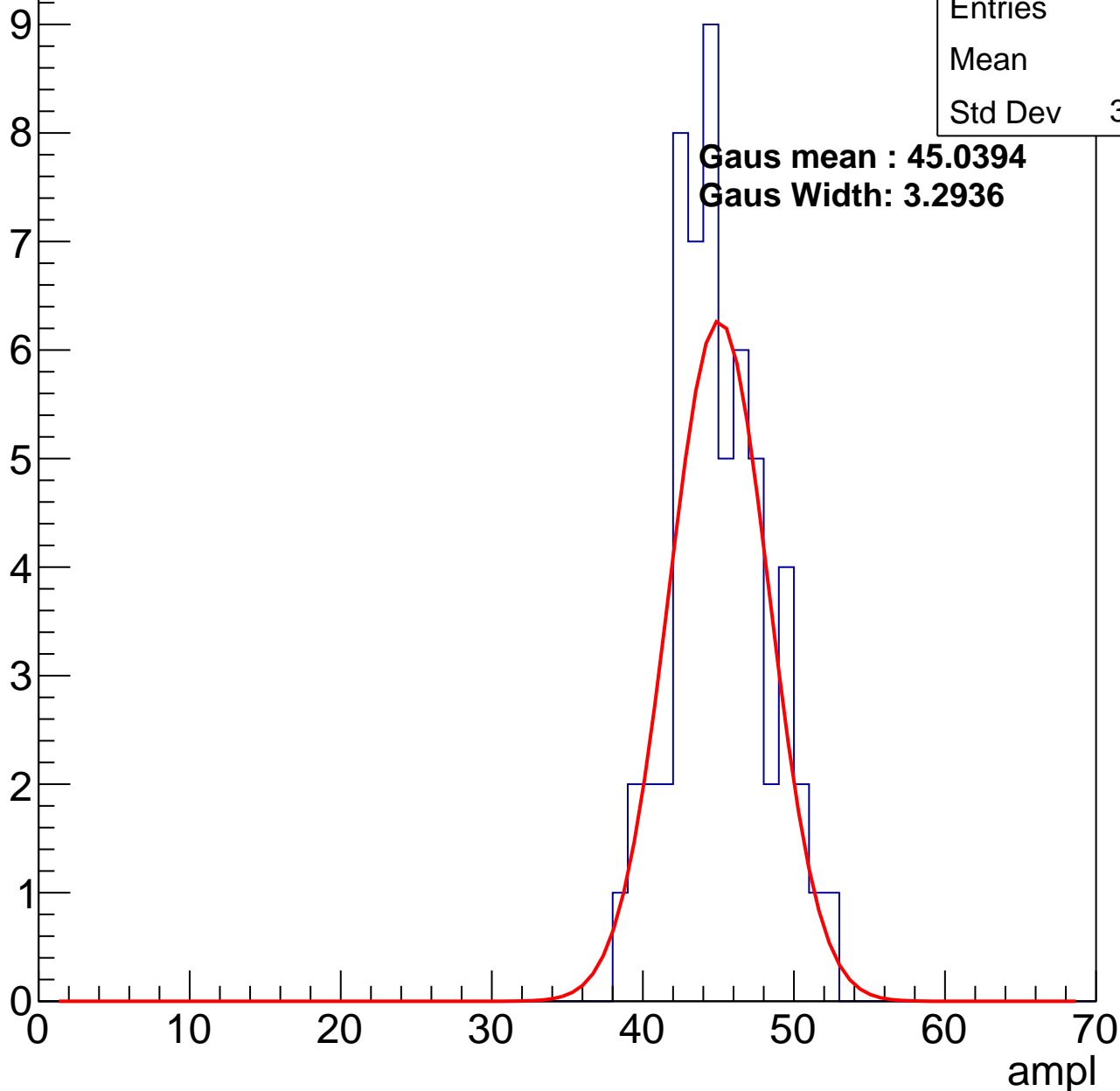
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.6
Std Dev	3.094

Gaus mean : 45.0394

Gaus Width: 3.2936

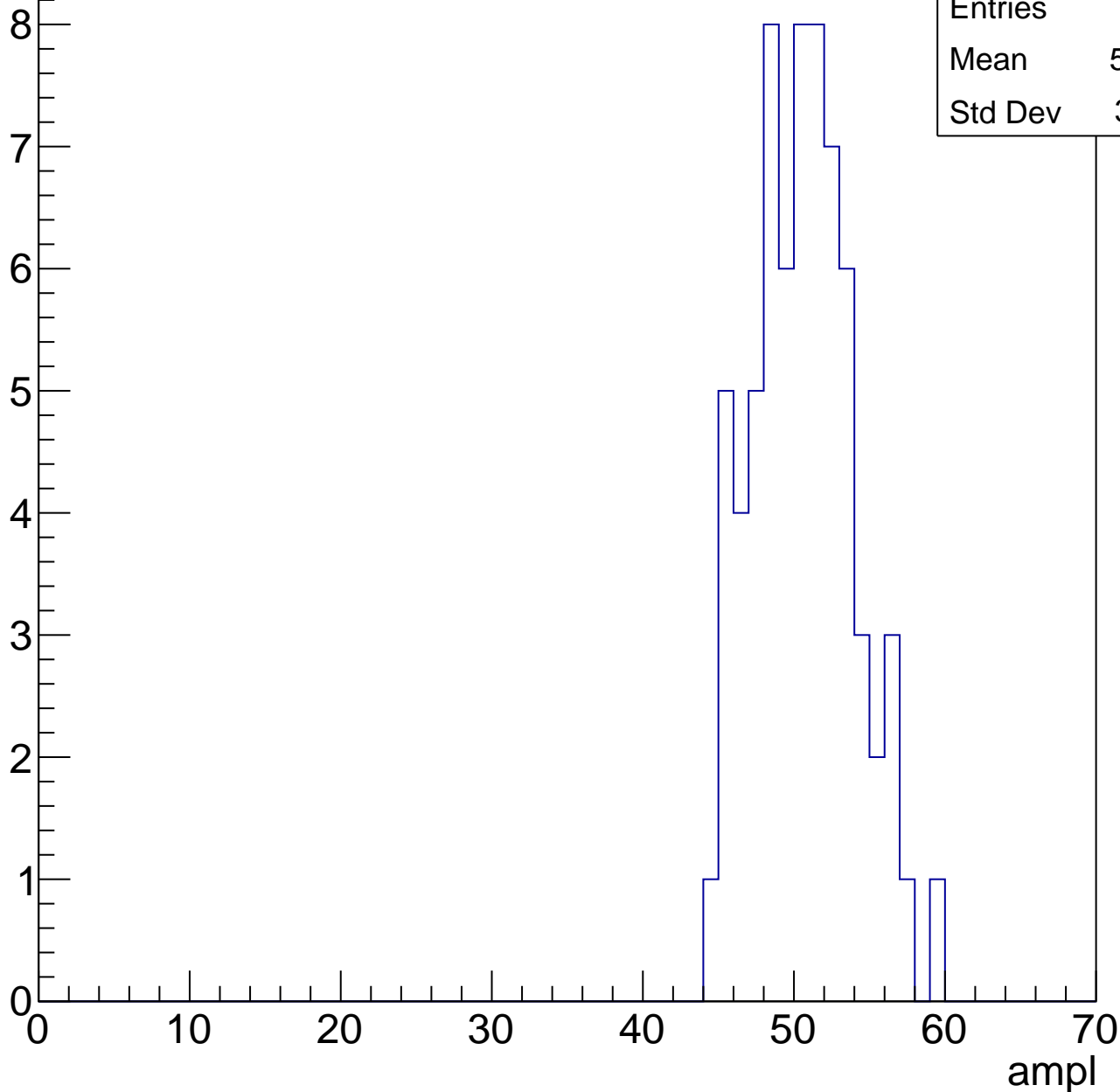


B0L001S, U19-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

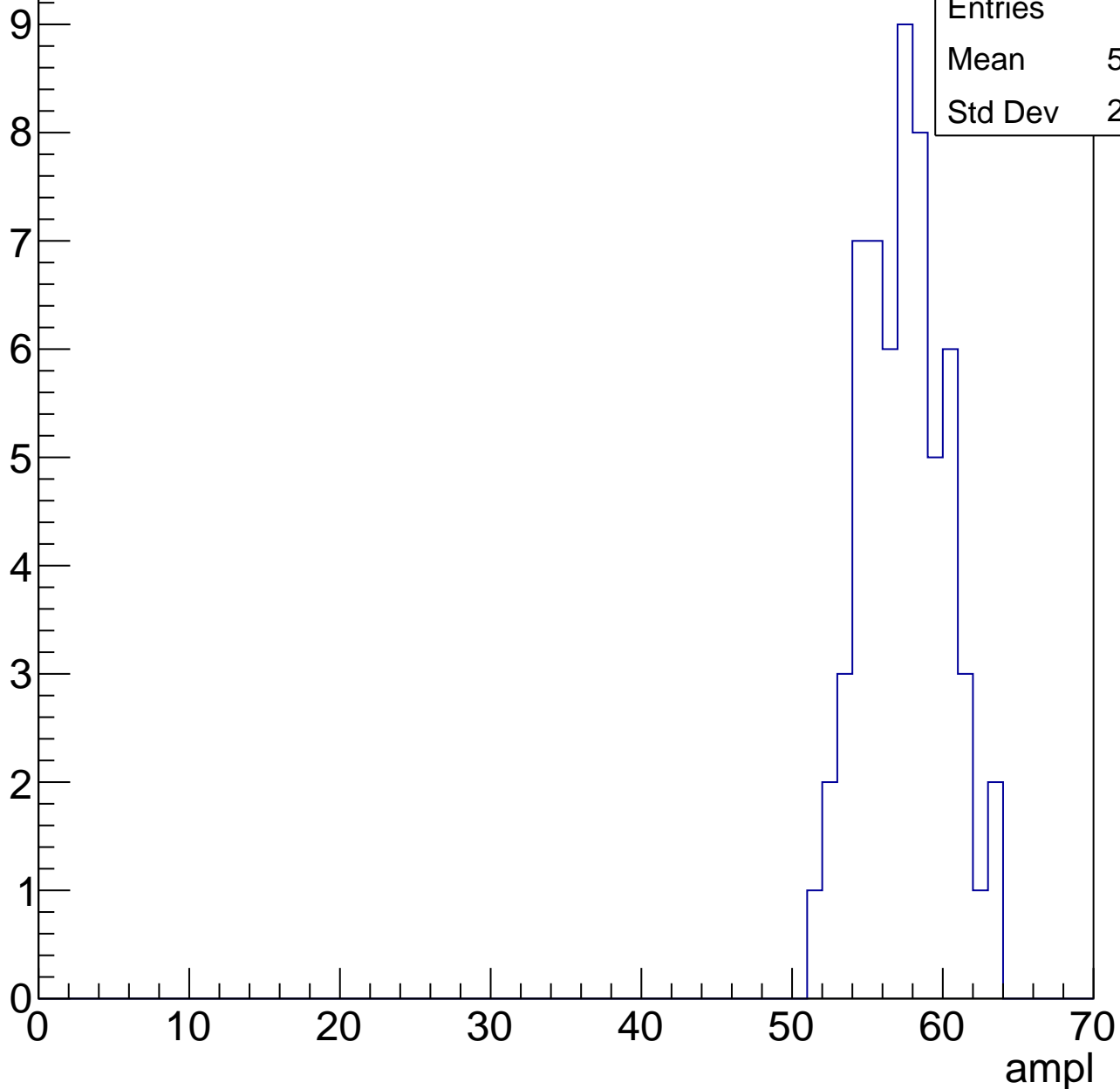
Entries	68
Mean	50.18
Std Dev	3.281



B0L001S, U19-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

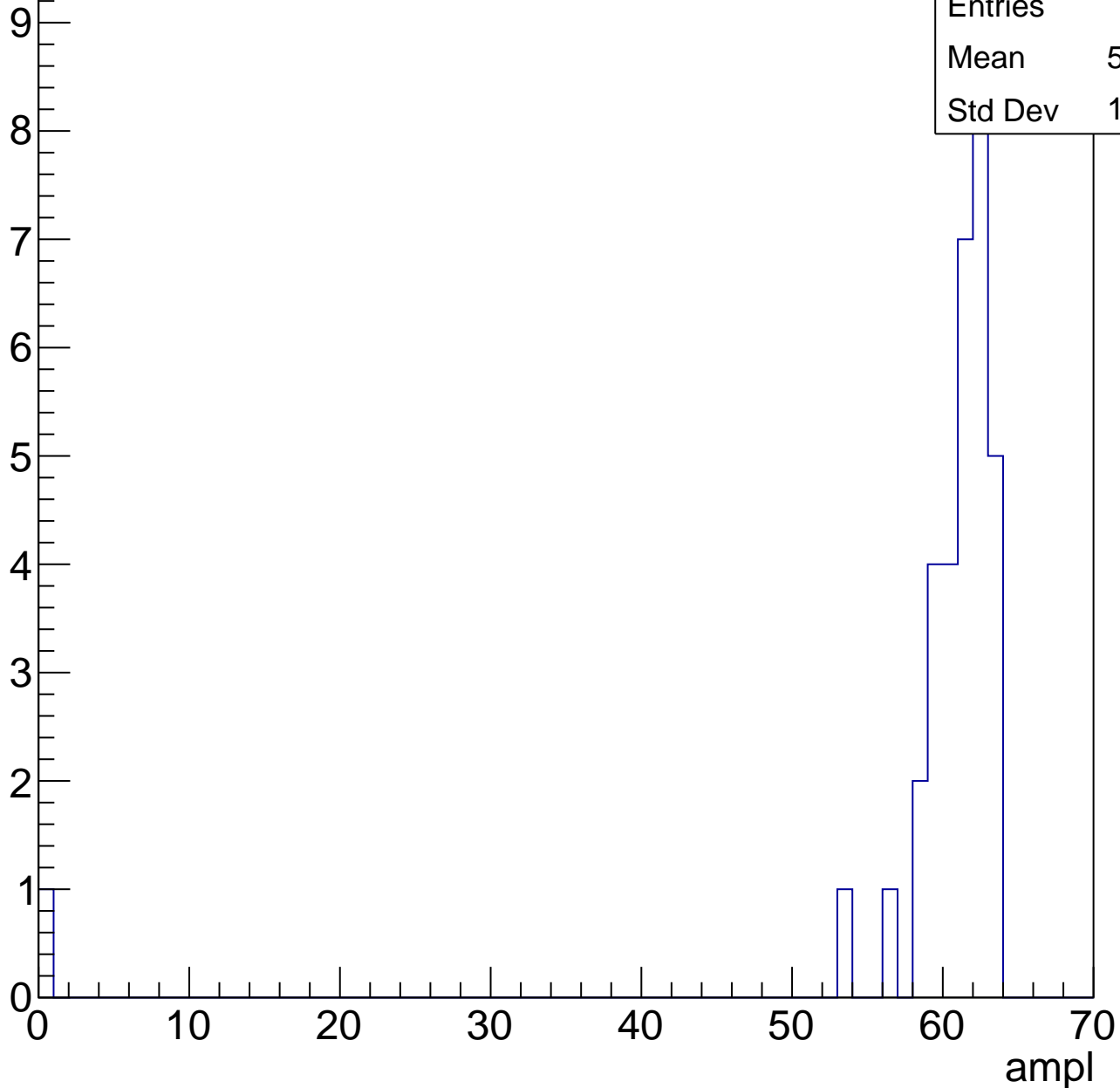


B0L001S, U19-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.85
Std Dev	10.46



B0L001S, U19-ch33, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch34, adc0

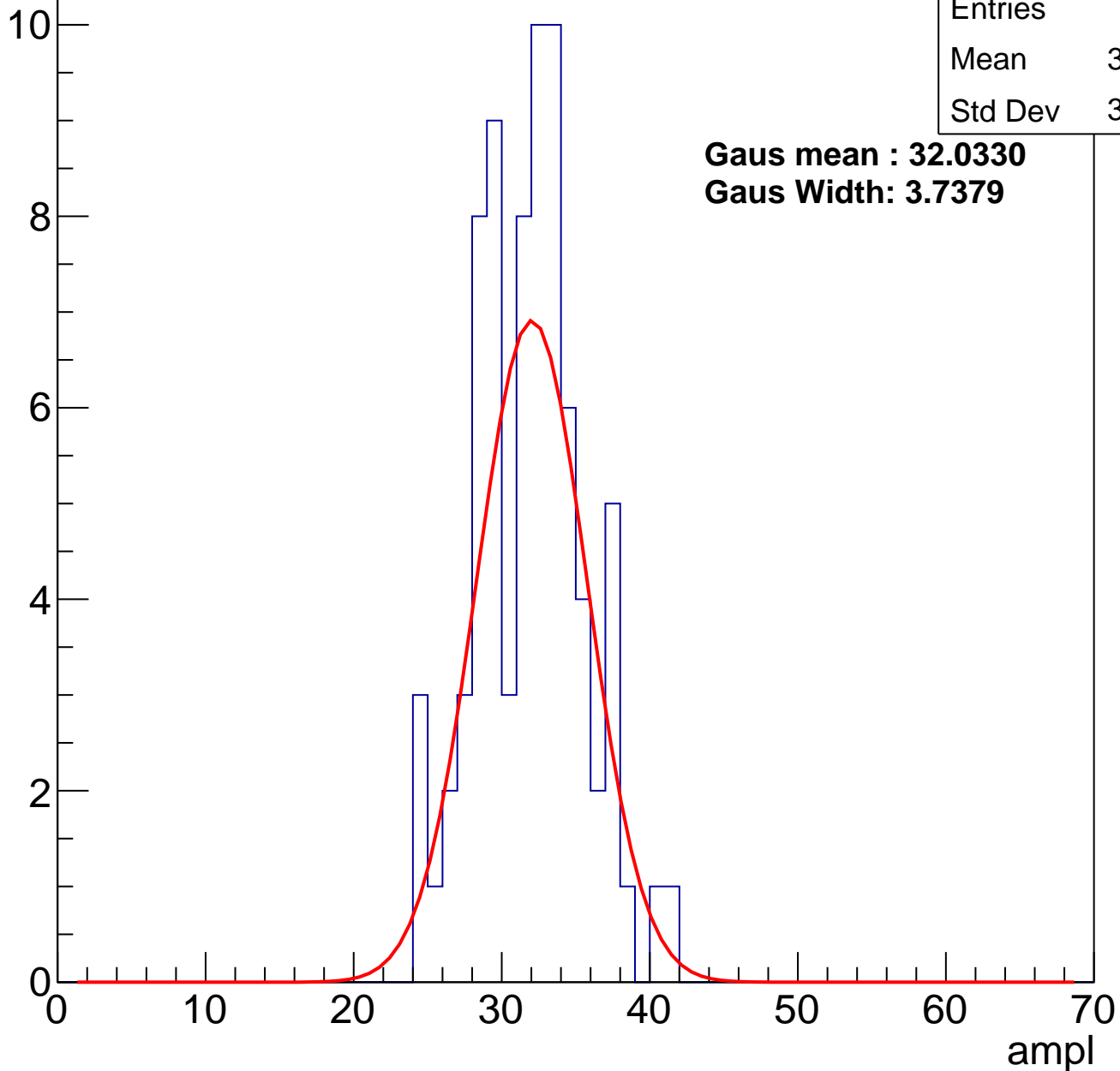
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	31.47
Std Dev	3.609

Gaus mean : 32.0330

Gaus Width: 3.7379

Entry



B0L001S, U19-ch34, adc1

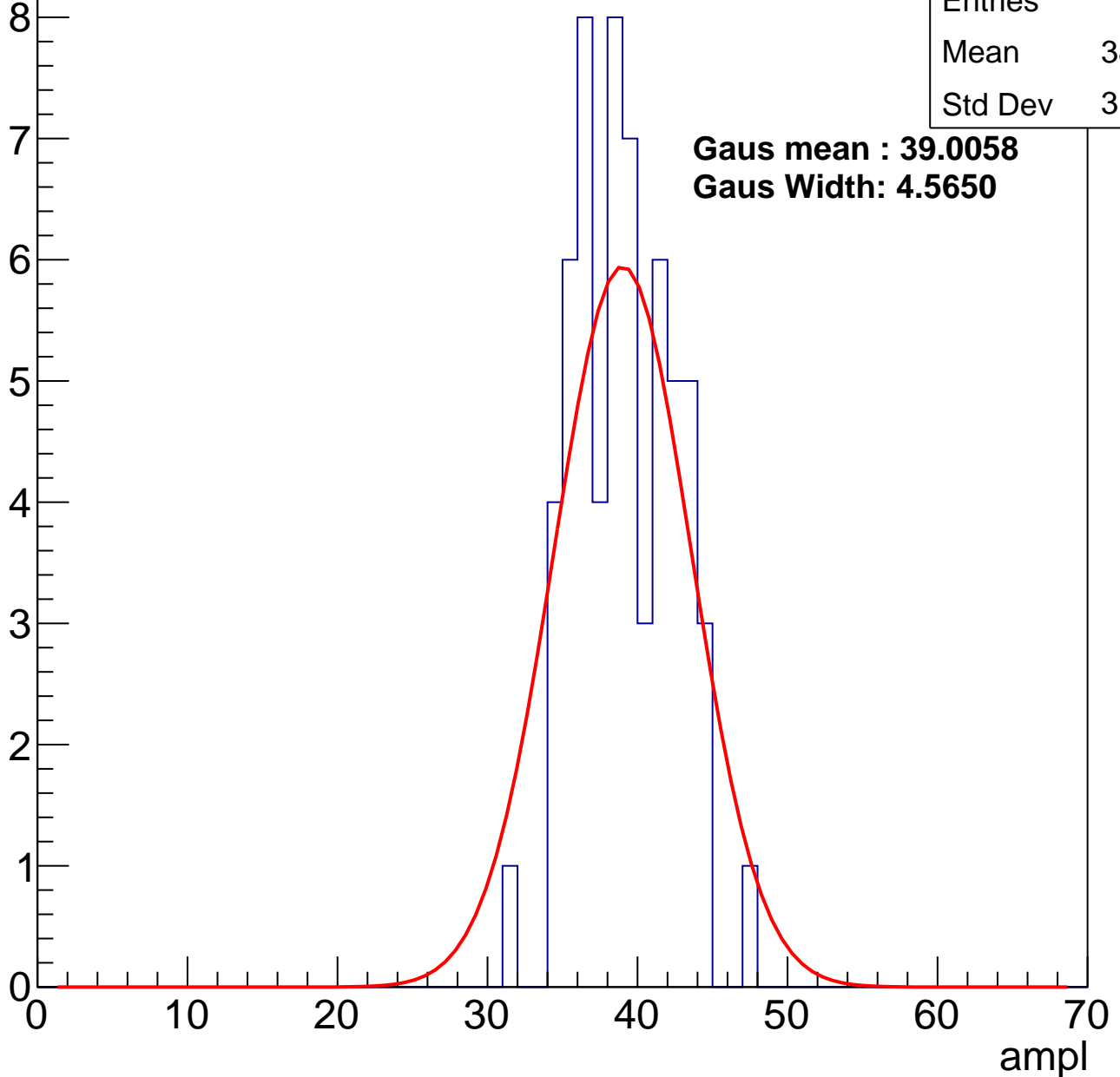
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	38.69
Std Dev	3.242

Gaus mean : 39.0058

Gaus Width: 4.5650



B0L001S, U19-ch34, adc2

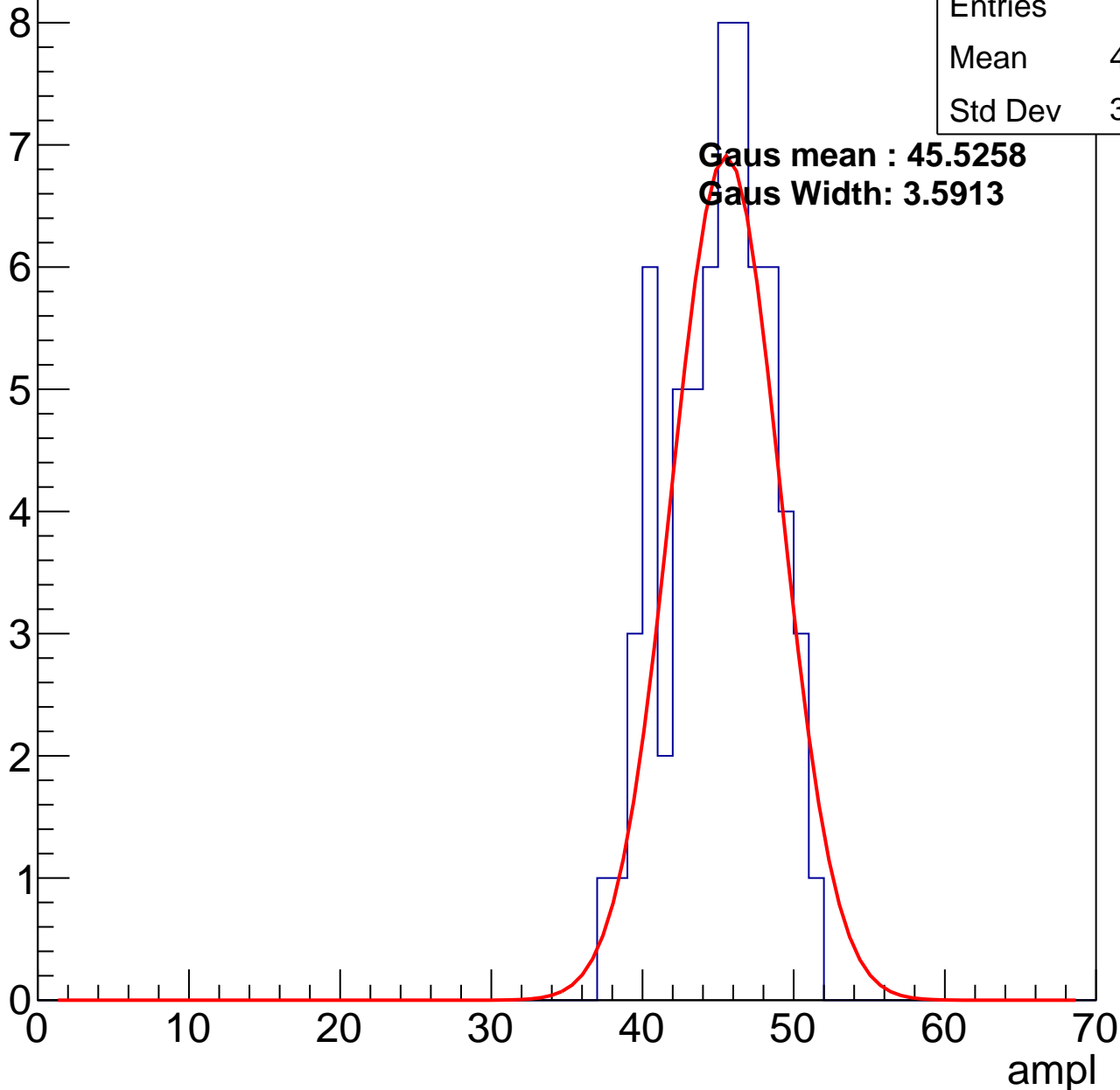
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.58
Std Dev	3.337

Gaus mean : 45.5258

Gaus Width: 3.5913

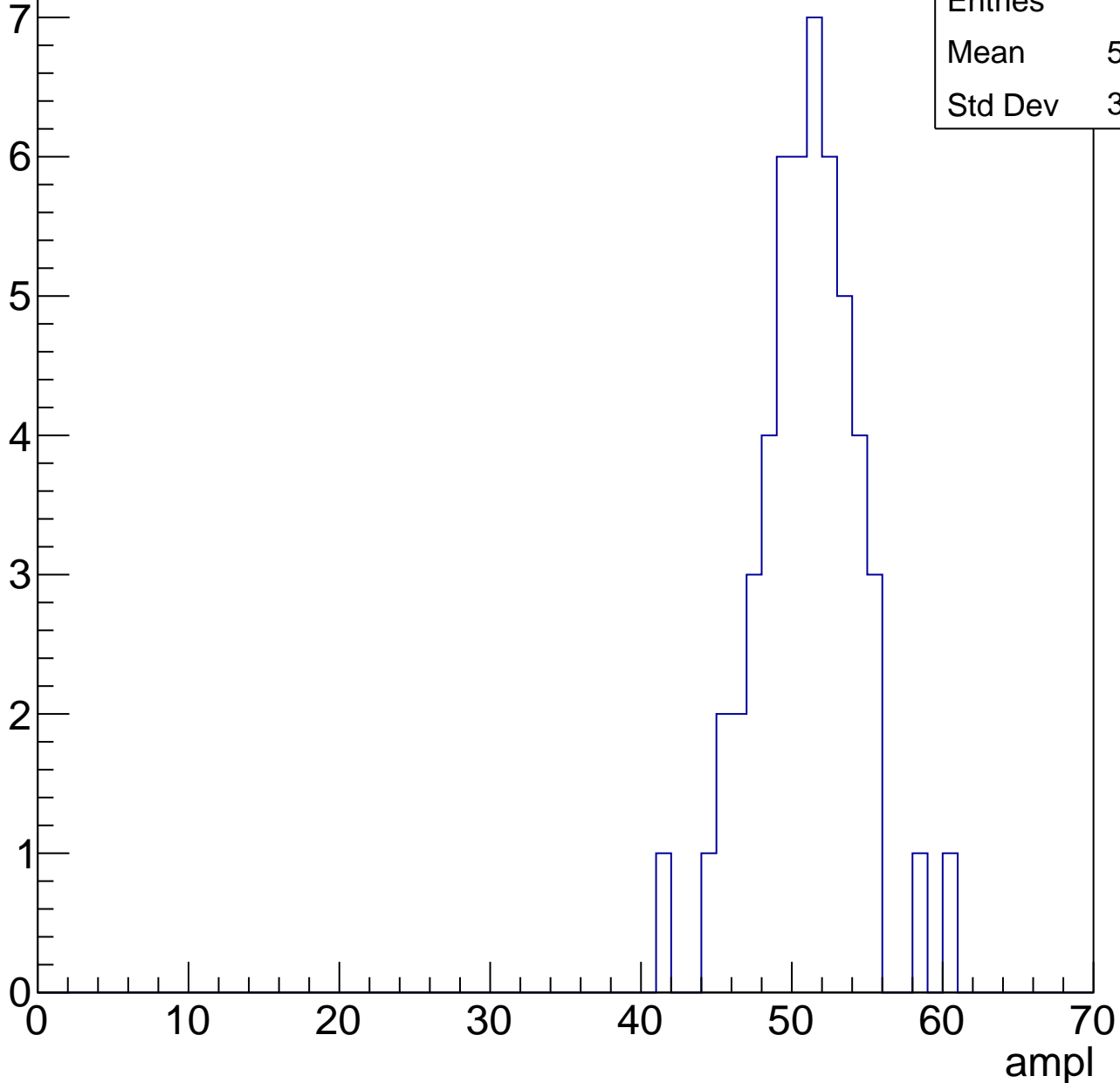


B0L001S, U19-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

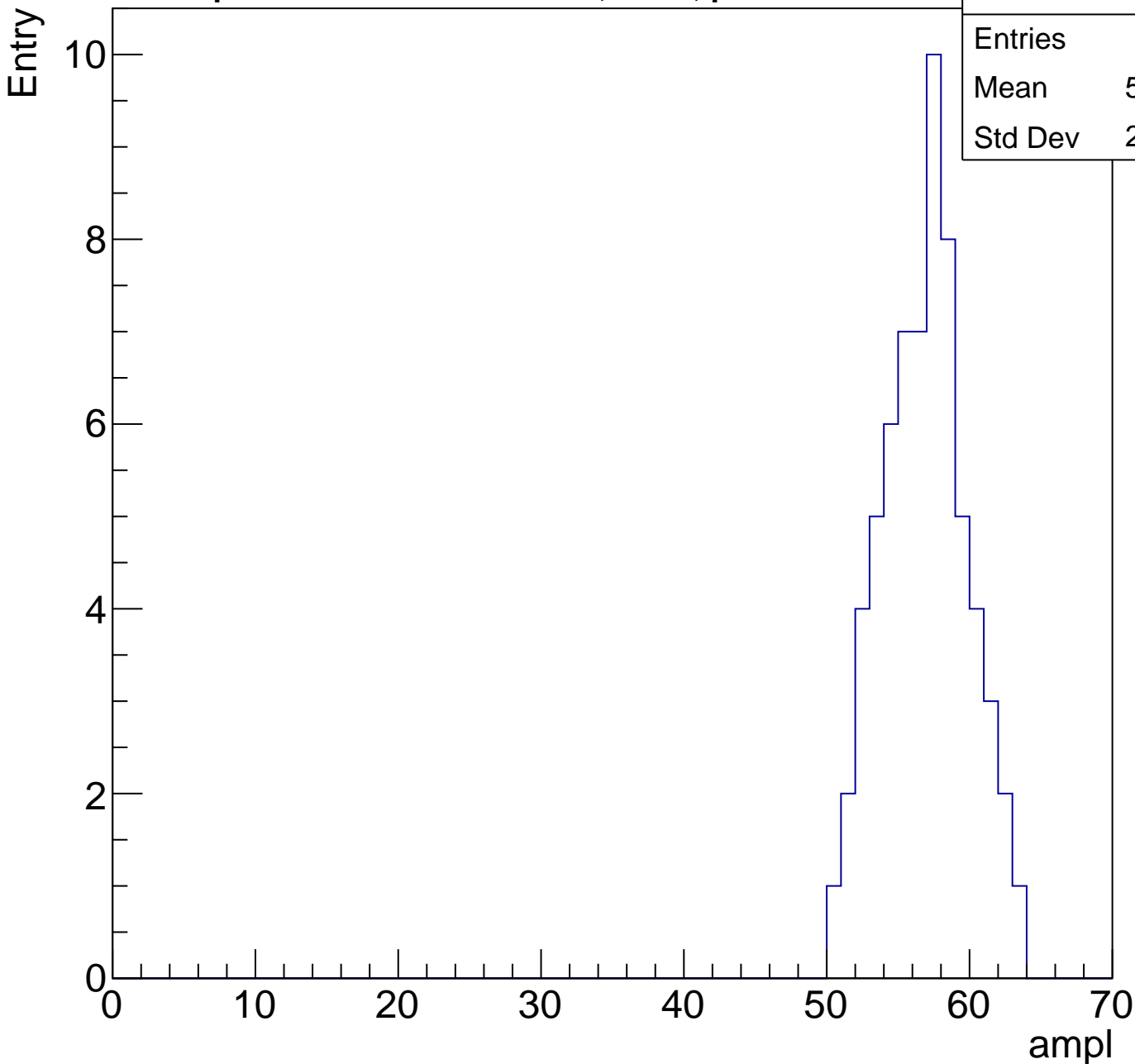
Entries	52
Mean	50.52
Std Dev	3.433



B0L001S, U19-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	56.38
Std Dev	2.944

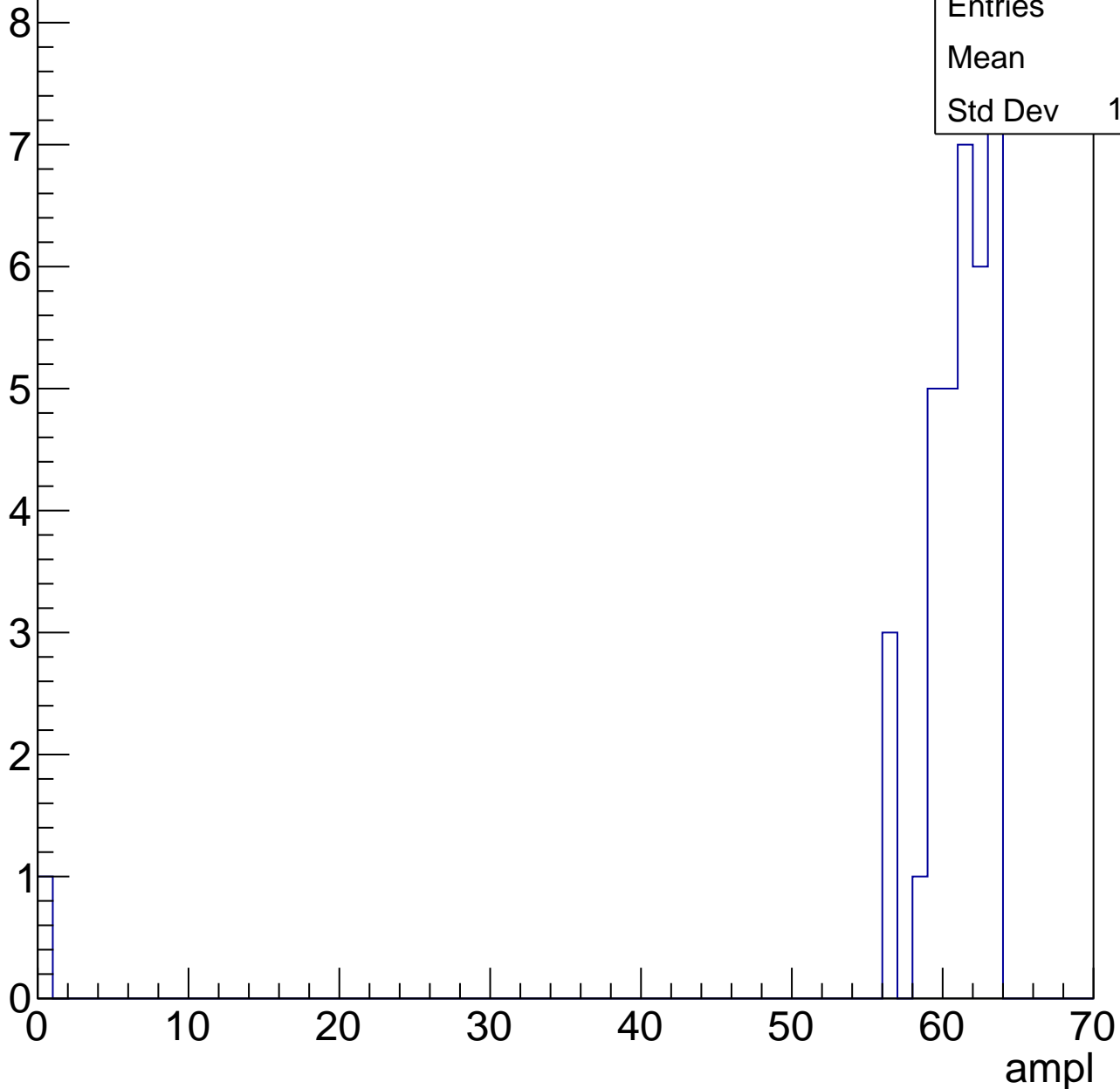


B0L001S, U19-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59
Std Dev	10.17



B0L001S, U19-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch35, adc0

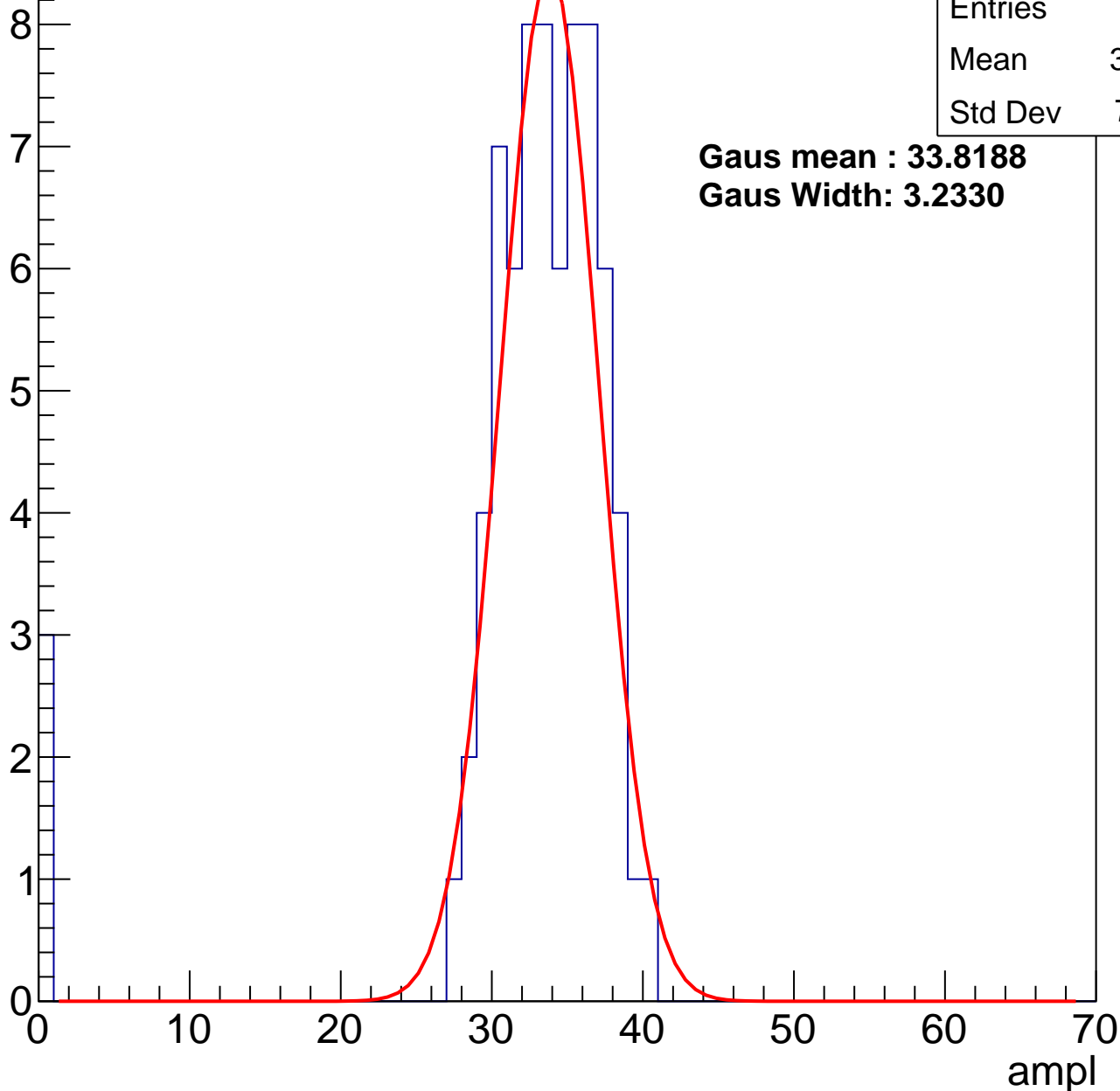
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	32.05
Std Dev	7.251

Gaus mean : 33.8188

Gaus Width: 3.2330



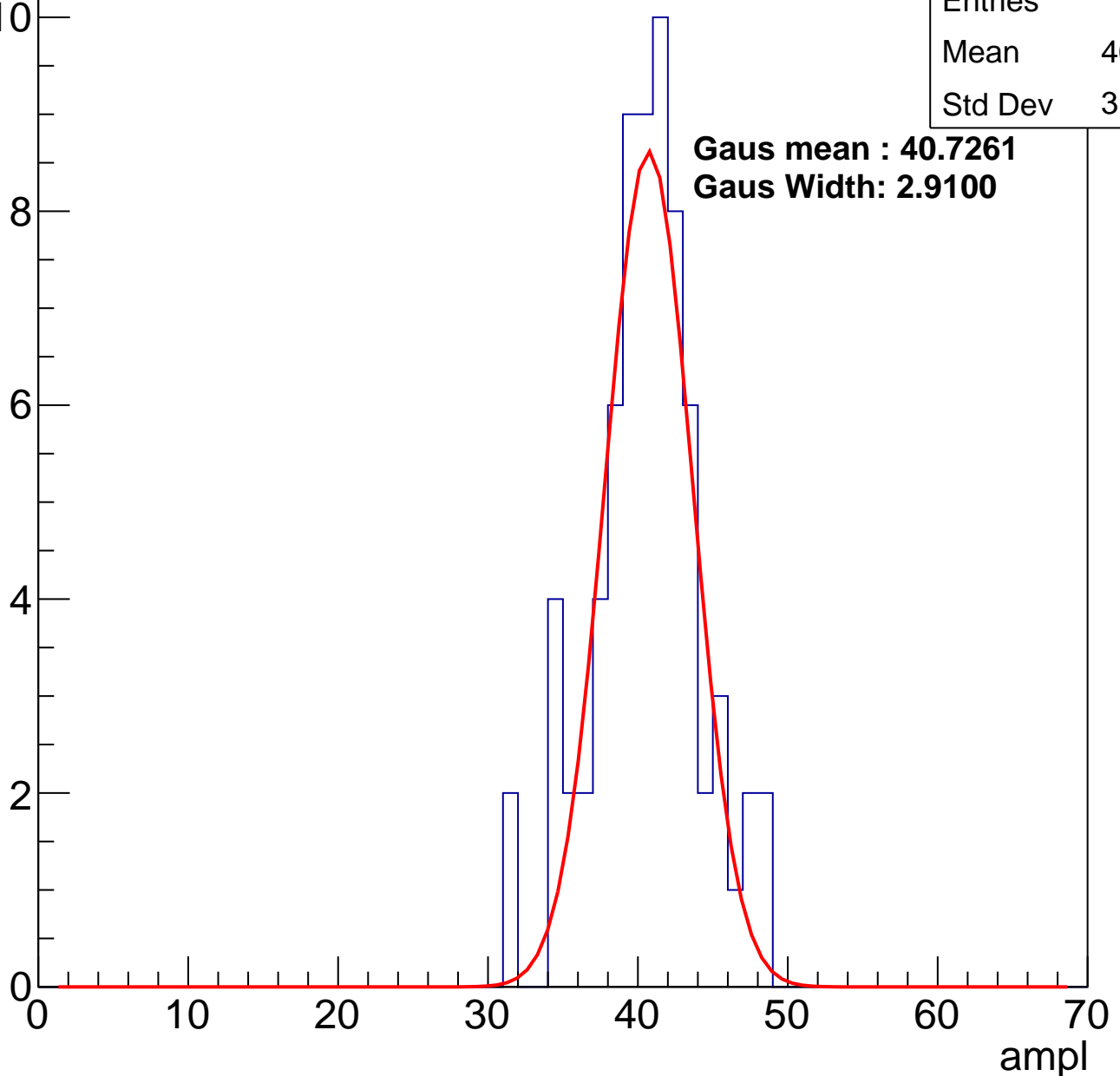
B0L001S, U19-ch35, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	40.14
Std Dev	3.576

Gaus mean : 40.7261
Gaus Width: 2.9100



B0L001S, U19-ch35, adc2

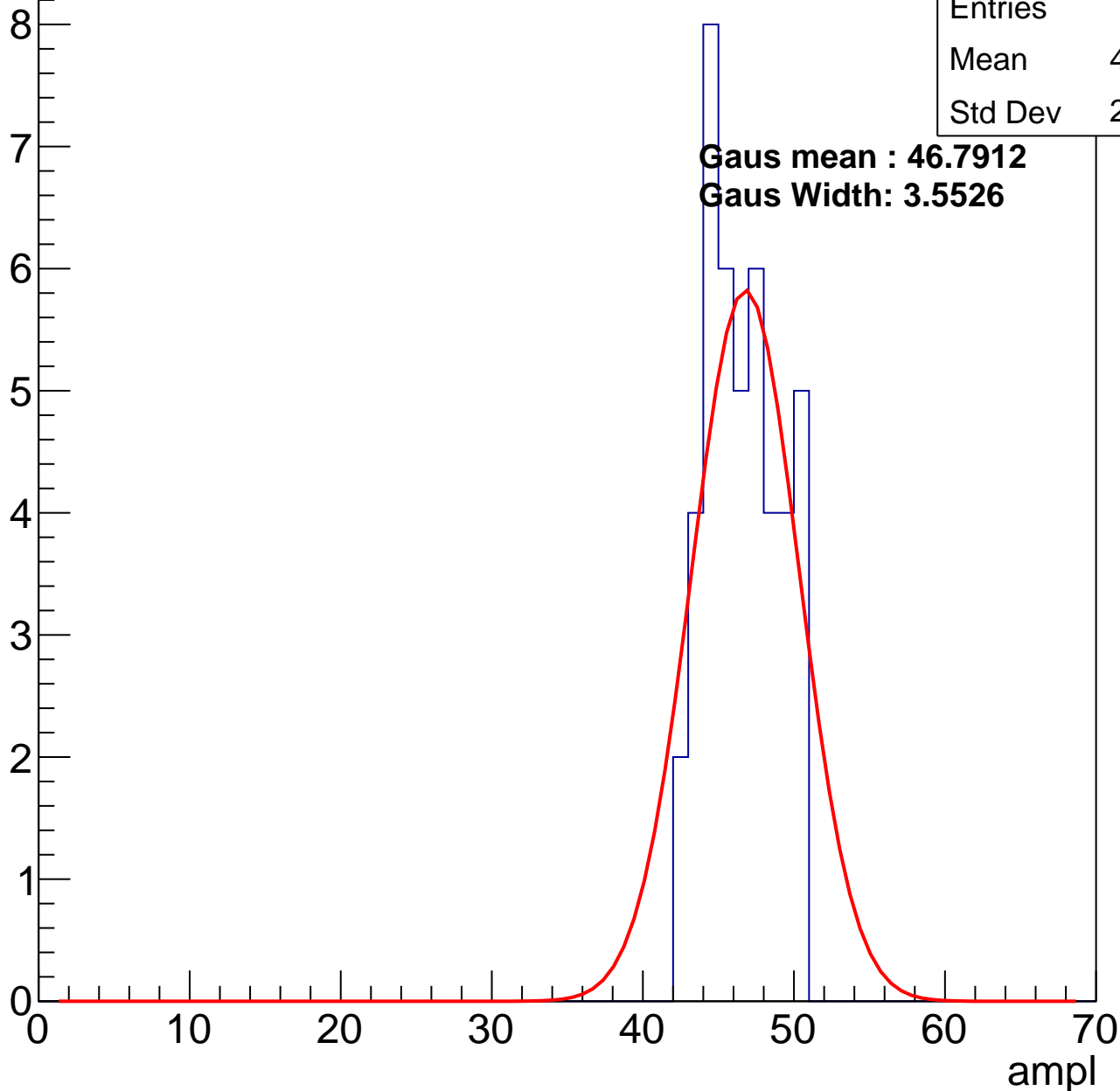
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	46.09
Std Dev	2.353

Gaus mean : 46.7912

Gaus Width: 3.5526

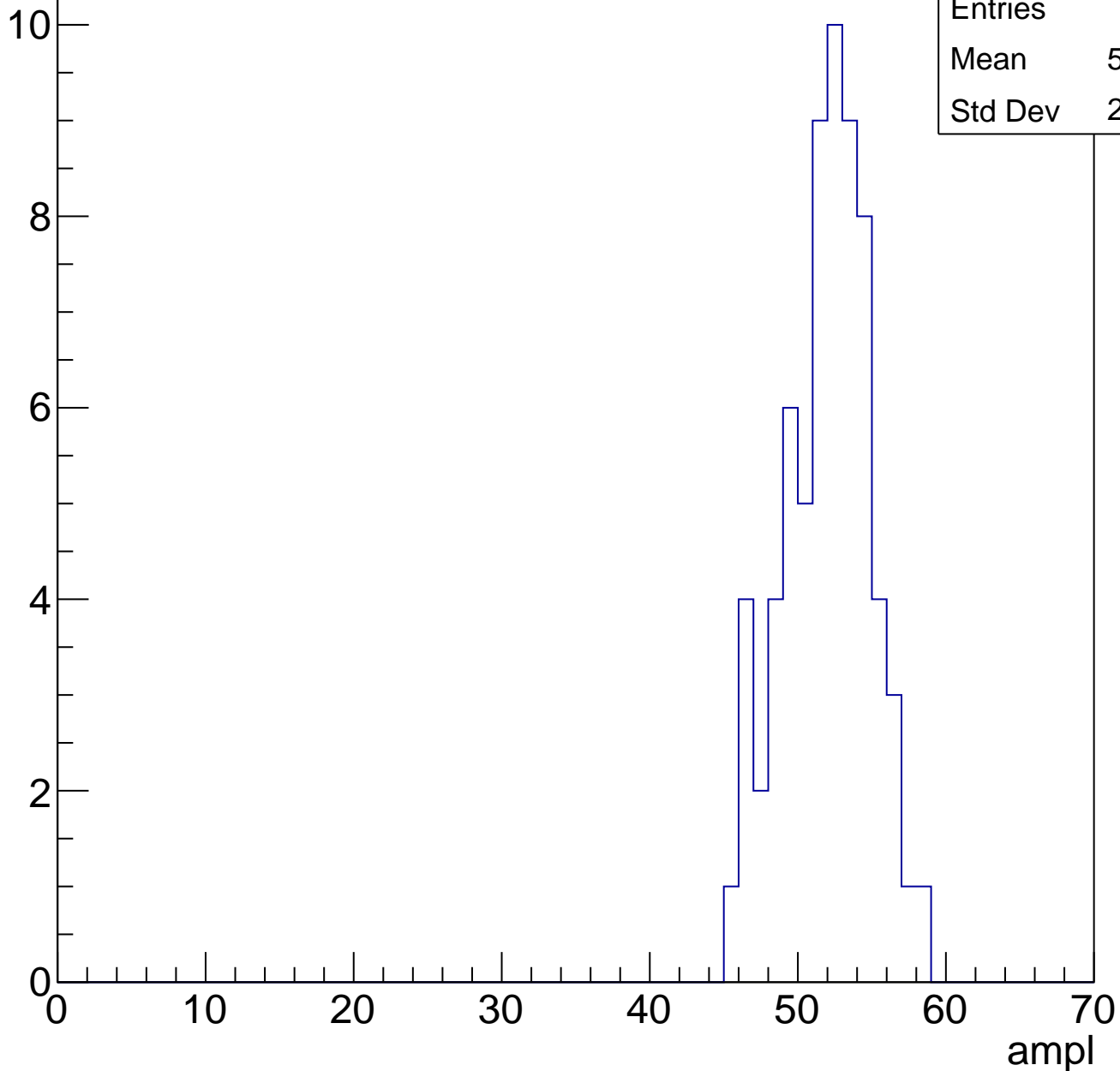


B0L001S, U19-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	51.49
Std Dev	2.888

Entry

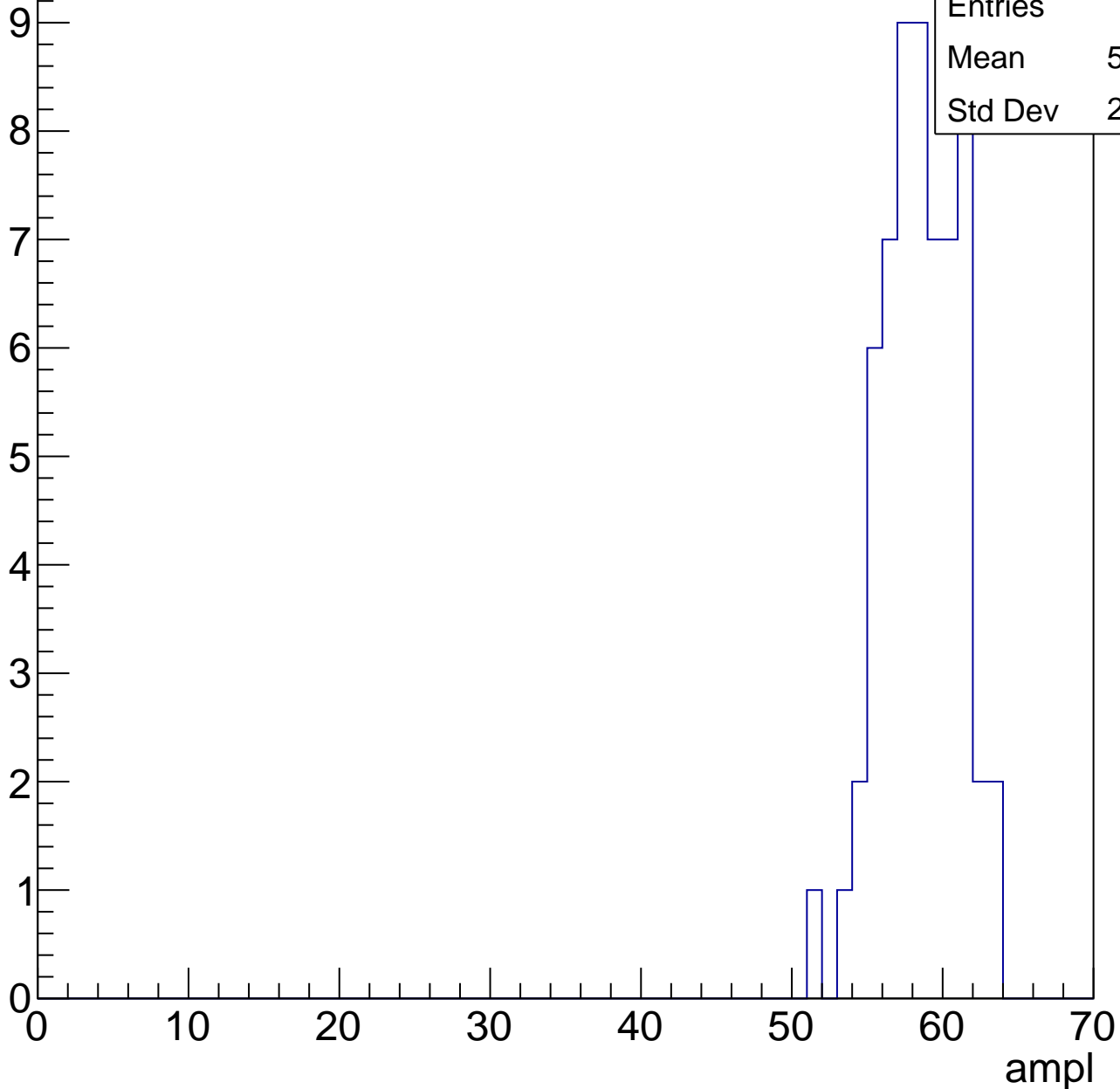


B0L001S, U19-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	58.03
Std Dev	2.515

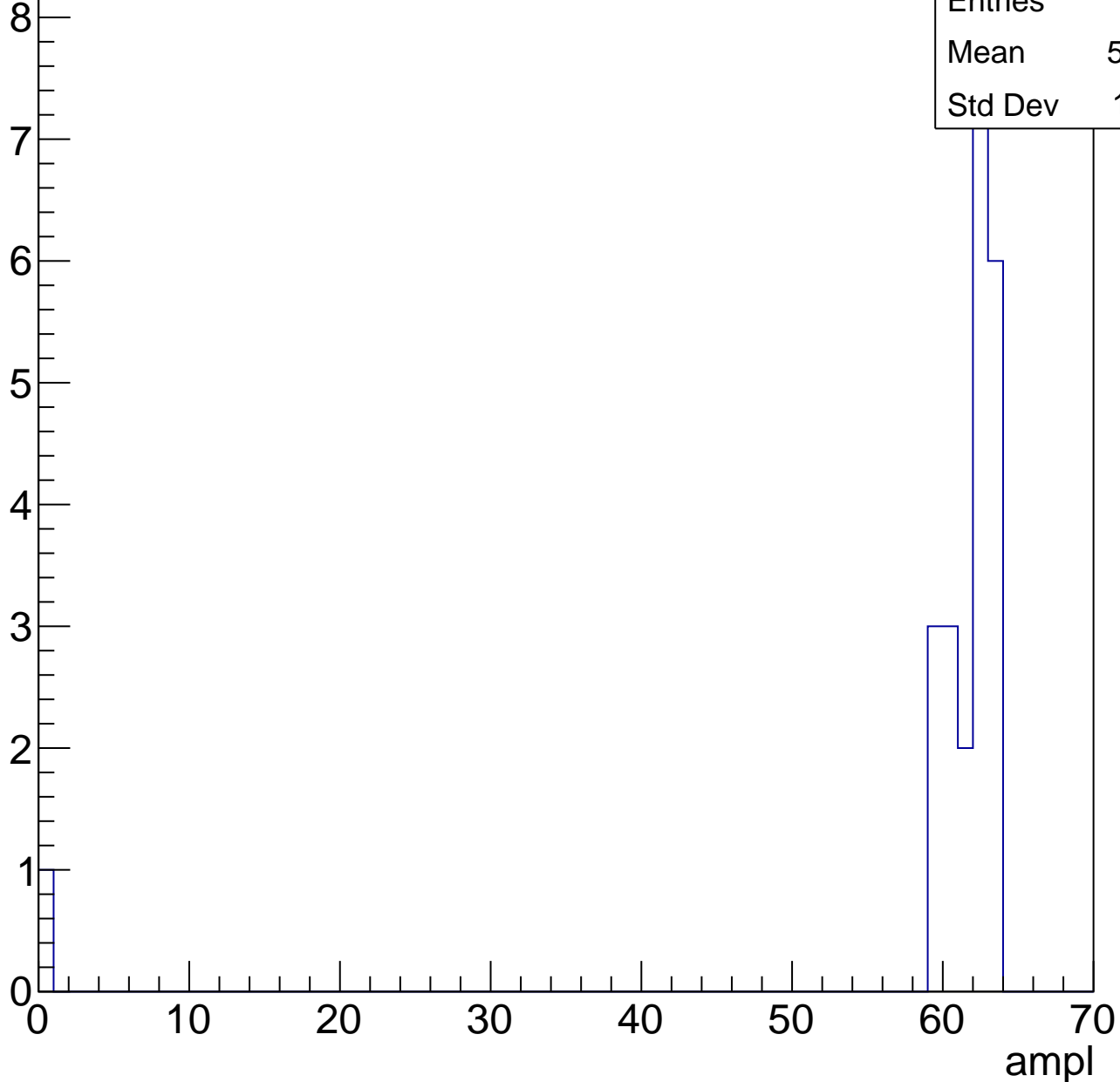


B0L001S, U19-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.83
Std Dev	12.61



B0L001S, U19-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

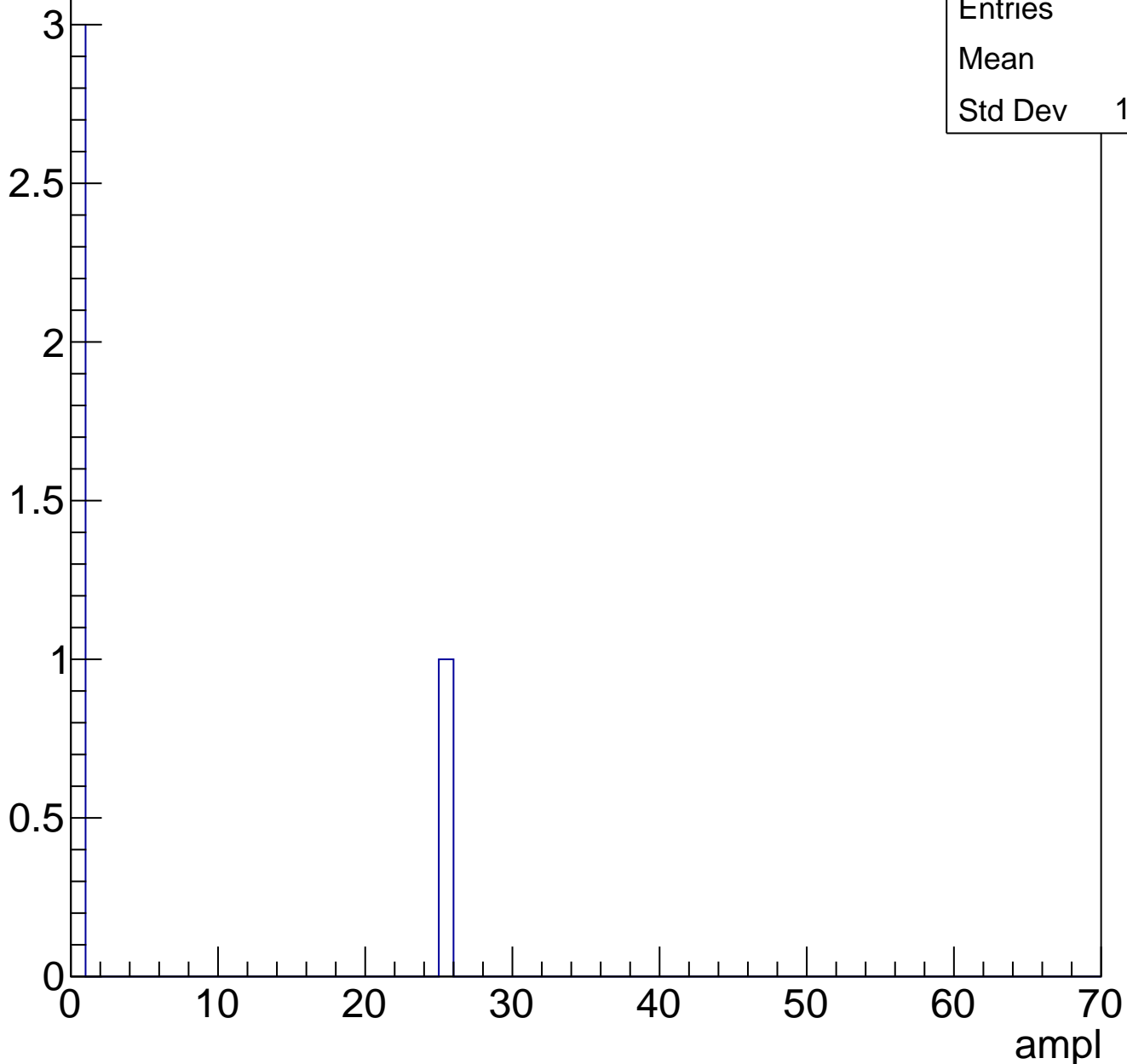
Entry



B0L001S, U19-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch36, adc0

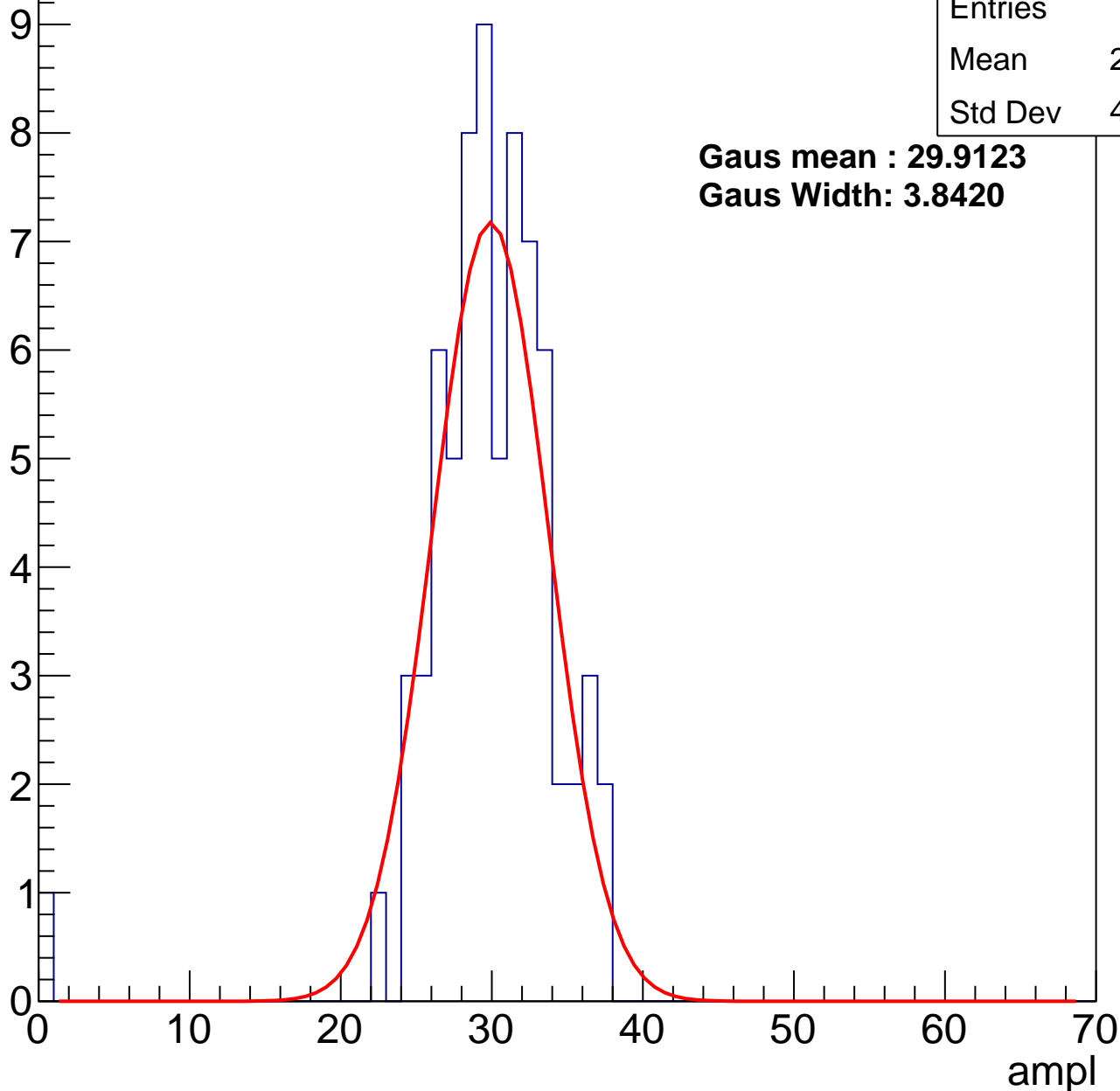
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	29.37
Std Dev	4.862

Gaus mean : 29.9123

Gaus Width: 3.8420



B0L001S, U19-ch36, adc1

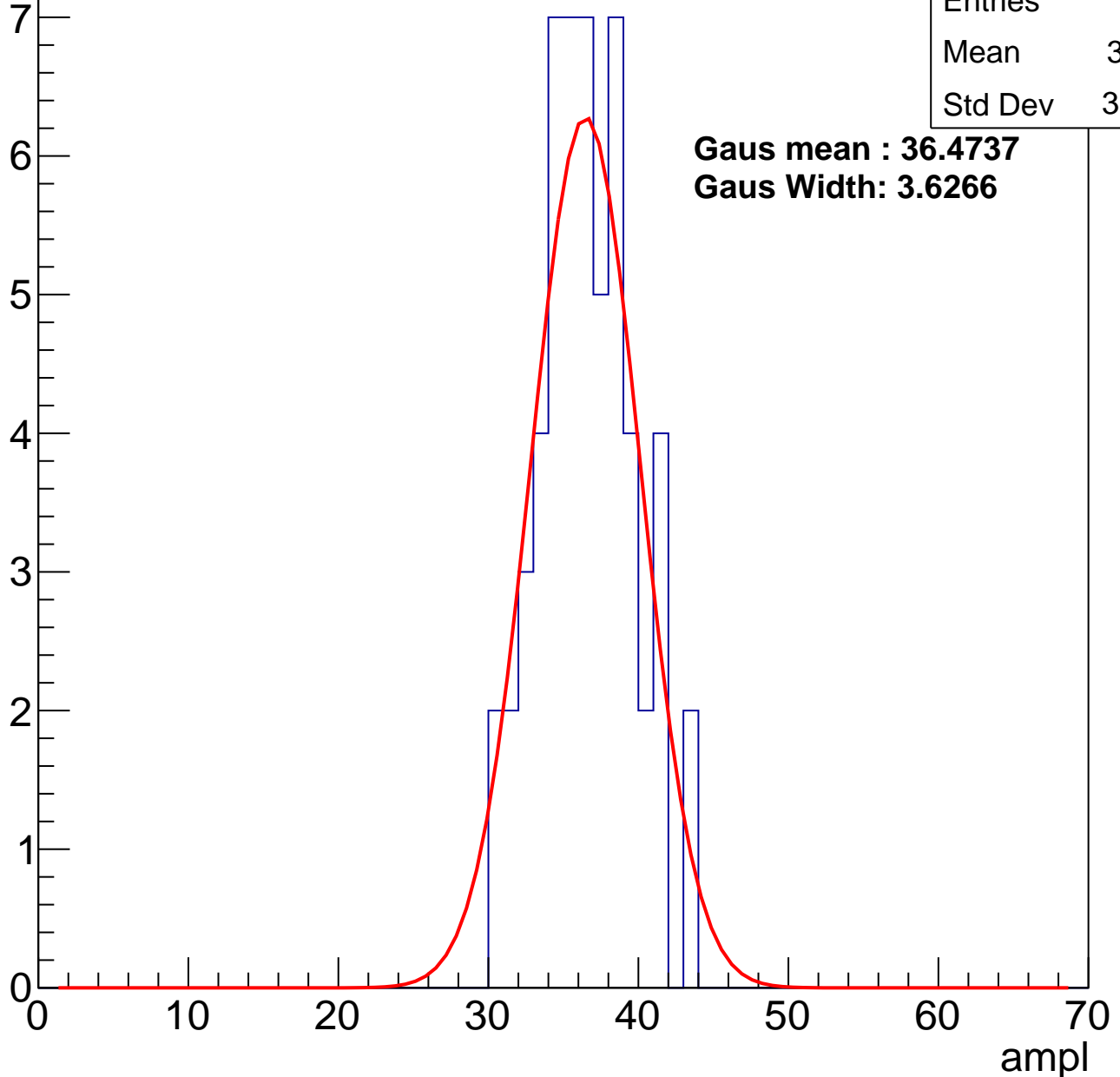
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	36.11
Std Dev	3.103

Gaus mean : 36.4737

Gaus Width: 3.6266



B0L001S, U19-ch36, adc2

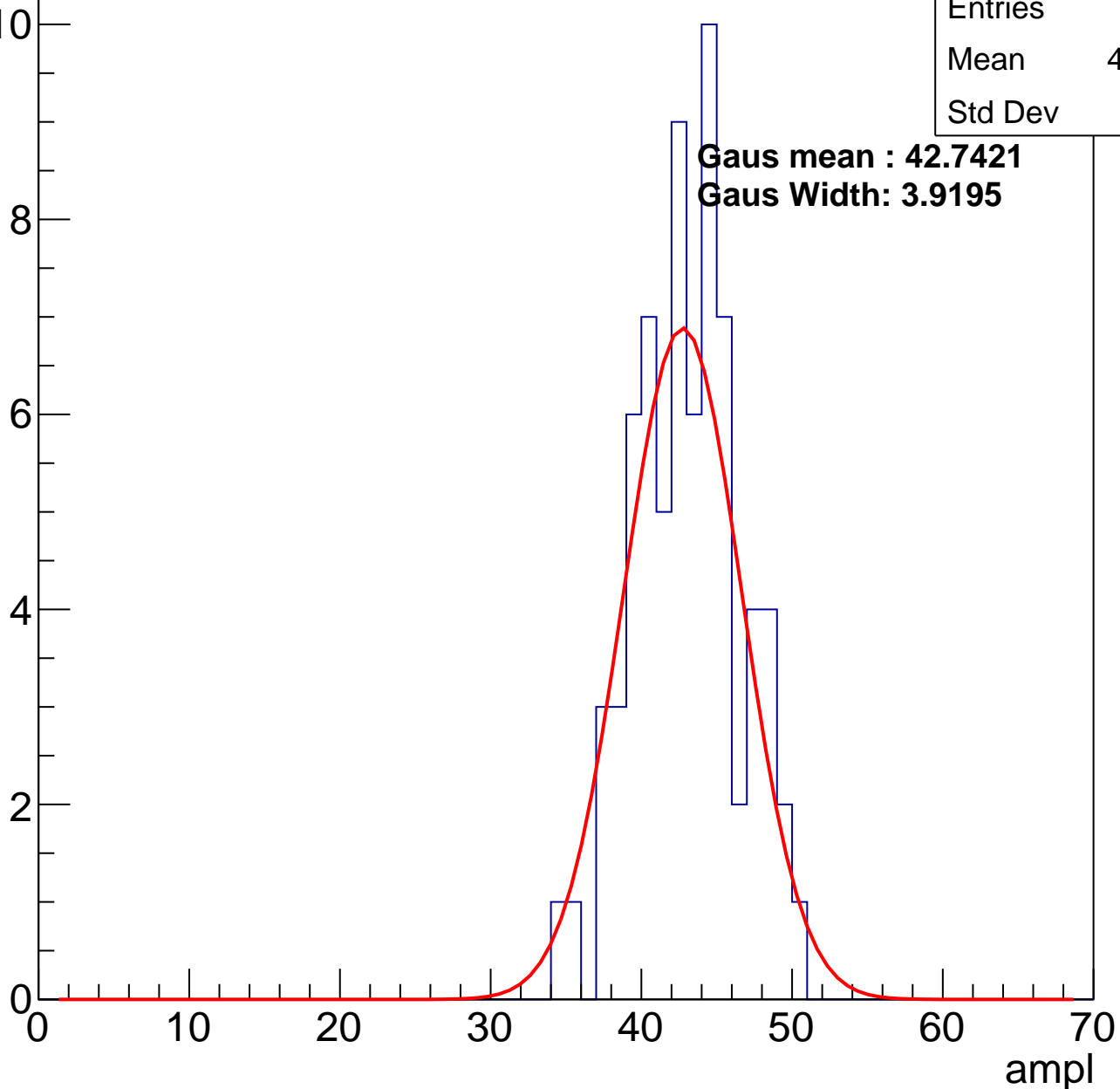
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	42.59
Std Dev	3.45

Gaus mean : 42.7421

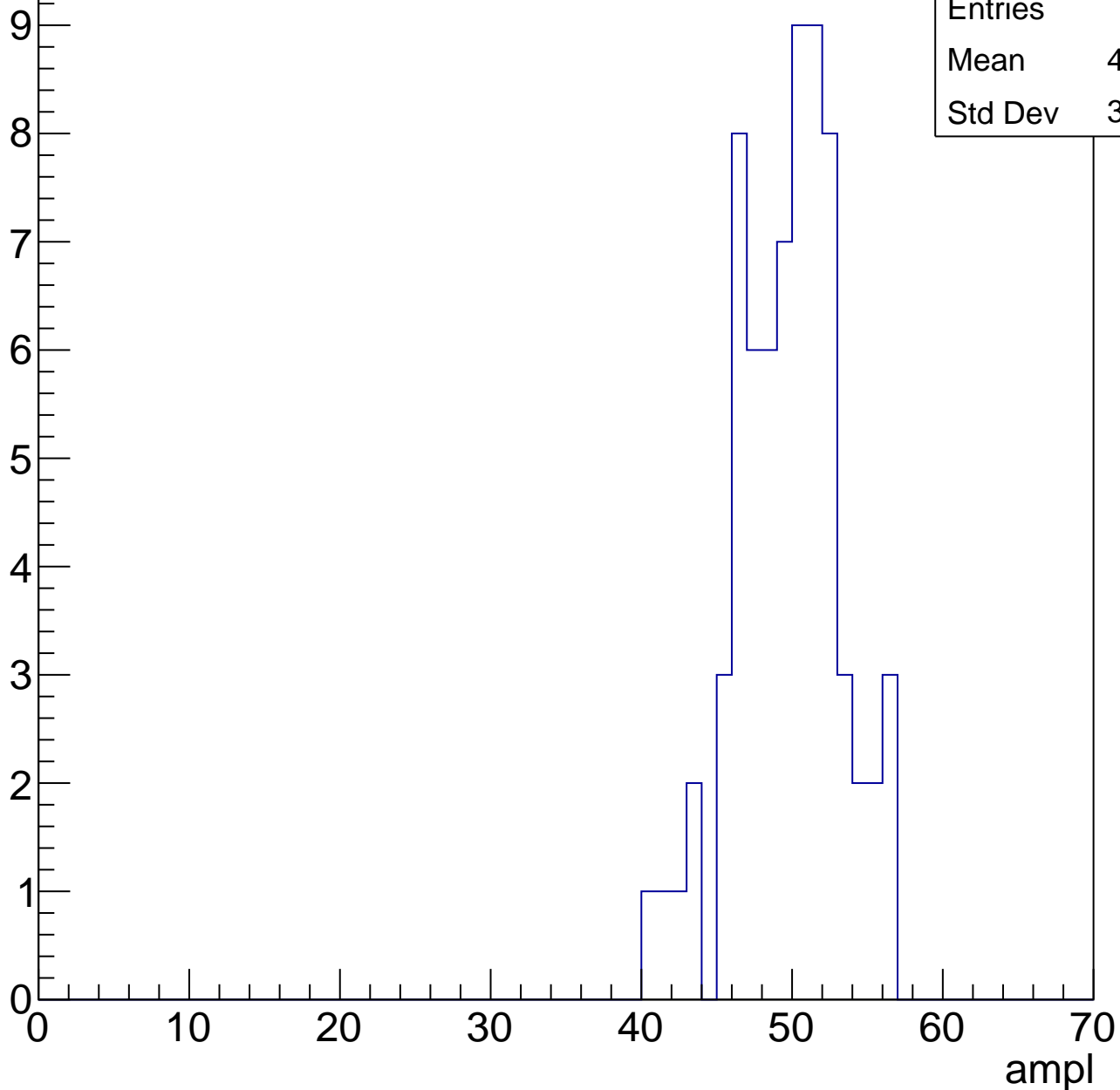
Gaus Width: 3.9195



B0L001S, U19-ch36, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

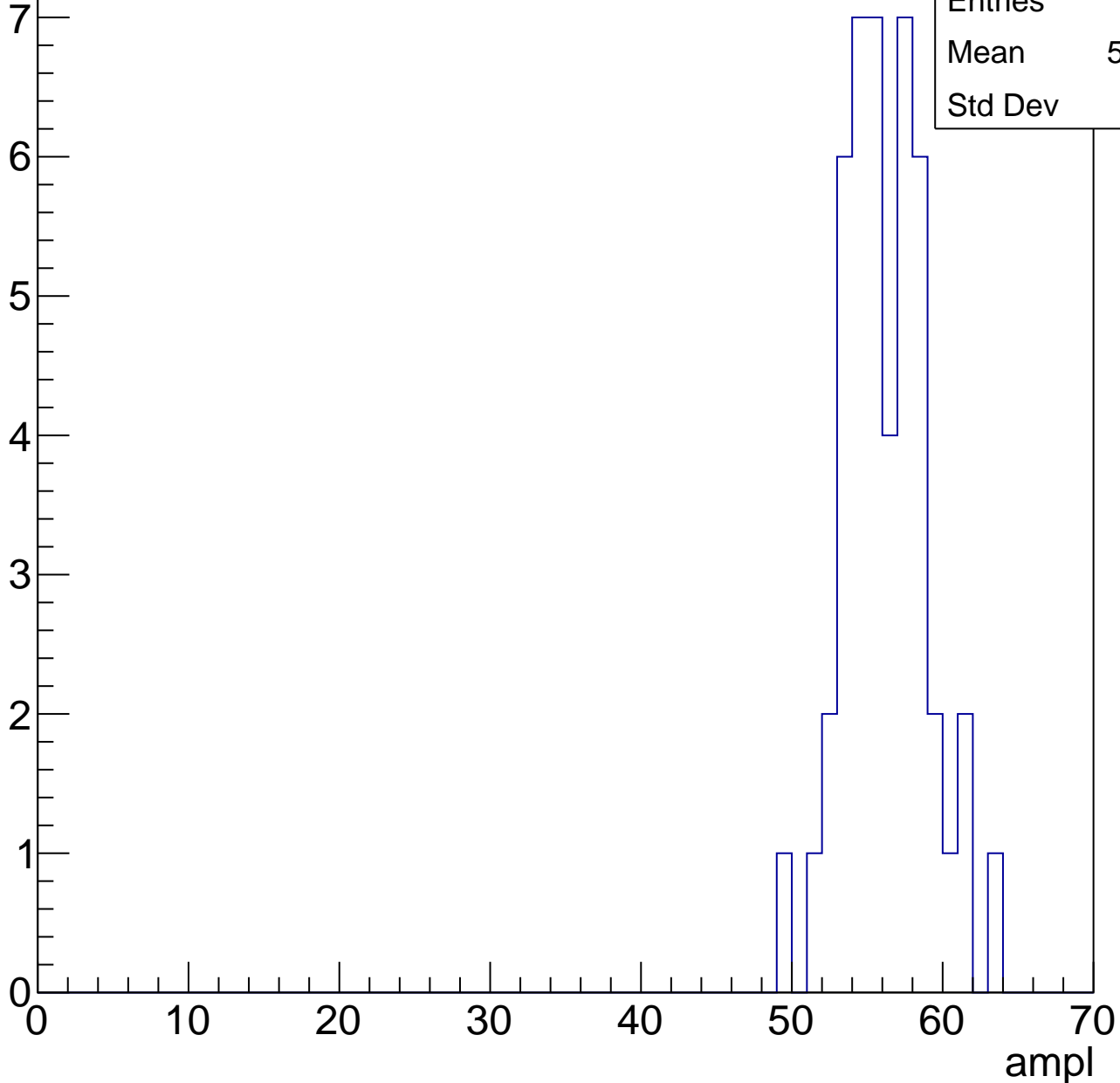


B0L001S, U19-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	55.72
Std Dev	2.75



B0L001S, U19-ch36, adc5

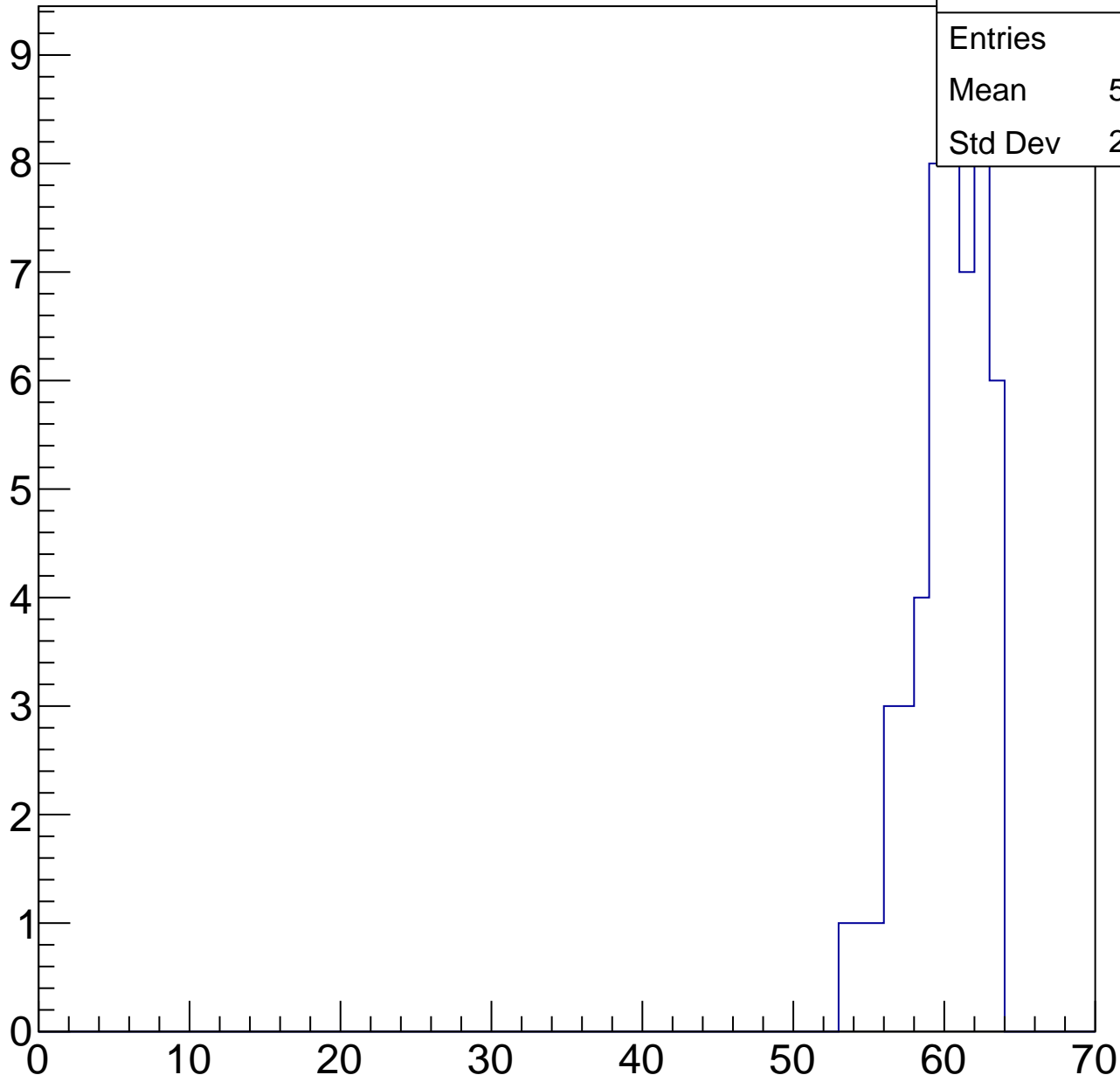
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.73
Std Dev	2.418

ampl



B0L001S, U19-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch36, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch37, adc0

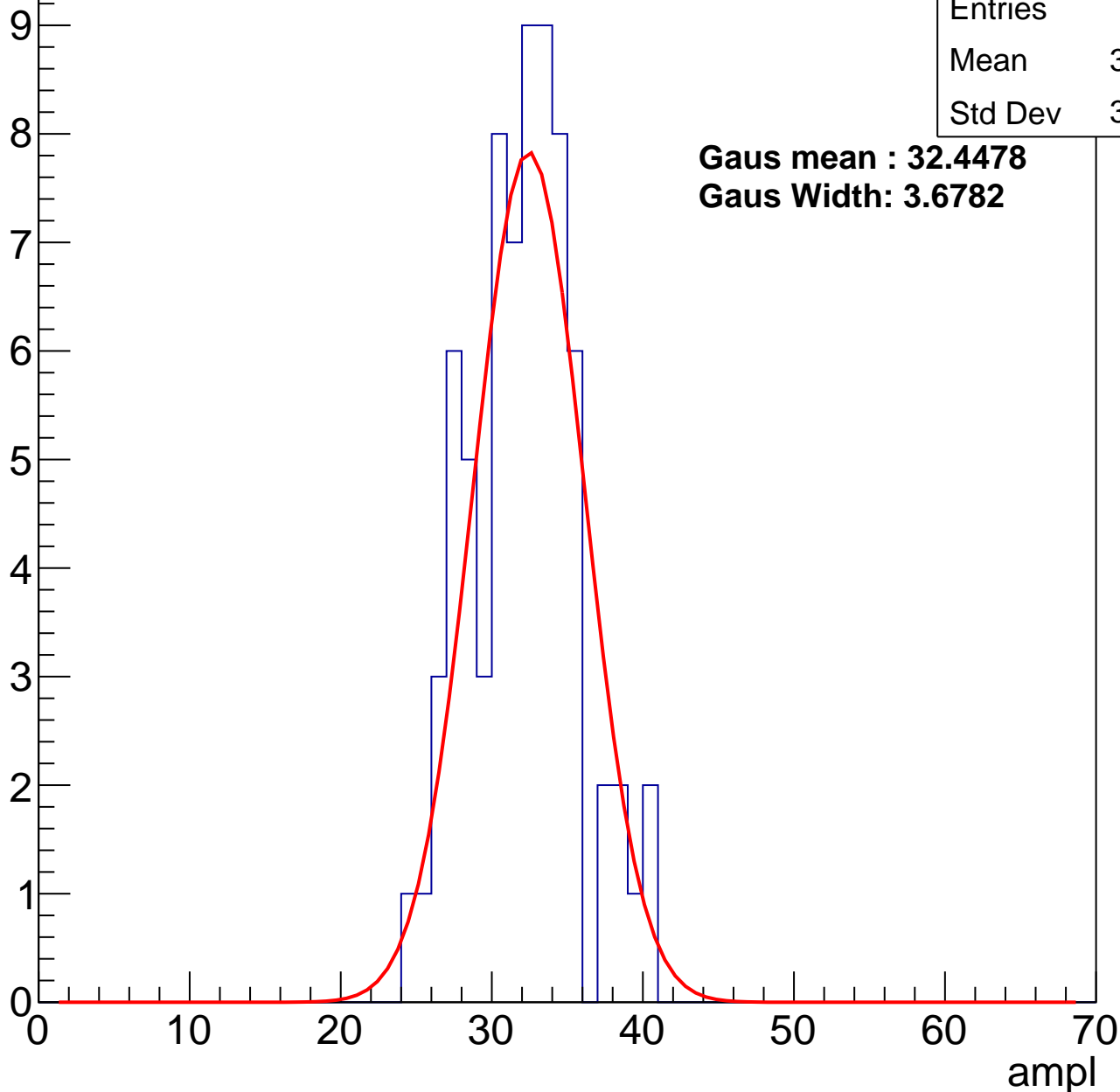
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	31.63
Std Dev	3.513

Gaus mean : 32.4478

Gaus Width: 3.6782



B0L001S, U19-ch37, adc1

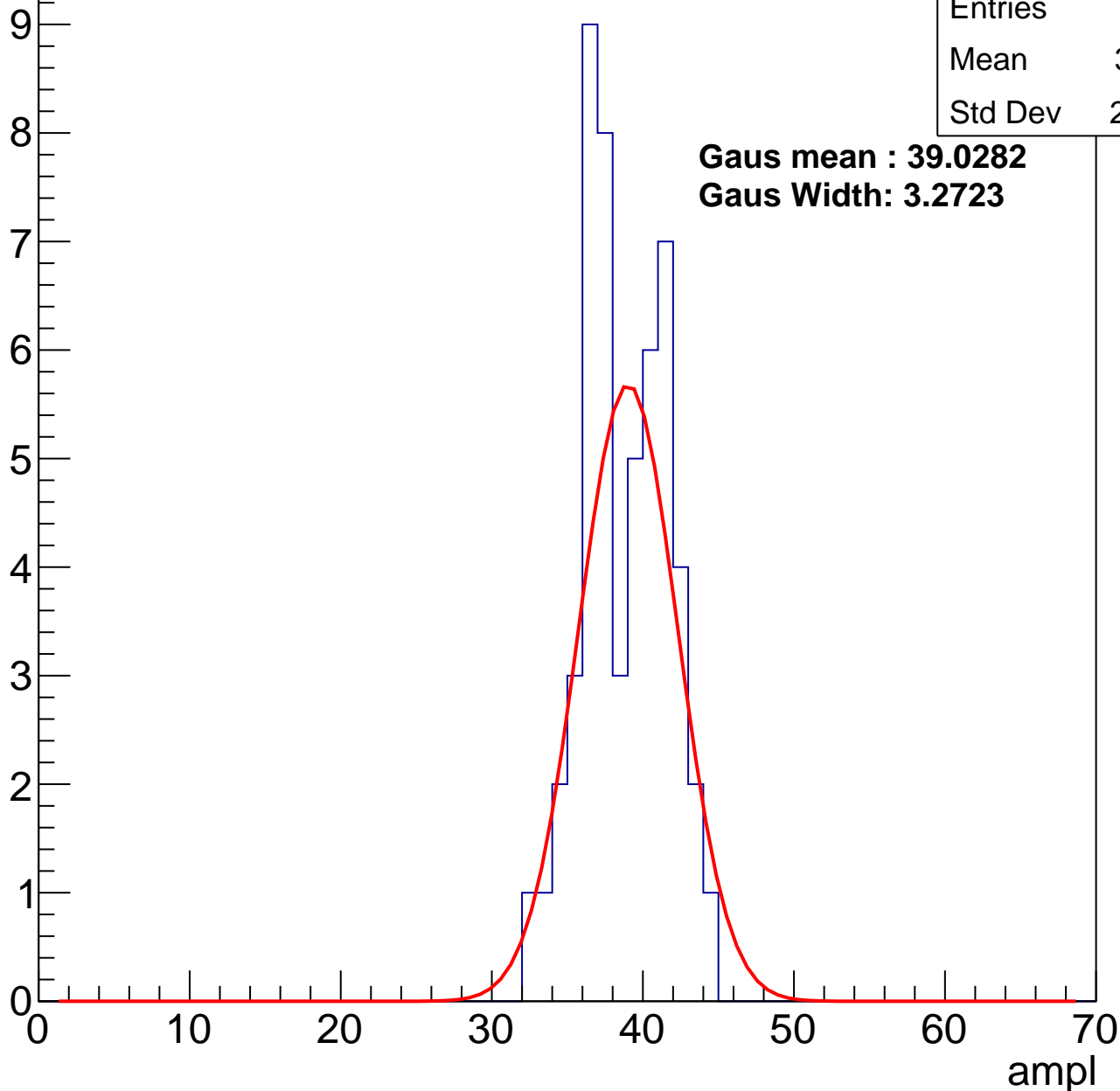
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	38.31
Std Dev	2.777

Gaus mean : 39.0282

Gaus Width: 3.2723



B0L001S, U19-ch37, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	44.03
Std Dev	3.413

Gaus mean : 45.2706

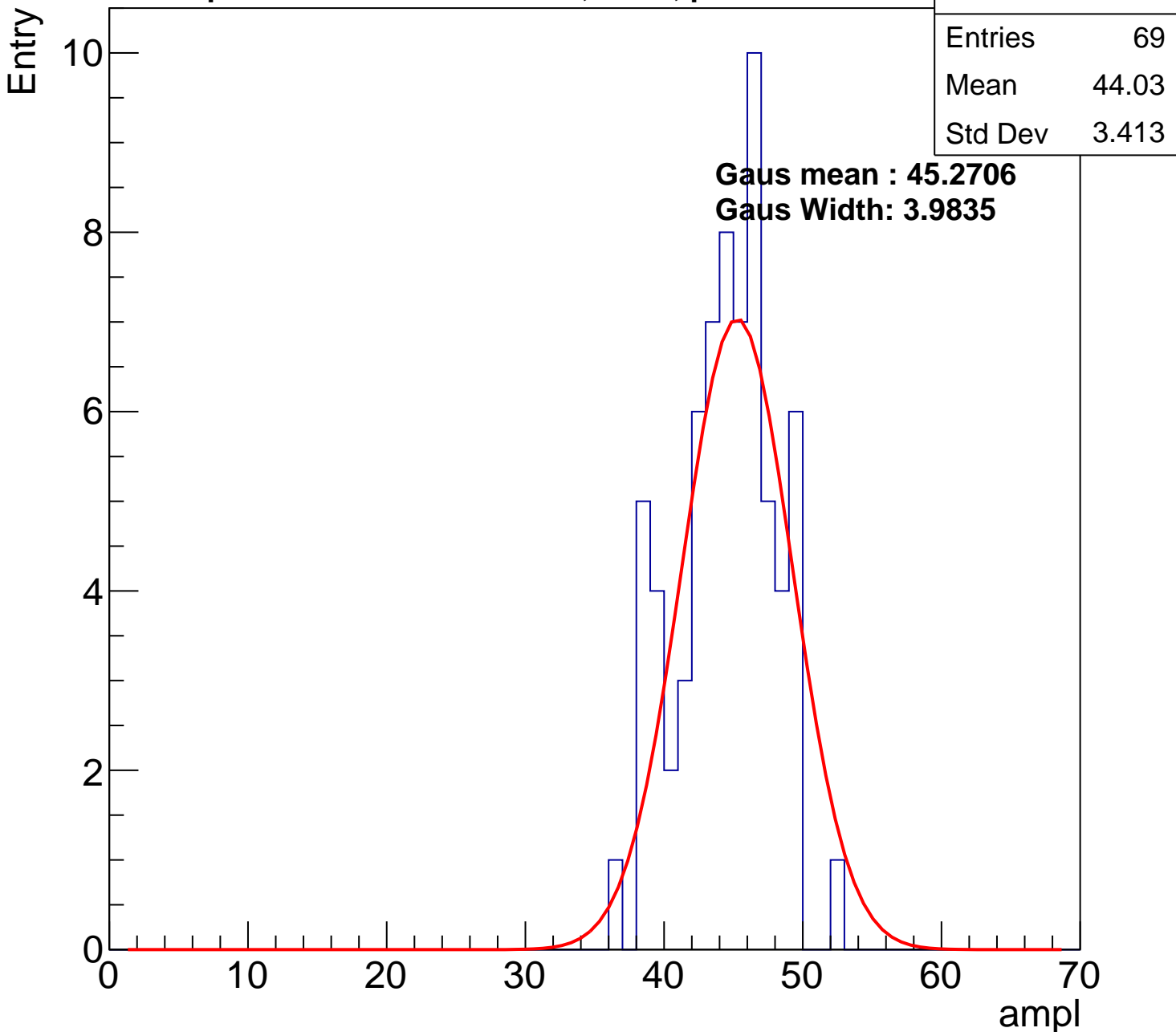
Gaus Width: 3.9835

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	51.59
Std Dev	3.331

Entry

10

8

6

4

2

0

0

10

20

30

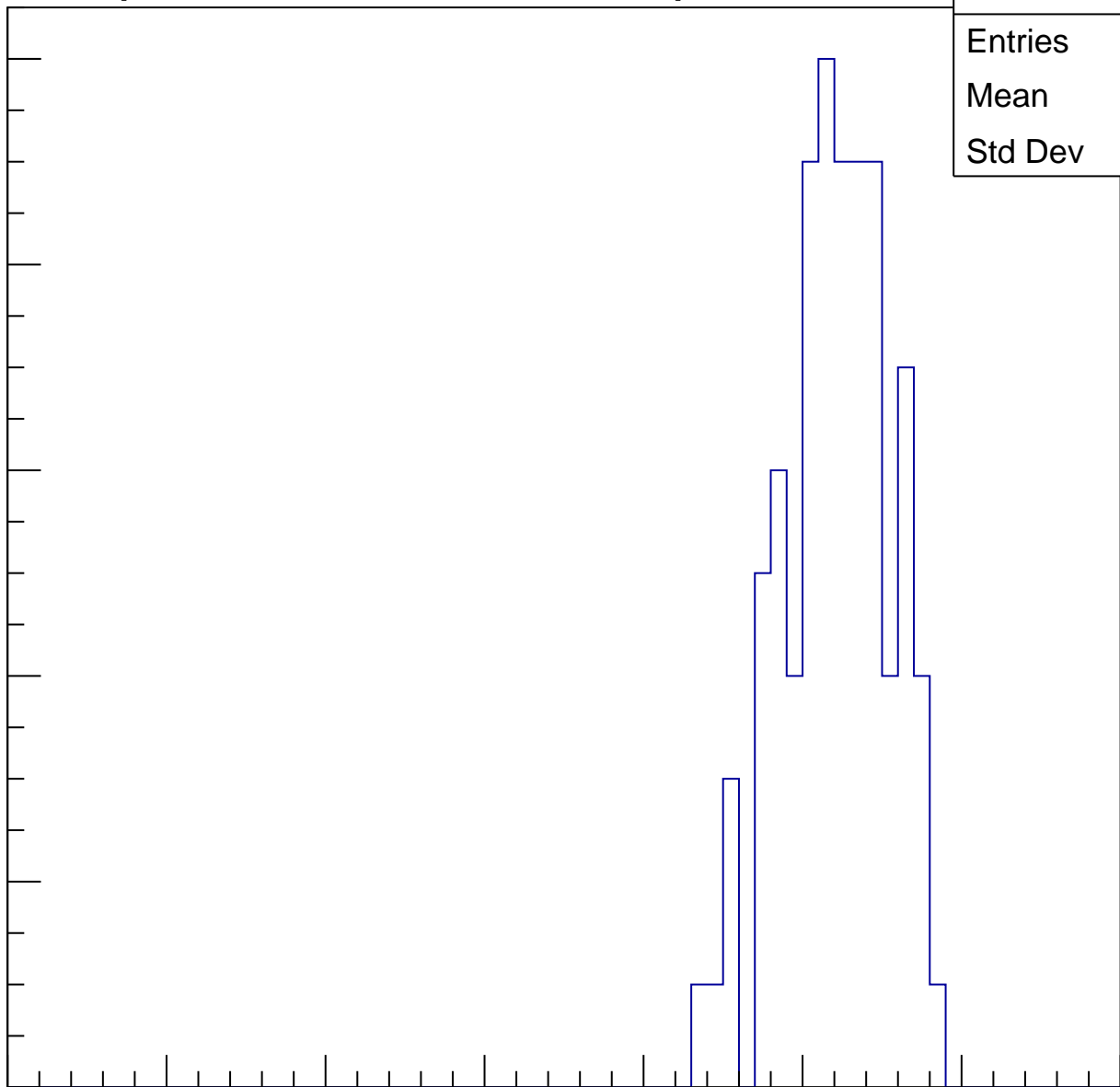
40

50

60

70

ampl

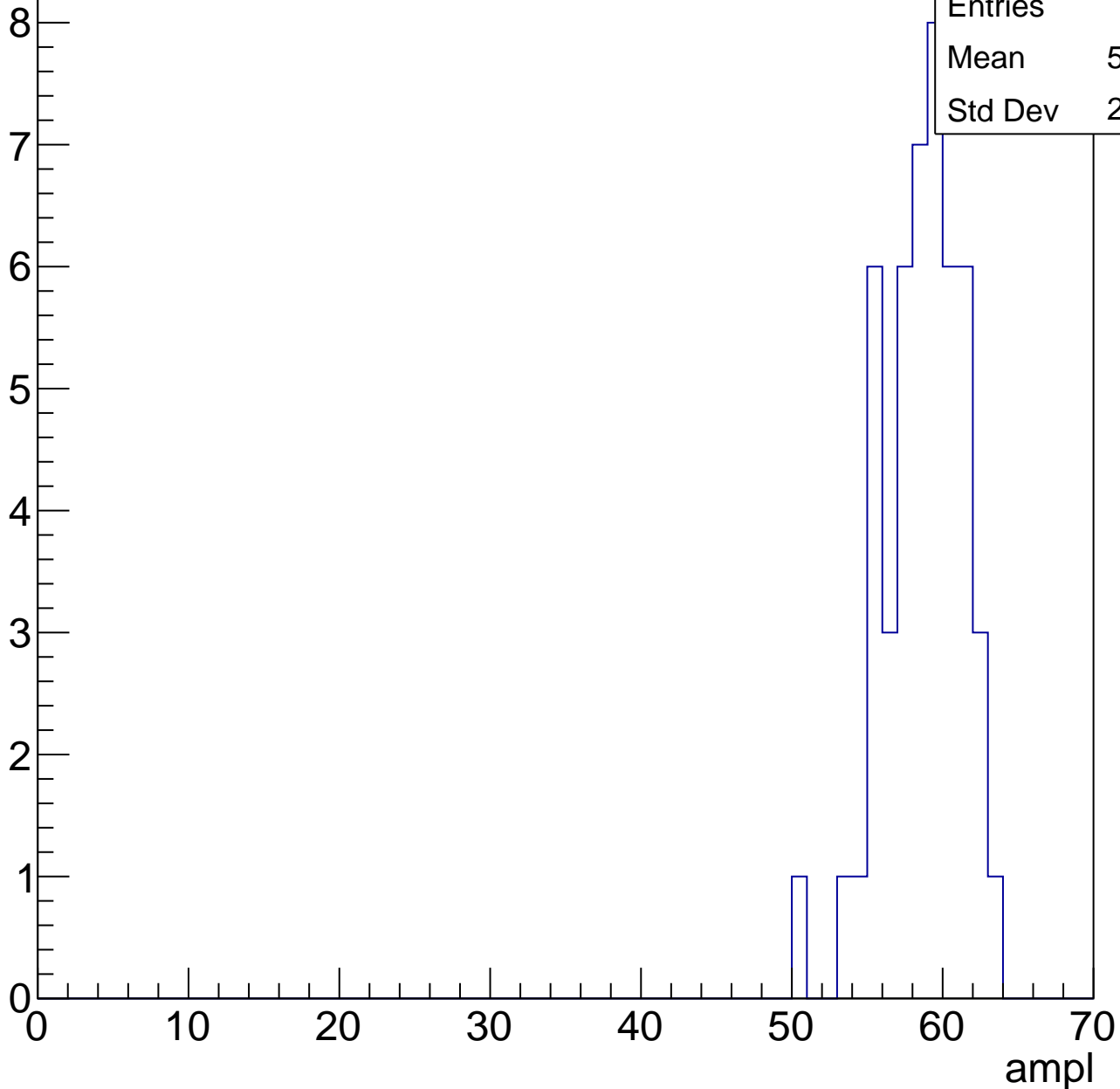


B0L001S, U19-ch37, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.16
Std Dev	2.614

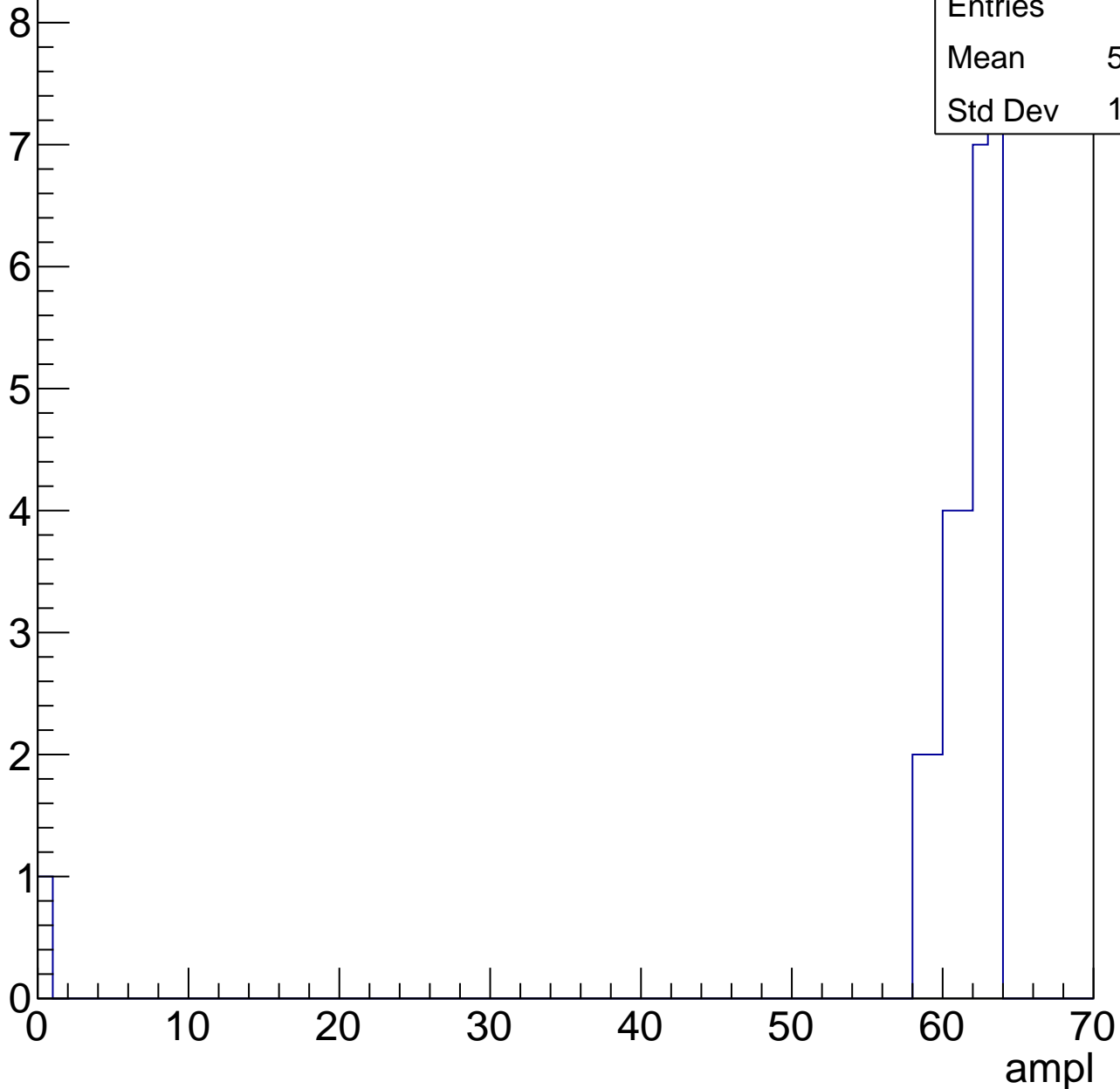


B0L001S, U19-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	59.14
Std Dev	11.49



B0L001S, U19-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch38, adc0

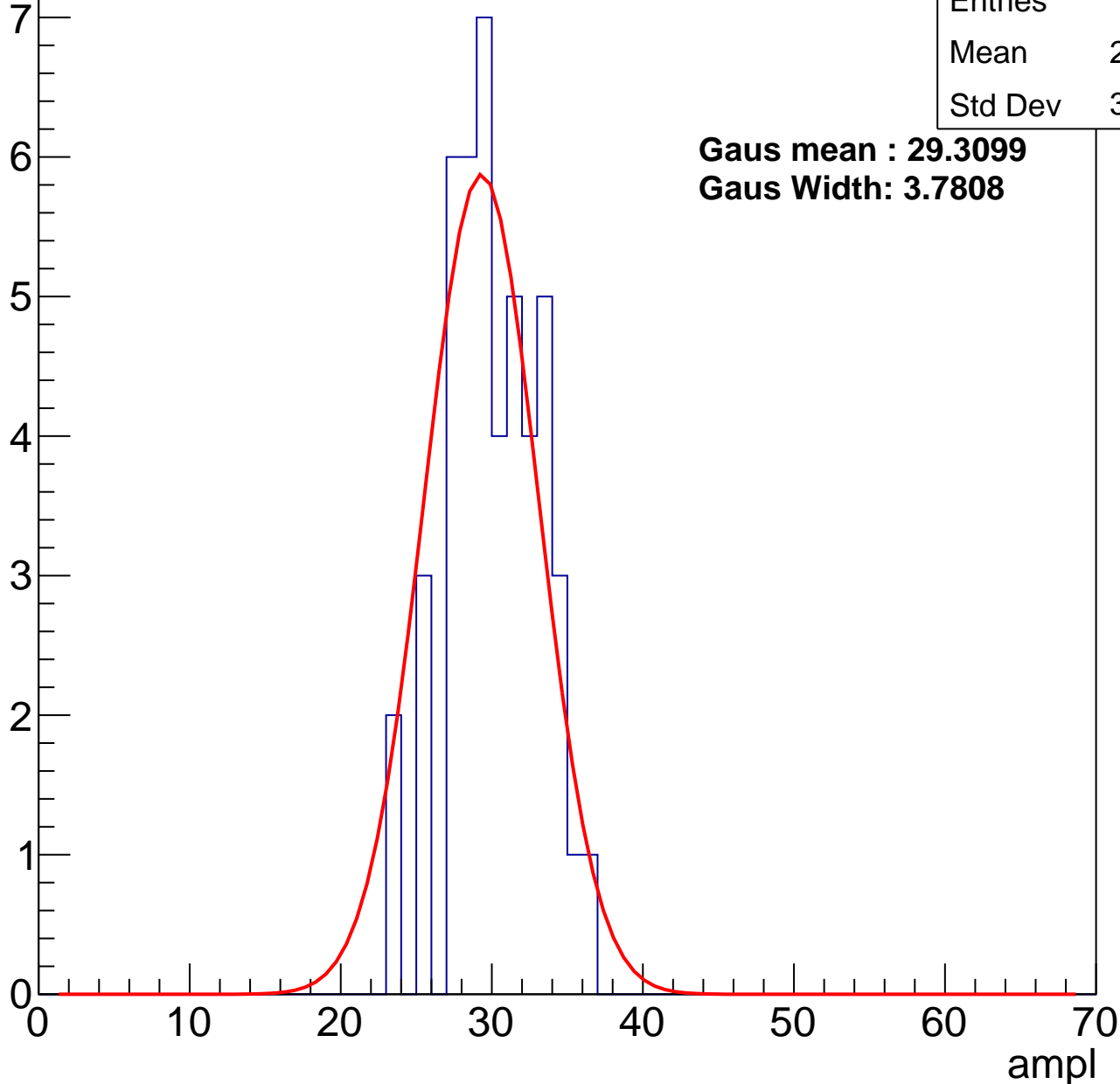
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	29.68
Std Dev	3.018

Gaus mean : 29.3099

Gaus Width: 3.7808



B0L001S, U19-ch38, adc1

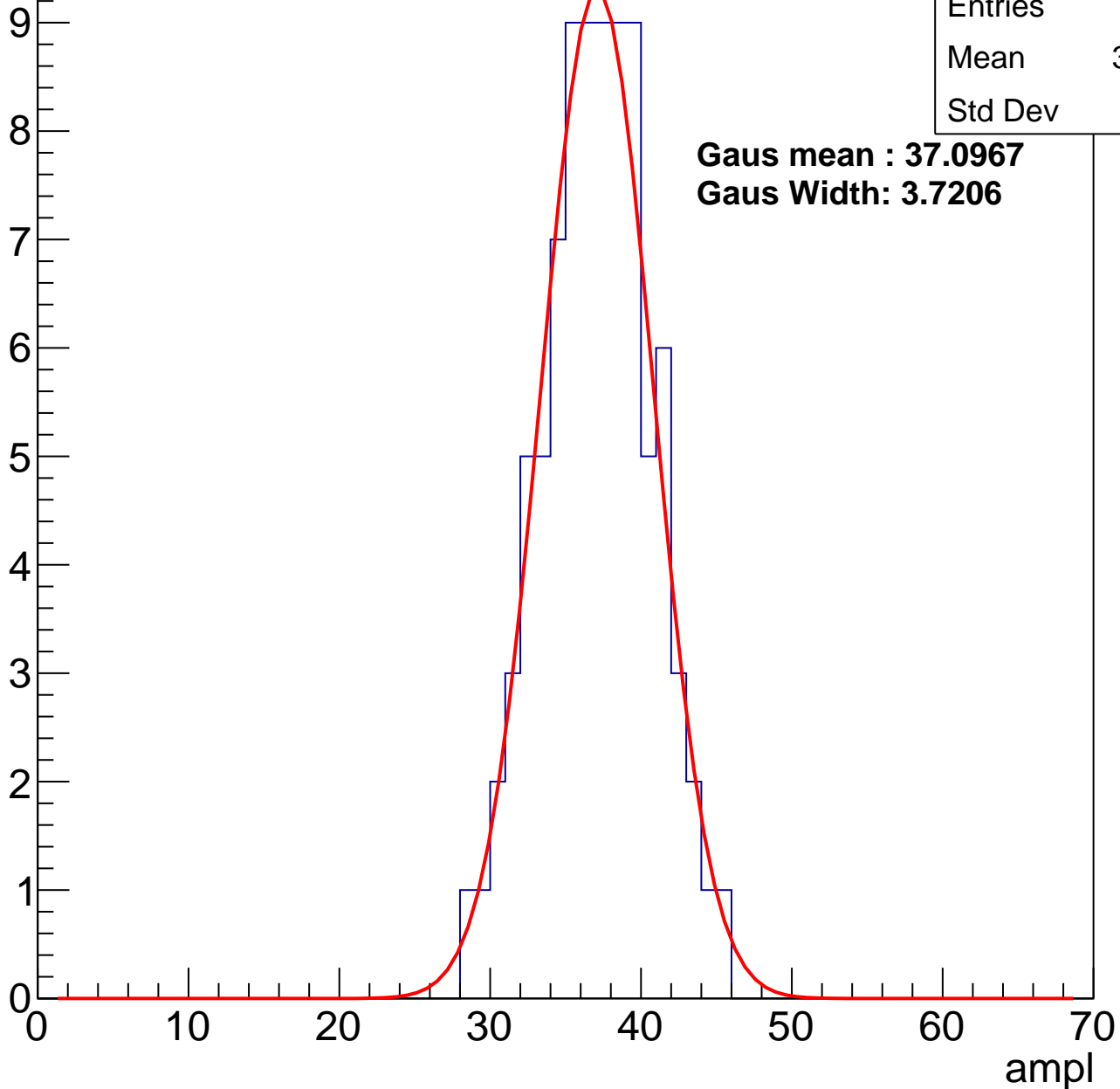
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	87
Mean	36.61
Std Dev	3.55

Gaus mean : 37.0967

Gaus Width: 3.7206



B0L001S, U19-ch38, adc2

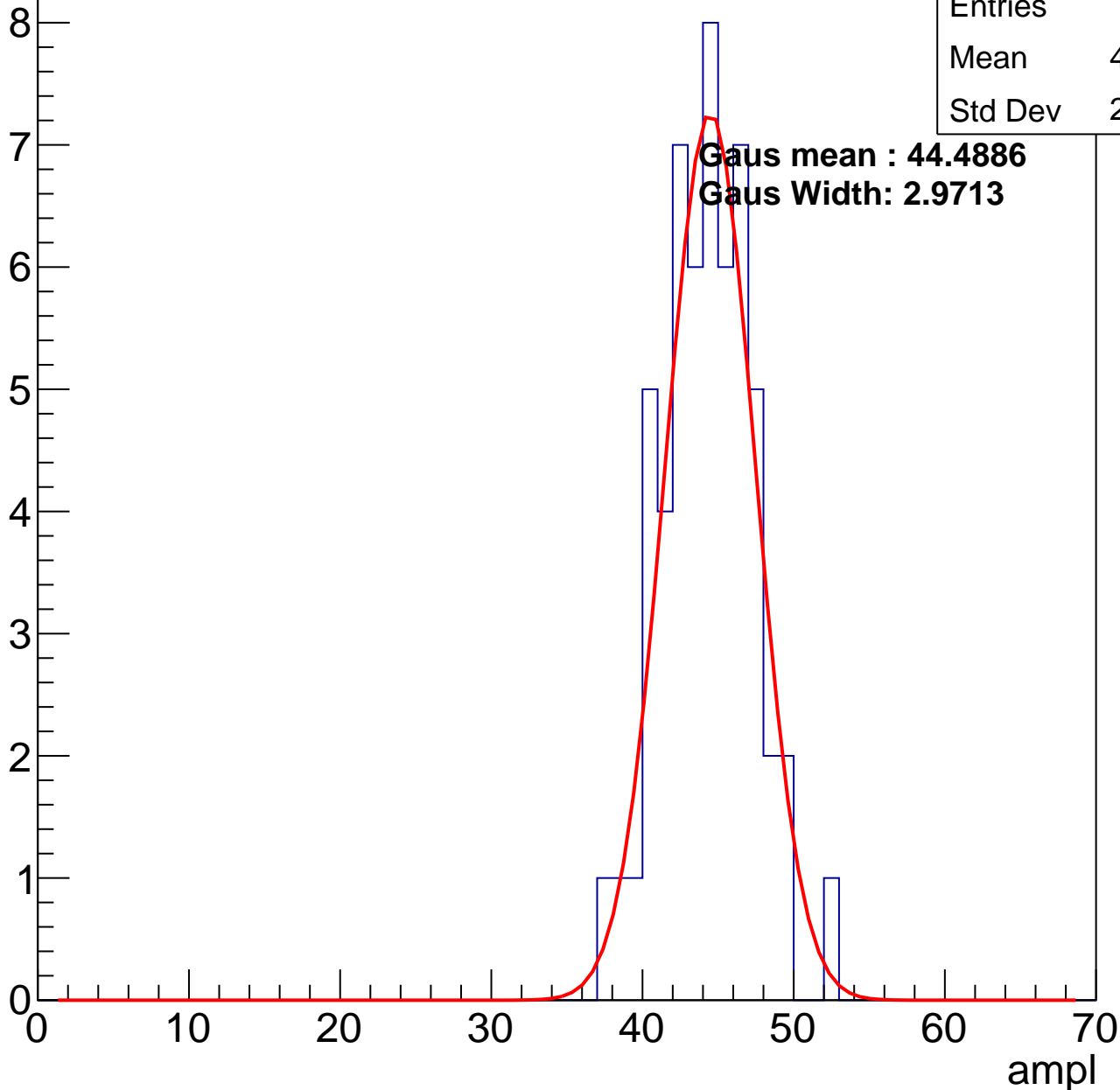
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.84
Std Dev	2.939

Gaus mean : 44.4886

Gaus Width: 2.9713



B0L001S, U19-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	50.44
Std Dev	3.23

Entry

10

8

6

4

2

0

0

10

20

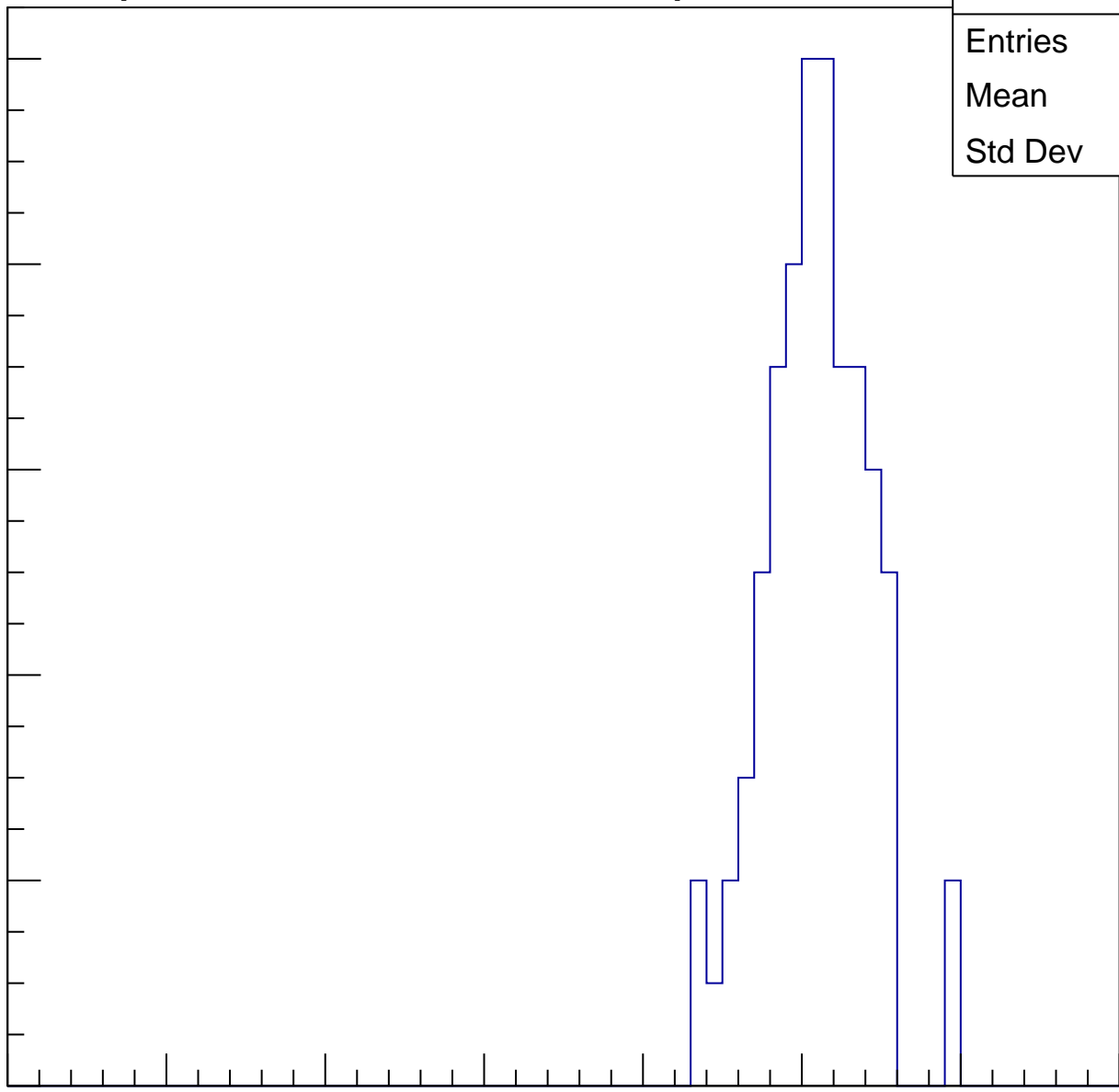
30

40

50

60

ampl



B0L001S, U19-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	53
Mean	56.53
Std Dev	3.225

Entry

10

8

6

4

2

0

0

10

20

30

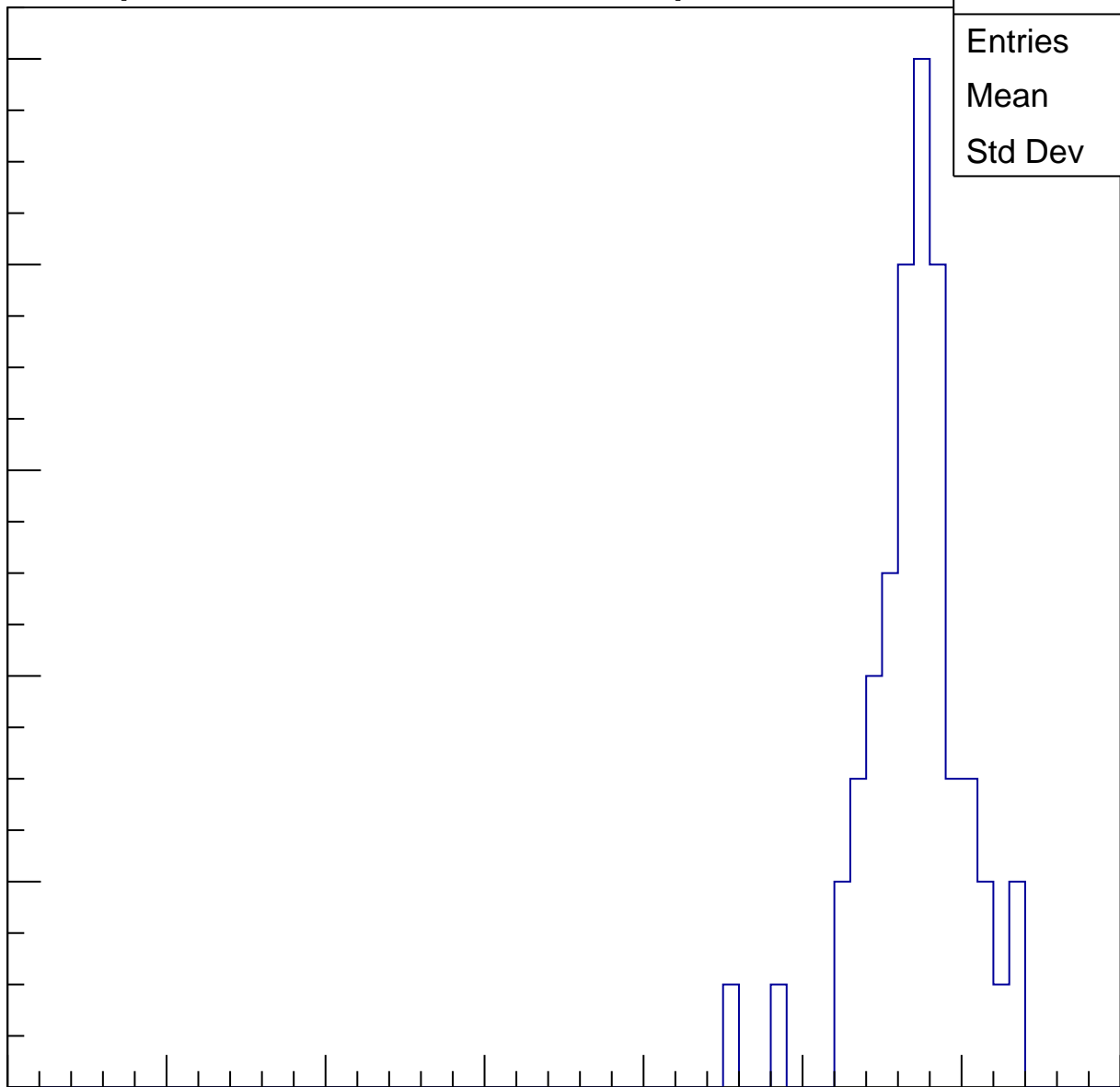
40

50

60

70

ampl

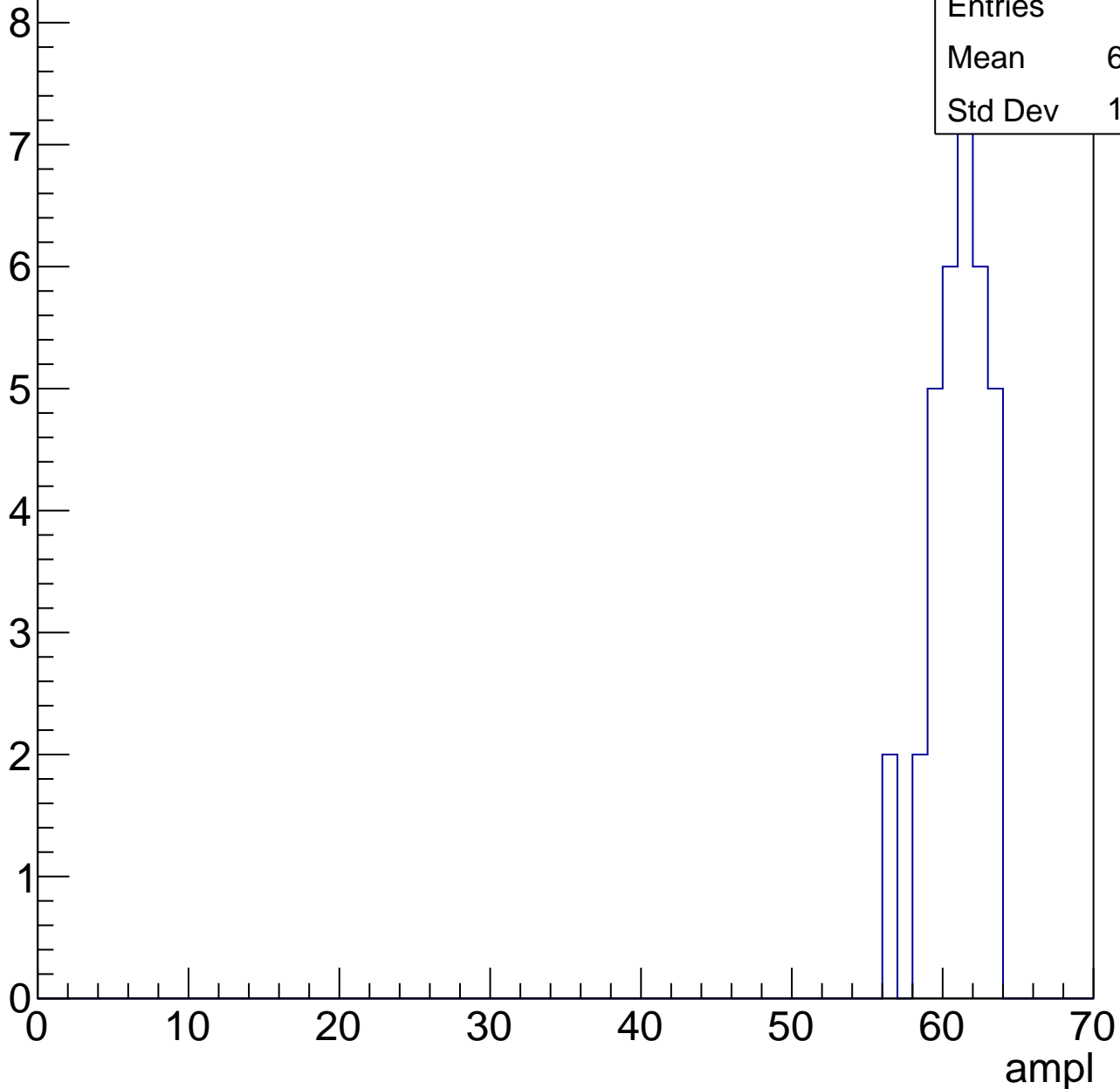


B0L001S, U19-ch38, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

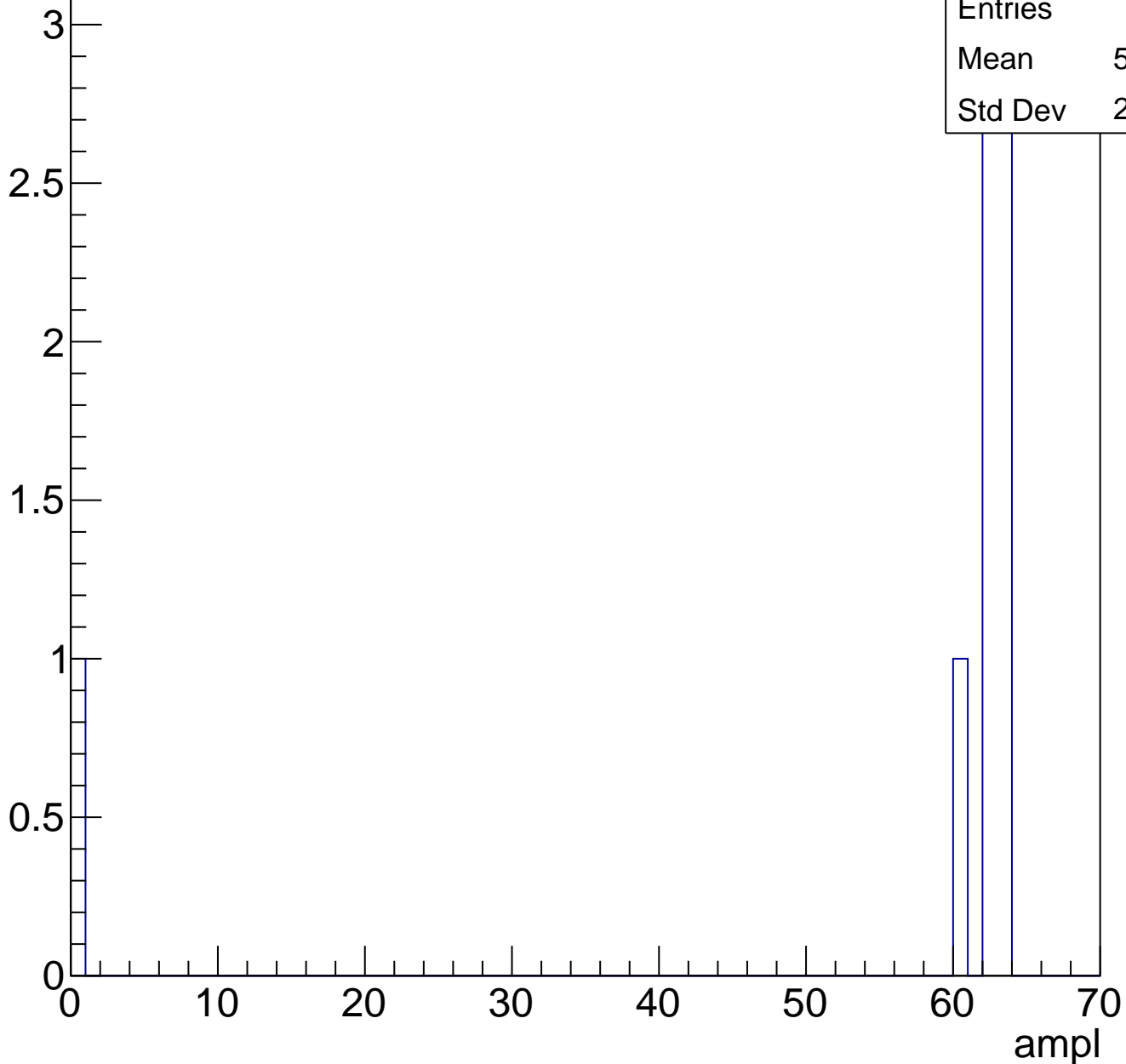
Entries	34
Mean	60.53
Std Dev	1.819



B0L001S, U19-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch39, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	30.51
Std Dev	5.031

Gaus mean : 31.3926

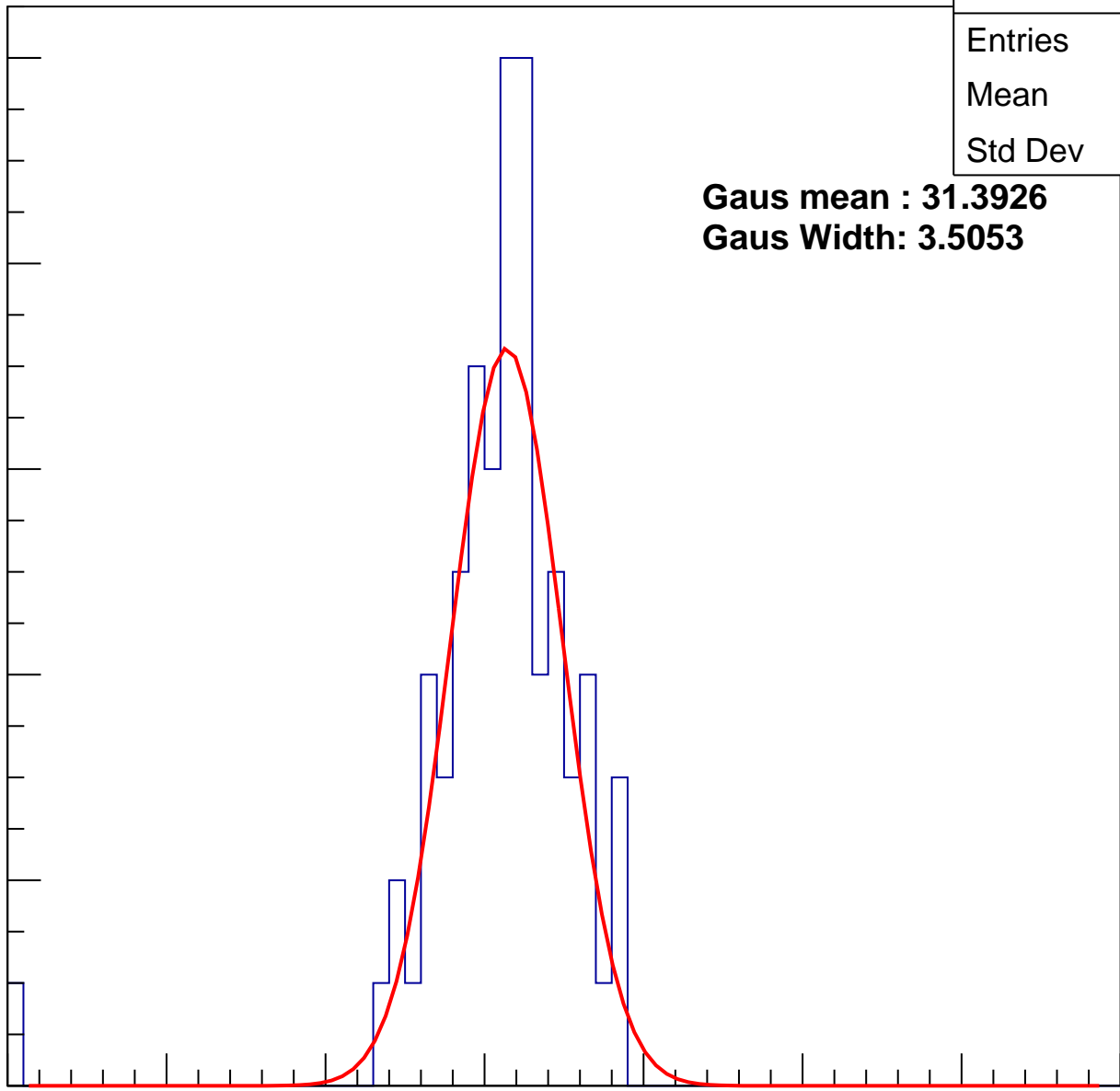
Gaus Width: 3.5053

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch39, adc1

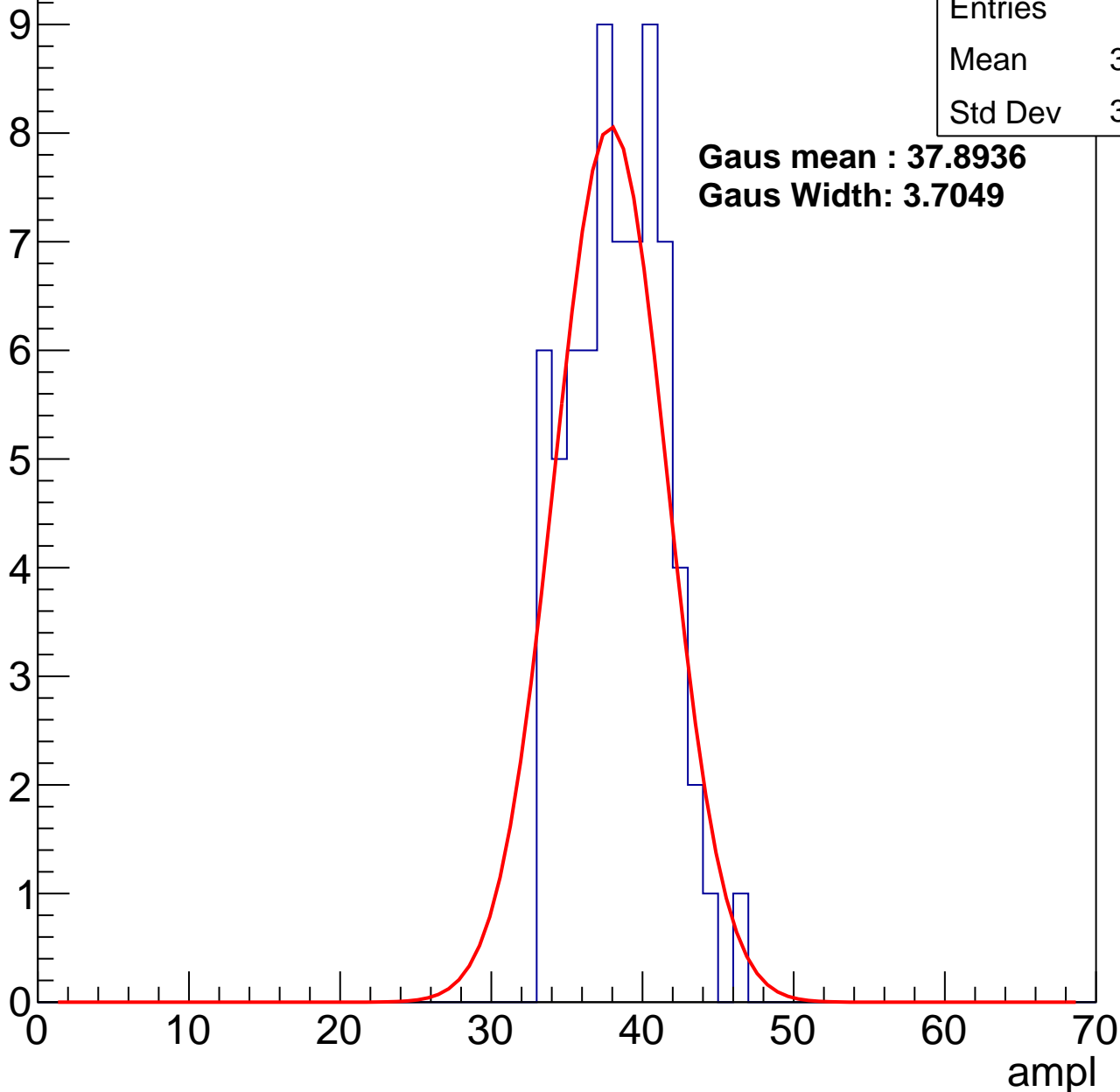
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.96
Std Dev	3.016

Gaus mean : 37.8936

Gaus Width: 3.7049



B0L001S, U19-ch39, adc2

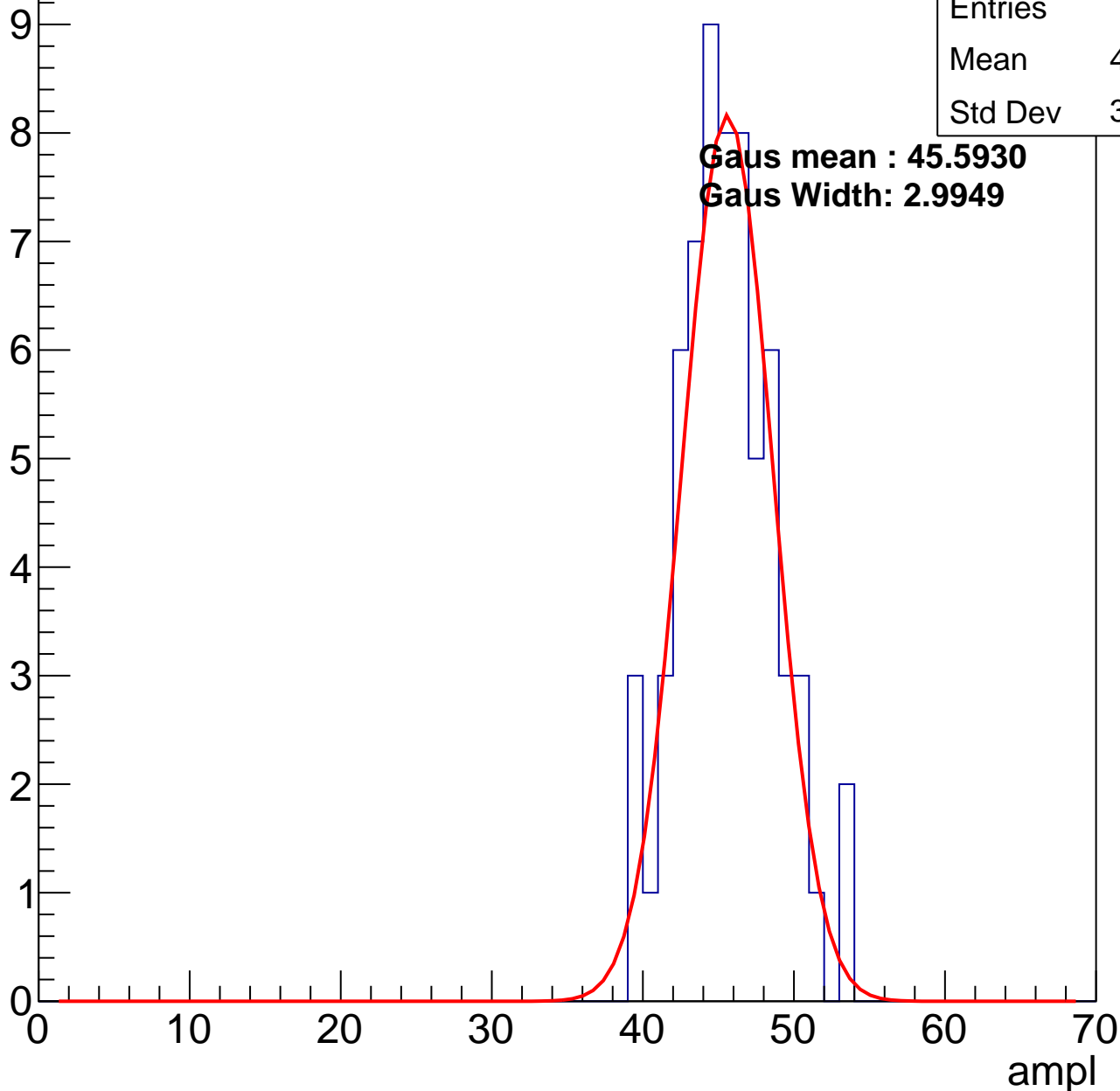
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	45.14
Std Dev	3.137

Gaus mean : 45.5930

Gaus Width: 2.9949

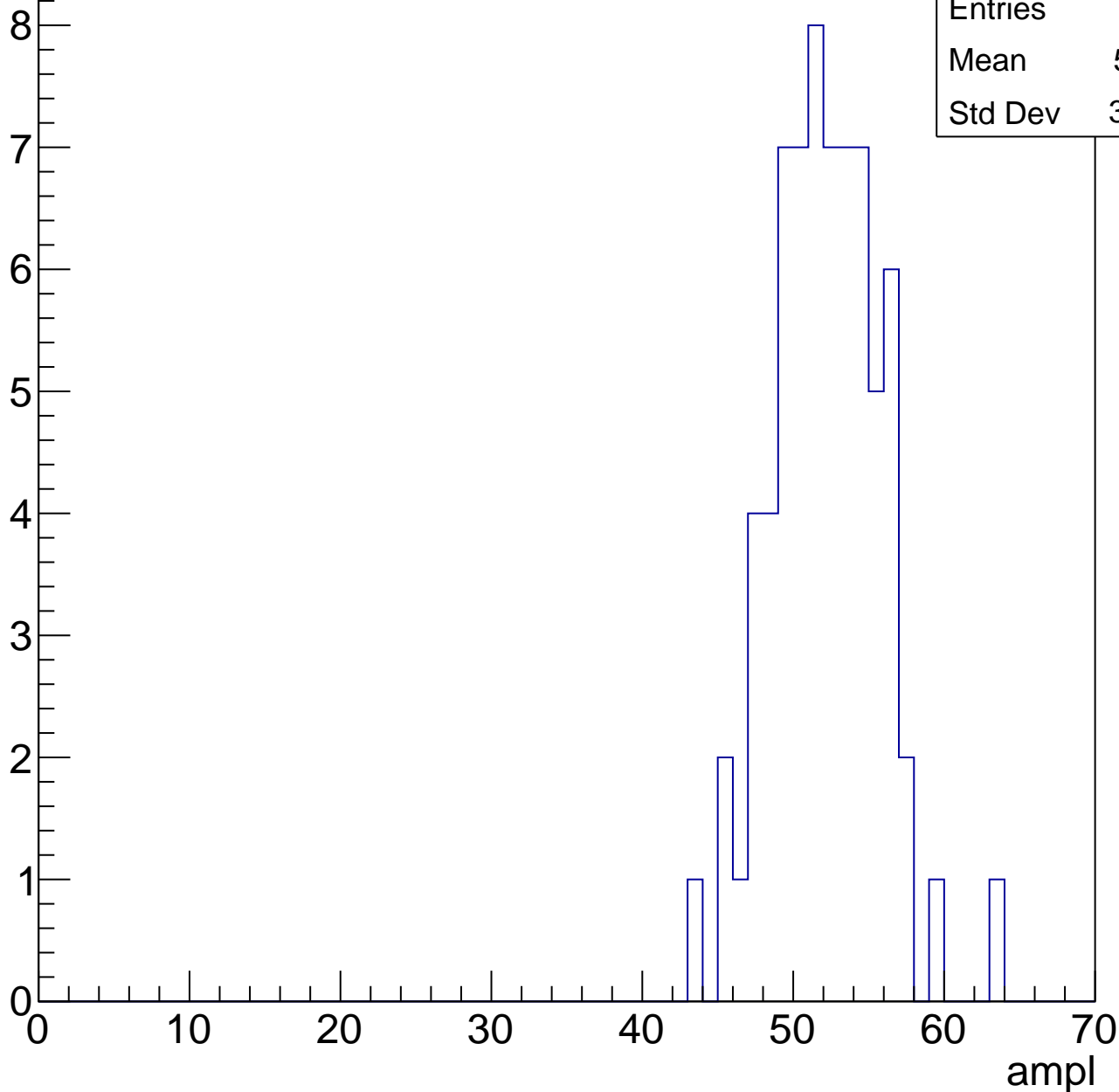


B0L001S, U19-ch39, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

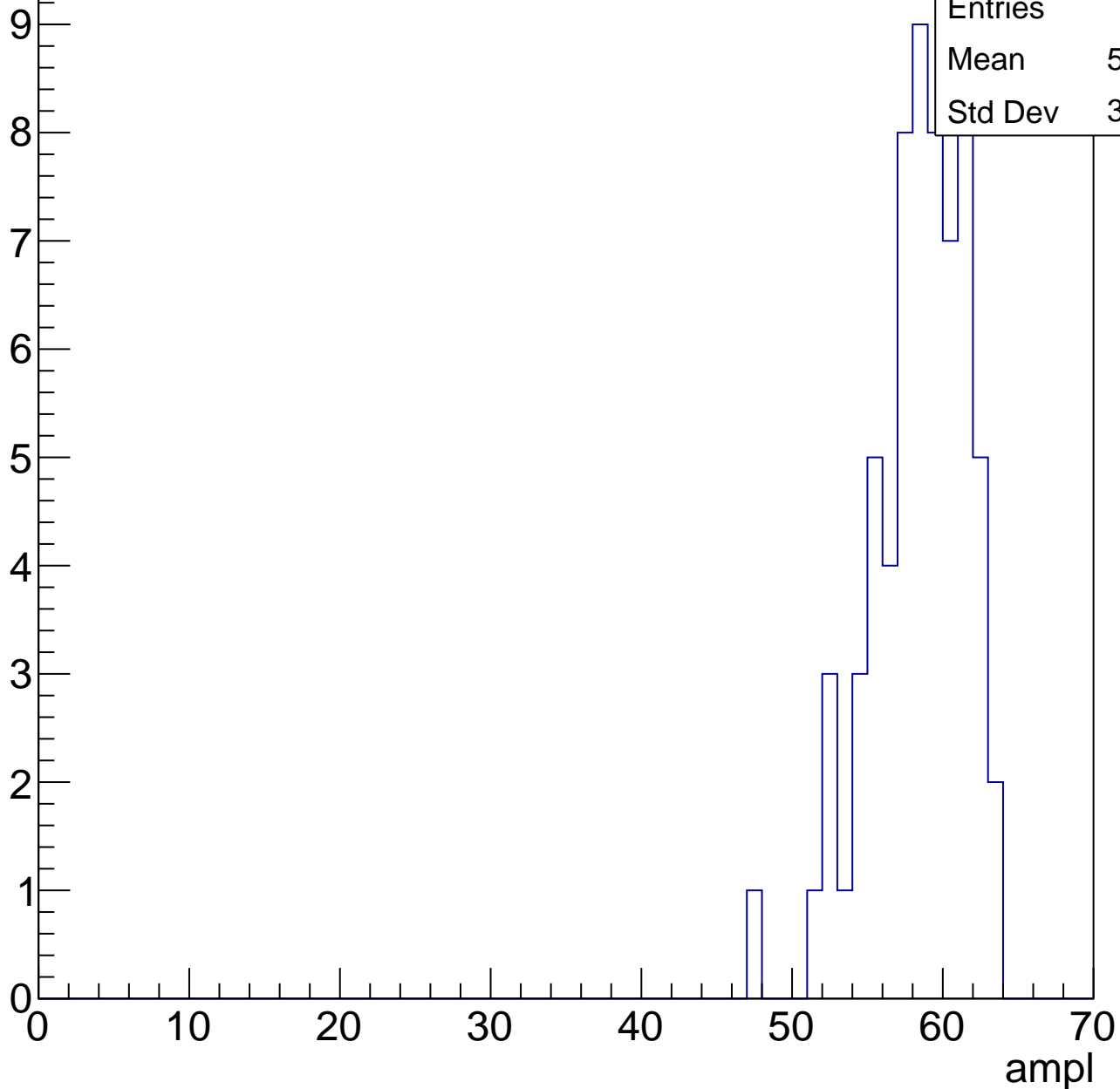
Entries	70
Mean	51.71
Std Dev	3.522



B0L001S, U19-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

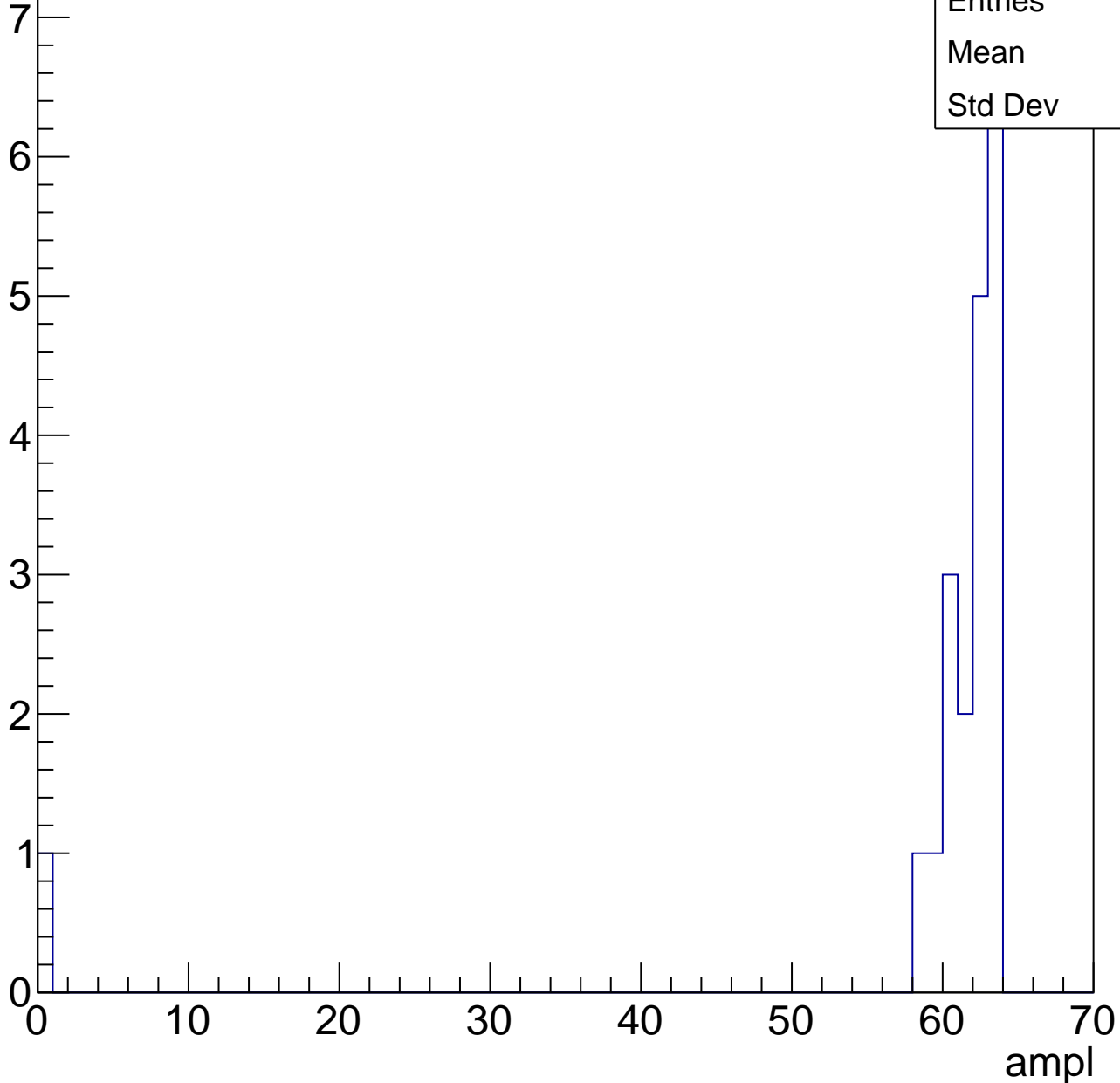


B0L001S, U19-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	20
Mean	58.5
Std Dev	13.5



B0L001S, U19-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch39, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U19-ch40, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	30.2
Std Dev	3.504

Gaus mean : 30.4469

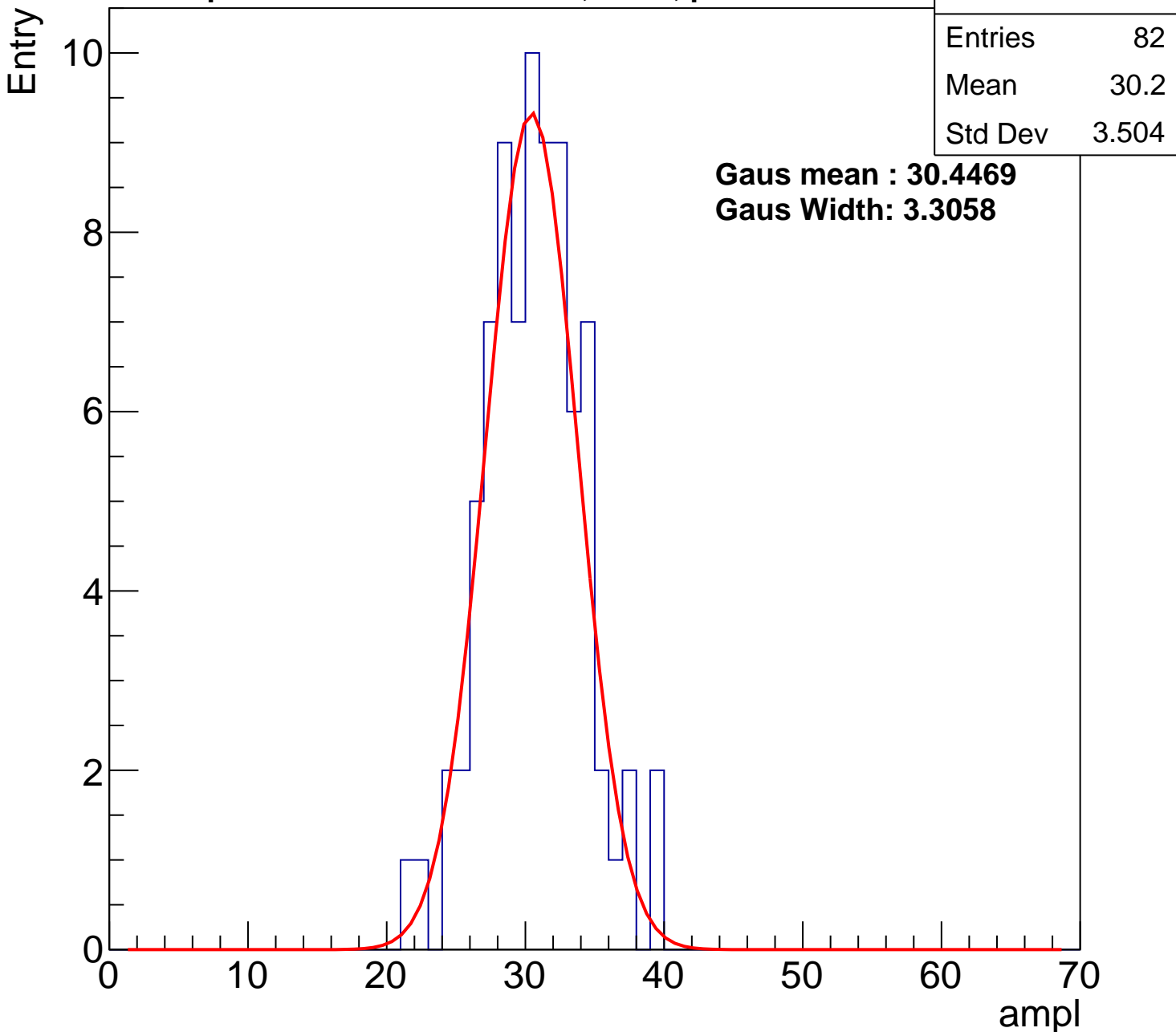
Gaus Width: 3.3058

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



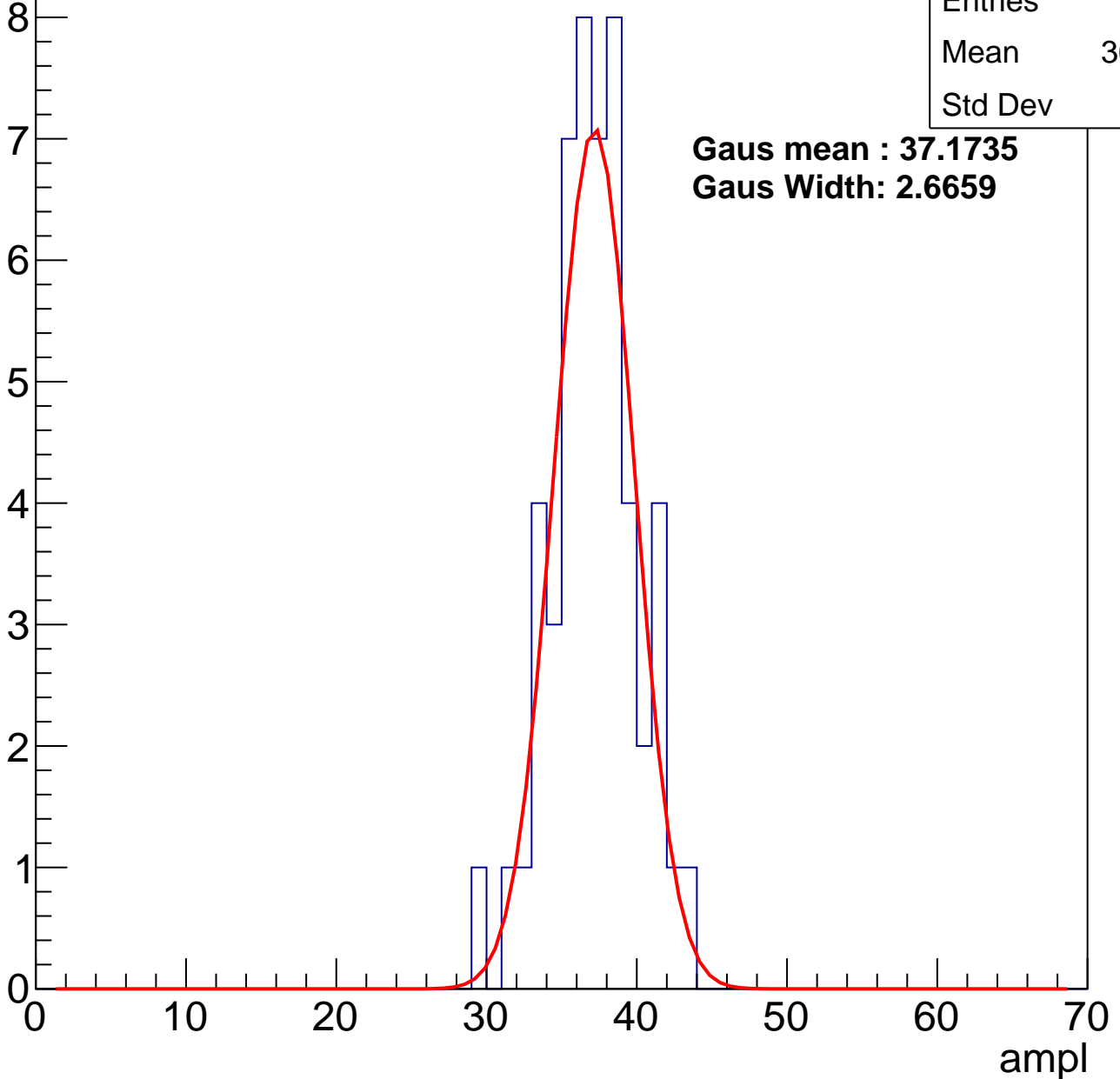
B0L001S, U19-ch40, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	36.67
Std Dev	2.82

Gaus mean : 37.1735
Gaus Width: 2.6659



B0L001S, U19-ch40, adc2

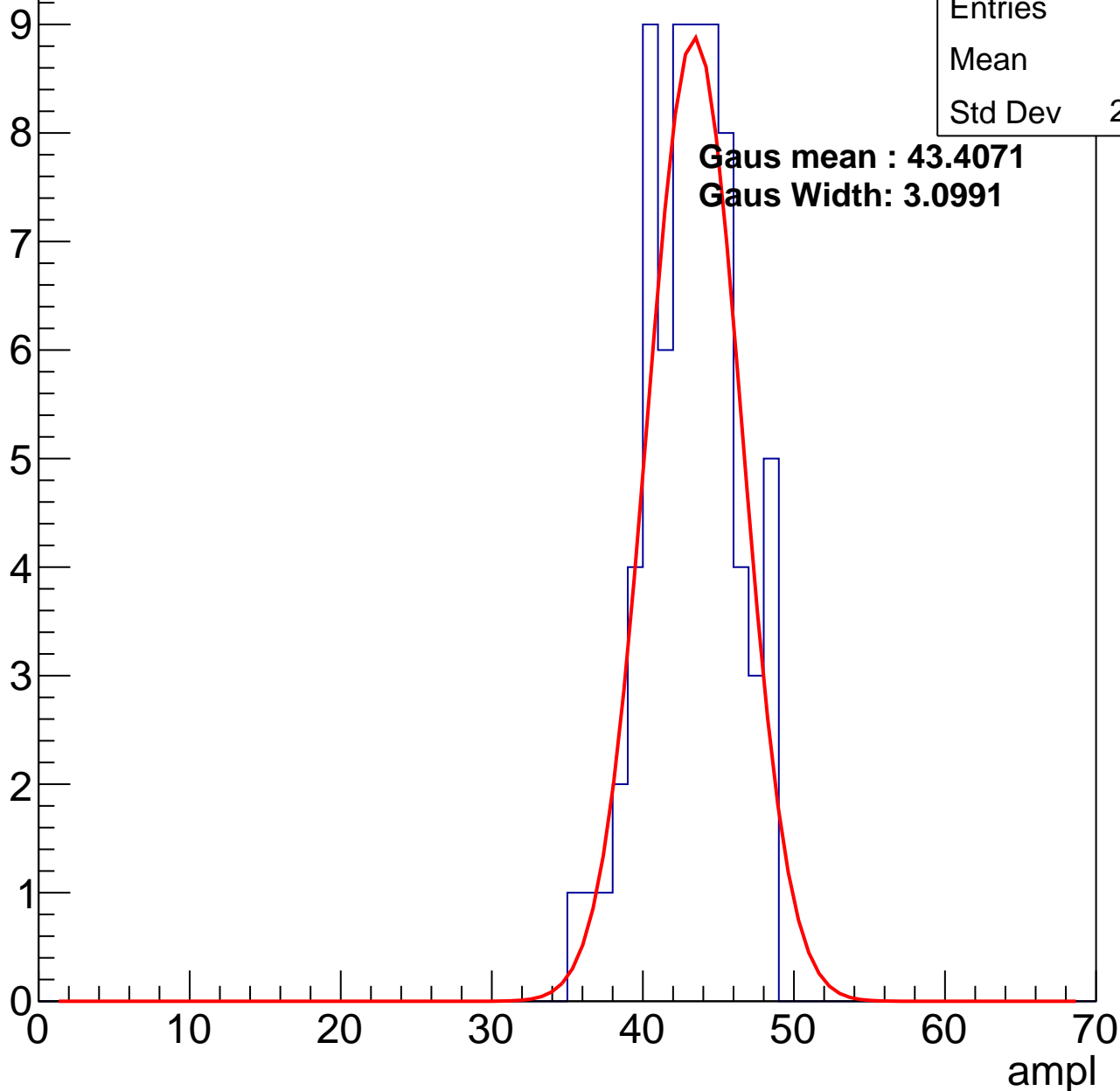
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	42.7
Std Dev	2.957

Gaus mean : 43.4071

Gaus Width: 3.0991



B0L001S, U19-ch40, adc3

calib_packv5_042523_0143.root, FC#9, port A1

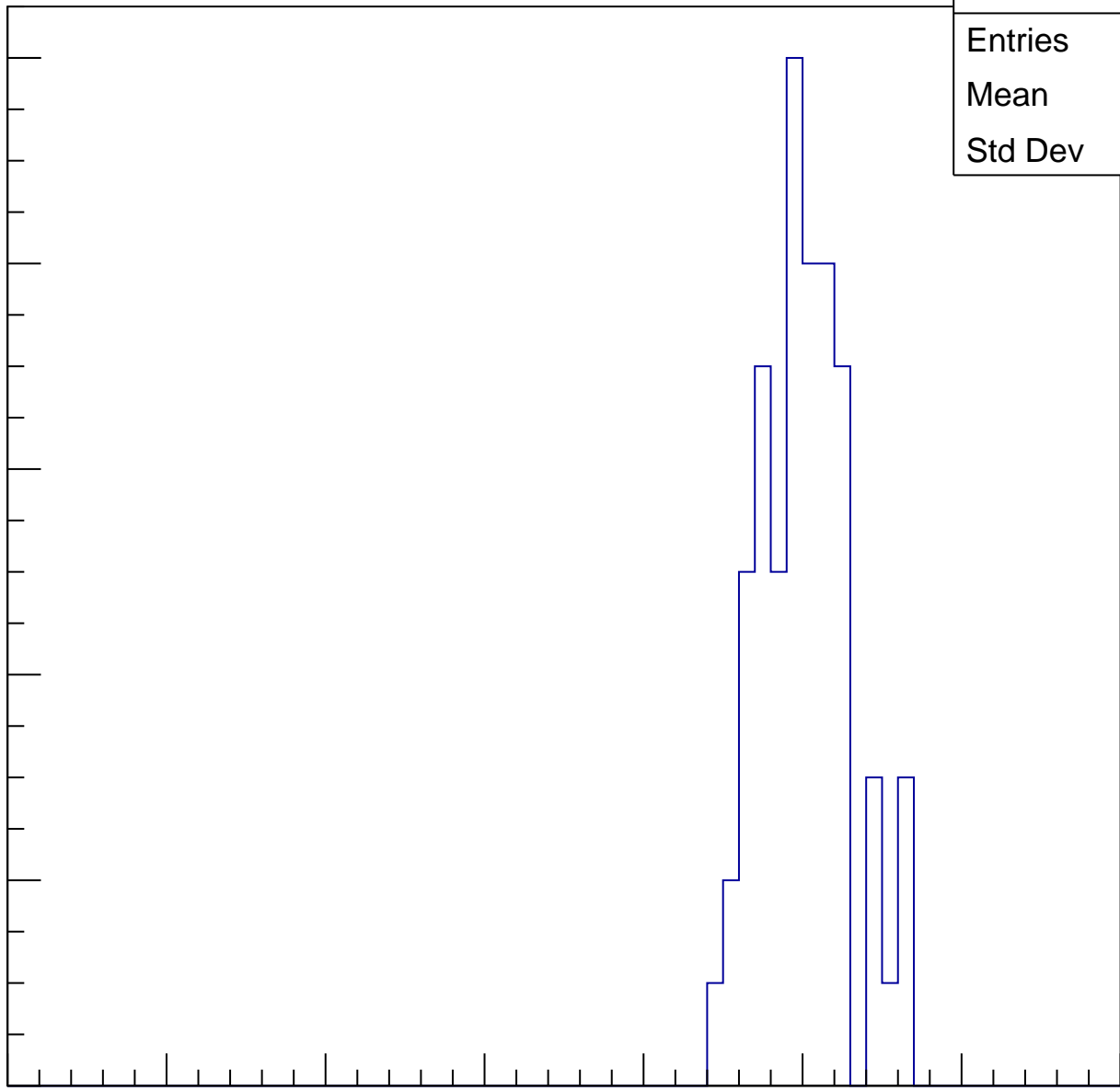
Entries	60
Mean	49.67
Std Dev	2.797

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch40, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	55.43
Std Dev	7.171

Entry

10

8

6

4

2

0

0

10

20

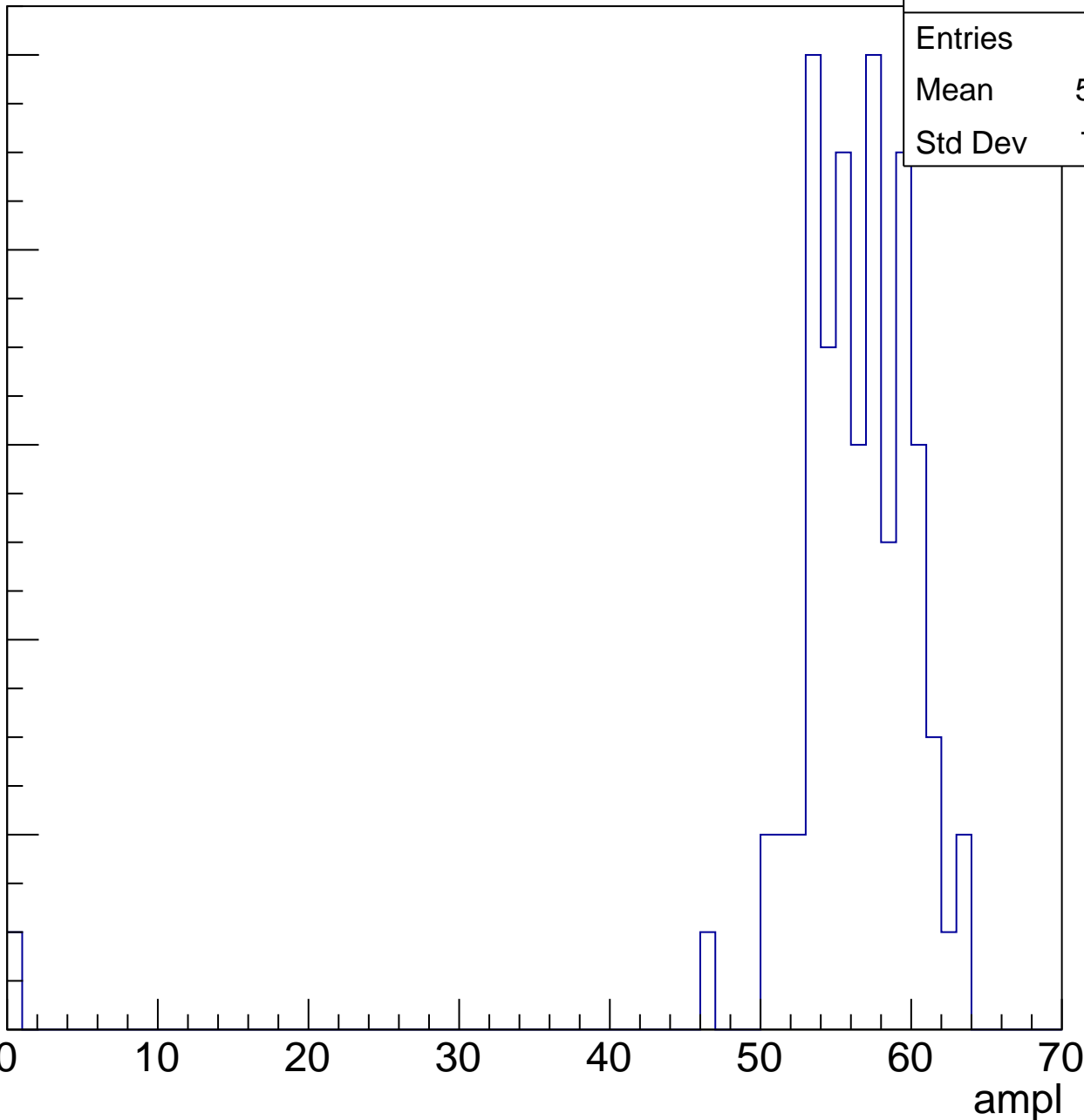
30

40

50

60

ampl

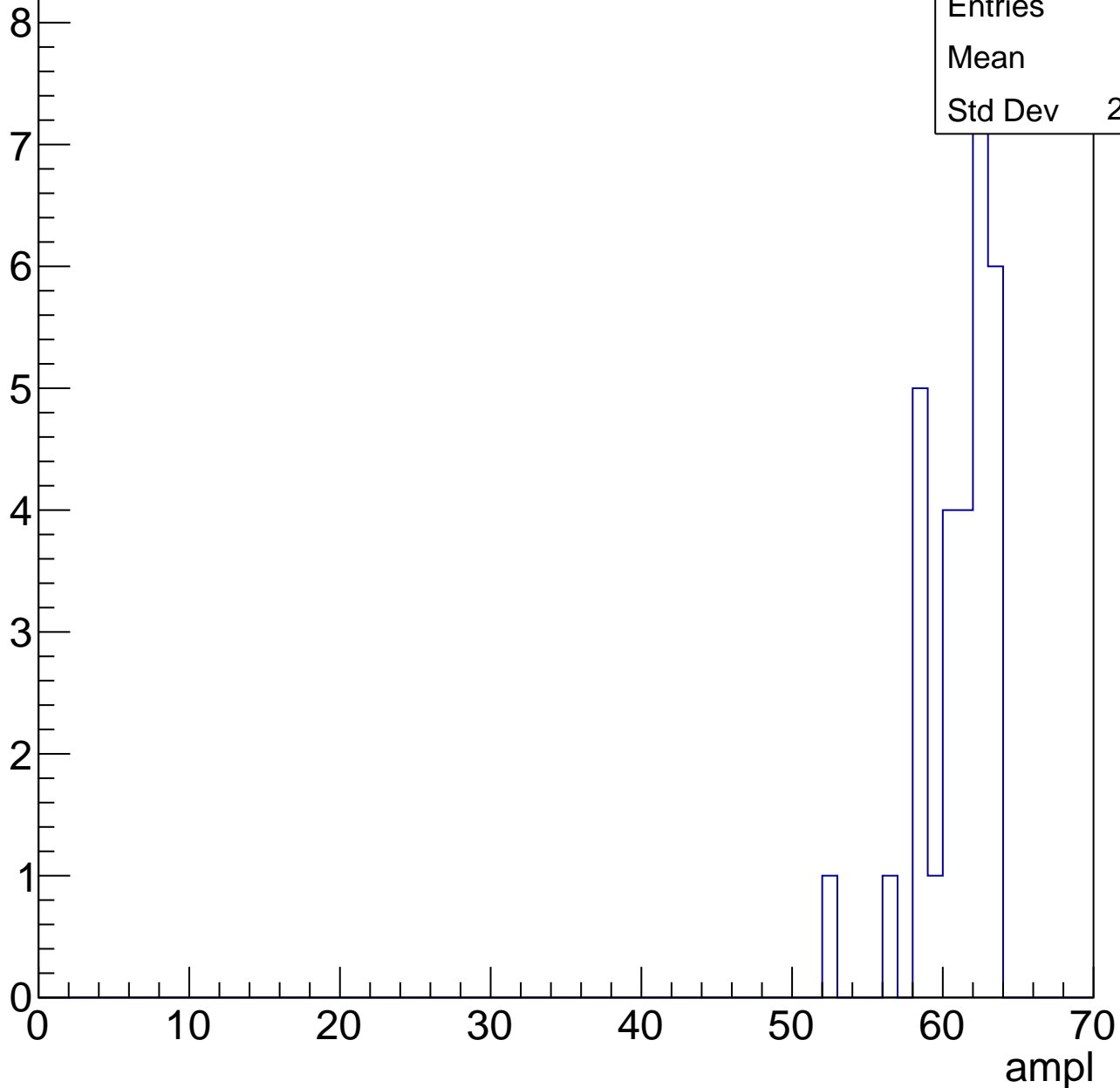


B0L001S, U19-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

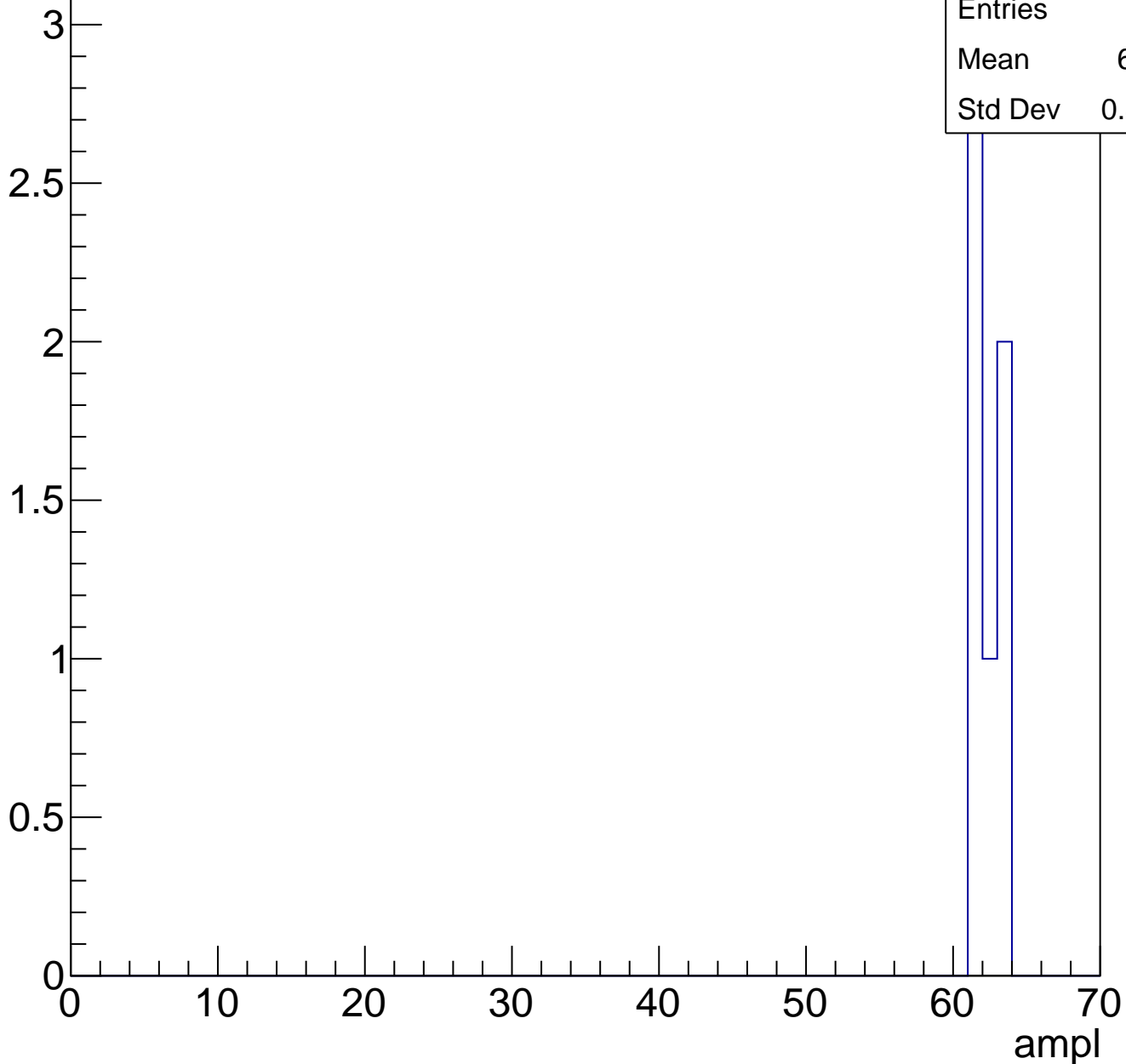
Entries	30
Mean	60.5
Std Dev	2.473



B0L001S, U19-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B0L001S, U19-ch41, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	29.92
Std Dev	5.054

Gaus mean : 30.8861

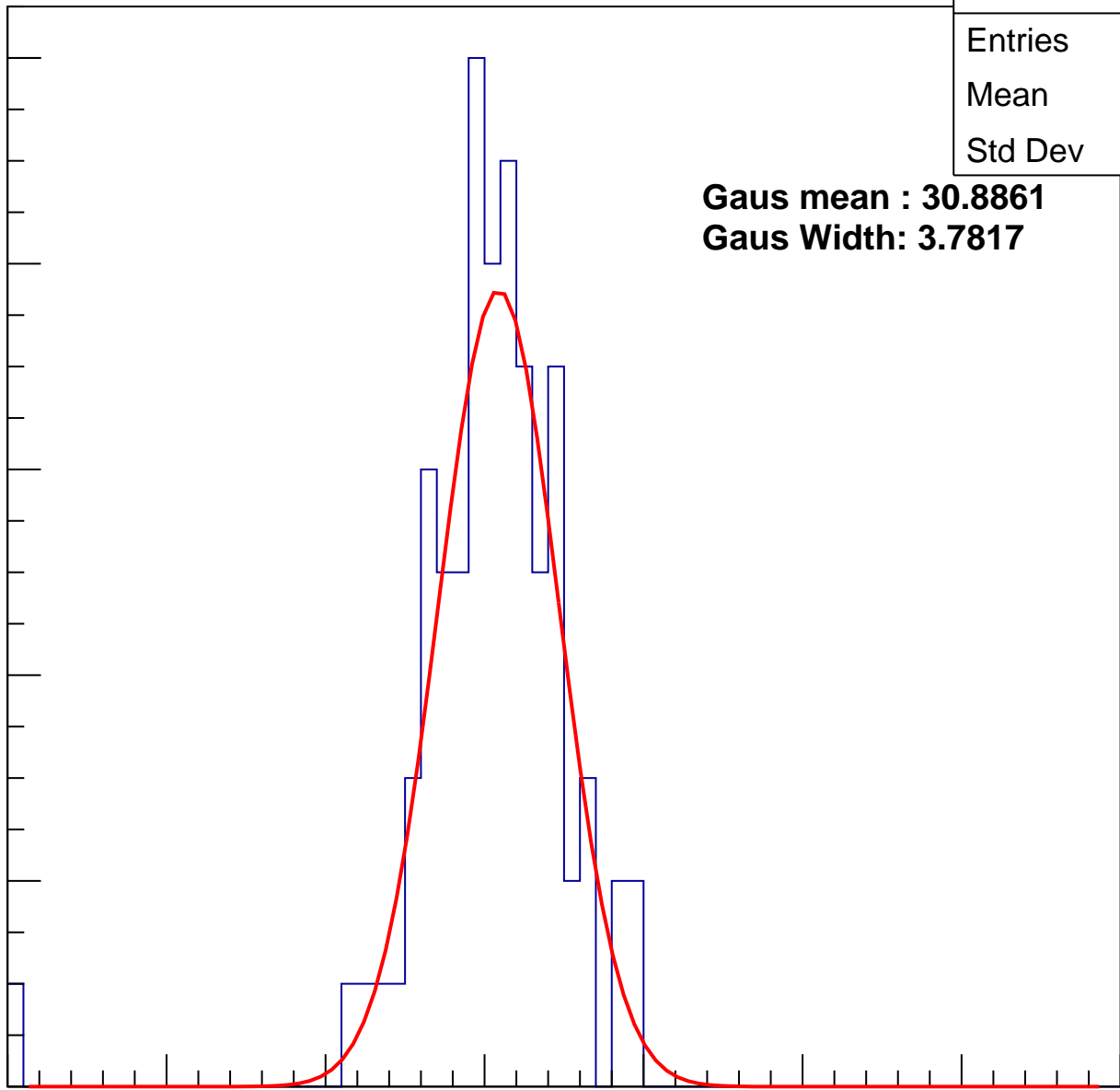
Gaus Width: 3.7817

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch41, adc1

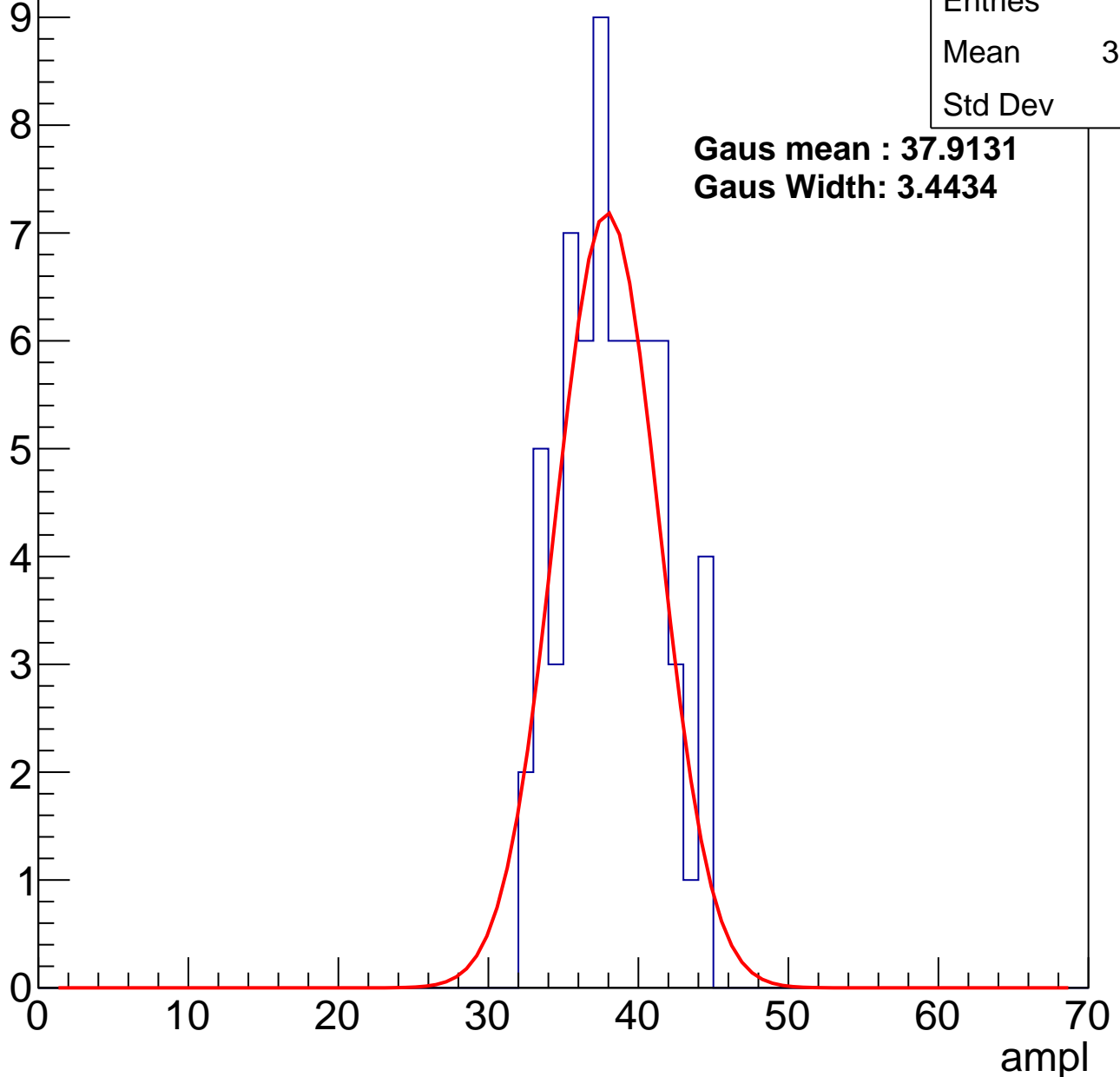
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.78
Std Dev	3.16

Gaus mean : 37.9131

Gaus Width: 3.4434



B0L001S, U19-ch41, adc2

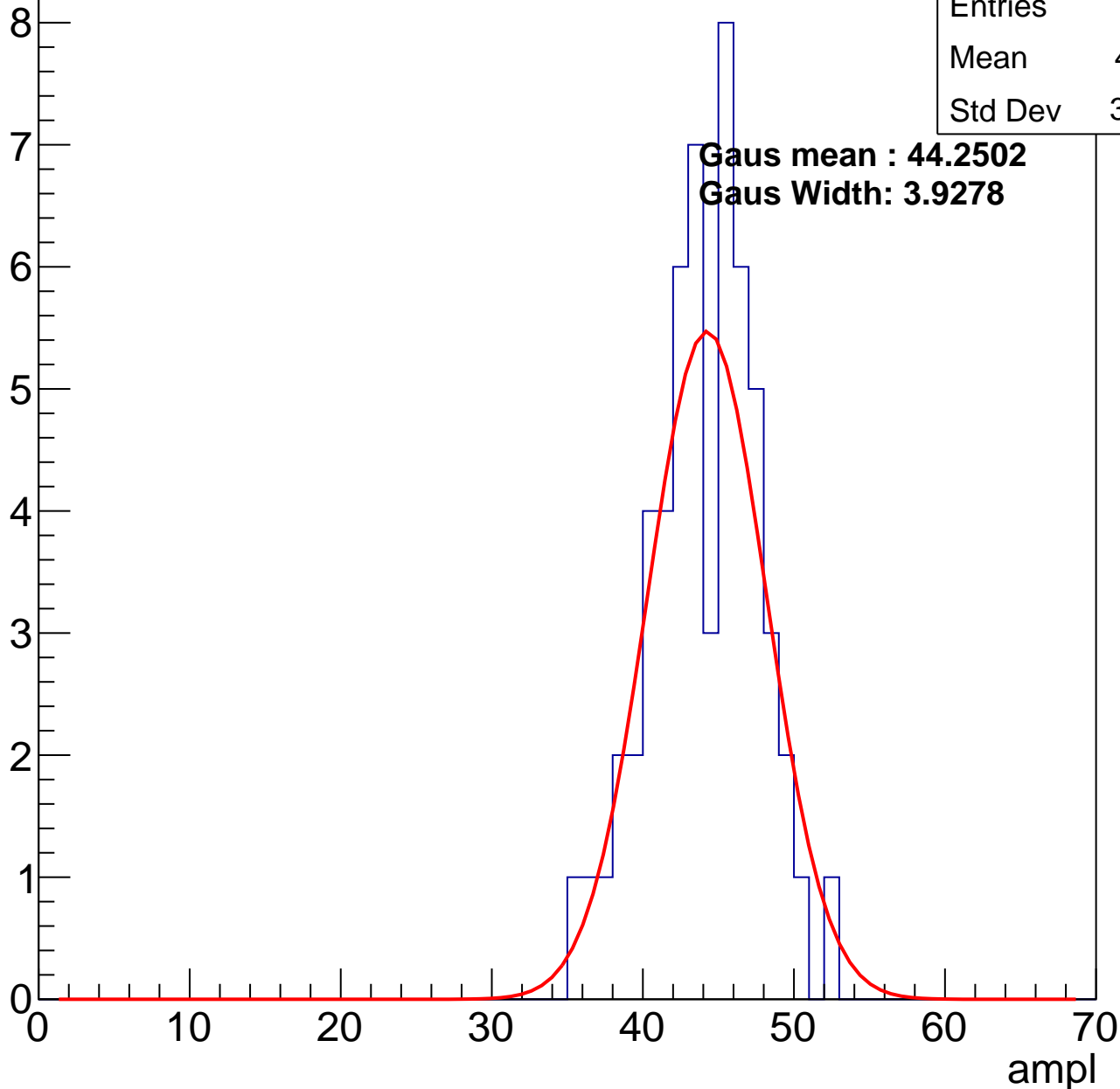
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.61
Std Dev	3.538

Gaus mean : 44.2502

Gaus Width: 3.9278



B0L001S, U19-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	50.03
Std Dev	3.388

Entry

10

8

6

4

2

0

0

10

20

30

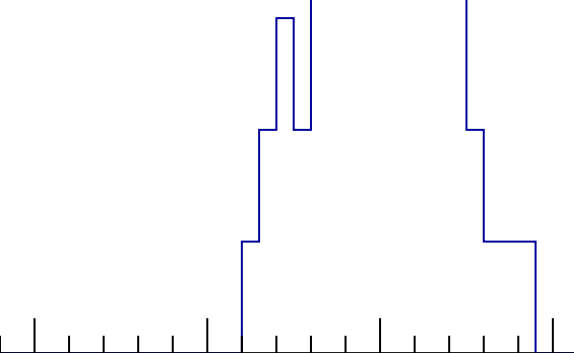
40

50

60

ampl

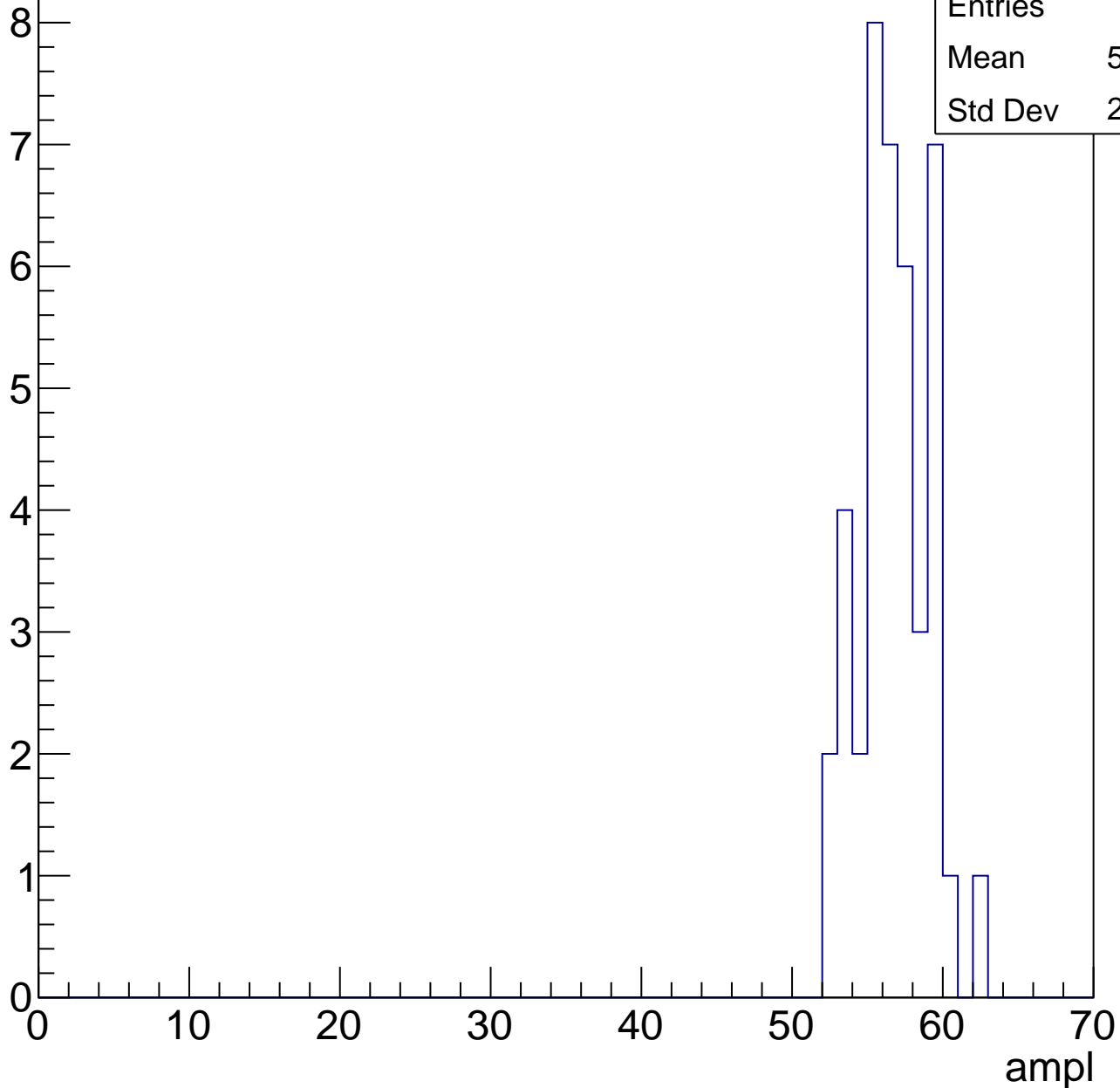
70



B0L001S, U19-ch41, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

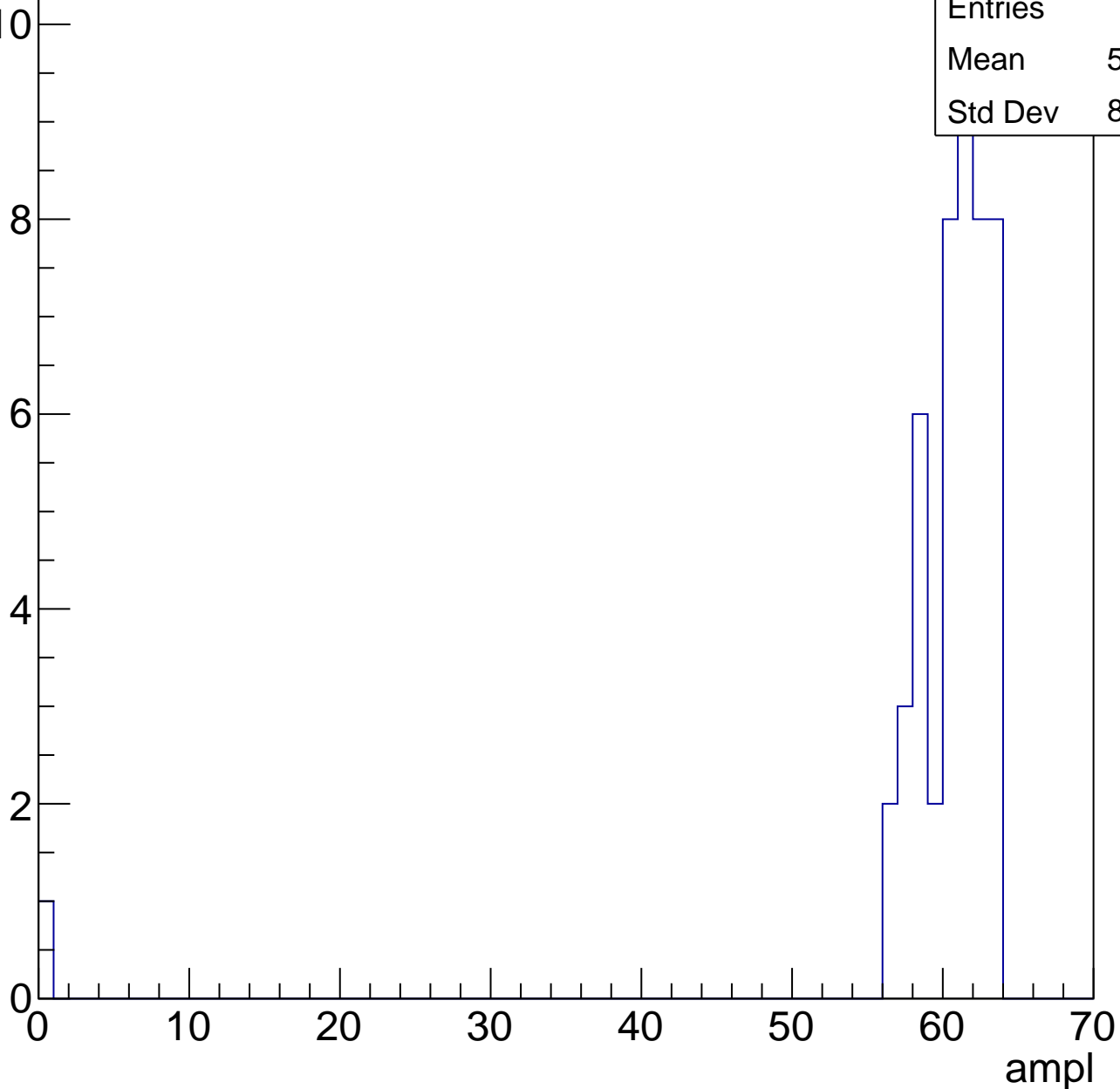


B0L001S, U19-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

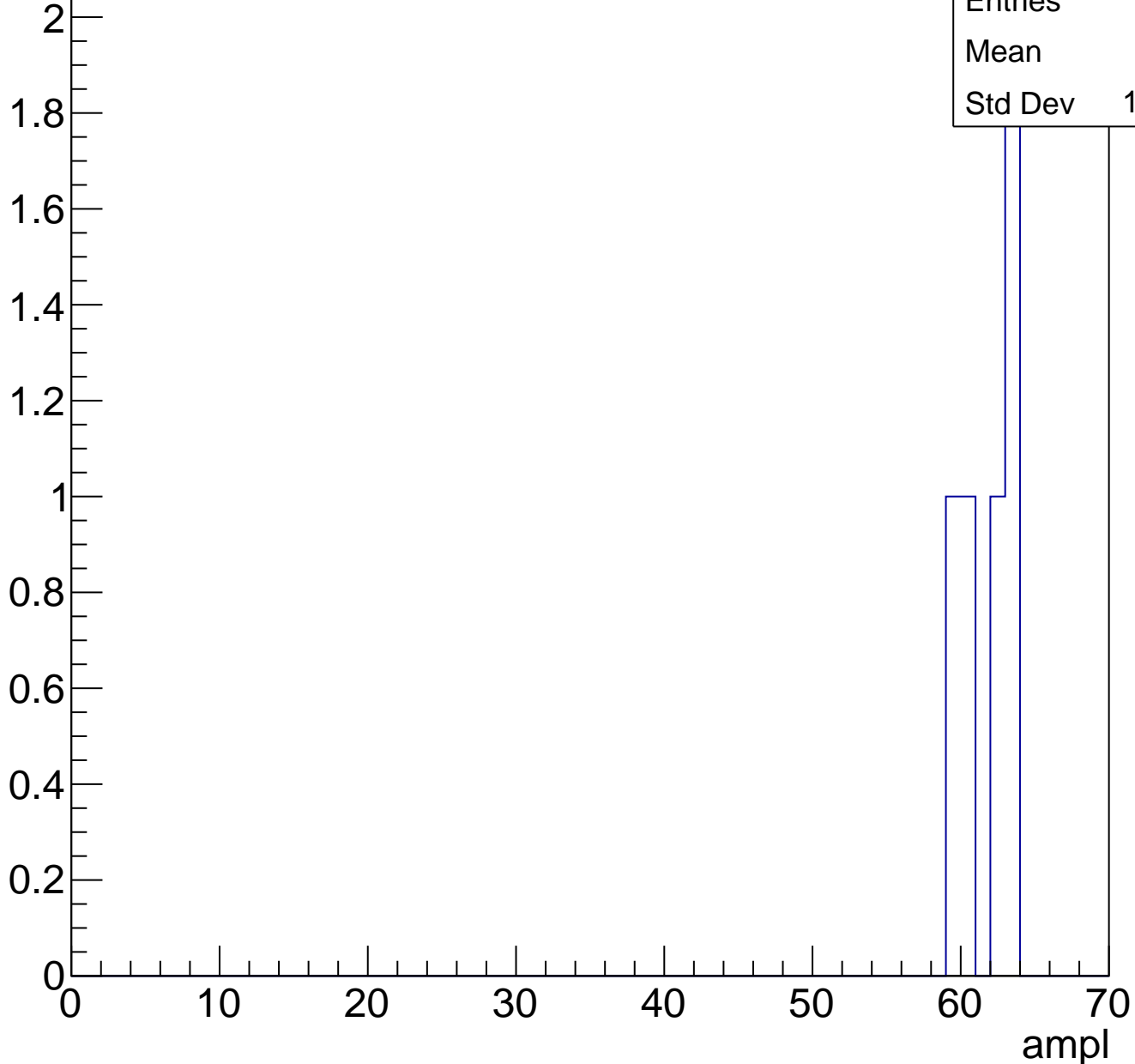
Entries	48
Mean	59.15
Std Dev	8.855



B0L001S, U19-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch41, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch42, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	30.54
Std Dev	3.067

Gaus mean : 30.9088

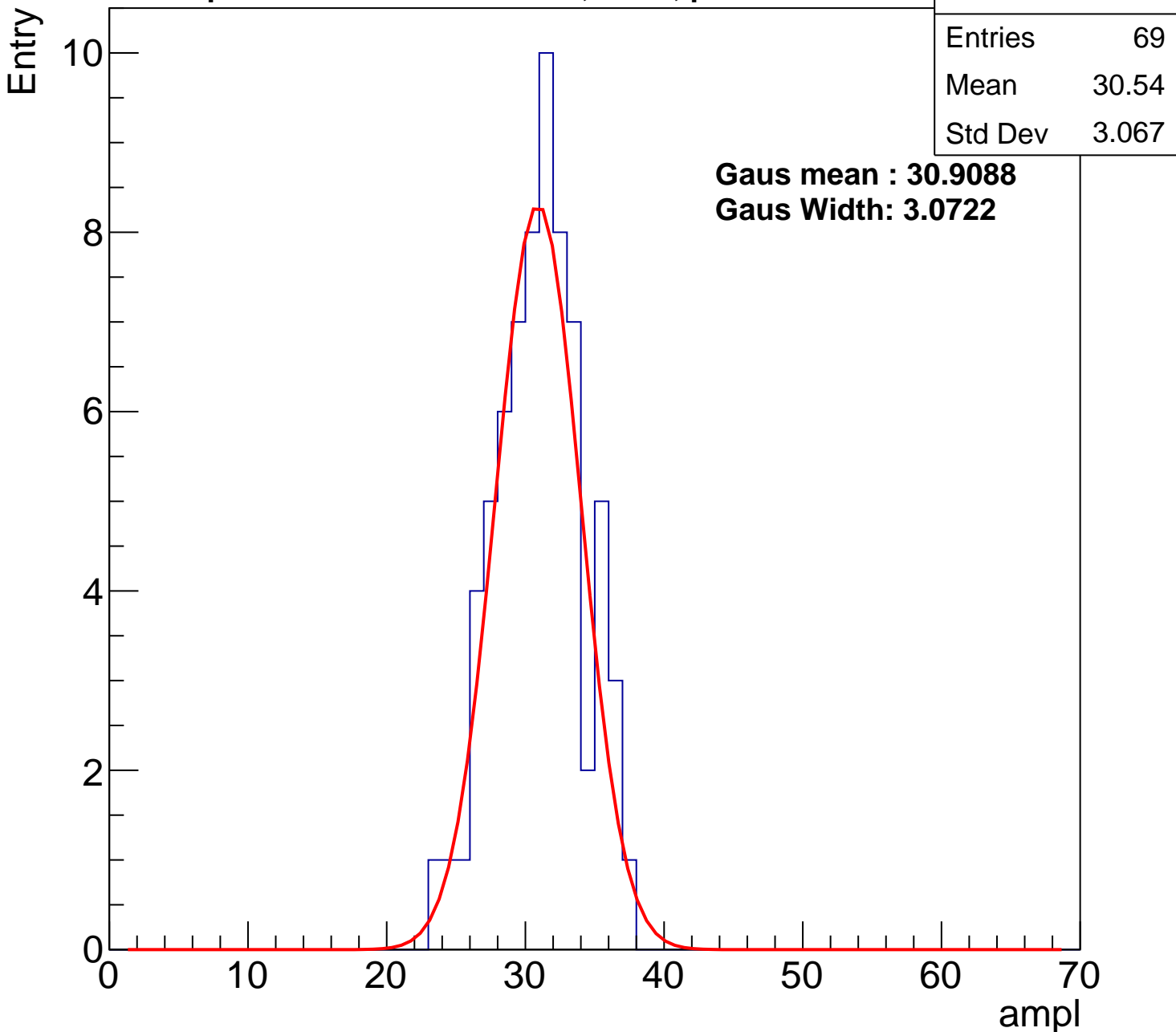
Gaus Width: 3.0722

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch42, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	37.72
Std Dev	2.765

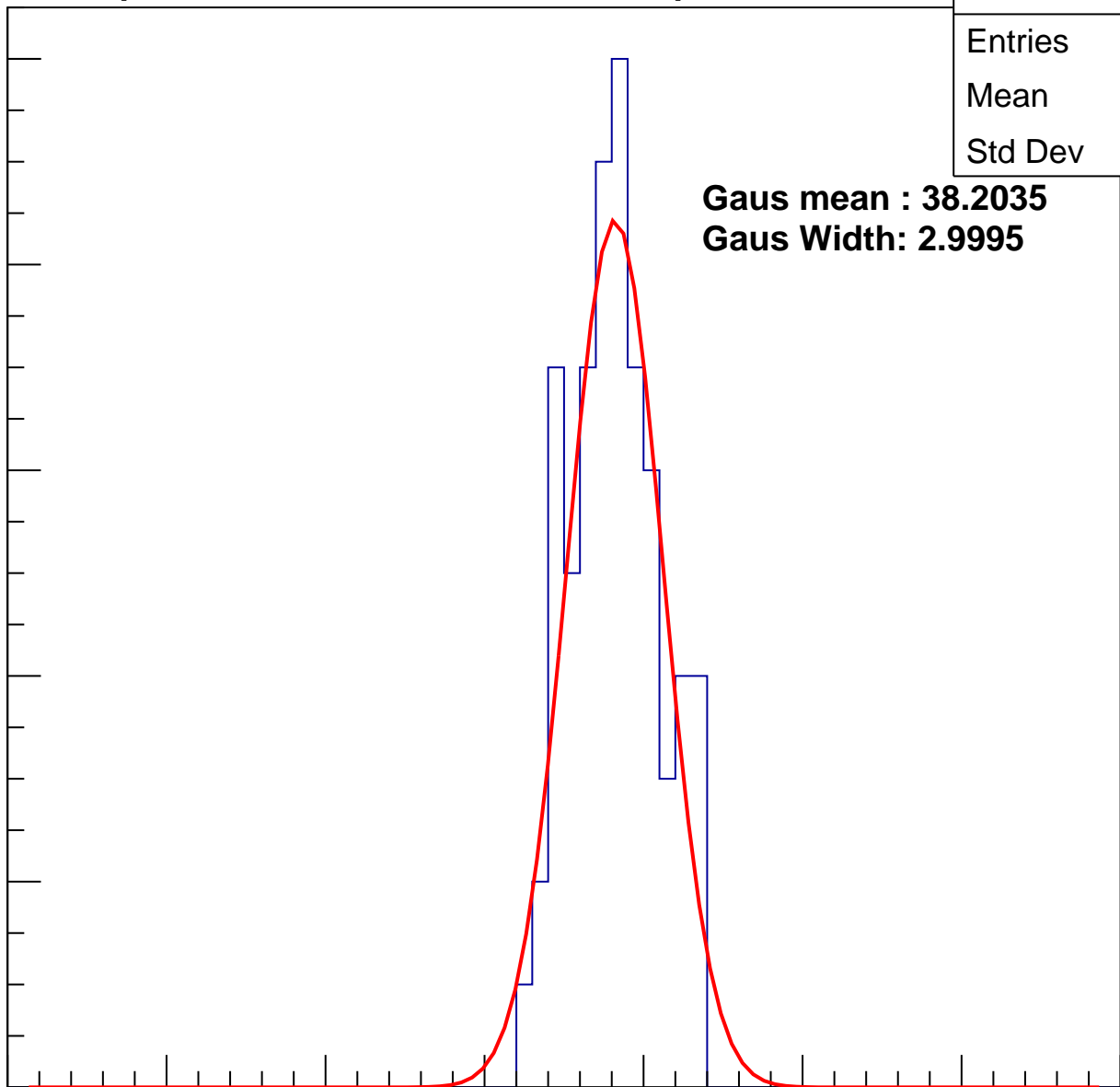
Gaus mean : 38.2035
Gaus Width: 2.9995

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch42, adc2

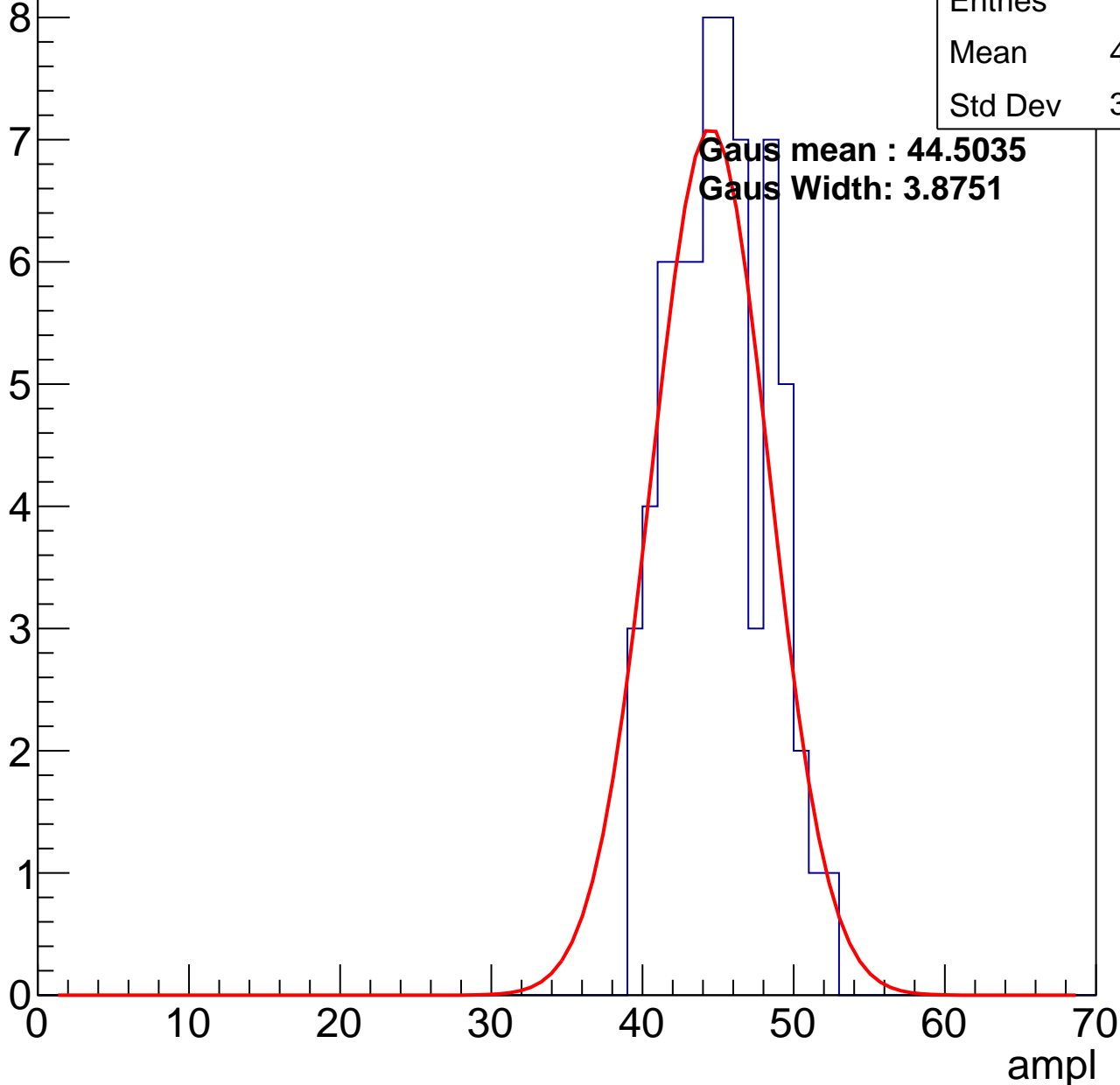
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.66
Std Dev	3.184

Gaus mean : 44.5035

Gaus Width: 3.8751

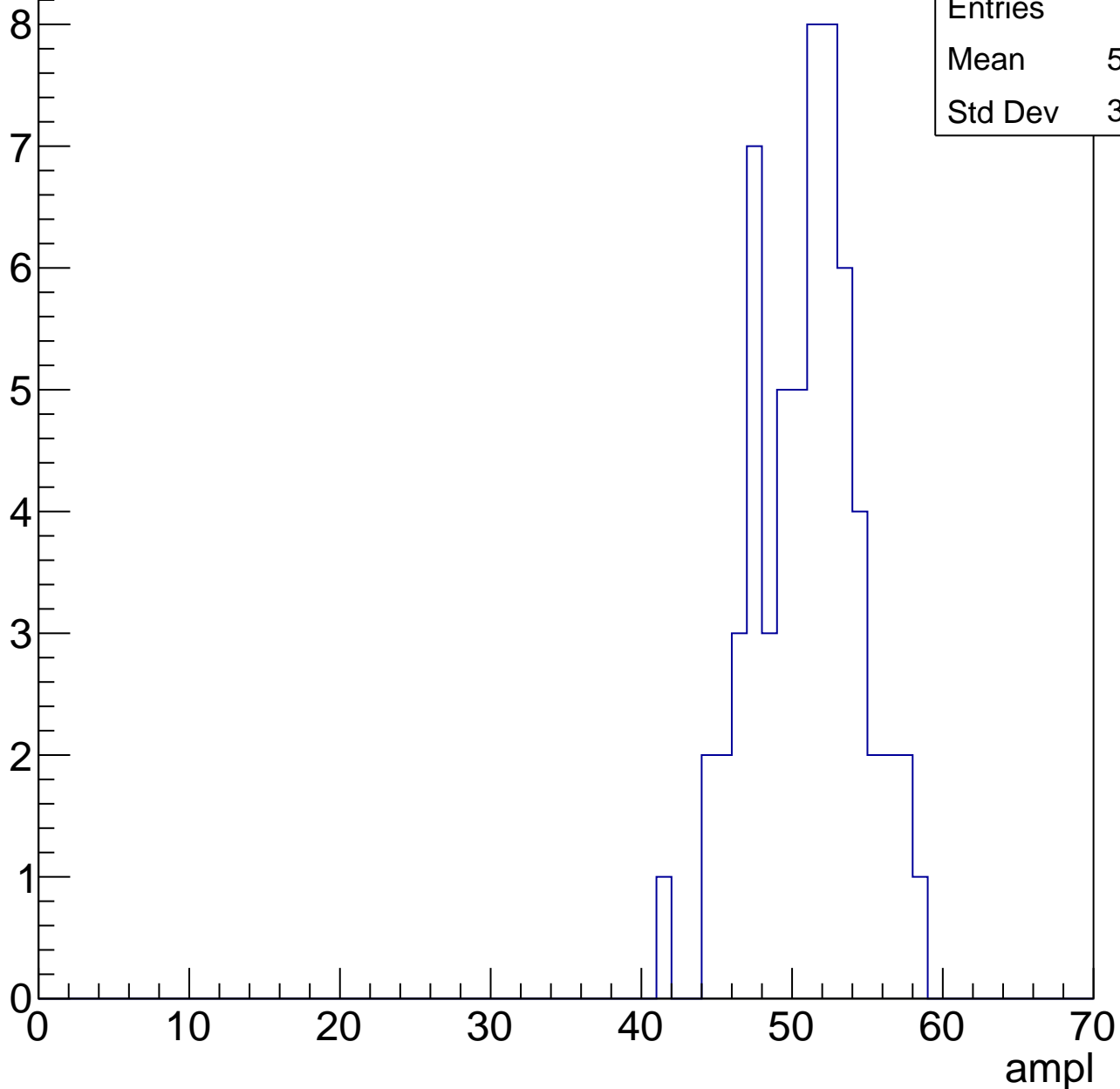


B0L001S, U19-ch42, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

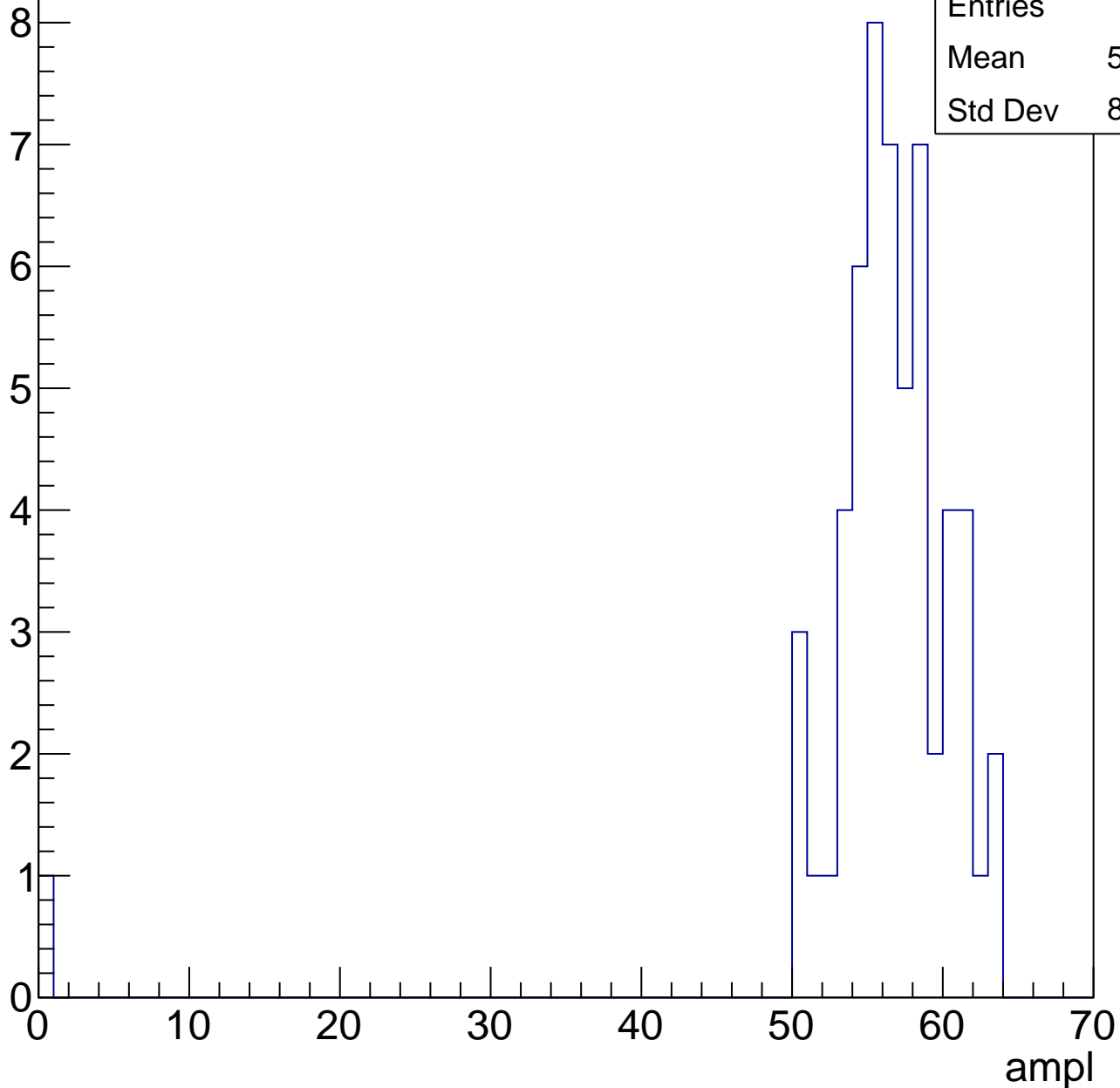
Entries	61
Mean	50.44
Std Dev	3.532



B0L001S, U19-ch42, adc4

calib_packv5_042523_0143.root, FC#9, port A1

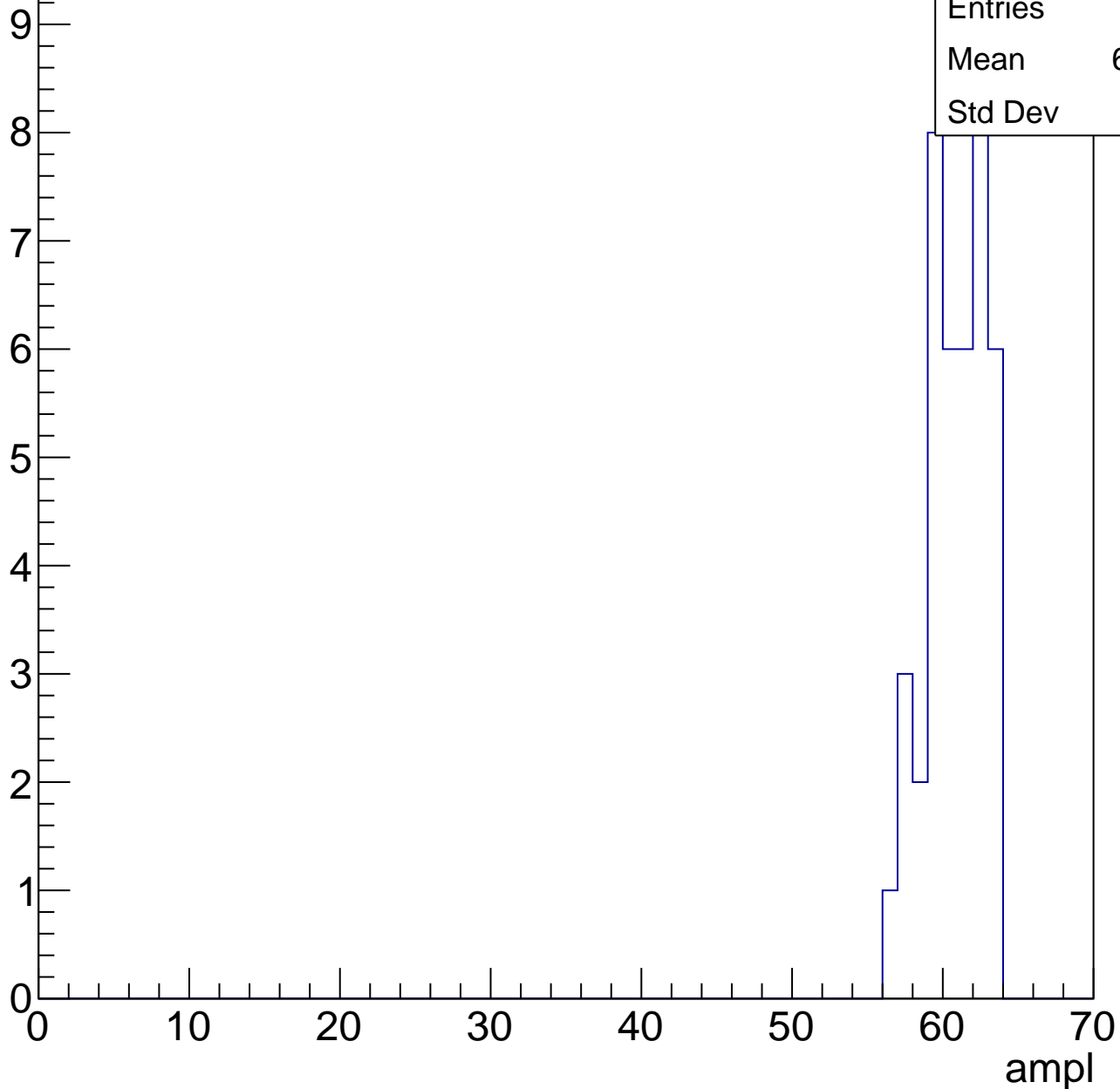
Entry



B0L001S, U19-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	41
Mean	60.41
Std Dev	1.9

B0L001S, U19-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch43, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	32.94
Std Dev	3.782

Gaus mean : 33.1423

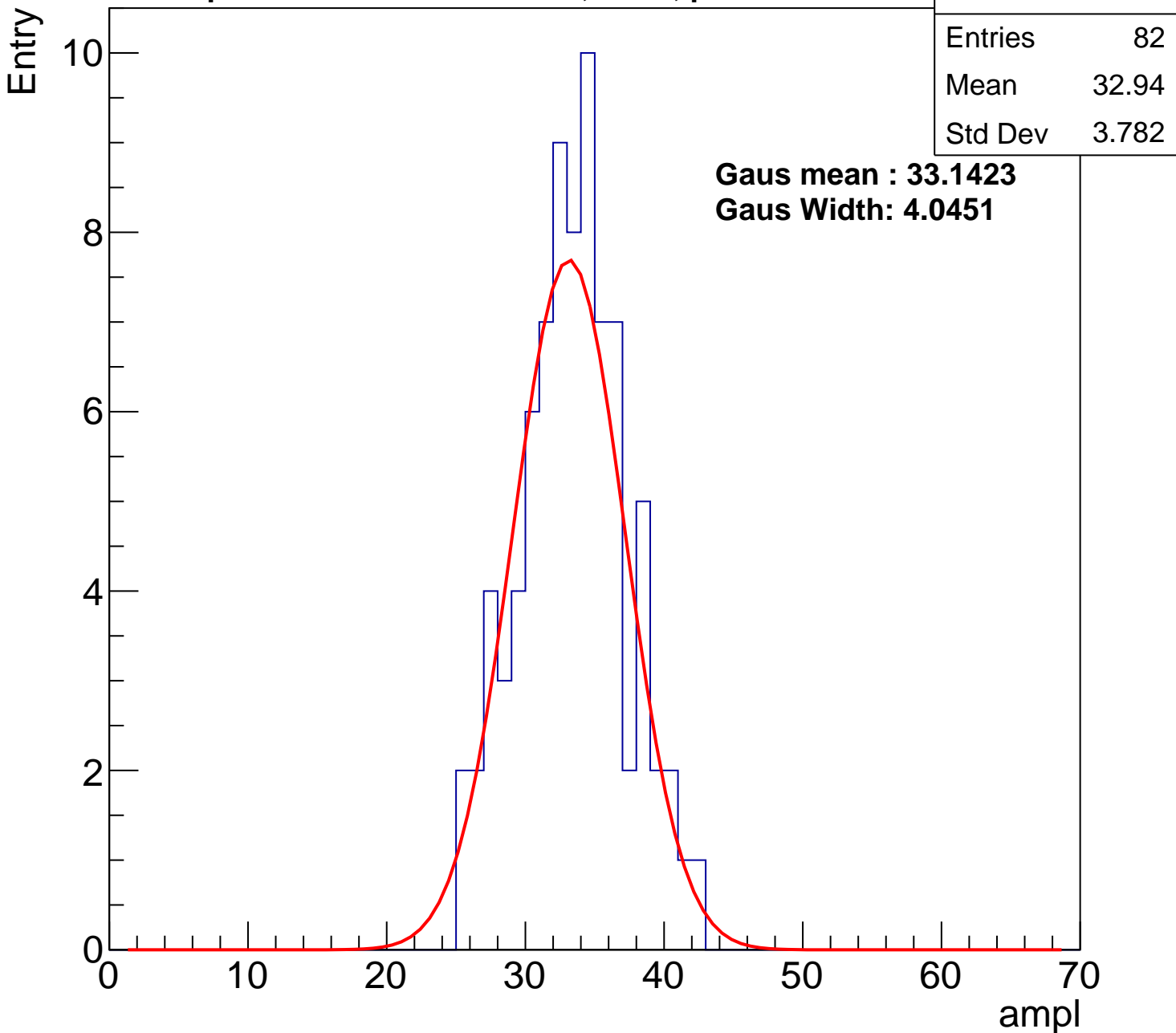
Gaus Width: 4.0451

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch43, adc1

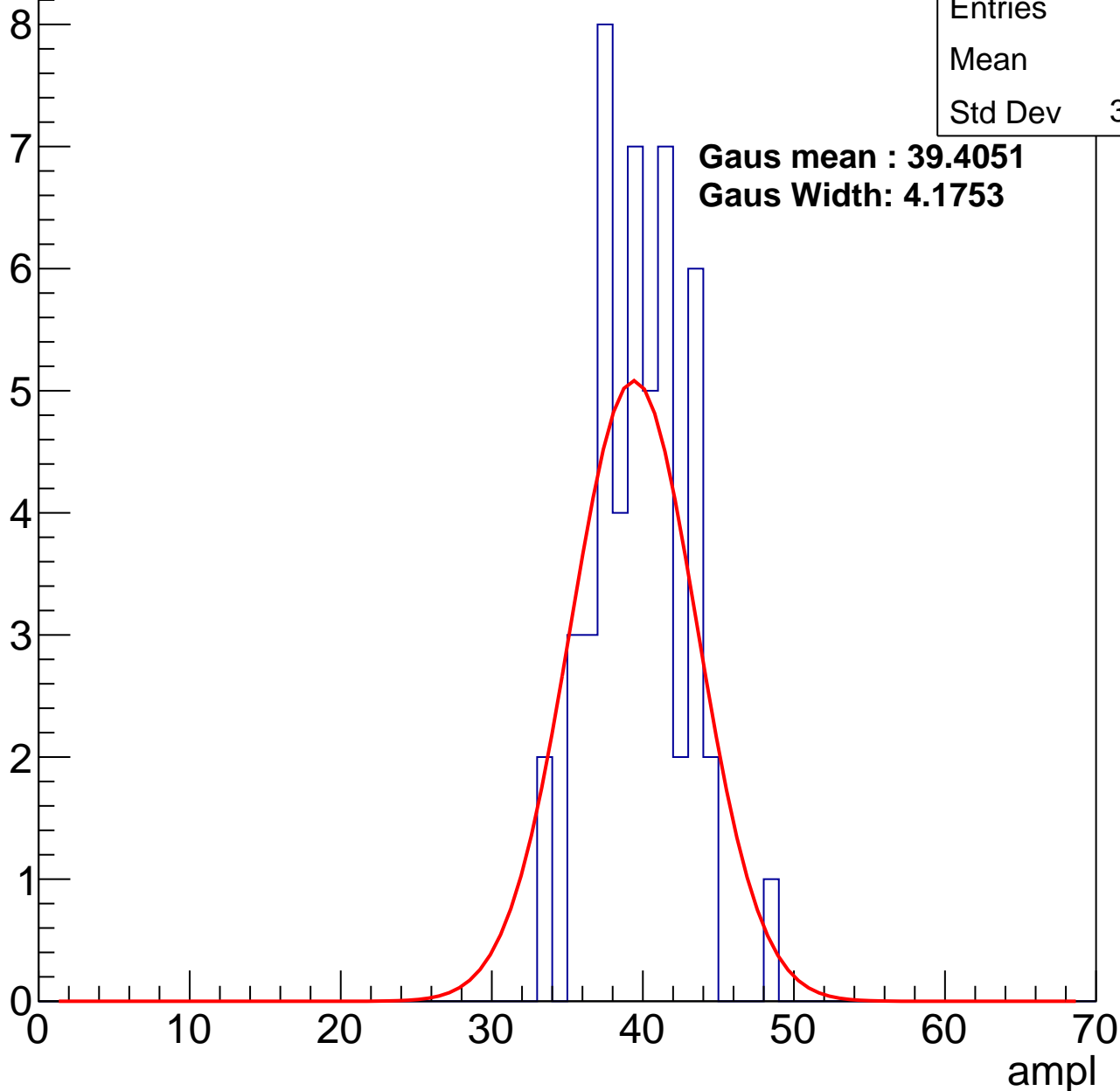
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	39.3
Std Dev	3.022

Gaus mean : 39.4051

Gaus Width: 4.1753



B0L001S, U19-ch43, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	45.53
Std Dev	3.341

Gaus mean : 45.9228

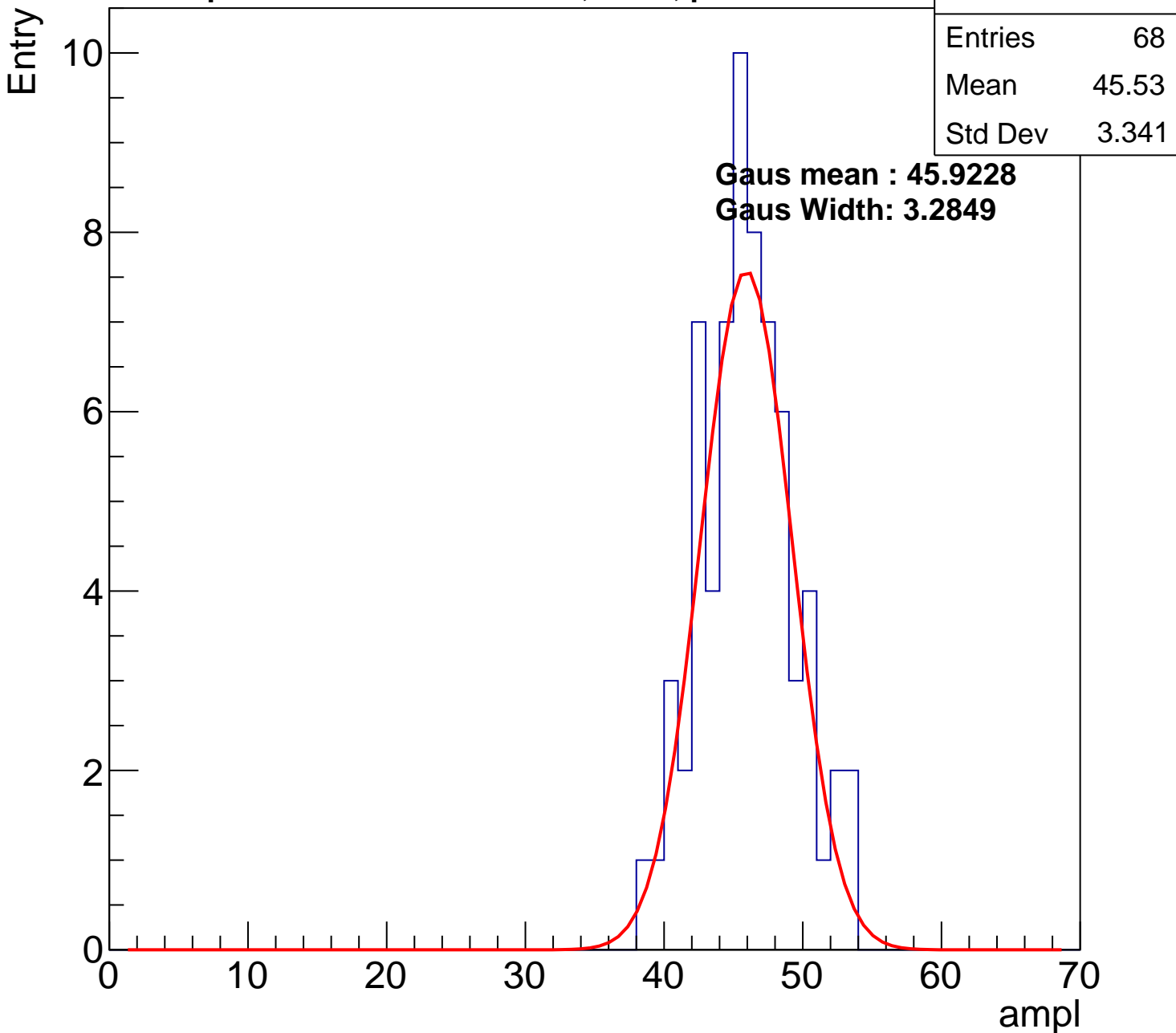
Gaus Width: 3.2849

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

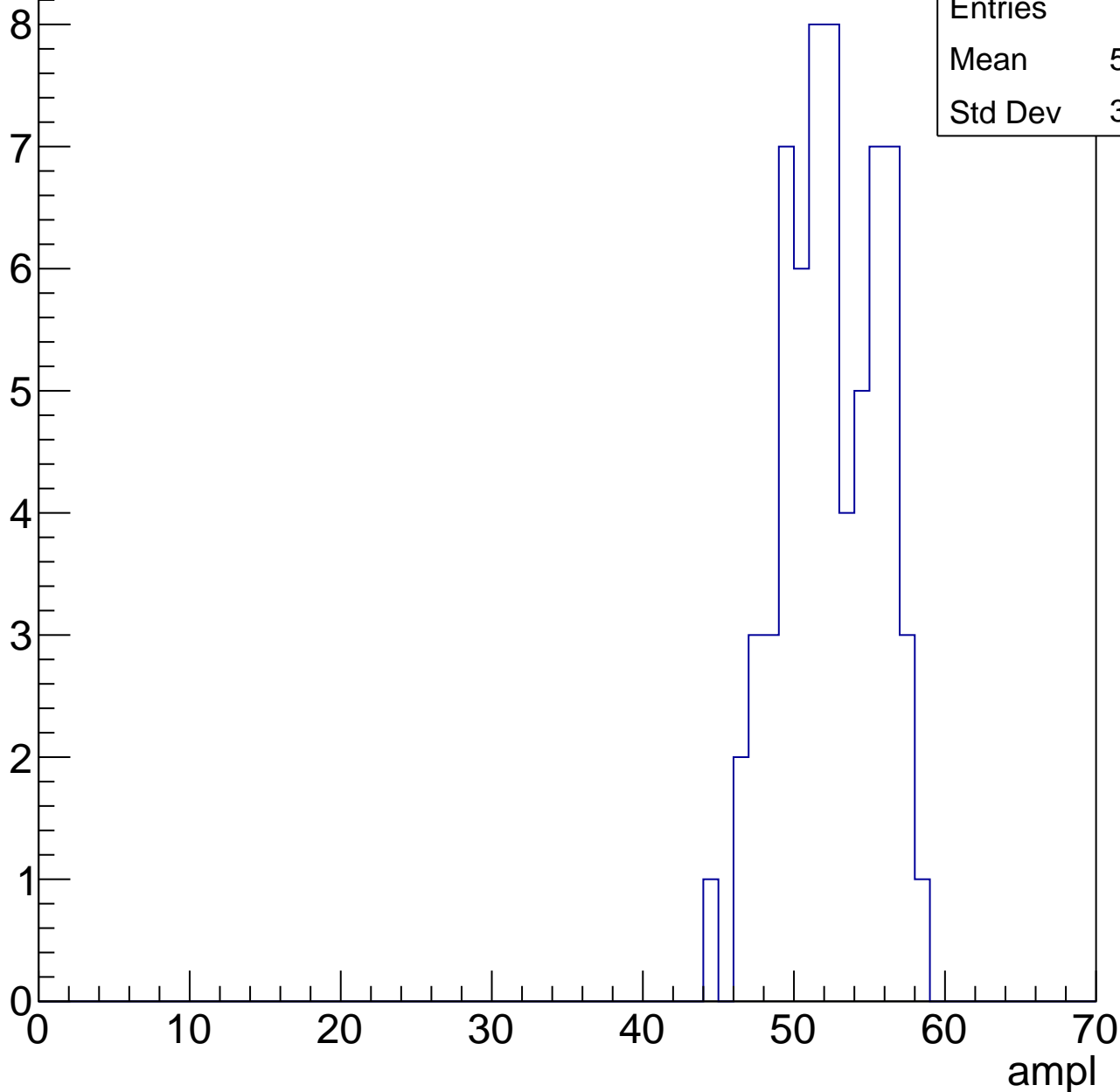


B0L001S, U19-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.94
Std Dev	3.196



B0L001S, U19-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

ampl

Entries	52
Mean	57.83
Std Dev	2.874

0

1

2

3

4

5

6

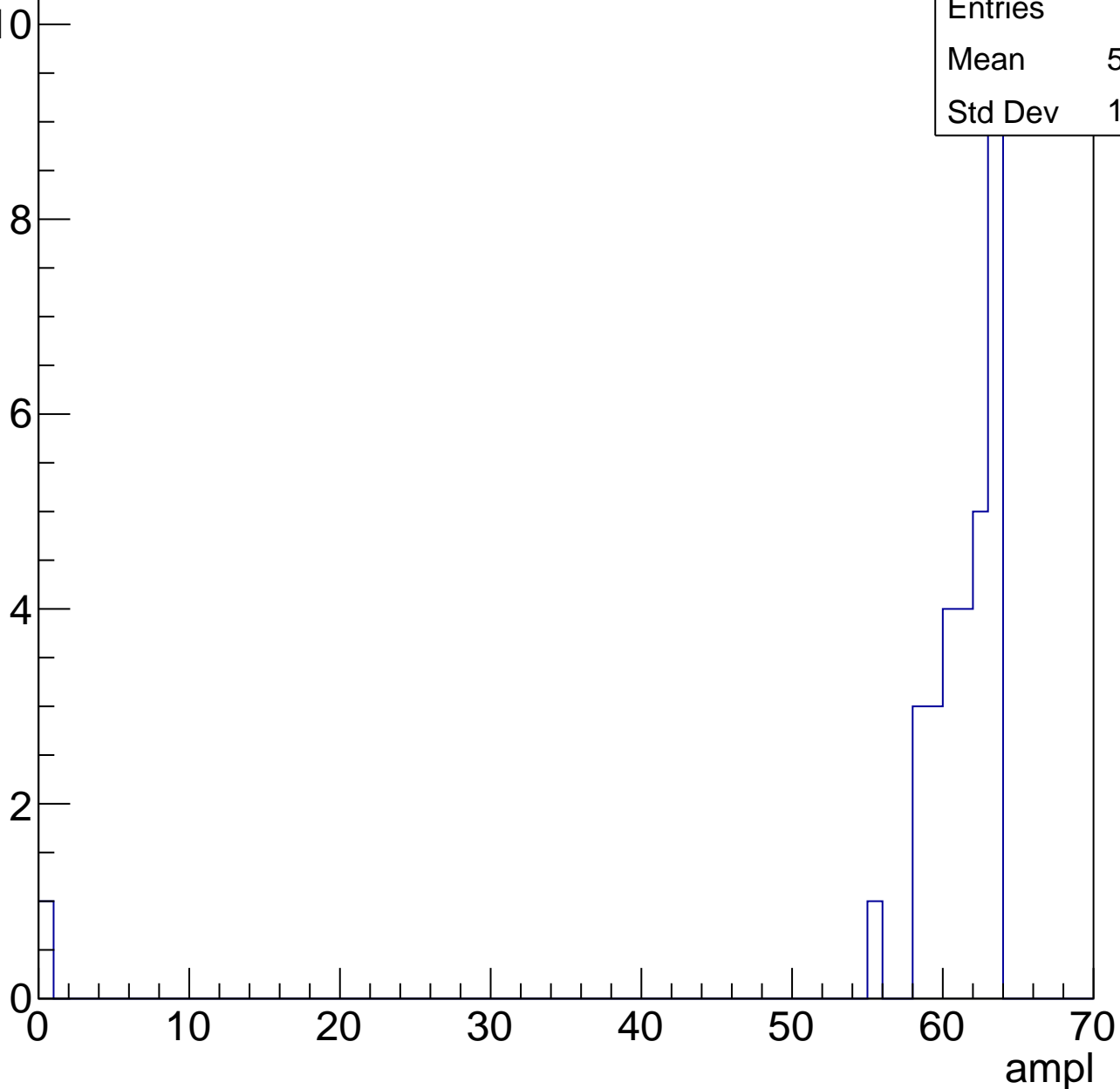
7

B0L001S, U19-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59.03
Std Dev	10.96



B0L001S, U19-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

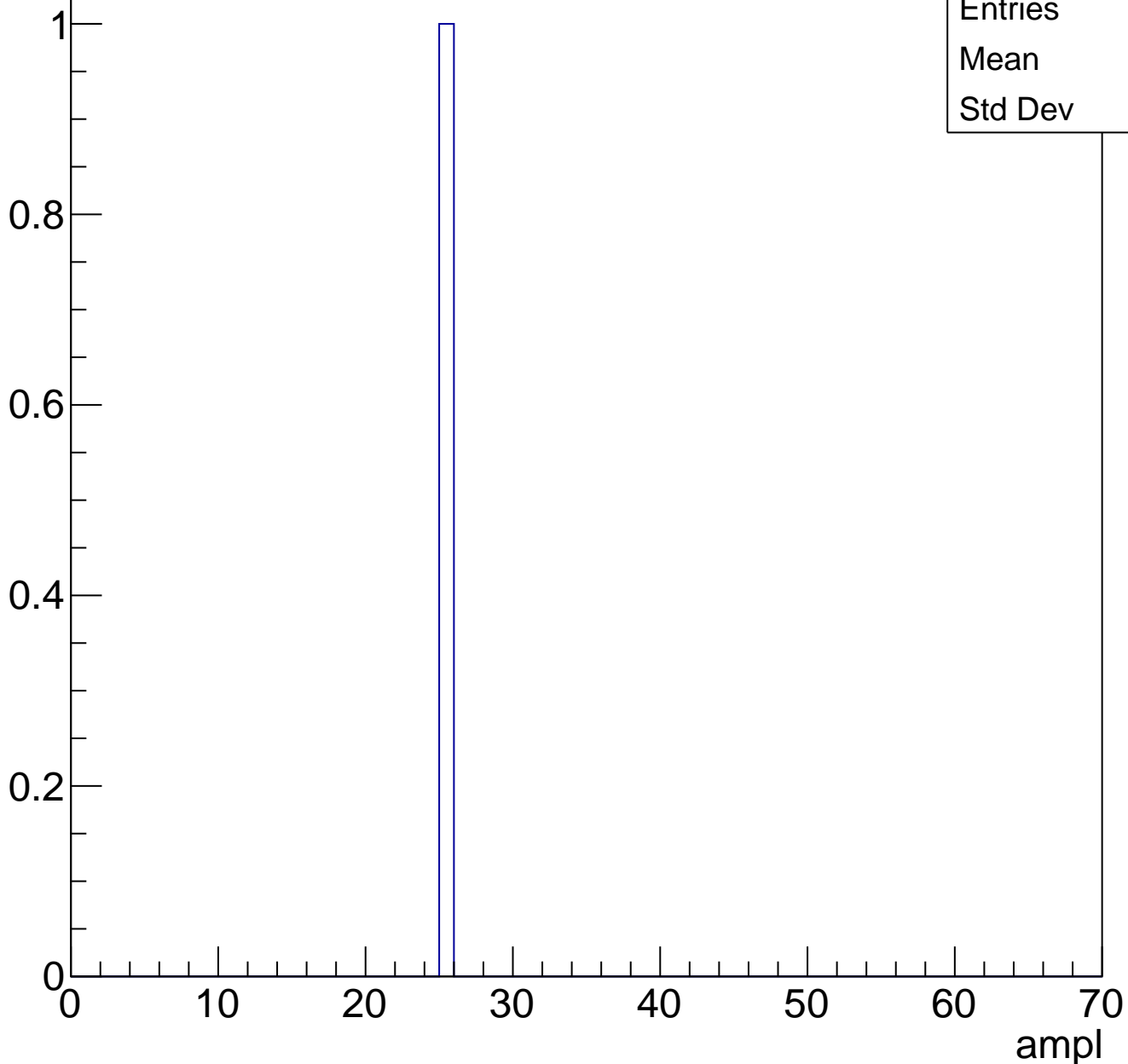
Entry



B0L001S, U19-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch44, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	30.97
Std Dev	3.509

Gaus mean : 31.2192

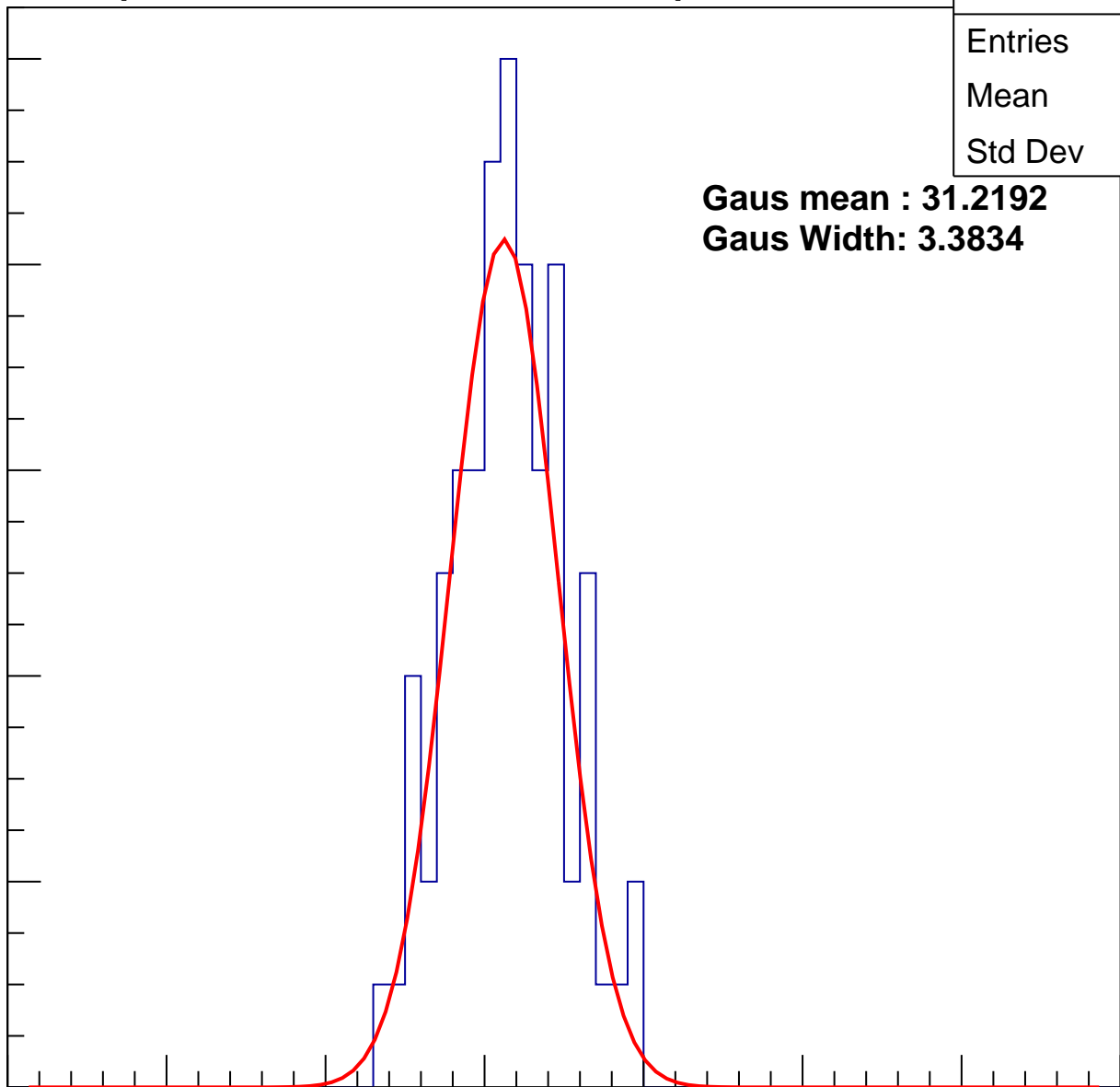
Gaus Width: 3.3834

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch44, adc1

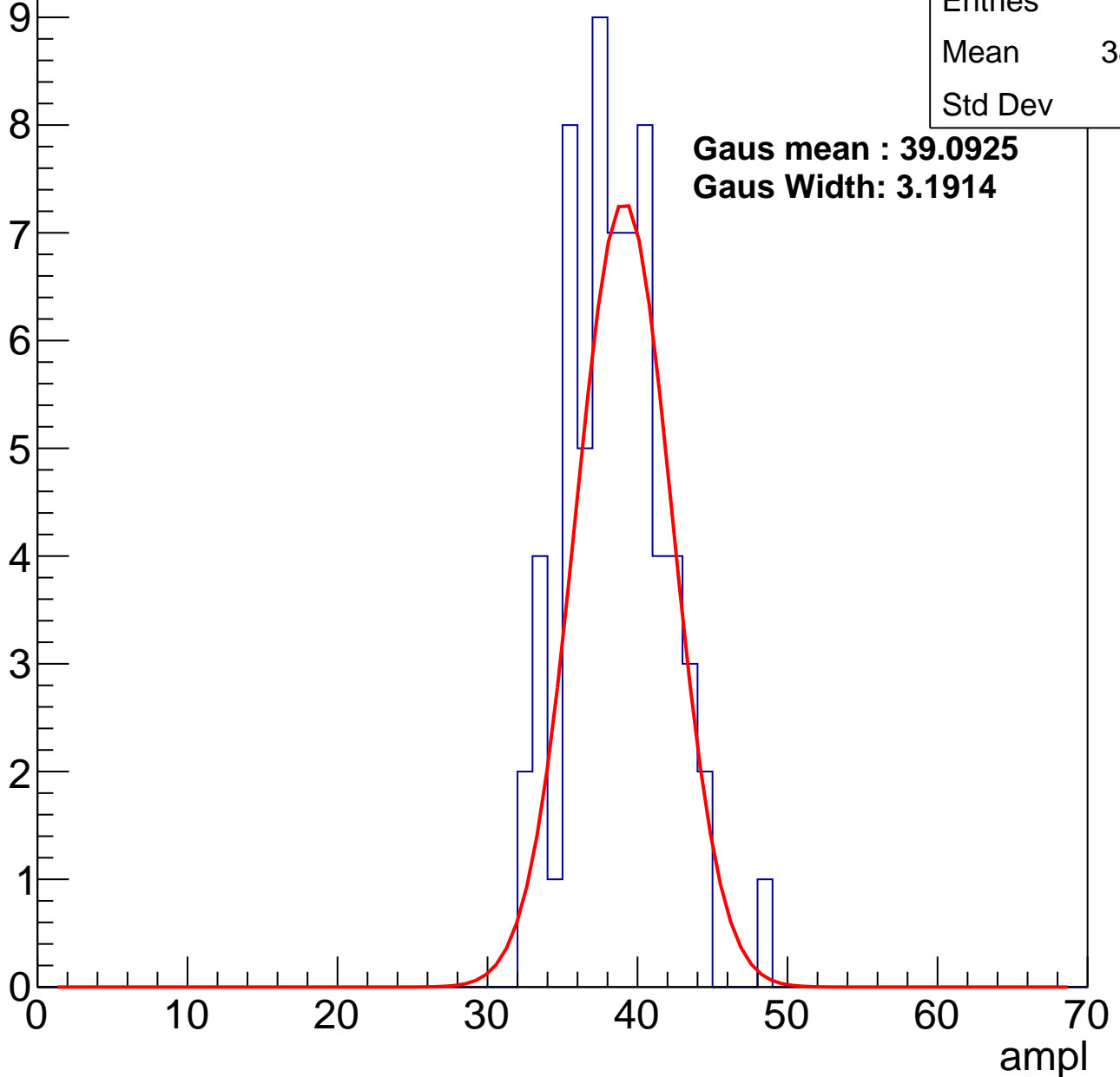
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.14
Std Dev	3.22

Gaus mean : 39.0925

Gaus Width: 3.1914



B0L001S, U19-ch44, adc2

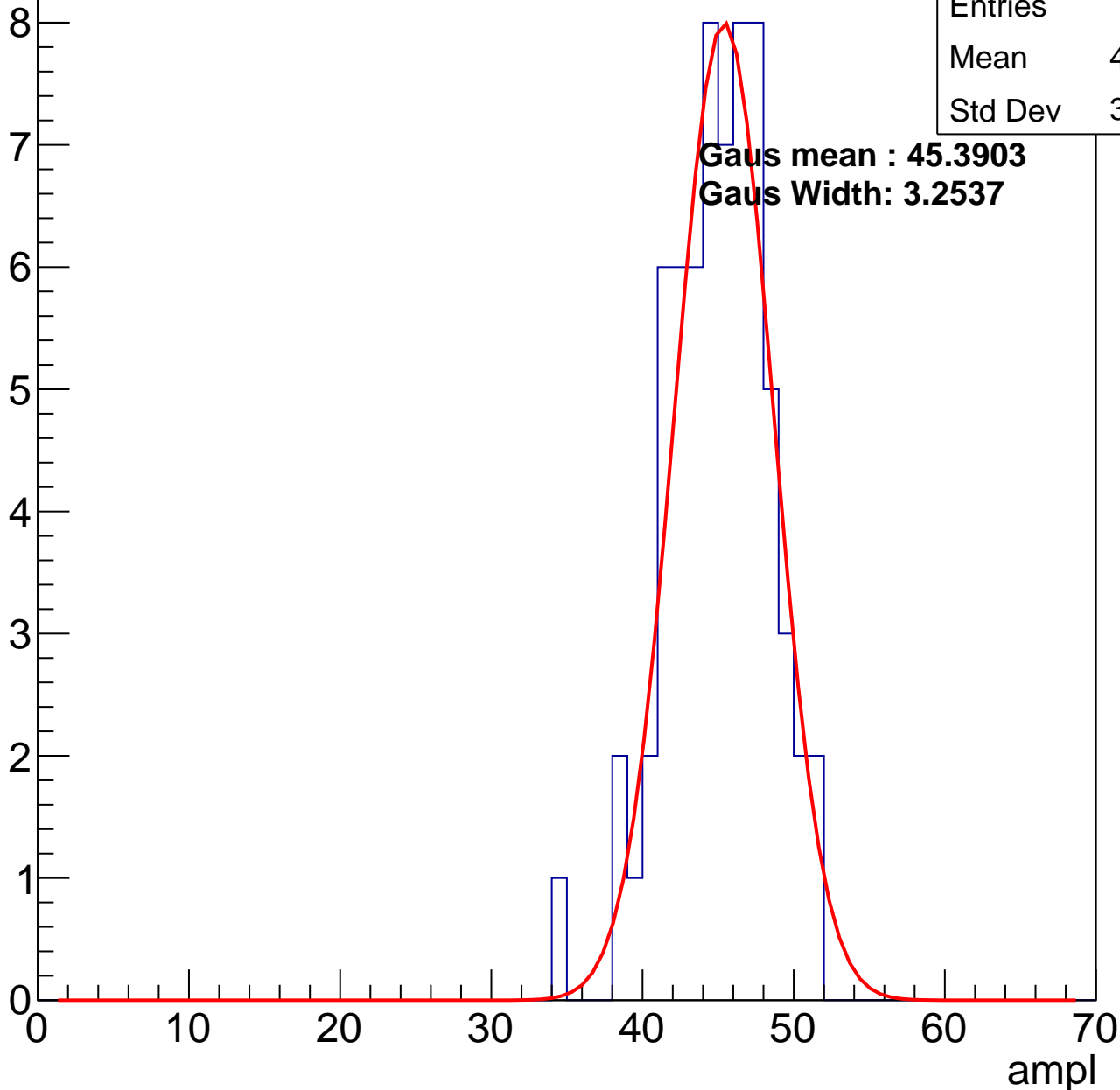
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.55
Std Dev	3.298

Gaus mean : 45.3903

Gaus Width: 3.2537

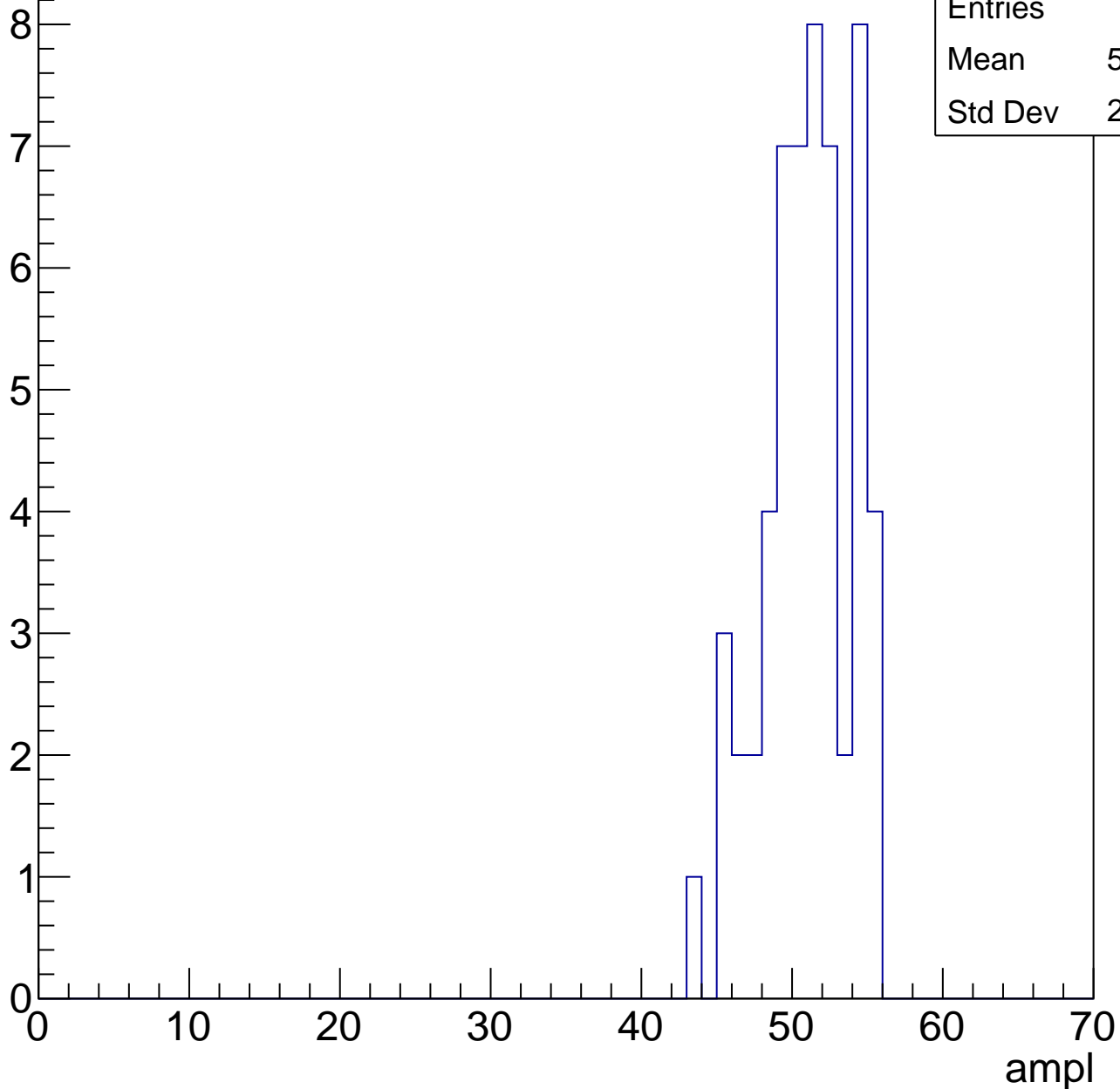


B0L001S, U19-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.53
Std Dev	2.904

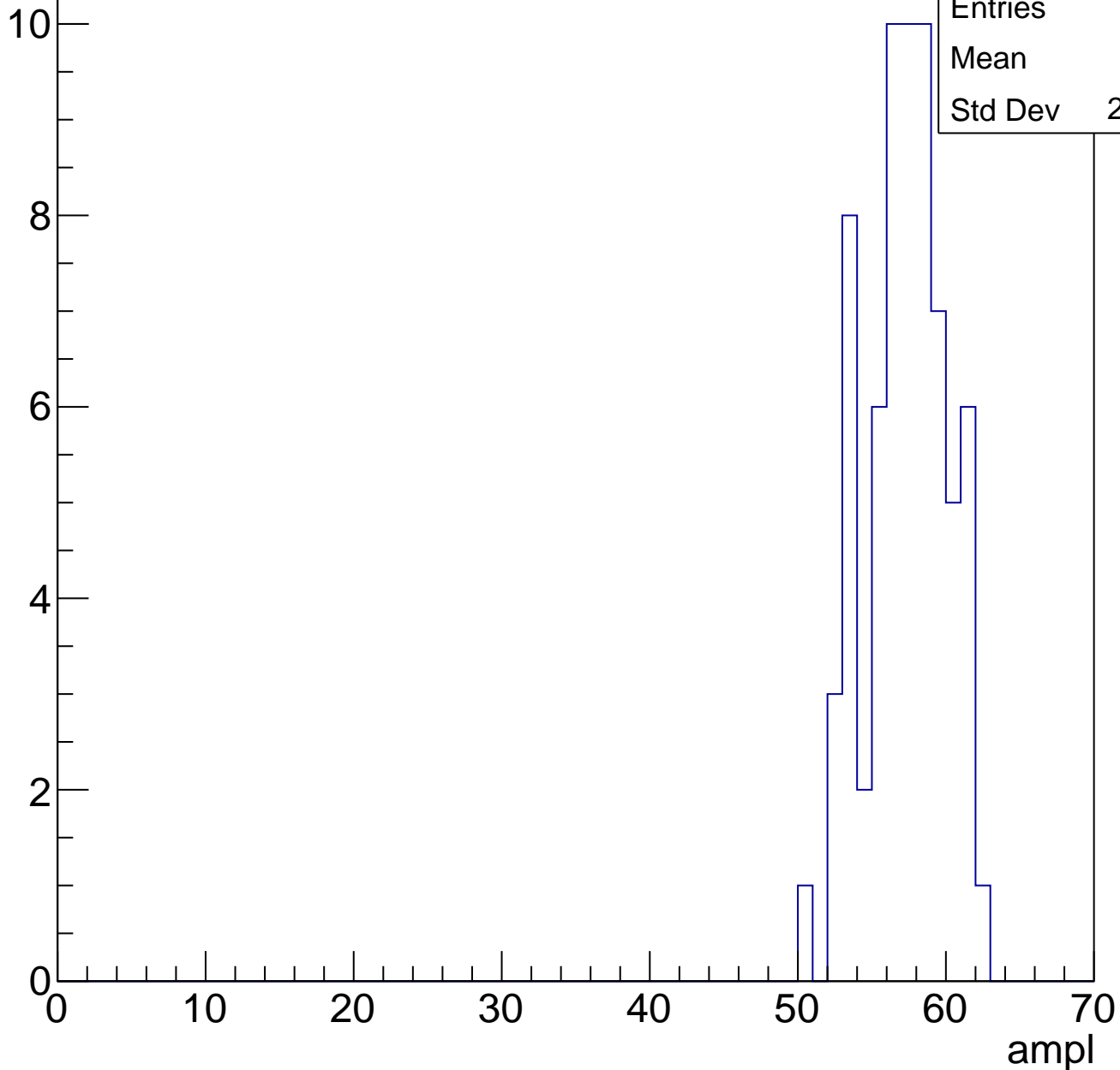


B0L001S, U19-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	56.8
Std Dev	2.706

Entry

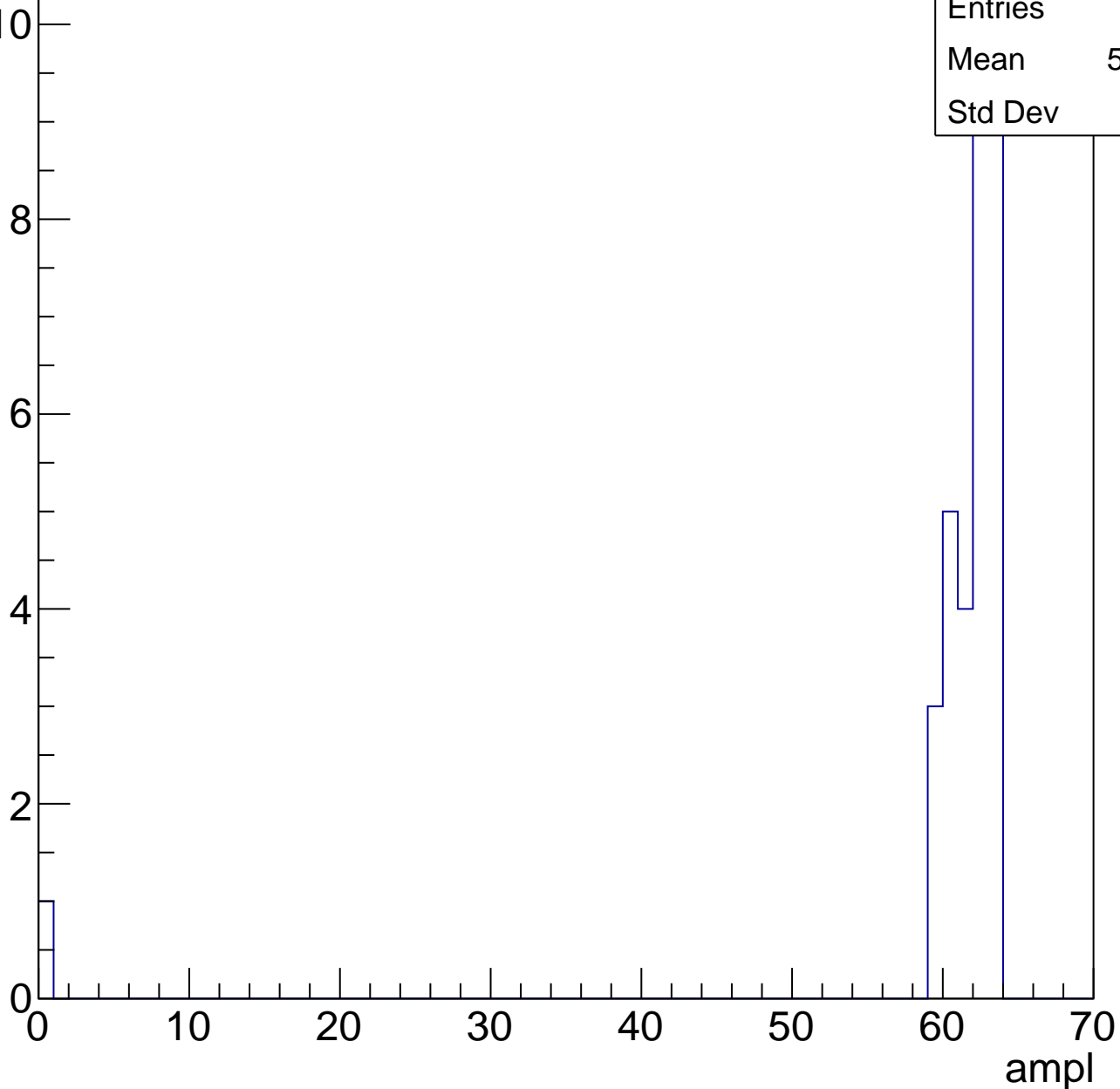


B0L001S, U19-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	59.66
Std Dev	10.8



B0L001S, U19-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch45, adc0

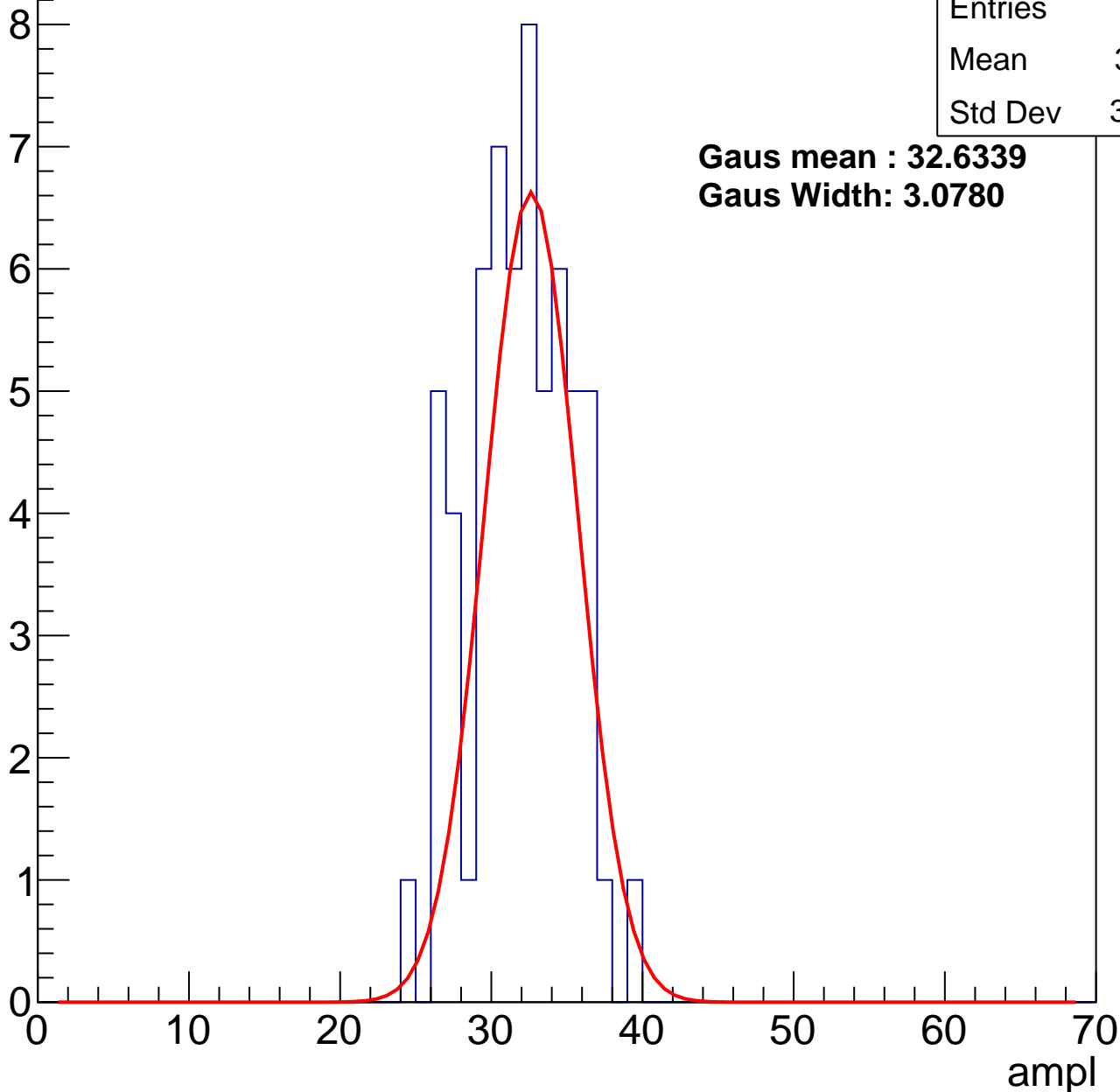
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	31.41
Std Dev	3.276

Gaus mean : 32.6339

Gaus Width: 3.0780



B0L001S, U19-ch45, adc1

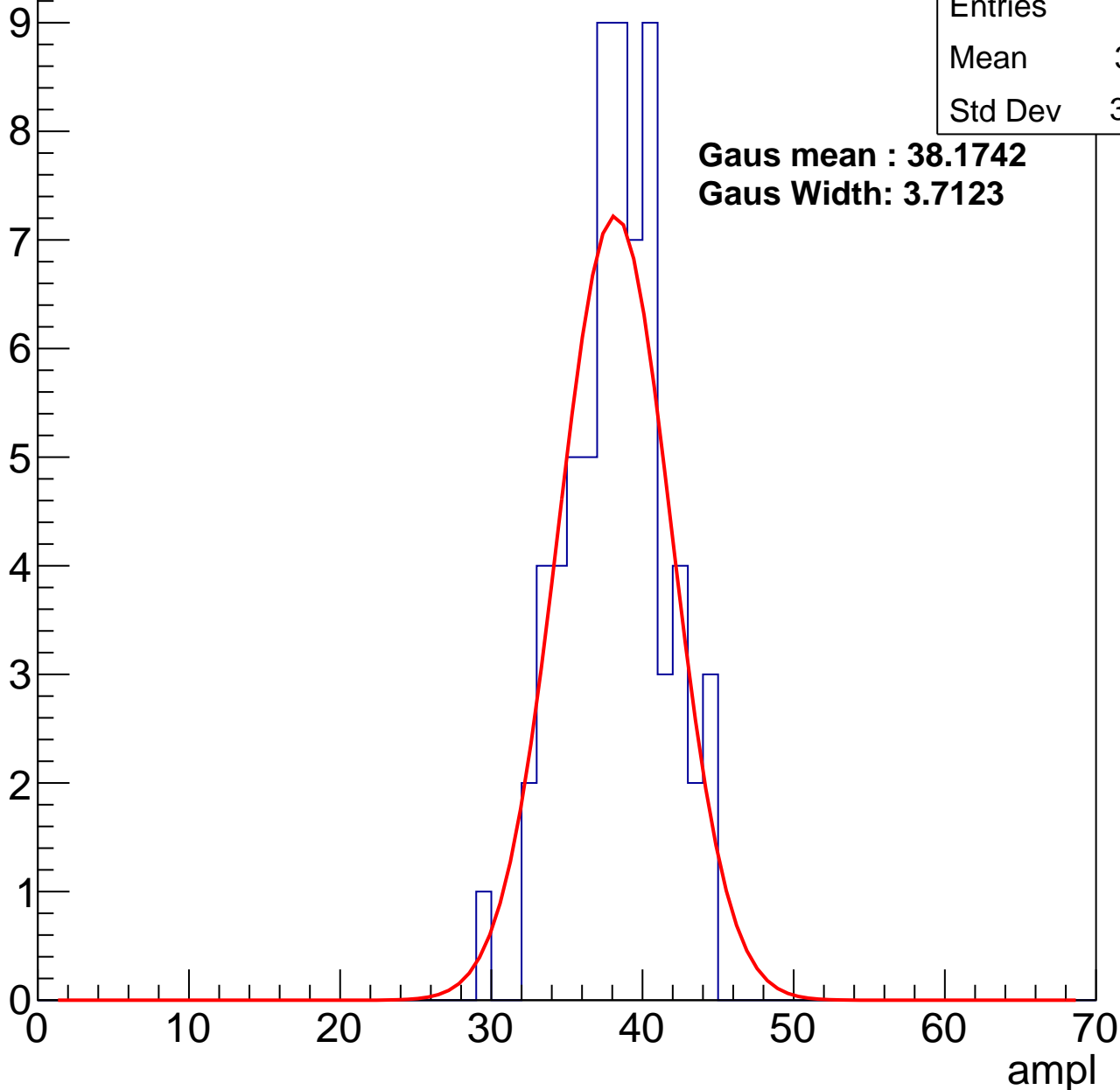
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.81
Std Dev	3.187

Gaus mean : 38.1742

Gaus Width: 3.7123



B0L001S, U19-ch45, adc2

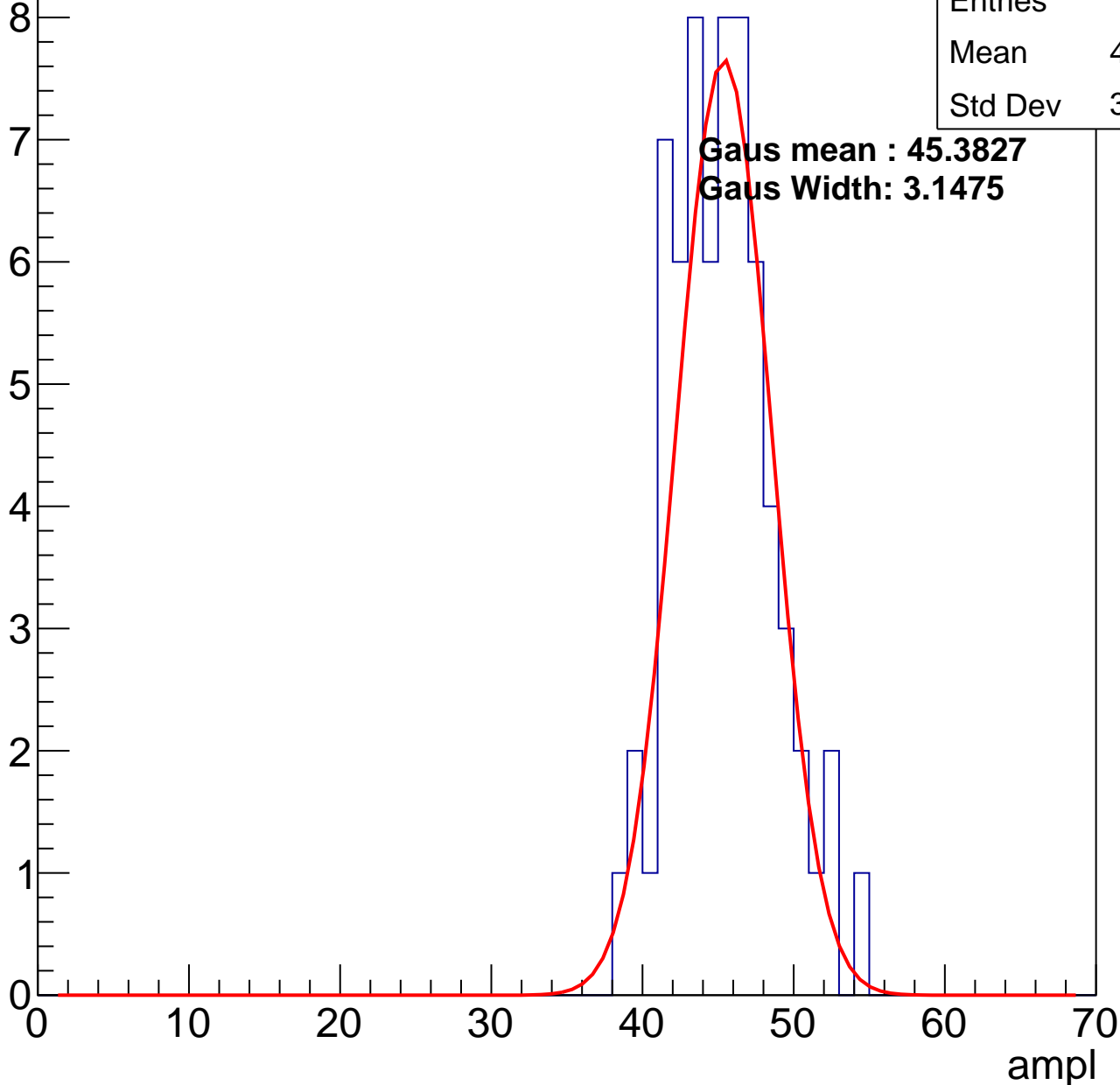
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.86
Std Dev	3.325

Gaus mean : 45.3827

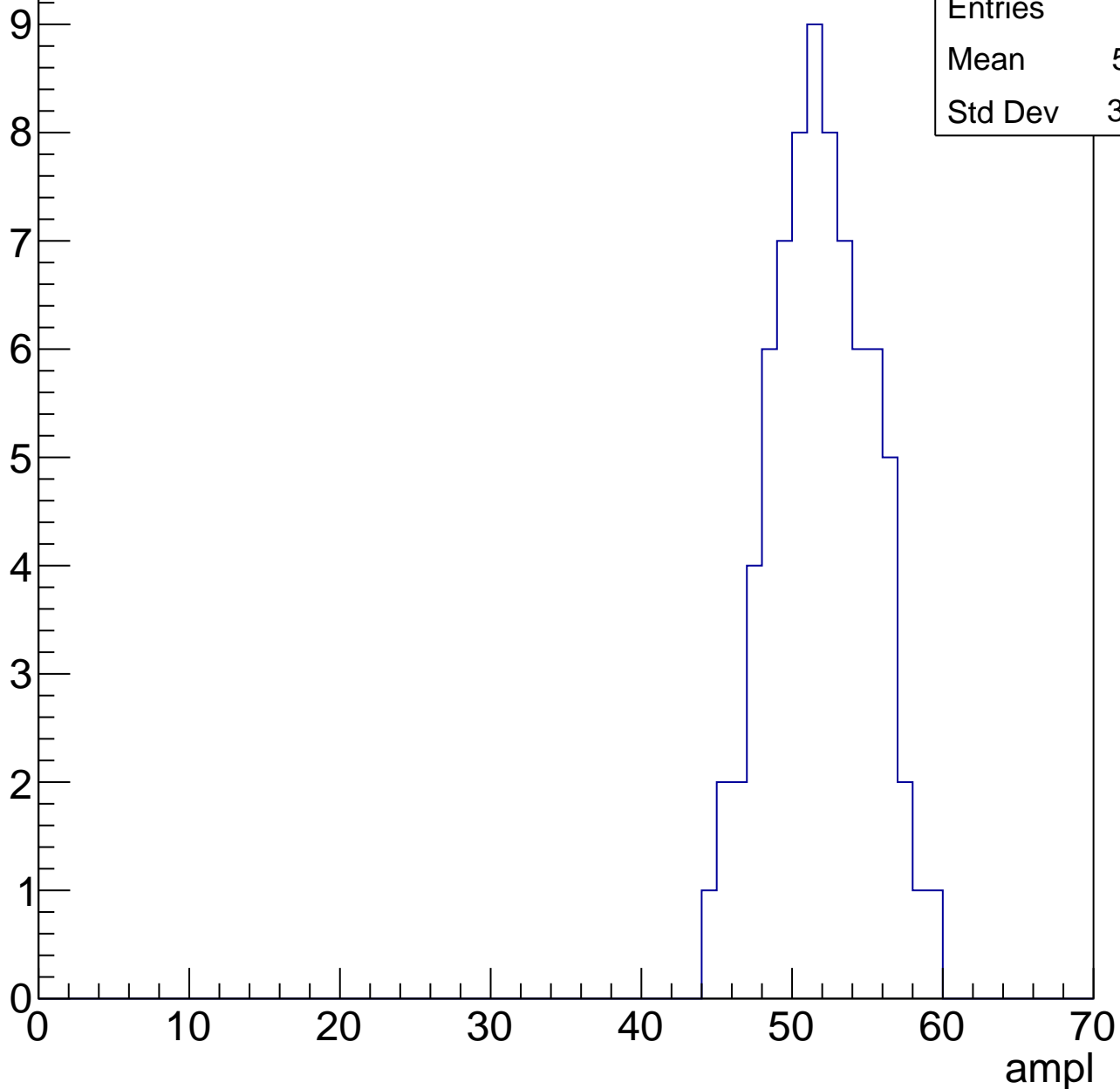
Gaus Width: 3.1475



B0L001S, U19-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

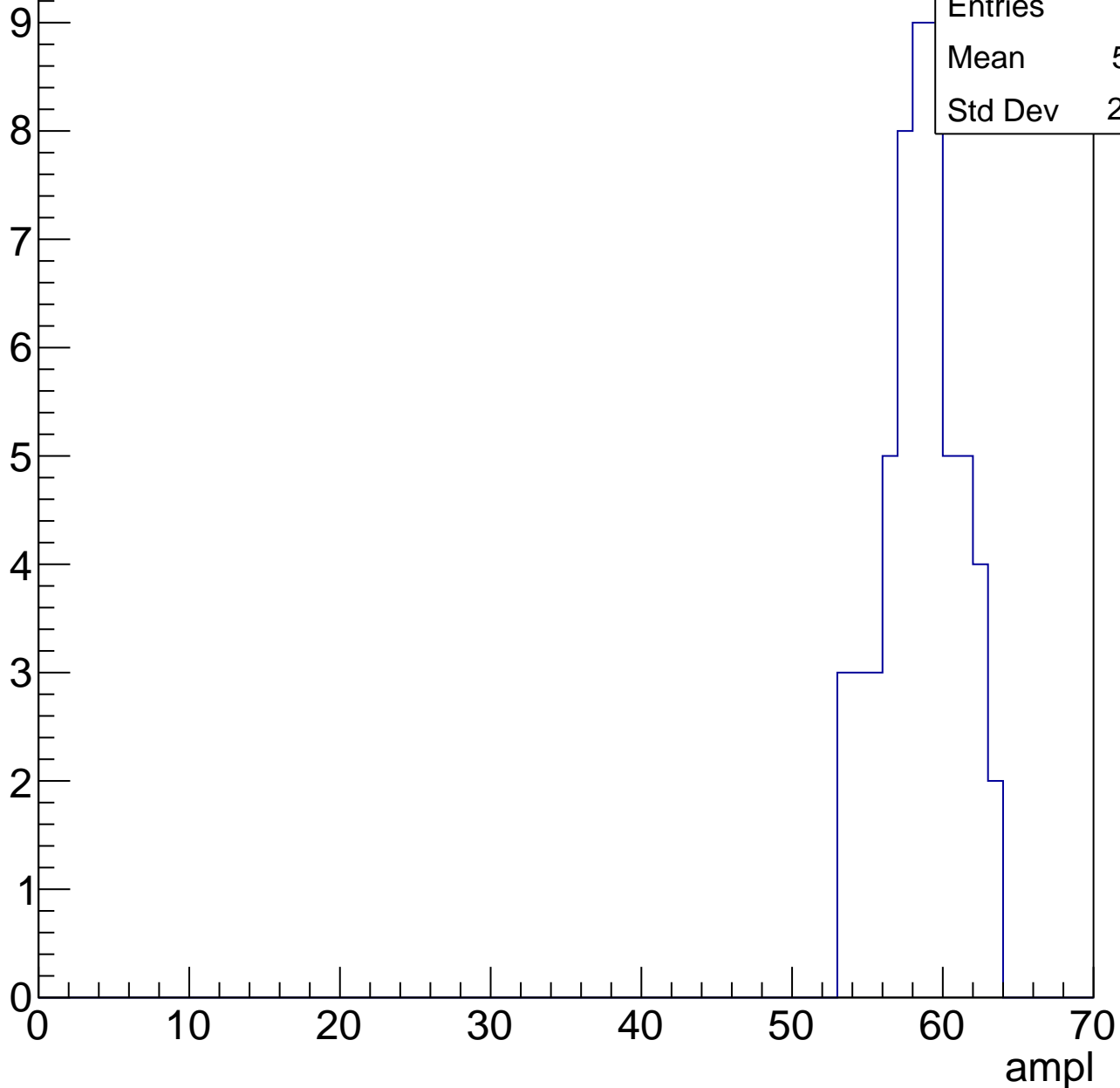


Entries	75
Mean	51.41
Std Dev	3.283

B0L001S, U19-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



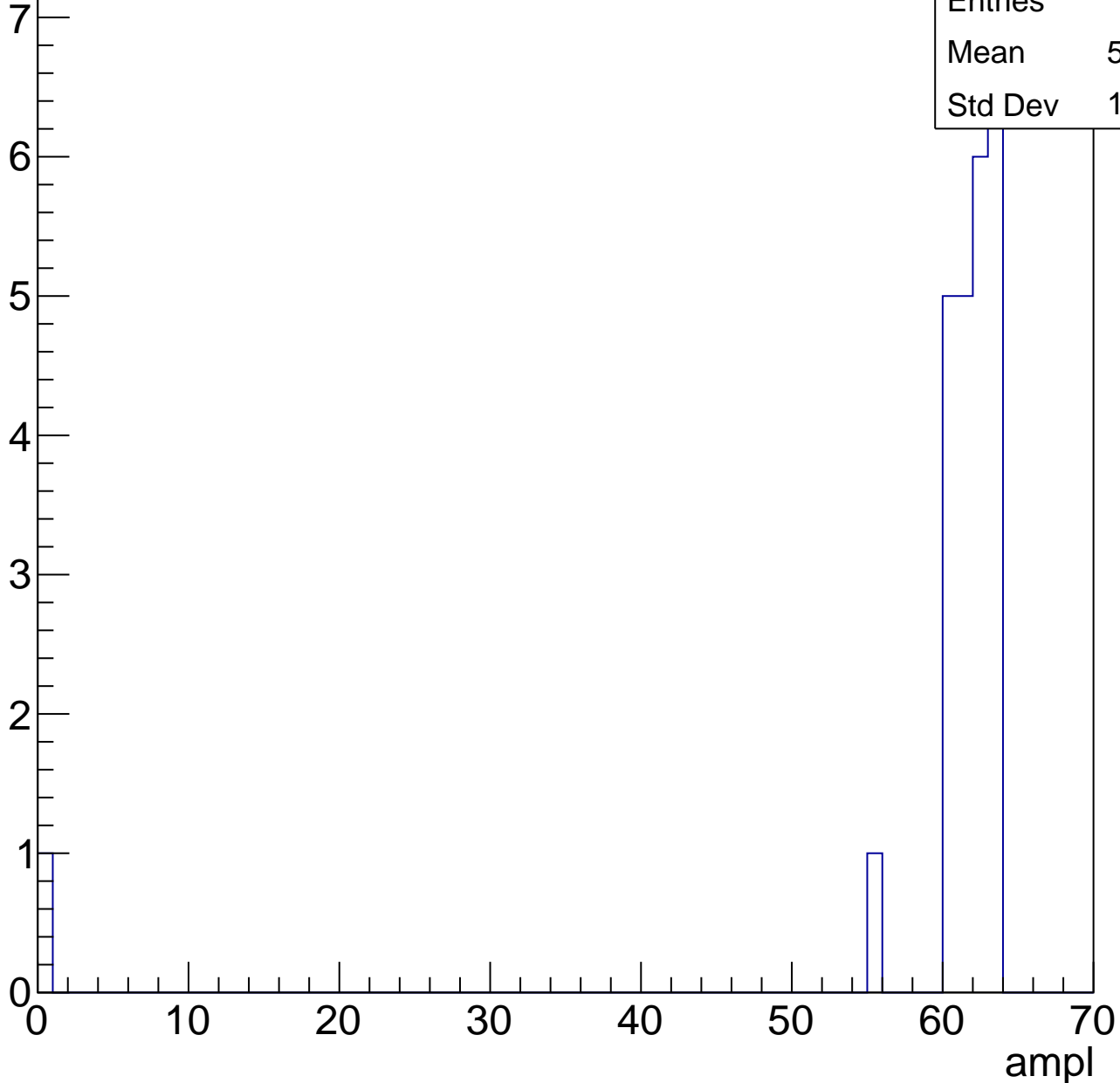
Entries	56
Mean	58.11
Std Dev	2.554

B0L001S, U19-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.92
Std Dev	12.15



B0L001S, U19-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch46, adc0

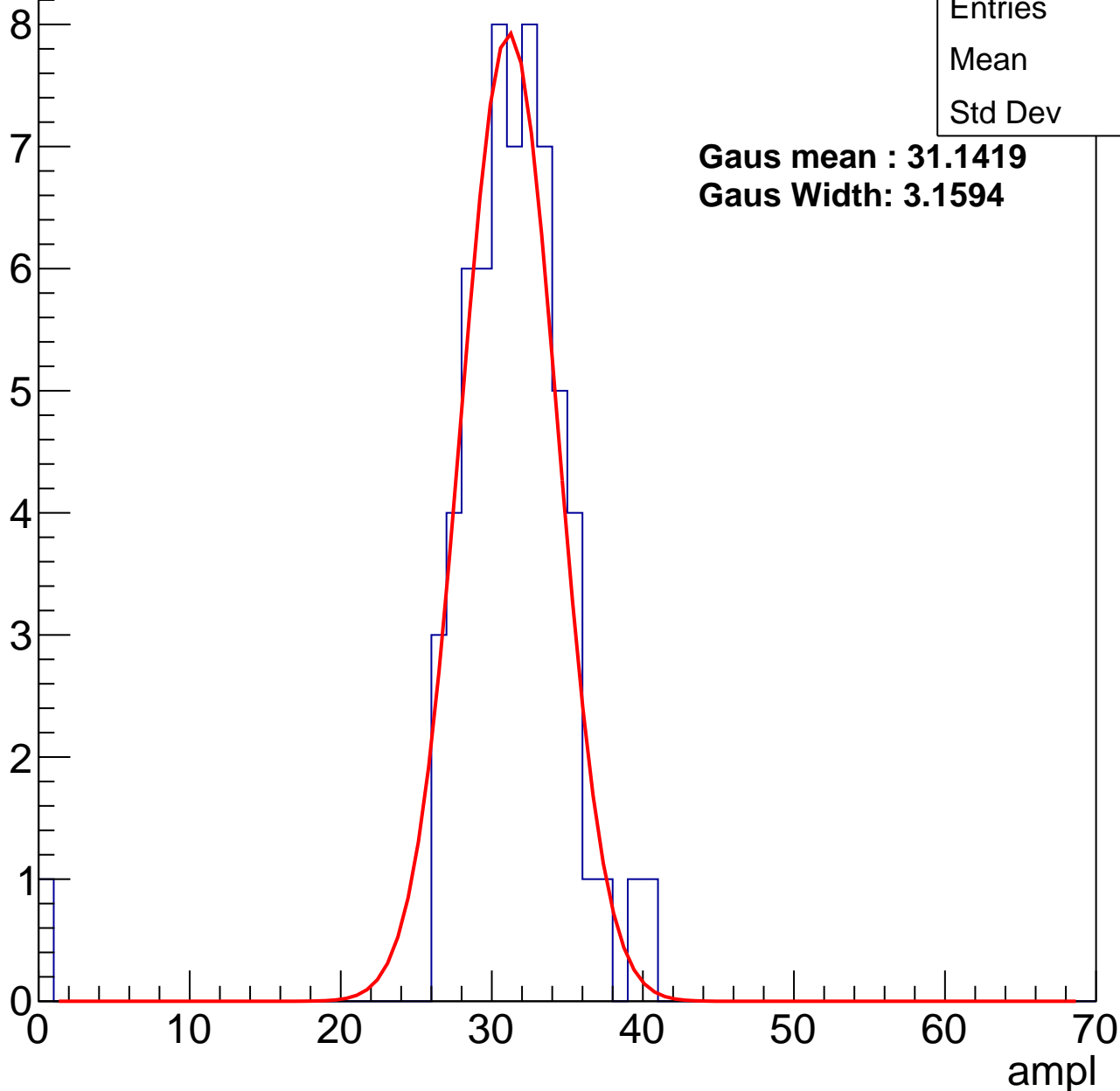
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	30.7
Std Dev	4.93

Gaus mean : 31.1419

Gaus Width: 3.1594



B0L001S, U19-ch46, adc1

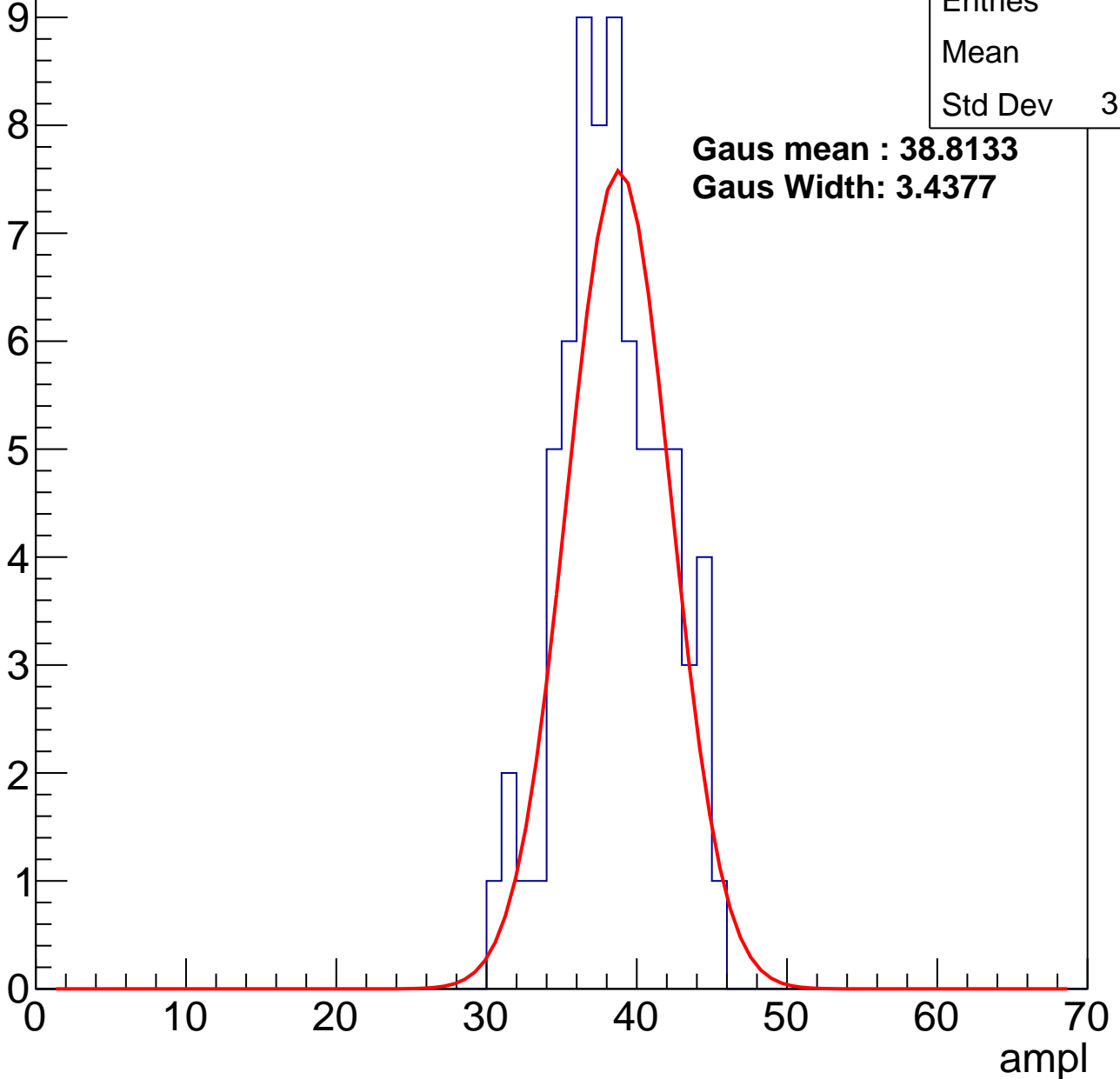
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38
Std Dev	3.398

Gaus mean : 38.8133

Gaus Width: 3.4377



B0L001S, U19-ch46, adc2

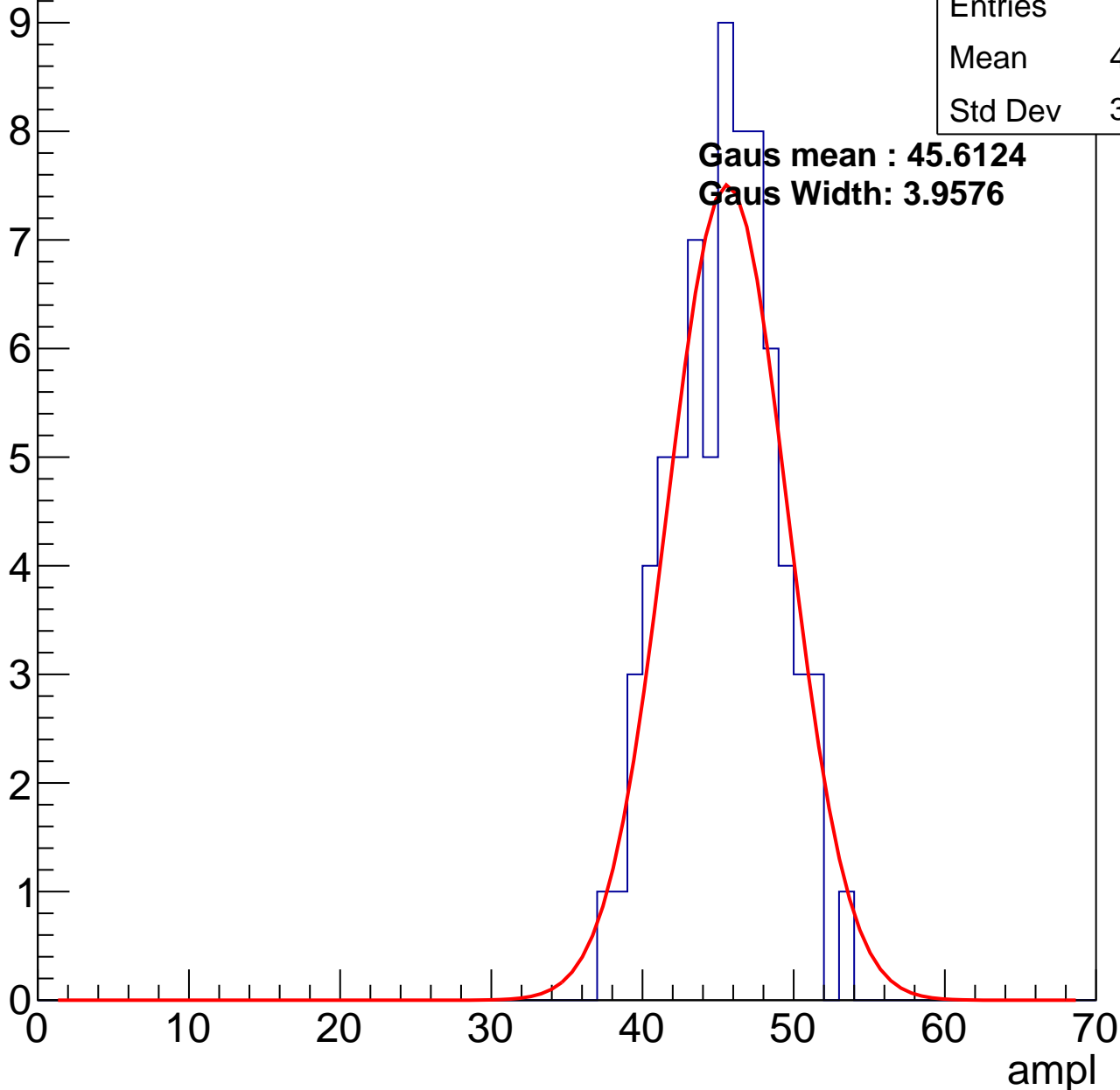
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	44.89
Std Dev	3.478

Gaus mean : 45.6124

Gaus Width: 3.9576

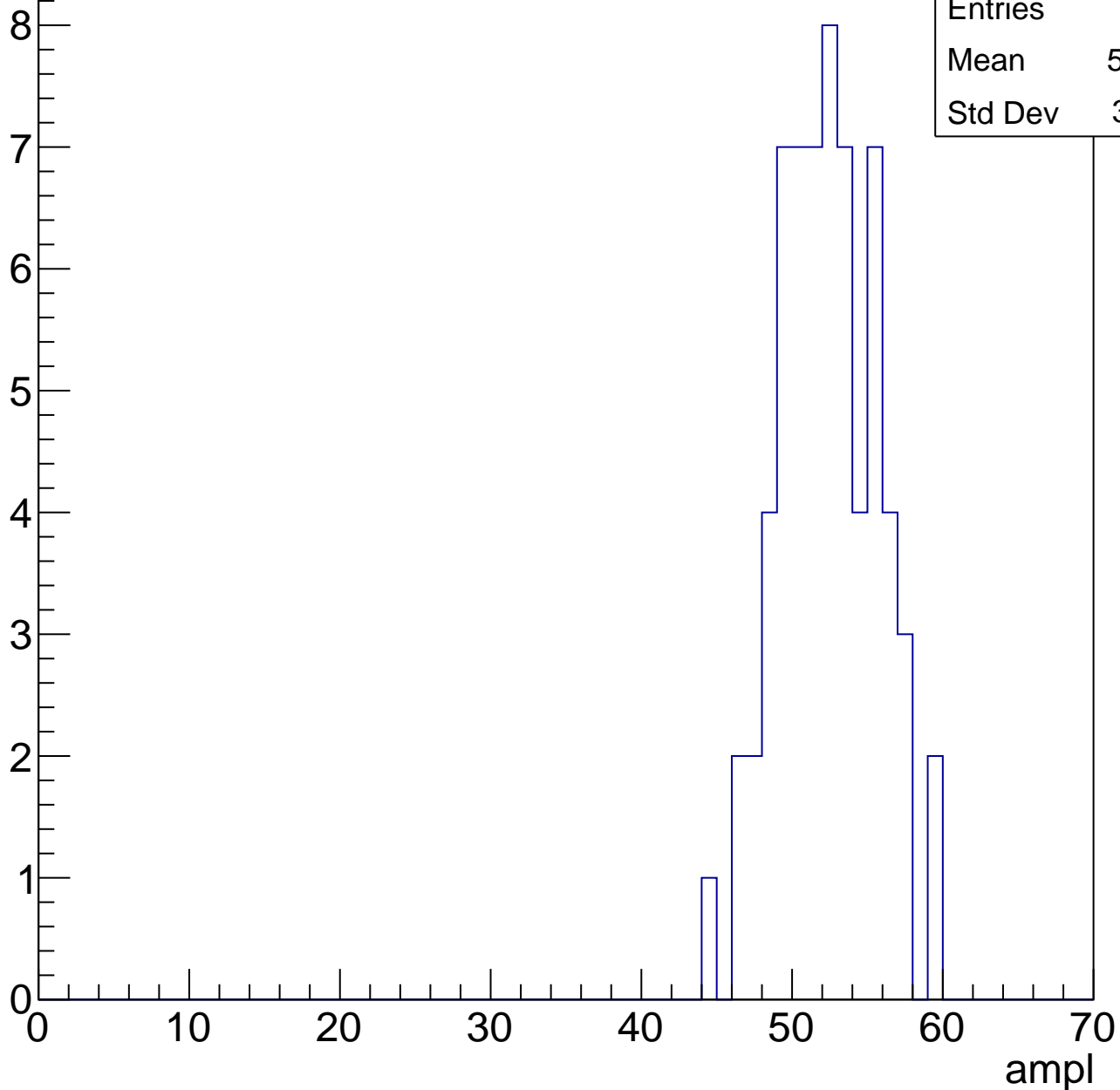


B0L001S, U19-ch46, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.89
Std Dev	3.211

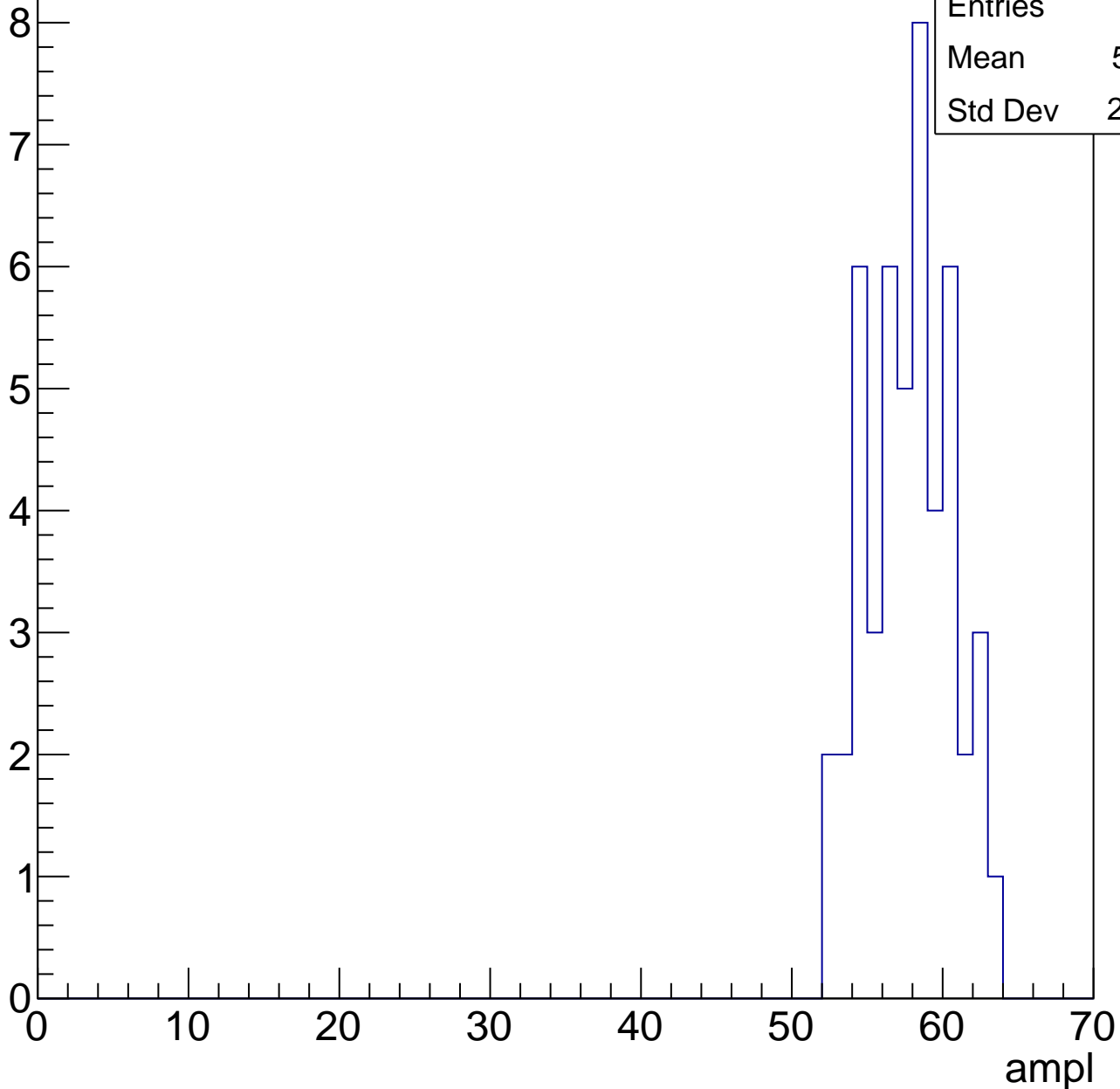


B0L001S, U19-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	57.31
Std Dev	2.778

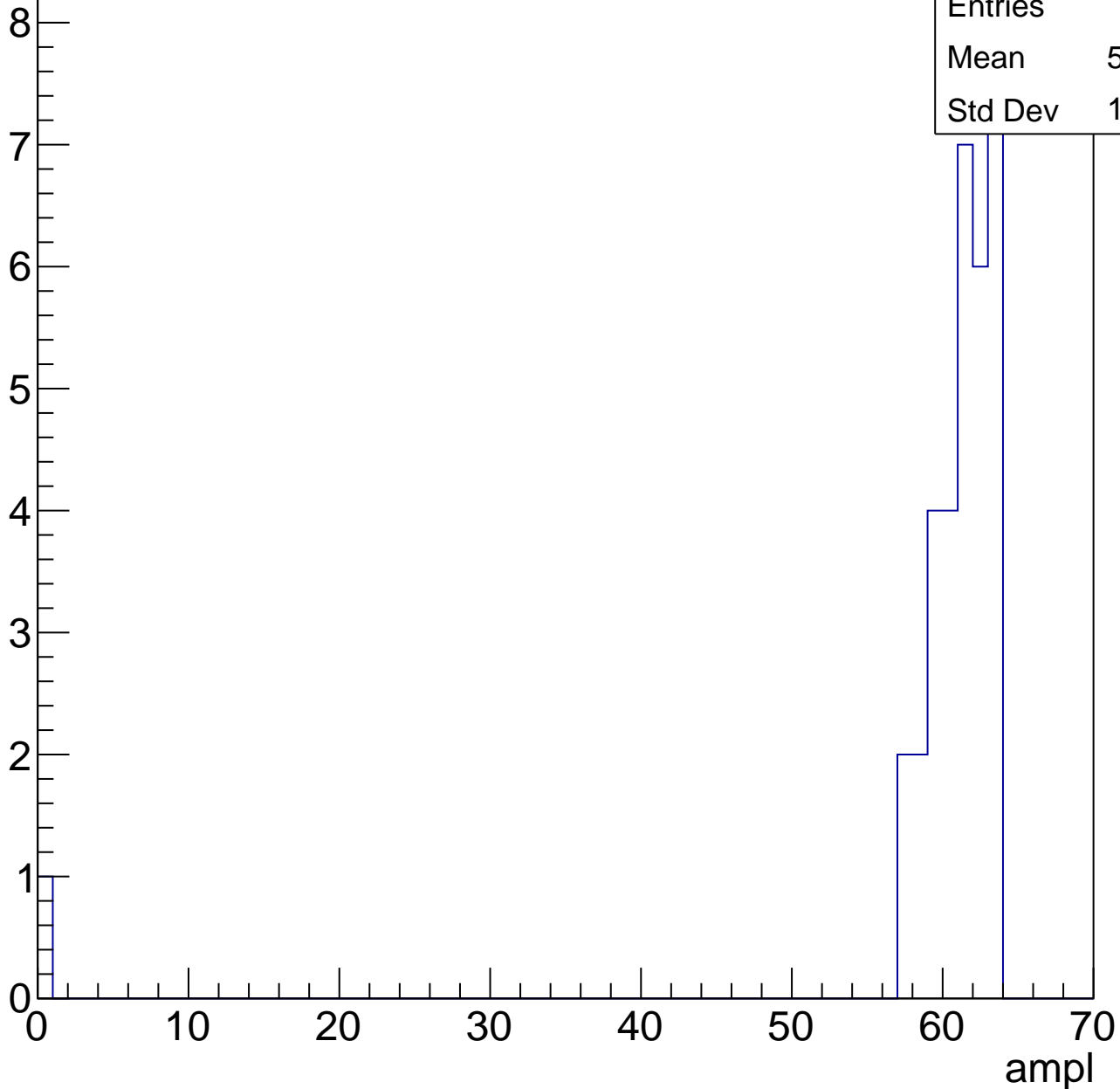


B0L001S, U19-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	59.09
Std Dev	10.44



B0L001S, U19-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L001S, U19-ch47, adc0

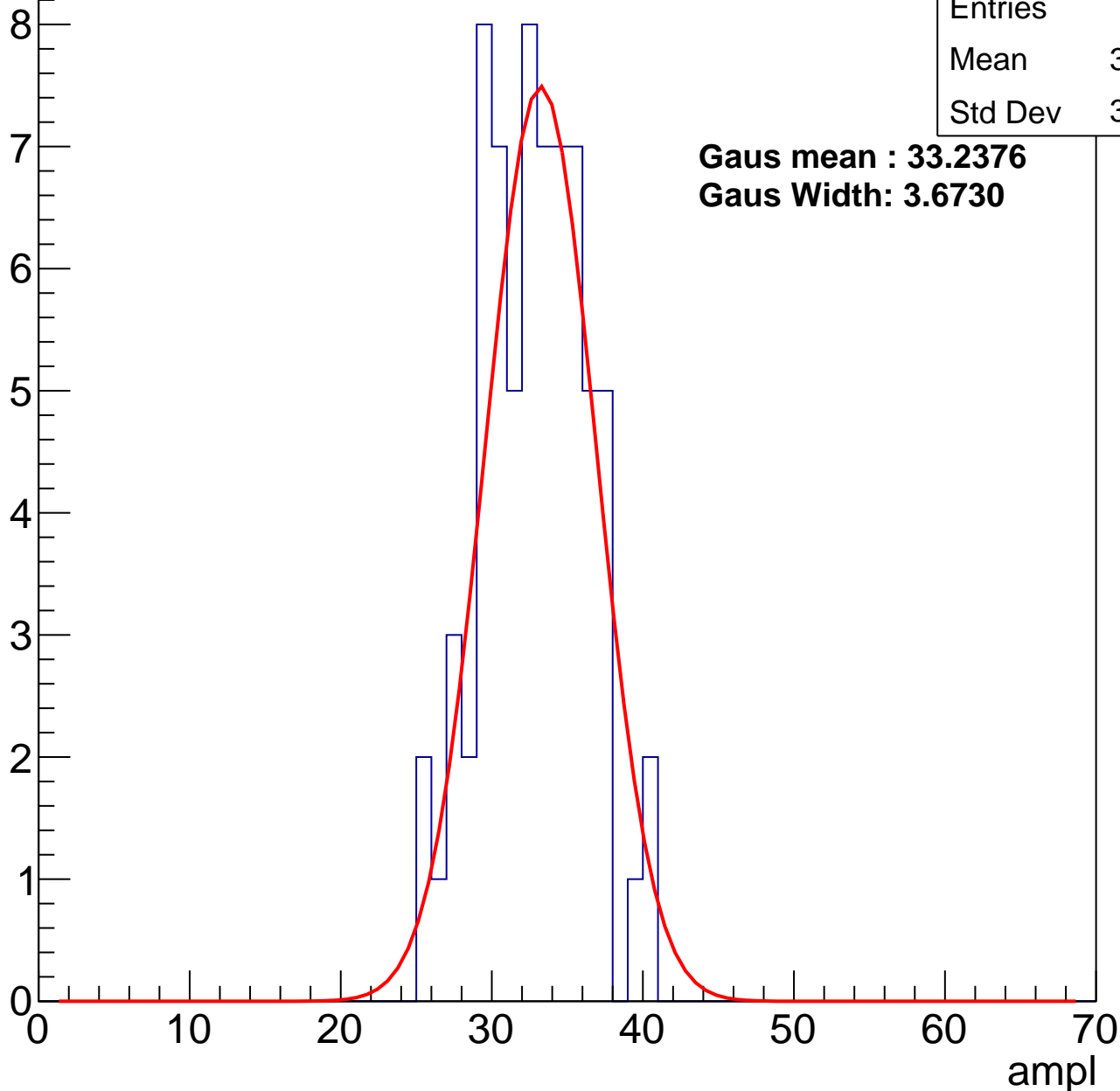
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	32.34
Std Dev	3.418

Gaus mean : 33.2376

Gaus Width: 3.6730



B0L001S, U19-ch47, adc1

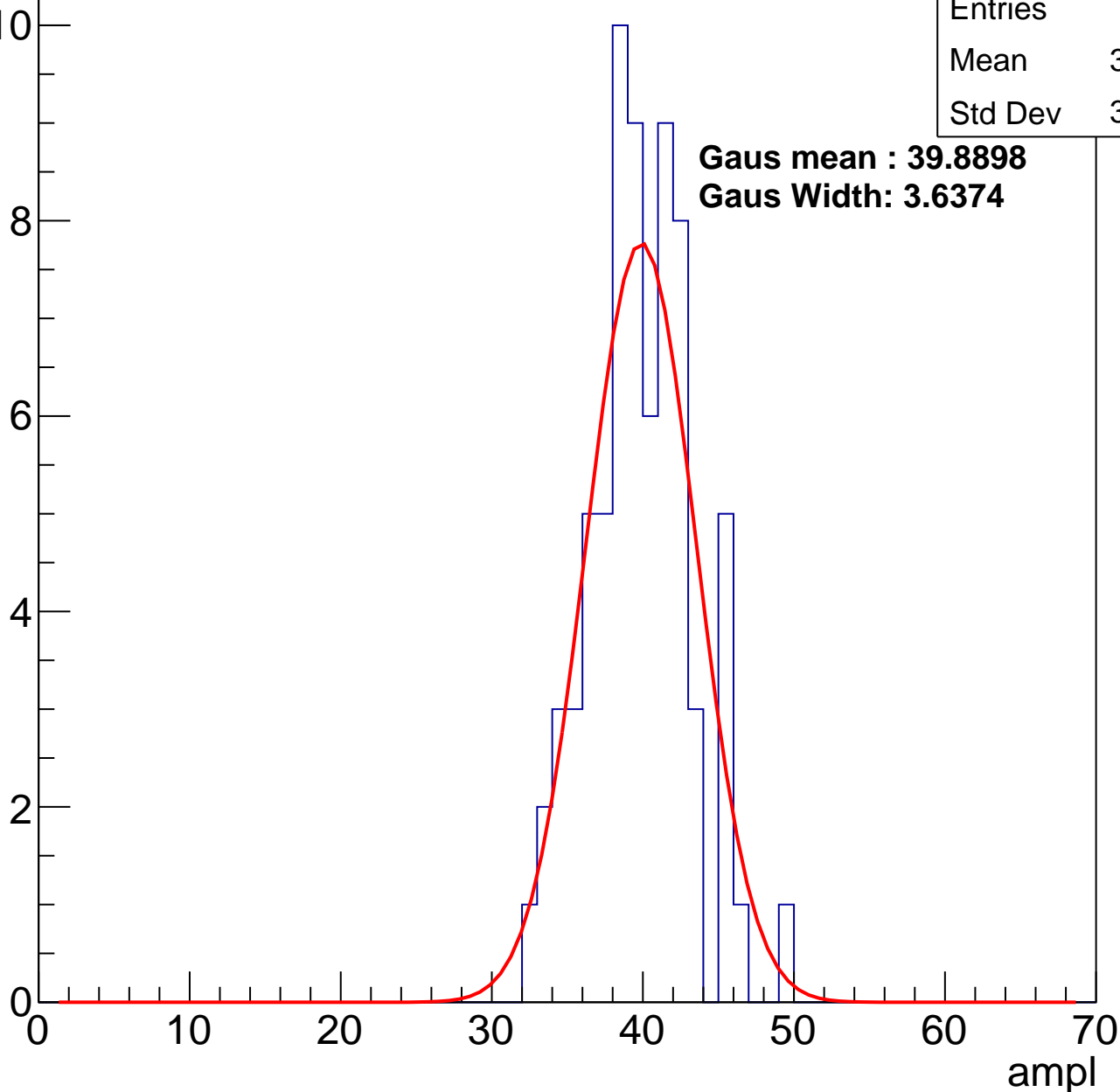
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	39.37
Std Dev	3.358

Gaus mean : 39.8898

Gaus Width: 3.6374



B0L001S, U19-ch47, adc2

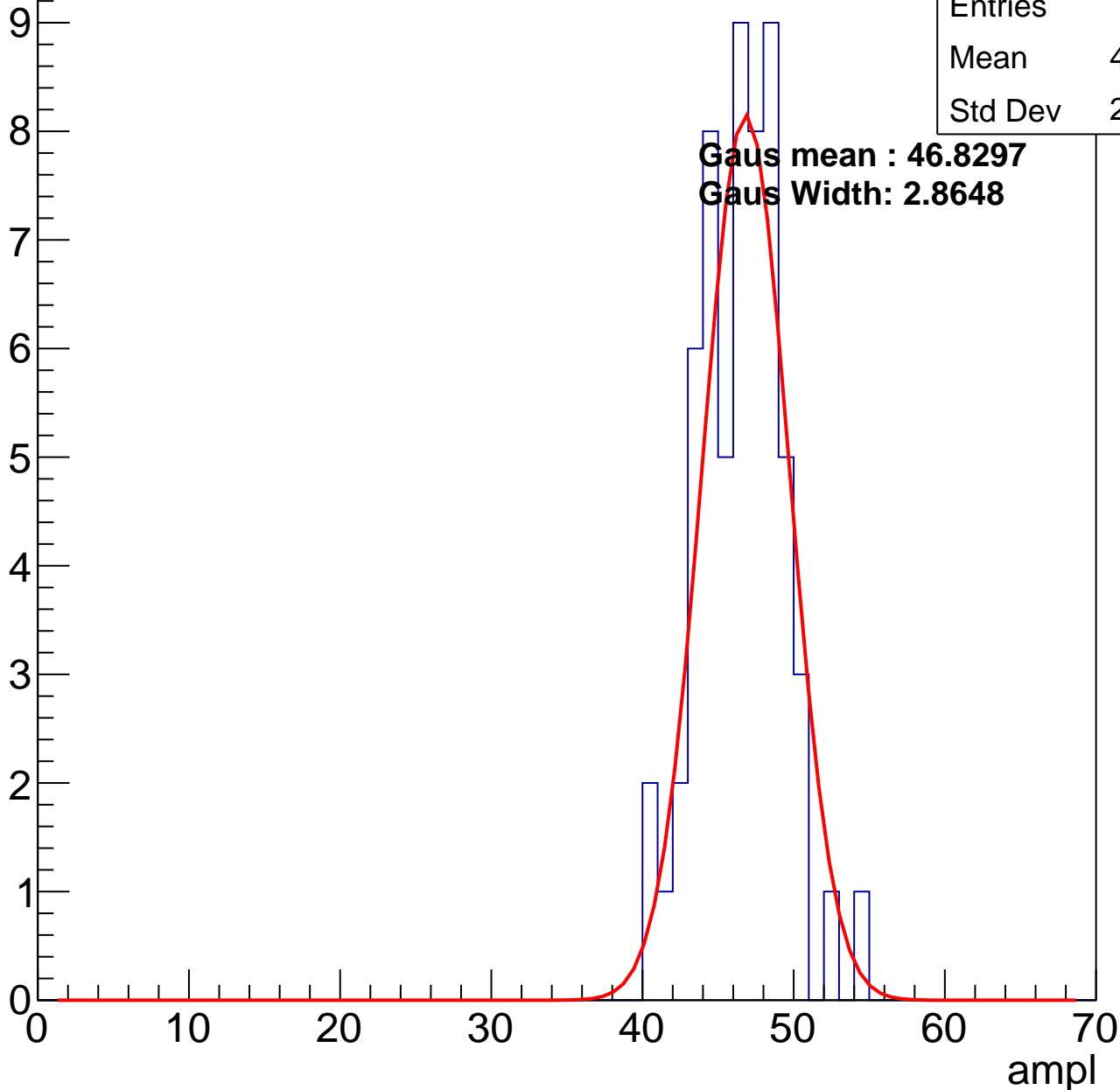
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	46.05
Std Dev	2.759

Gaus mean : 46.8297

Gaus Width: 2.8648



B0L001S, U19-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	52.77
Std Dev	3.498

Entry

10
8
6
4
2
0

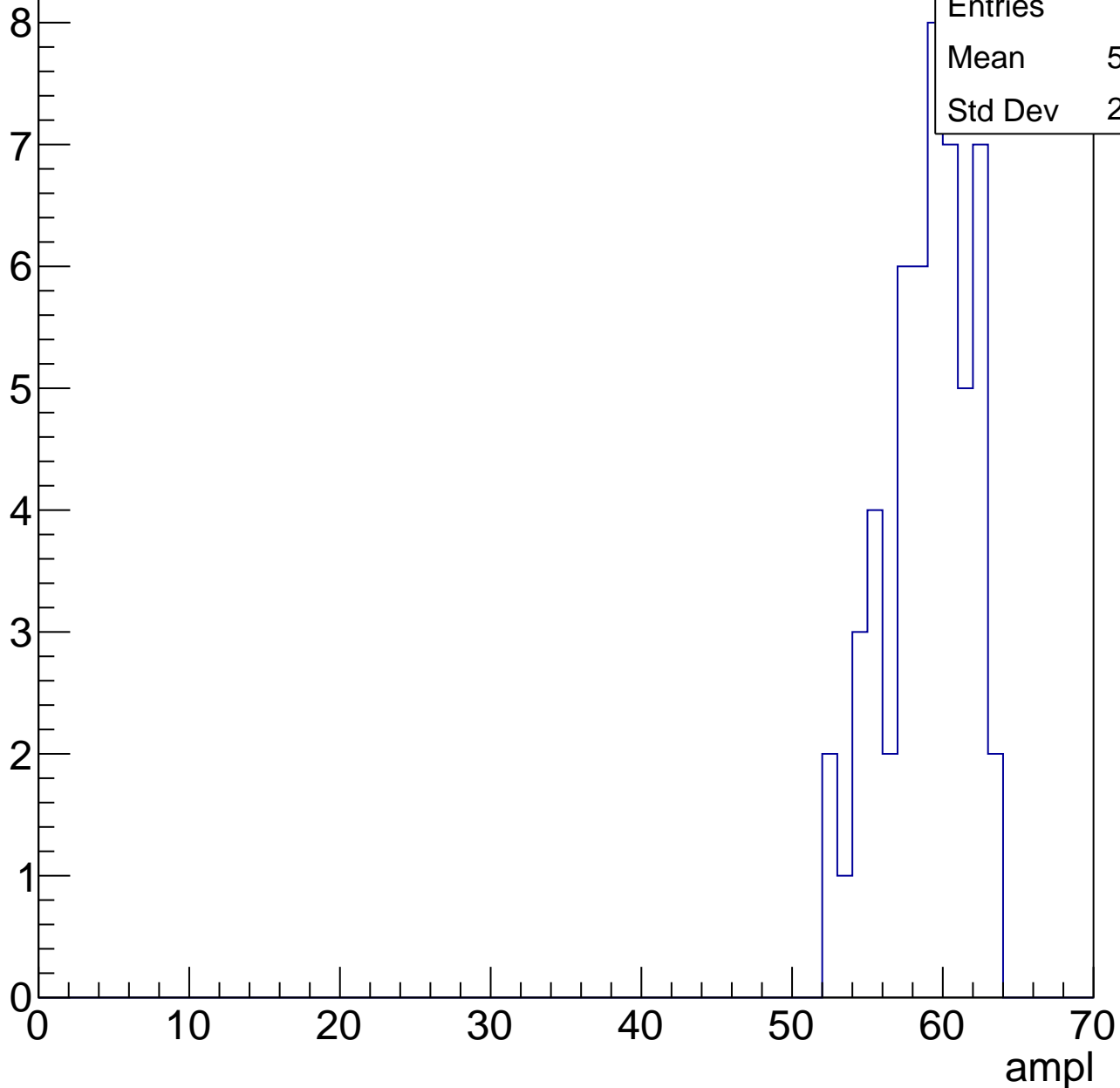
ampl

0 10 20 30 40 50 60 70

B0L001S, U19-ch47, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

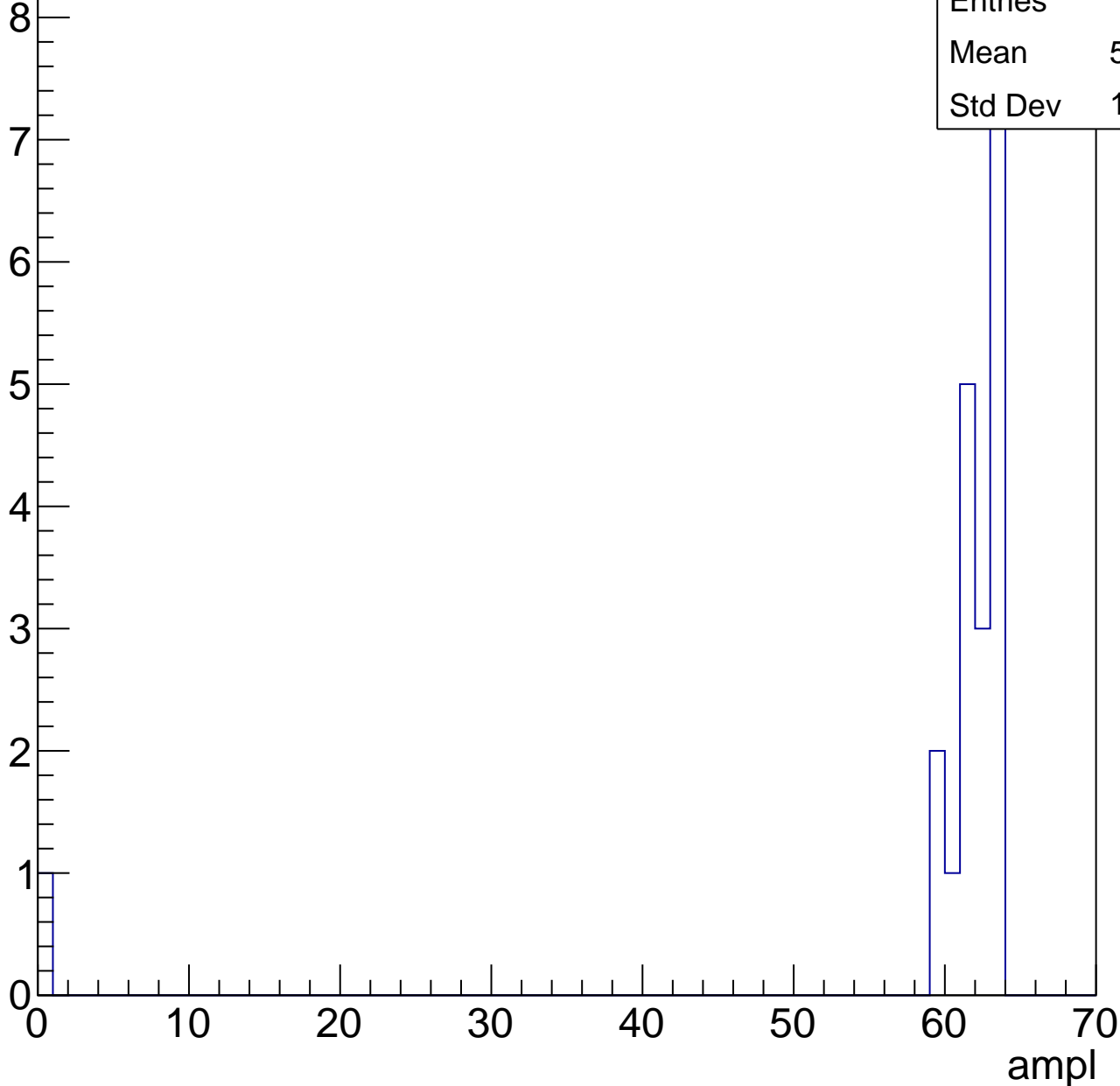


B0L001S, U19-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	20
Mean	58.65
Std Dev	13.52



B0L001S, U19-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch48, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	31.57
Std Dev	3.151

Gaus mean : 32.4292

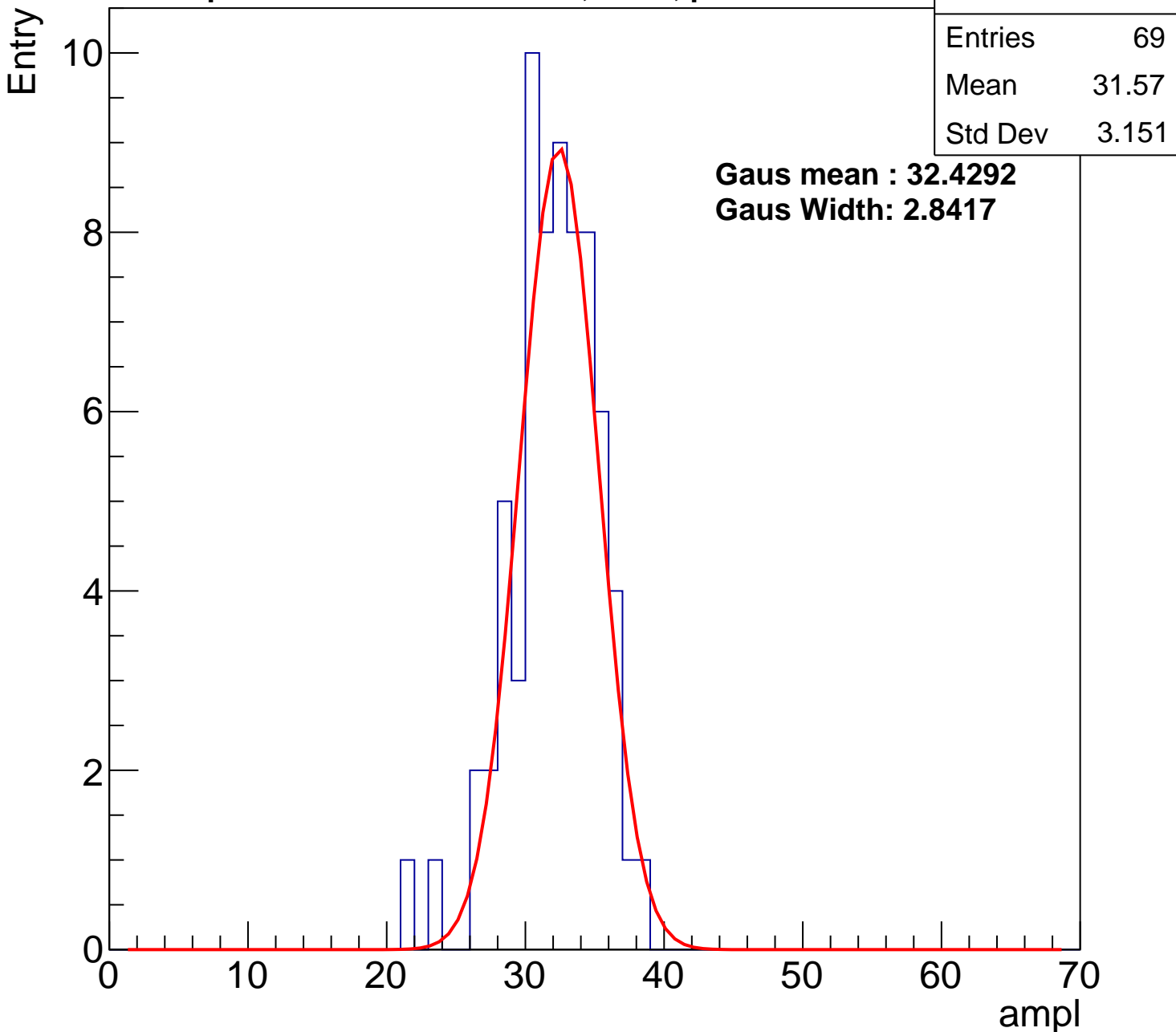
Gaus Width: 2.8417

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch48, adc1

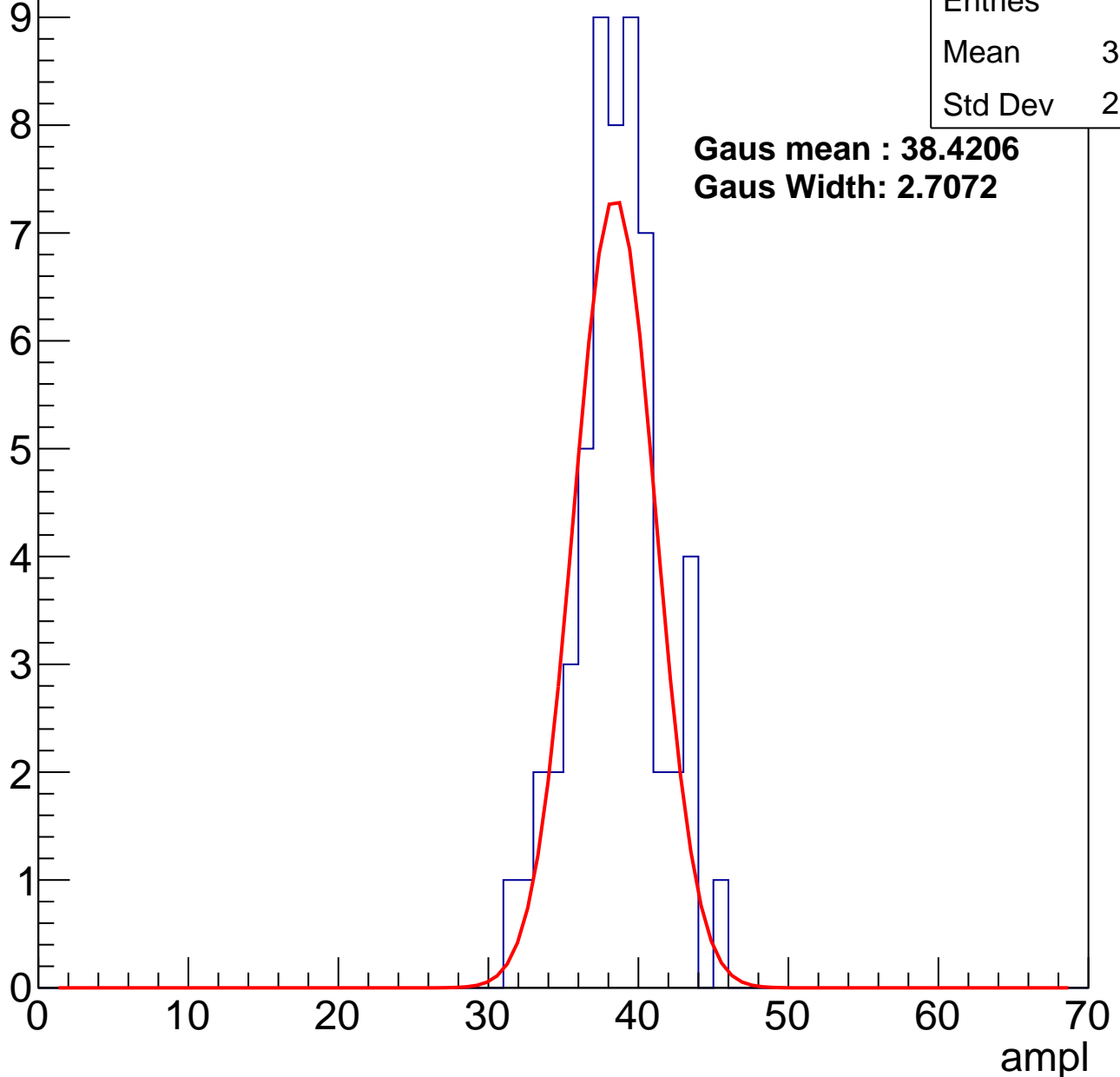
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	38.09
Std Dev	2.862

Gaus mean : 38.4206

Gaus Width: 2.7072



B0L001S, U19-ch48, adc2

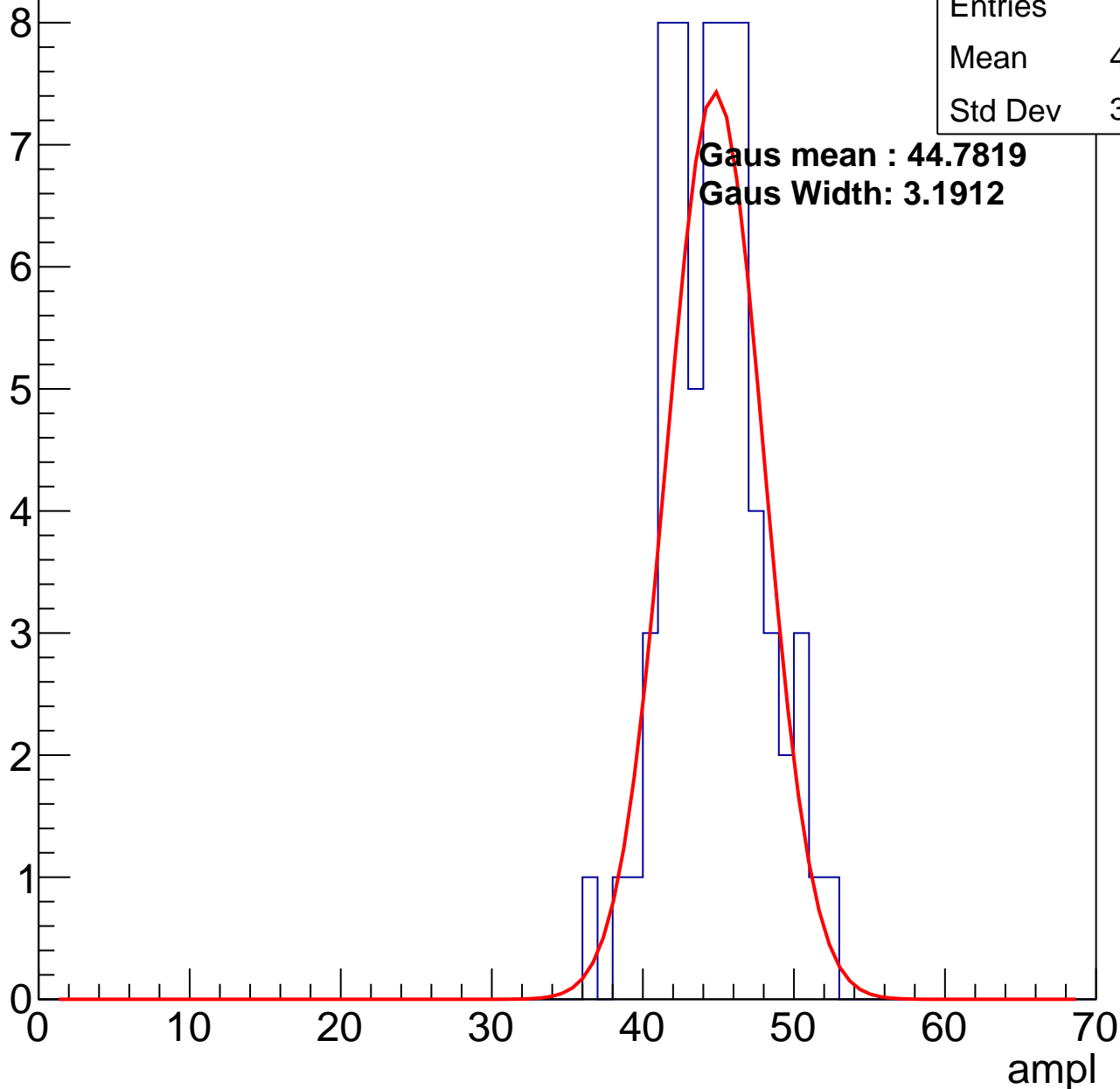
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.23
Std Dev	3.219

Gaus mean : 44.7819

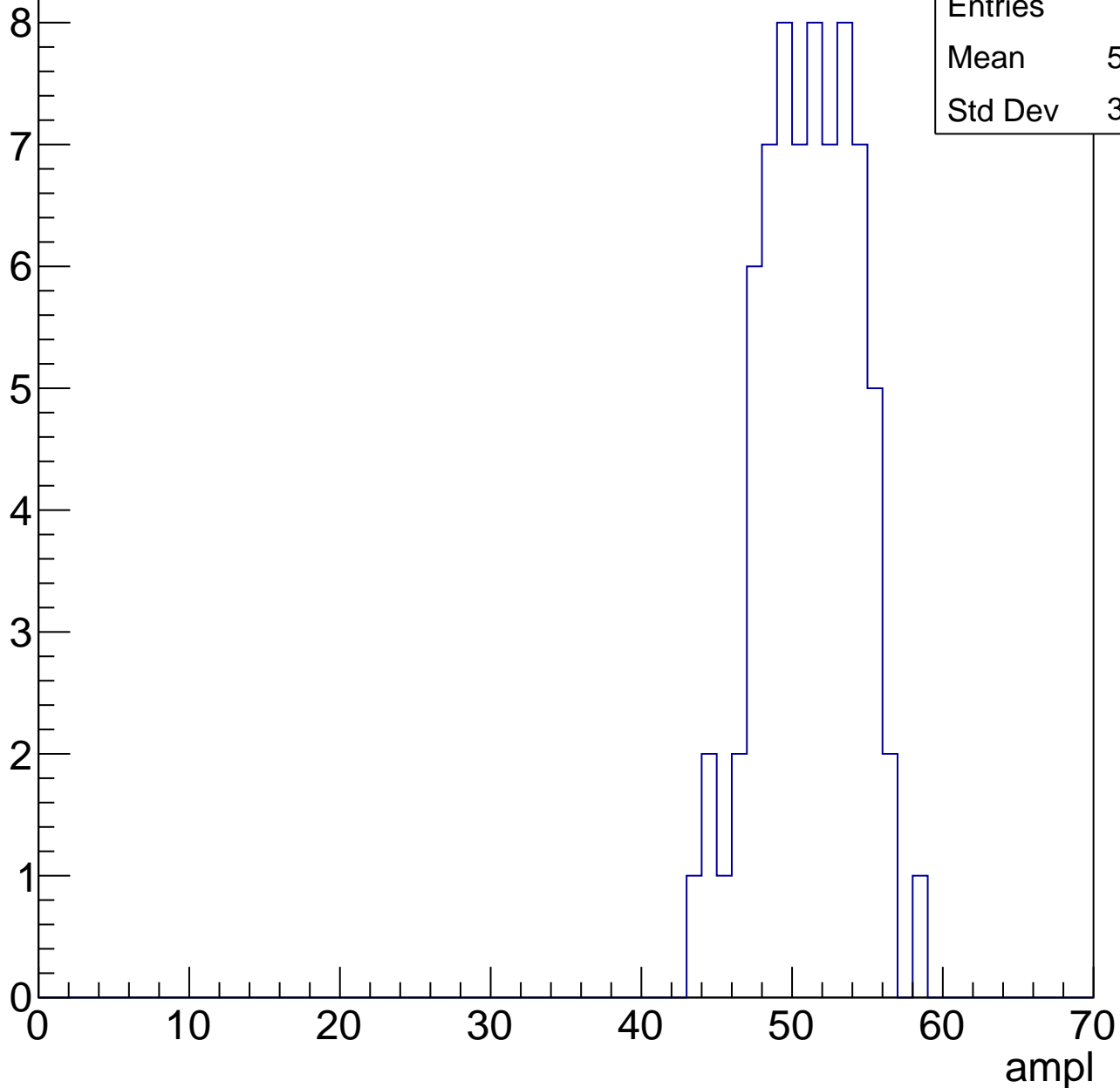
Gaus Width: 3.1912



B0L001S, U19-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

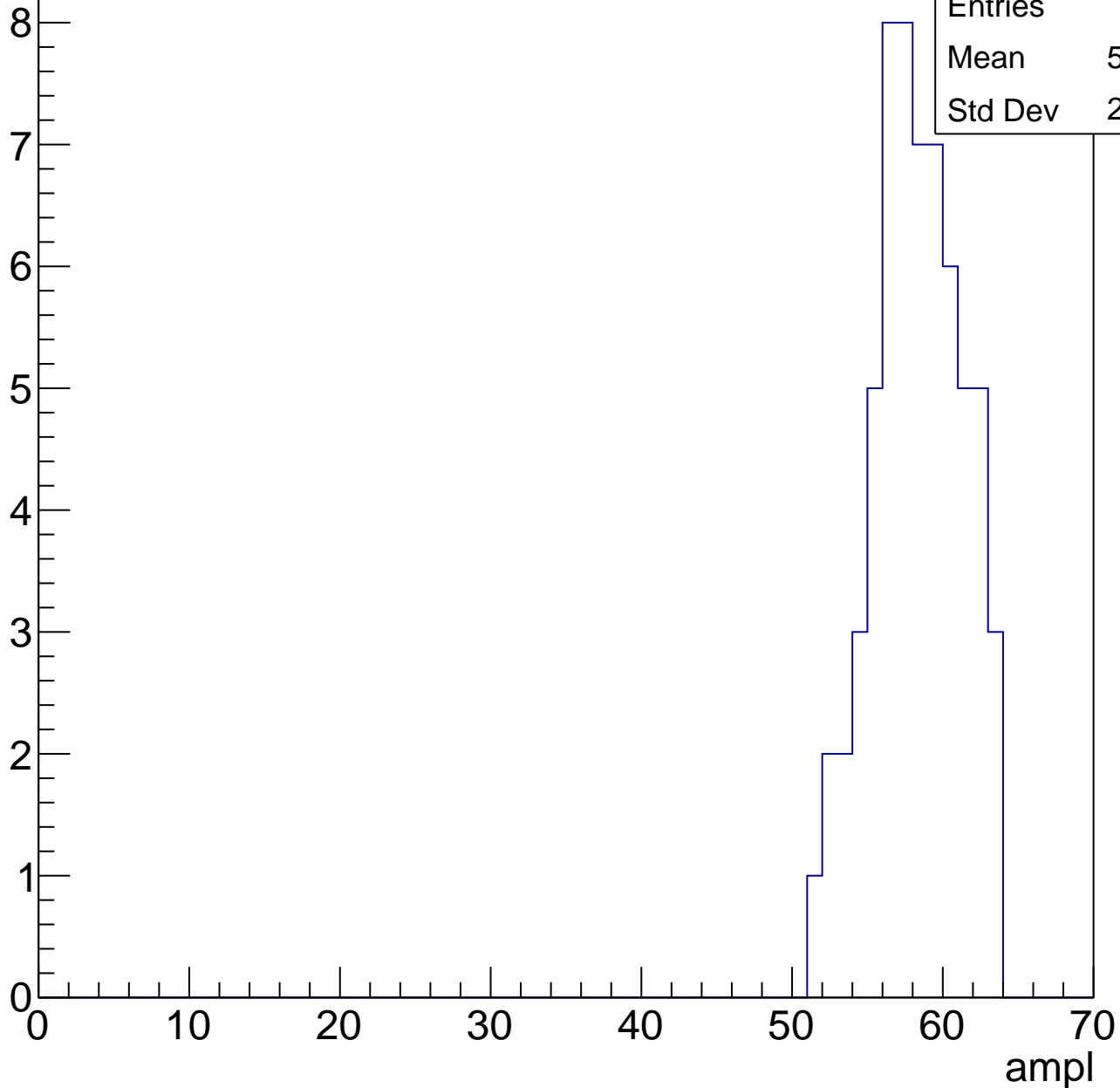


Entries	72
Mean	50.65
Std Dev	3.159

B0L001S, U19-ch48, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

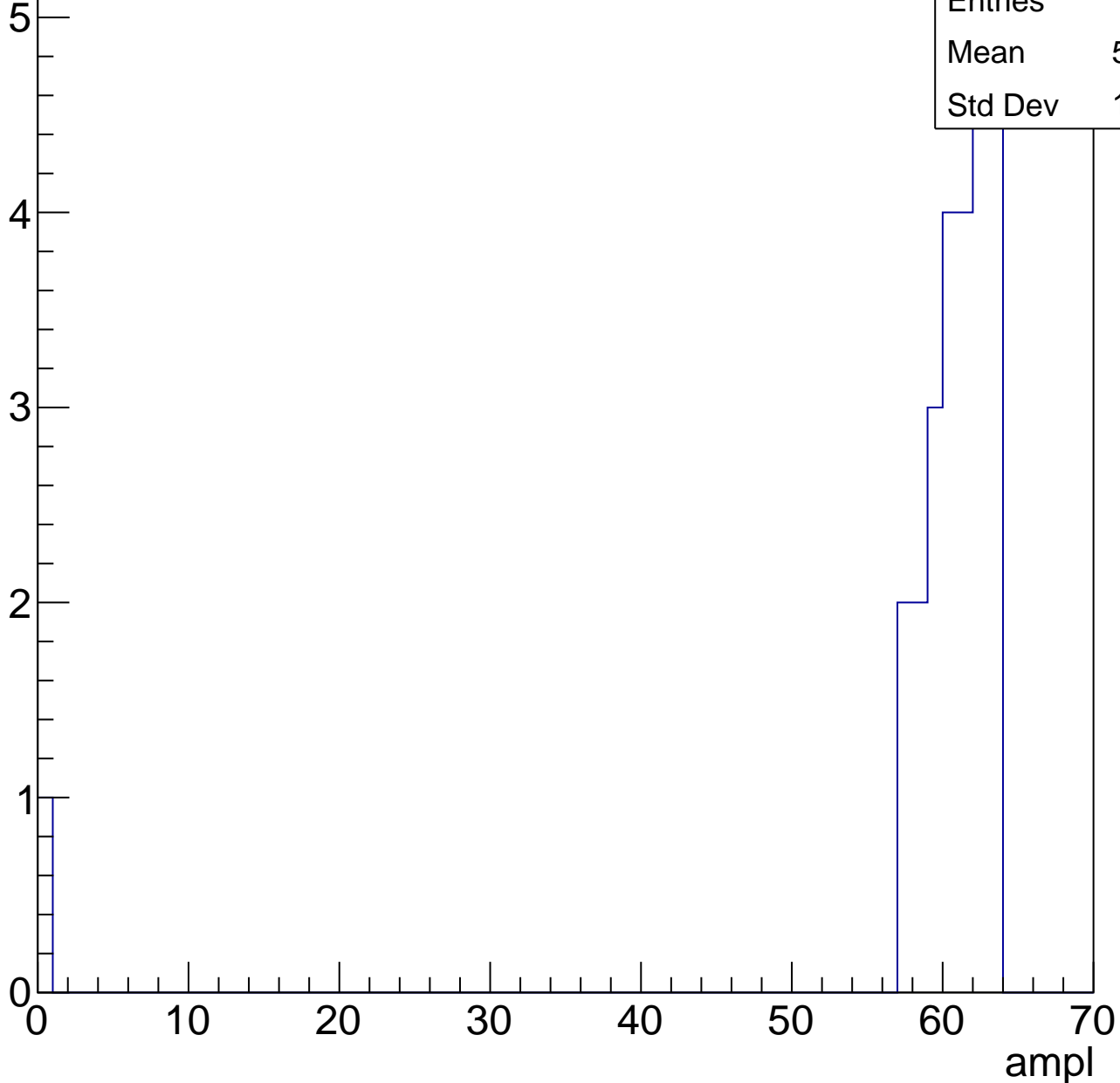


B0L001S, U19-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.31
Std Dev	11.81



B0L001S, U19-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch49, adc0

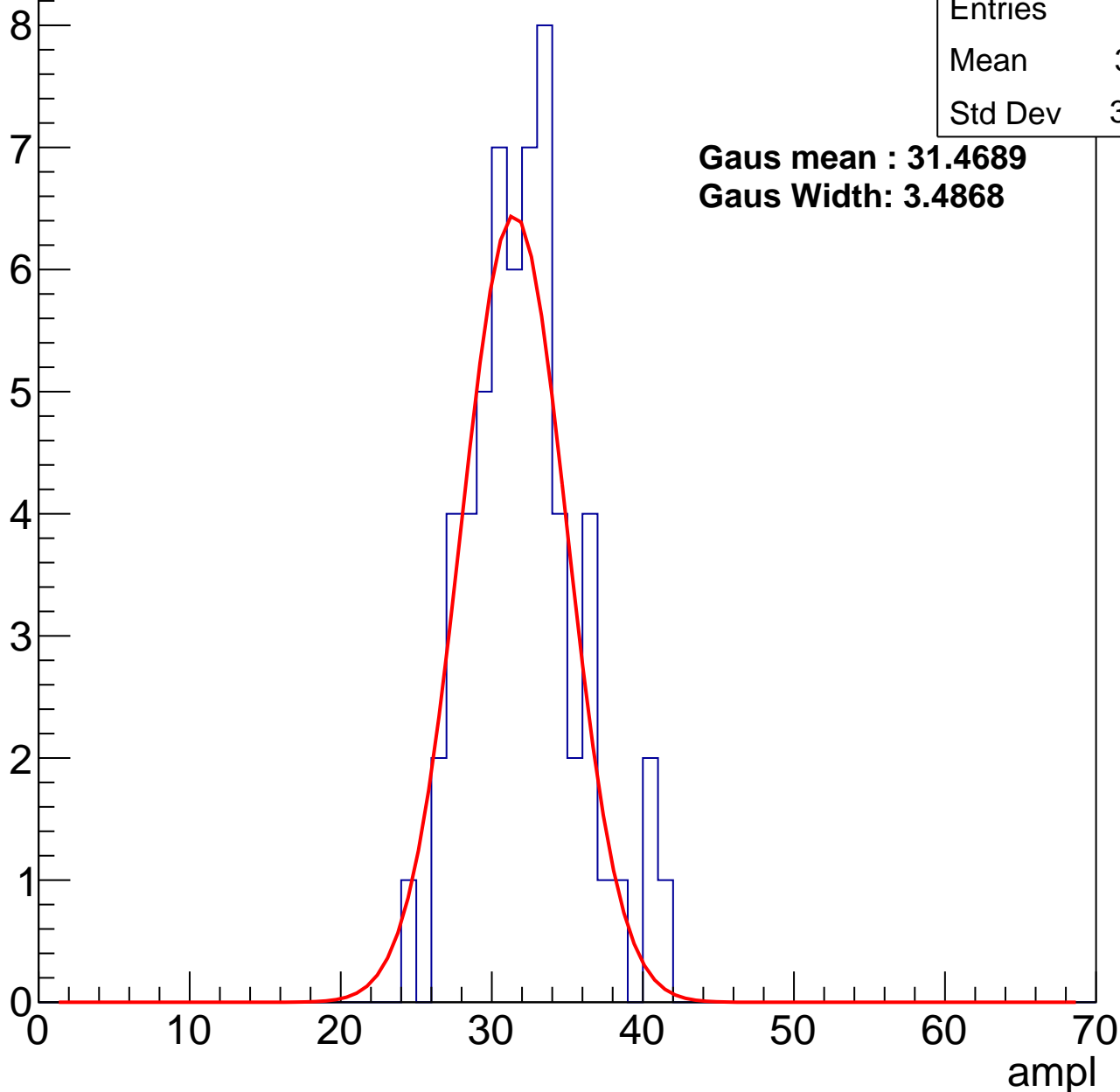
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.71
Std Dev	3.566

Gaus mean : 31.4689

Gaus Width: 3.4868



B0L001S, U19-ch49, adc1

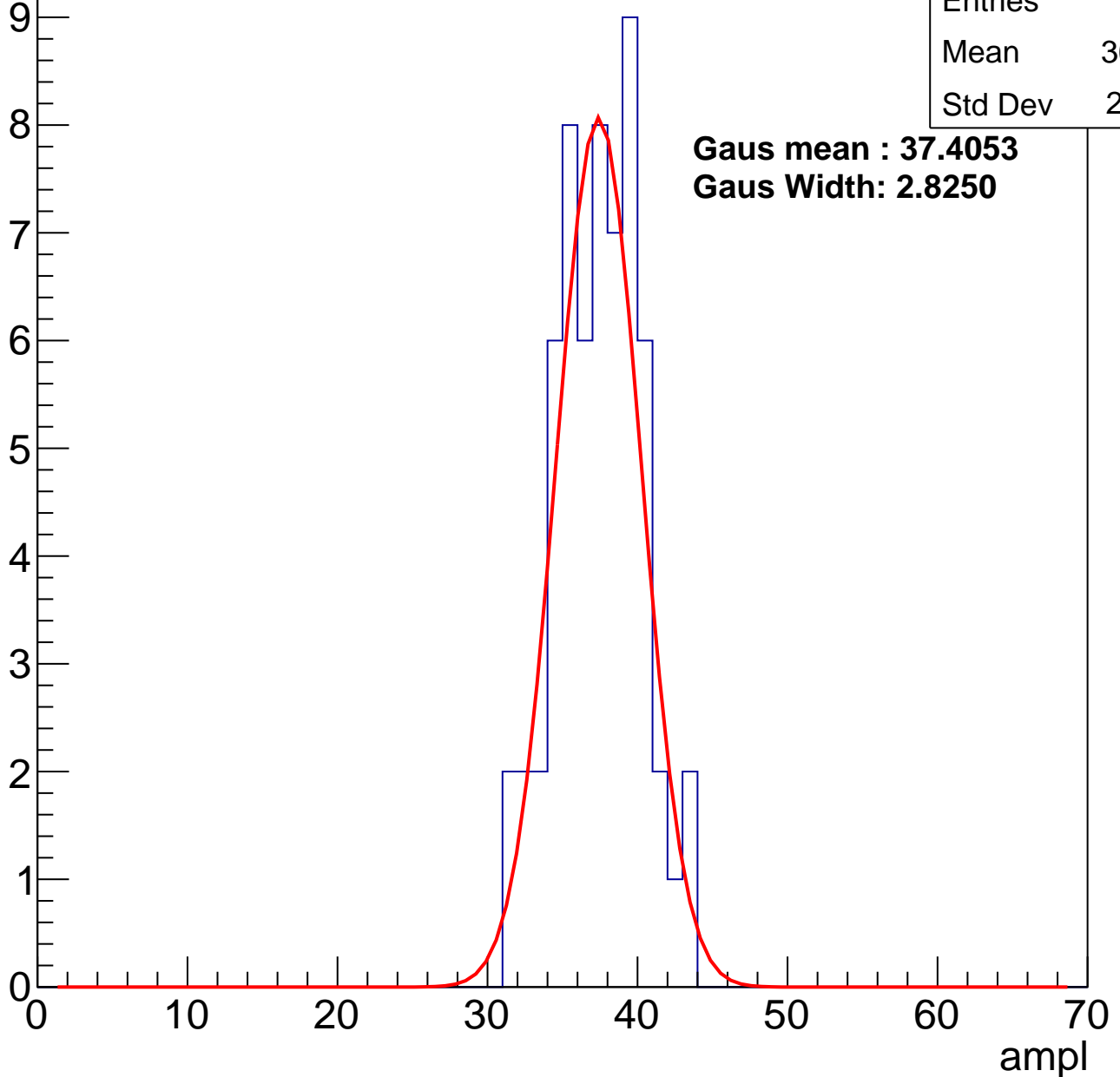
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	36.97
Std Dev	2.781

Gaus mean : 37.4053

Gaus Width: 2.8250



B0L001S, U19-ch49, adc2

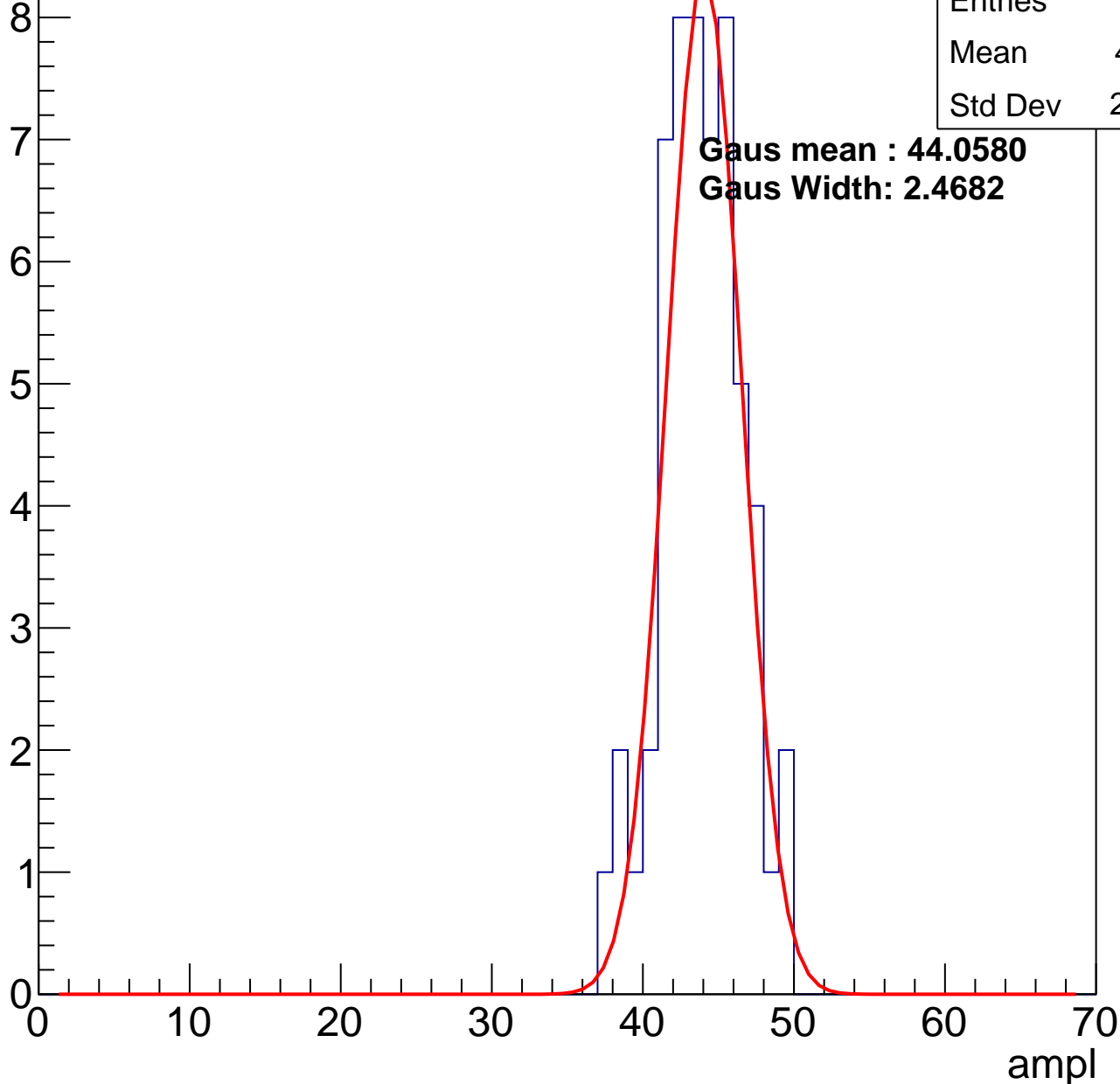
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.41
Std Dev	2.644

Gaus mean : 44.0580

Gaus Width: 2.4682



B0L001S, U19-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

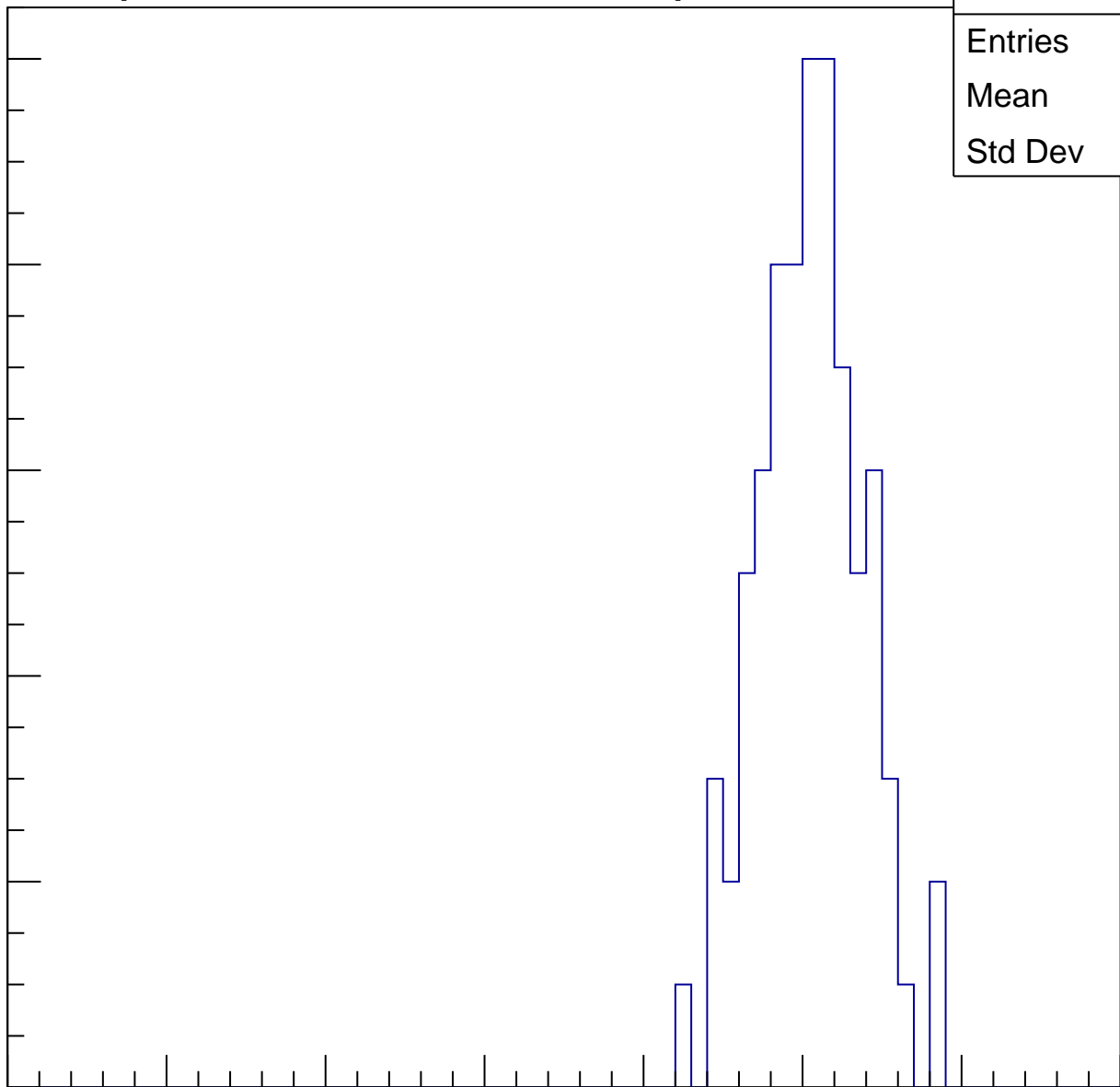
Entries	77
Mean	50.03
Std Dev	3.251

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

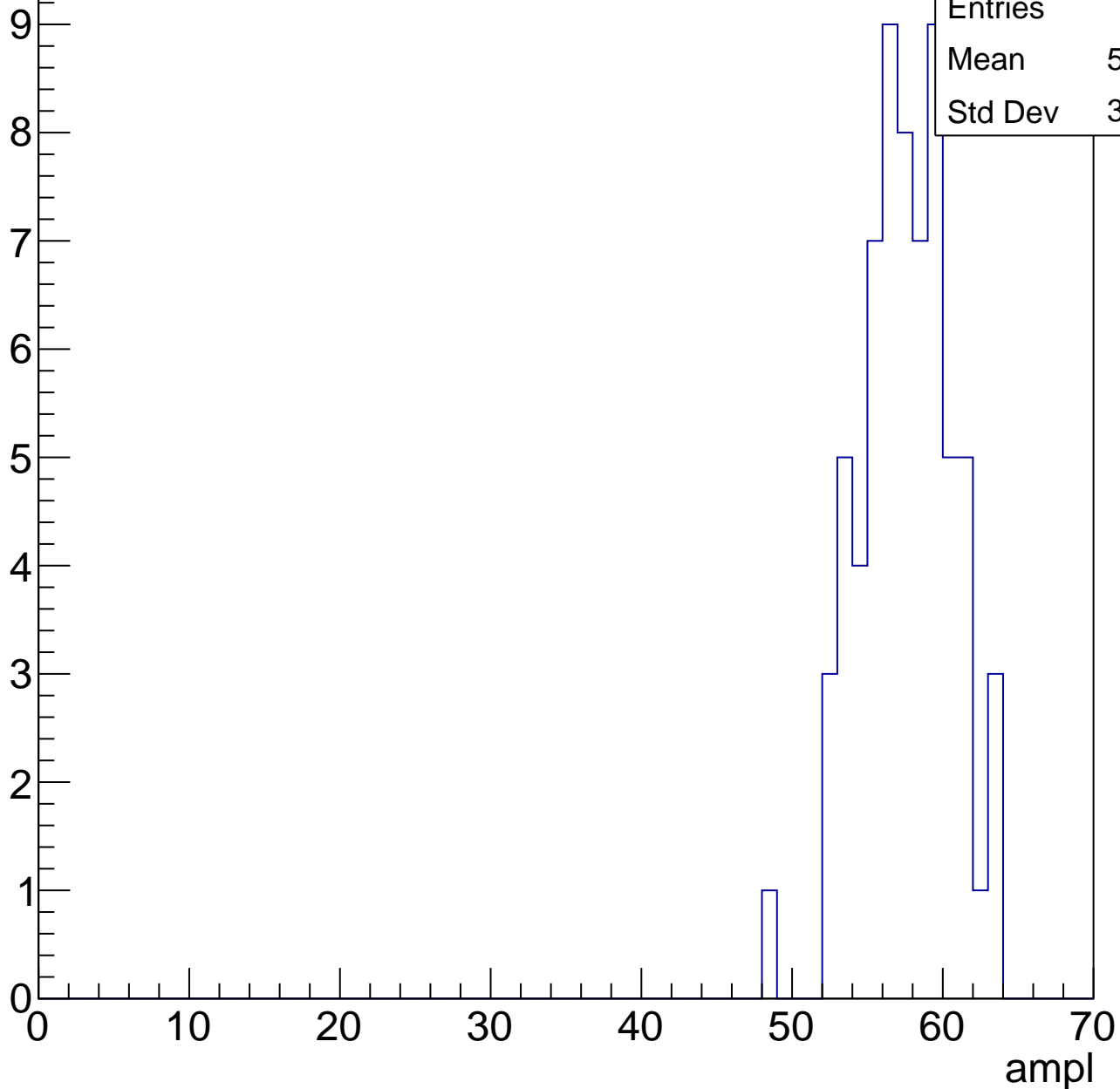
ampl



B0L001S, U19-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

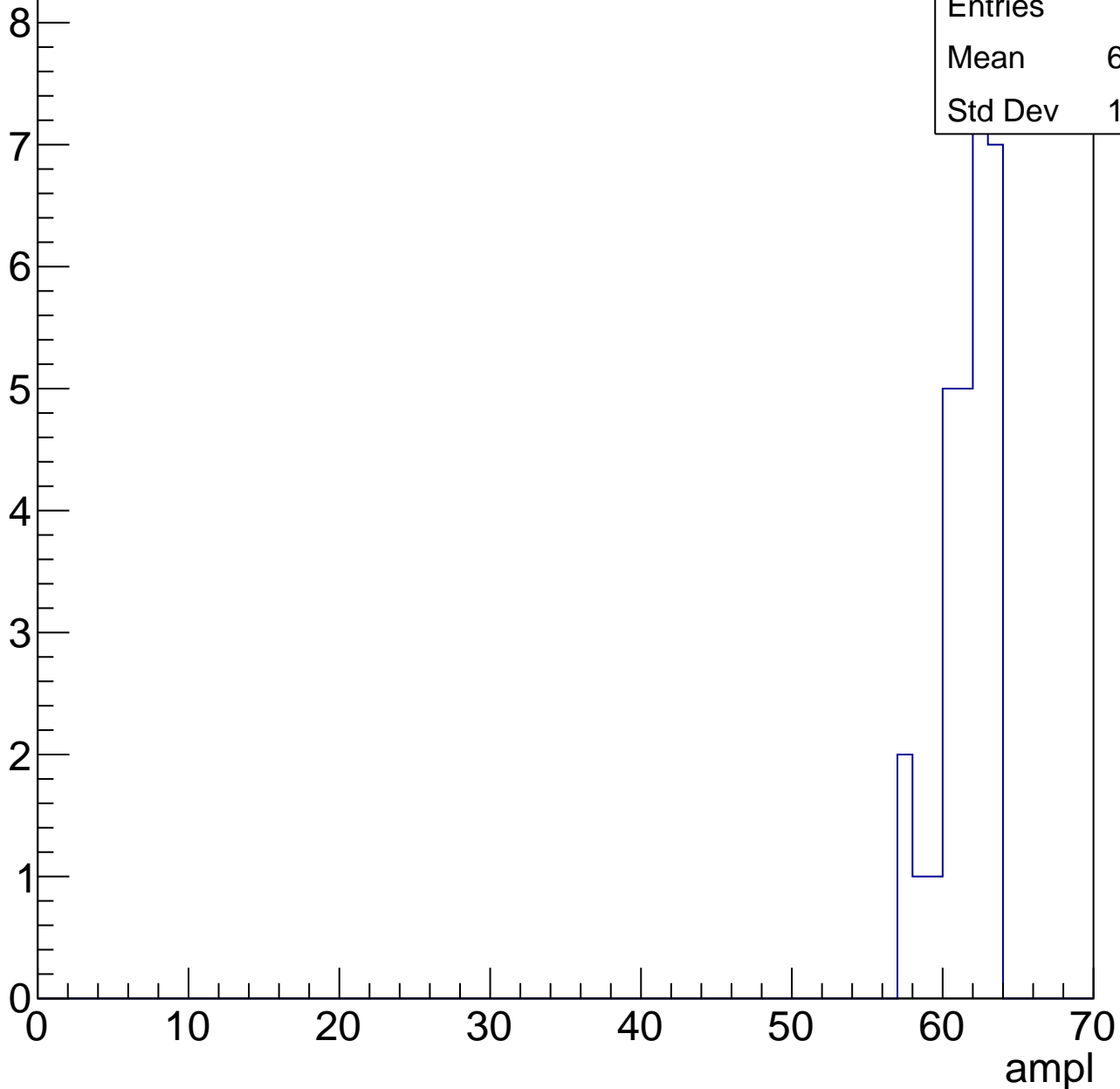


B0L001S, U19-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	61.14
Std Dev	1.717



B0L001S, U19-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch50, adc0

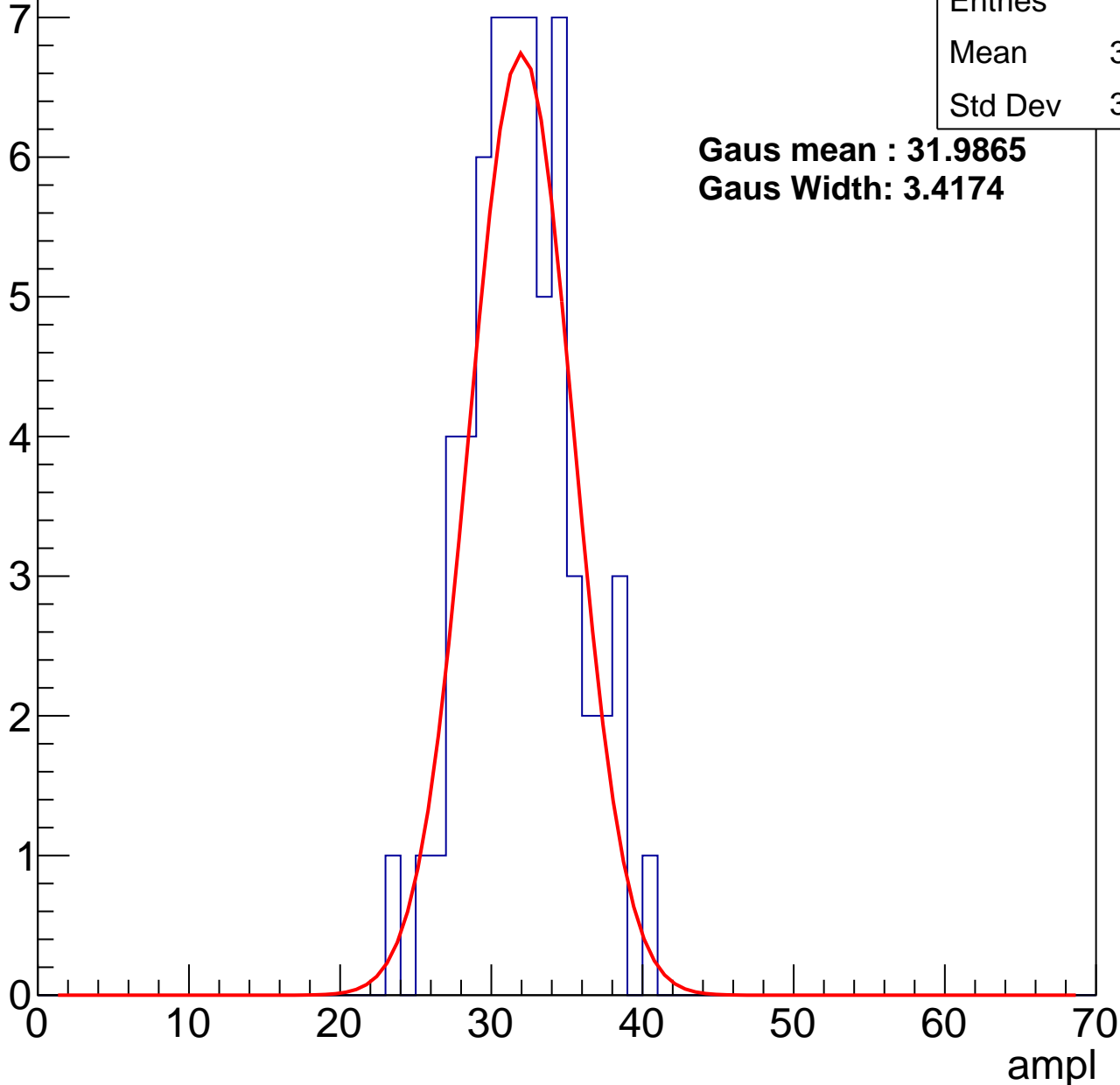
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	31.59
Std Dev	3.442

Gaus mean : 31.9865

Gaus Width: 3.4174



B0L001S, U19-ch50, adc1

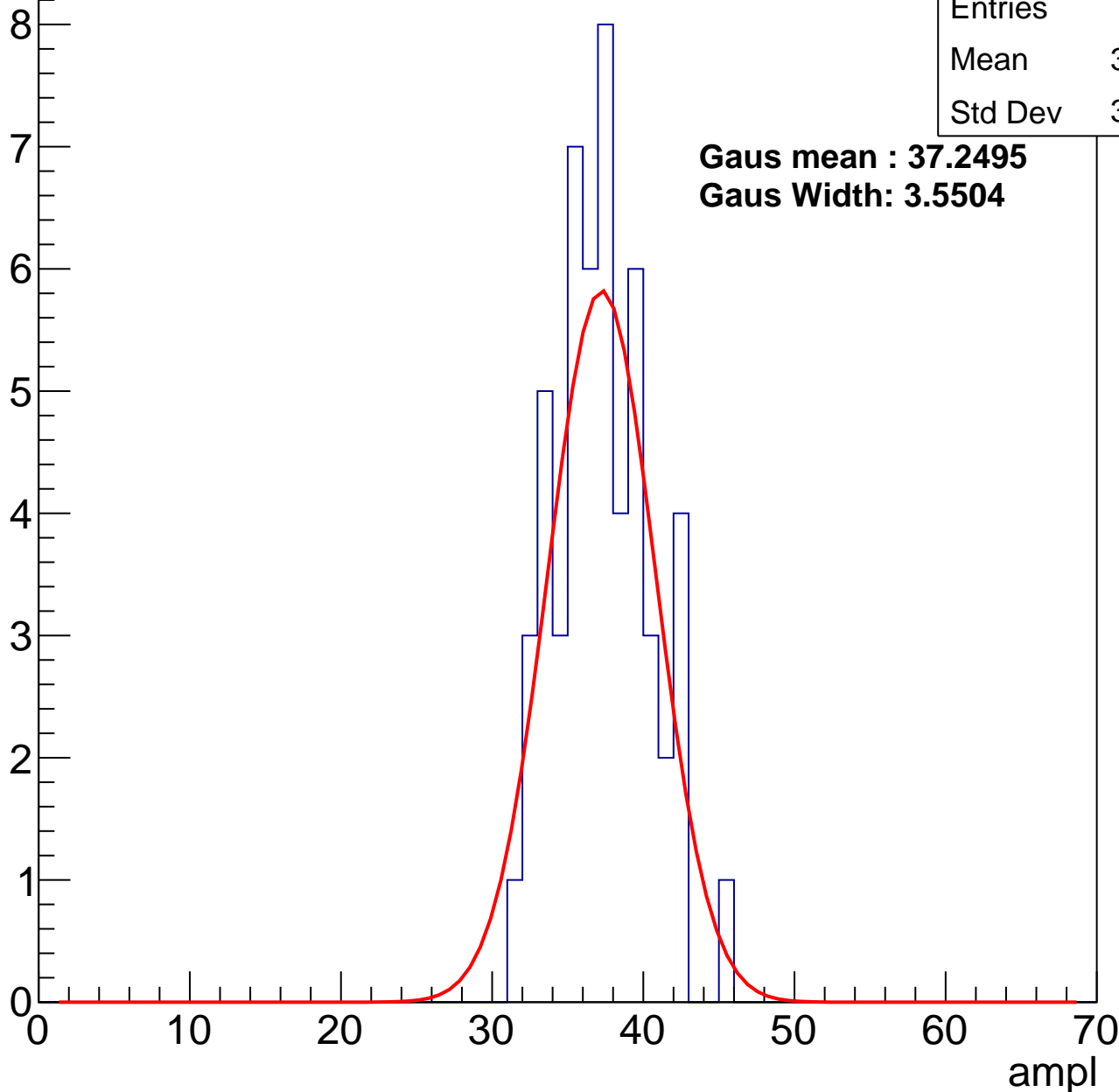
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	36.83
Std Dev	3.076

Gaus mean : 37.2495

Gaus Width: 3.5504



B0L001S, U19-ch50, adc2

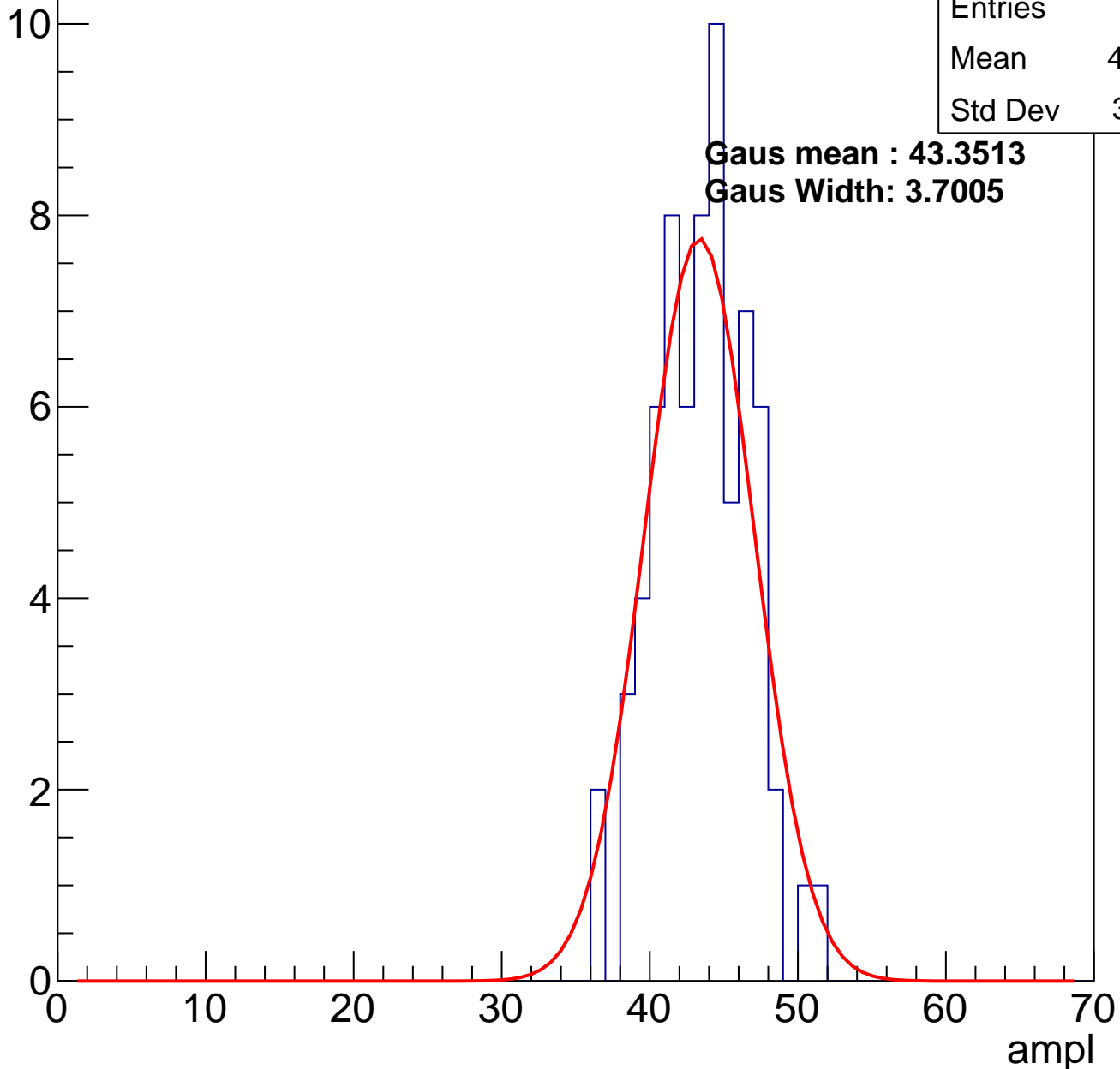
calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	43.07
Std Dev	3.141

Gaus mean : 43.3513

Gaus Width: 3.7005

Entry



B0L001S, U19-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

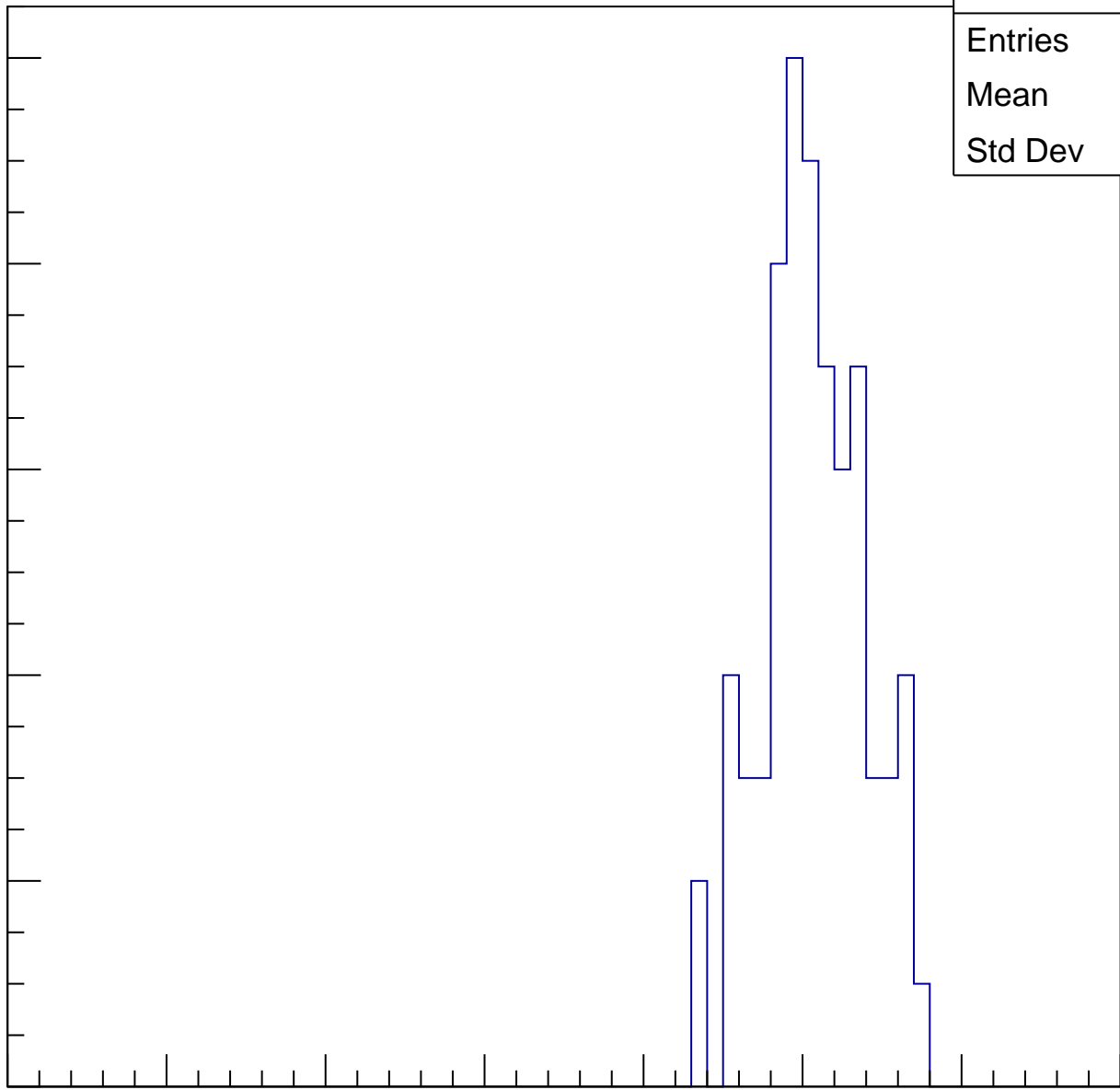
Entries	70
Mean	50.24
Std Dev	3.209

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

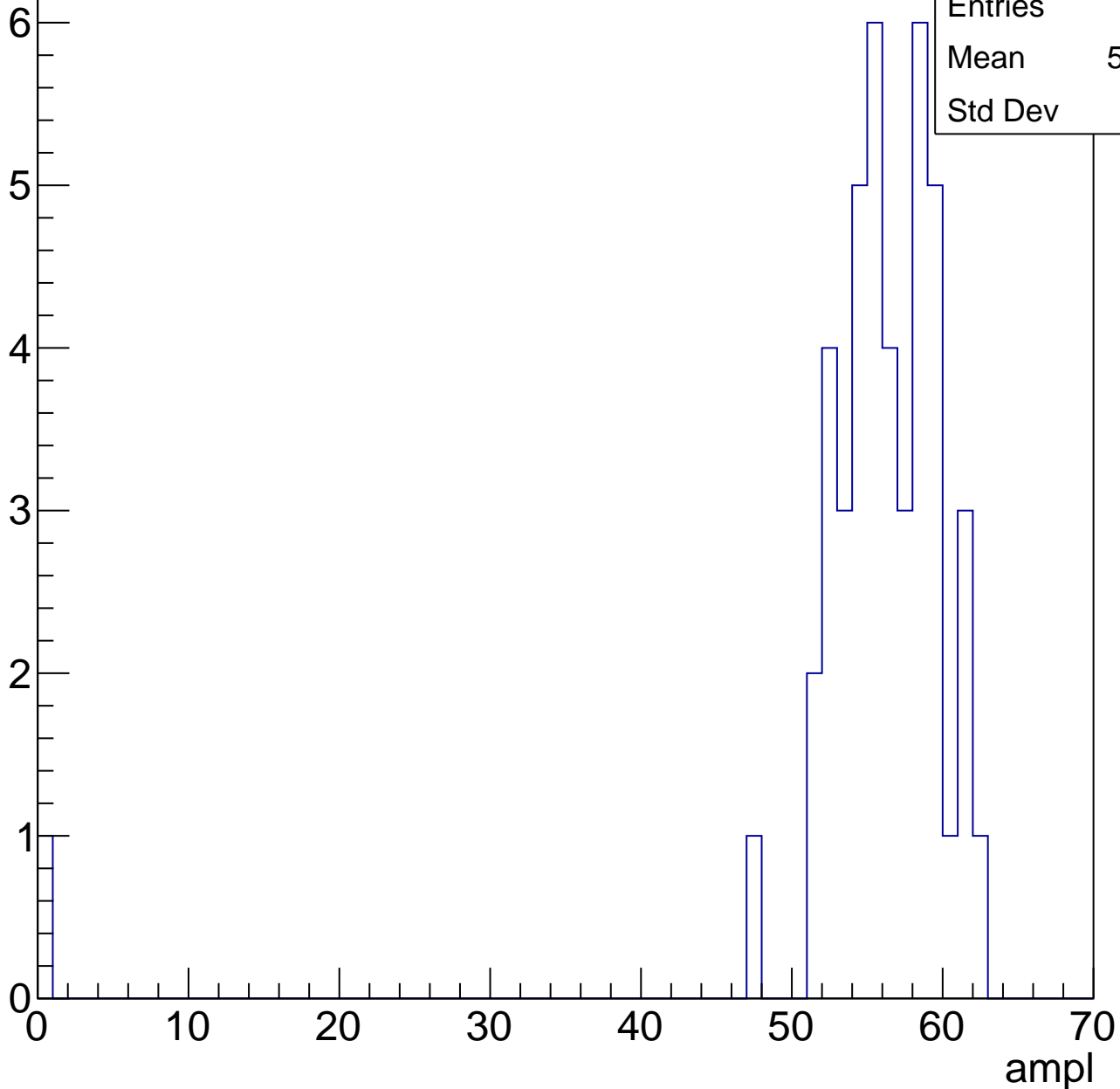


B0L001S, U19-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

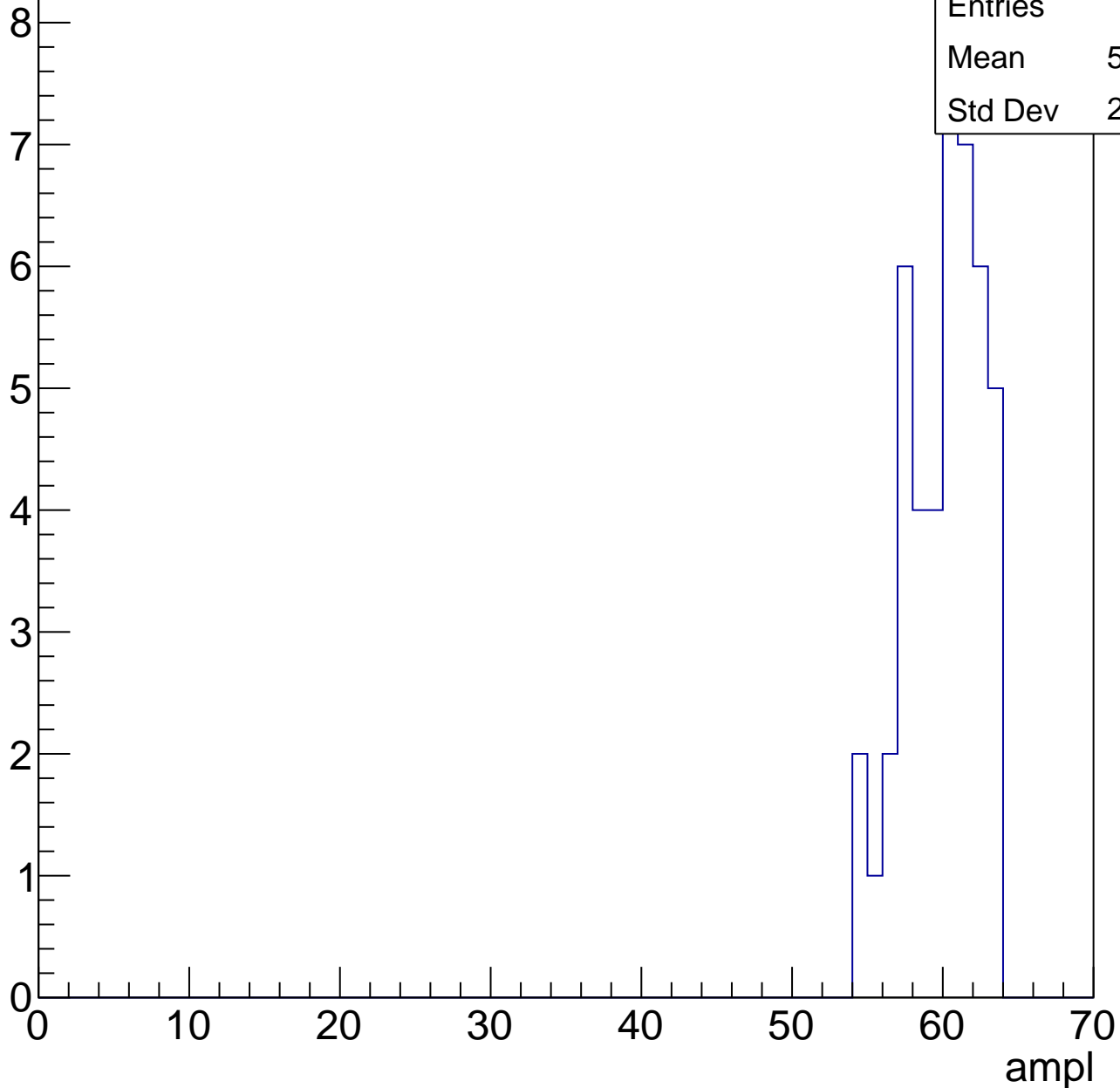
Entries	45
Mean	54.64
Std Dev	8.82



B0L001S, U19-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

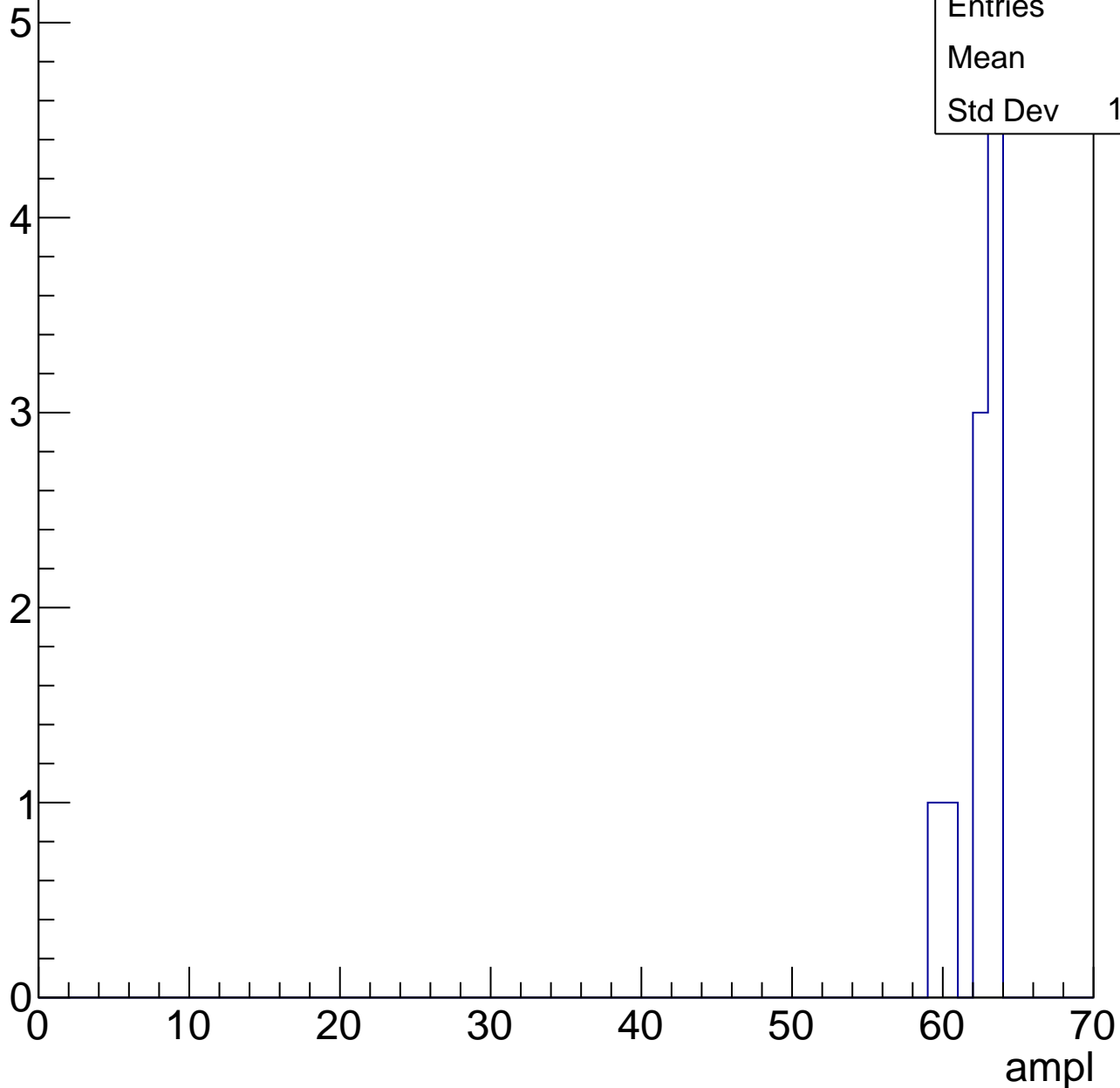


B0L001S, U19-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	10
Mean	62
Std Dev	1.342



B0L001S, U19-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch51, adc0

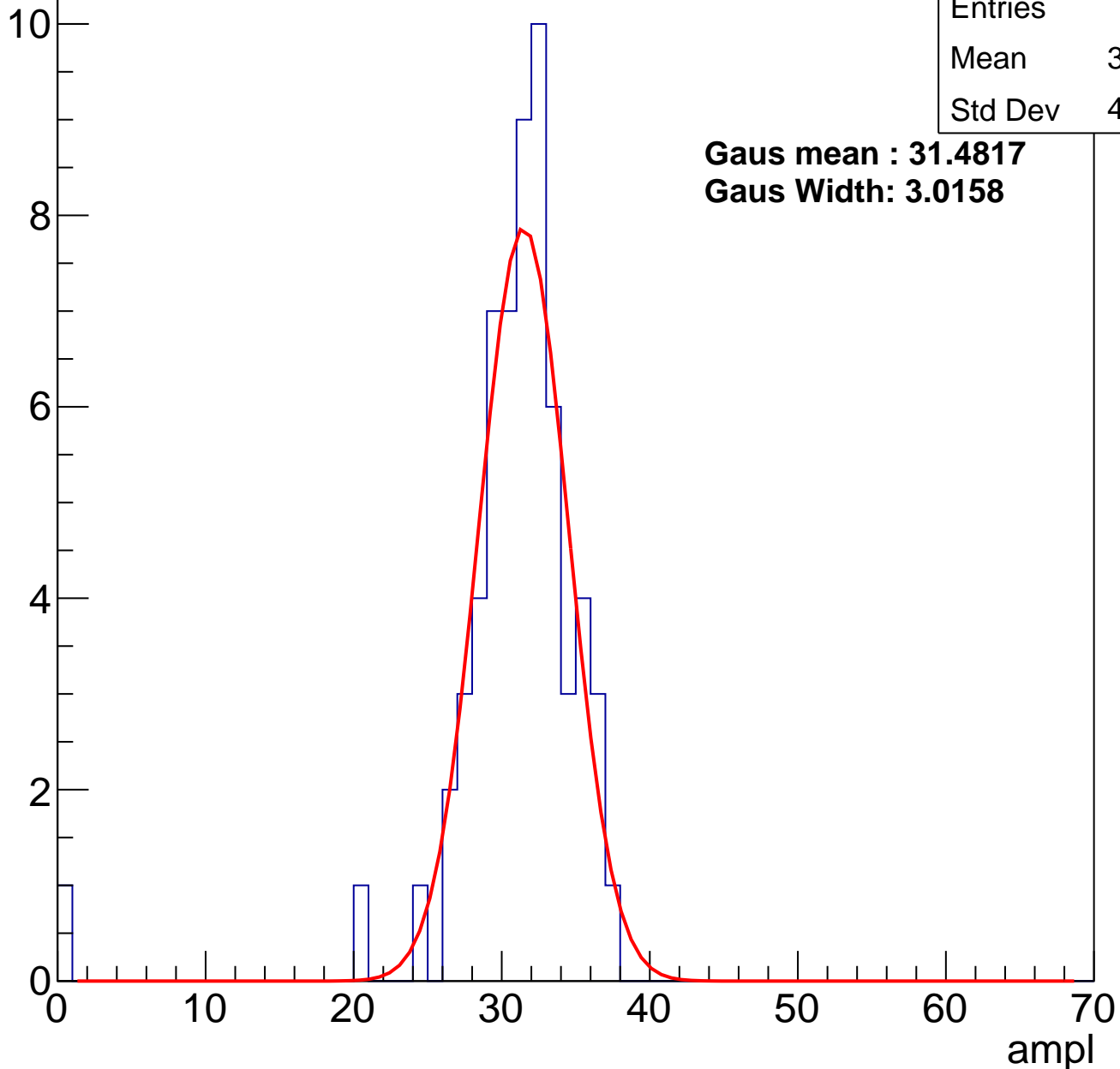
calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	30.42
Std Dev	4.943

Gaus mean : 31.4817

Gaus Width: 3.0158

Entry



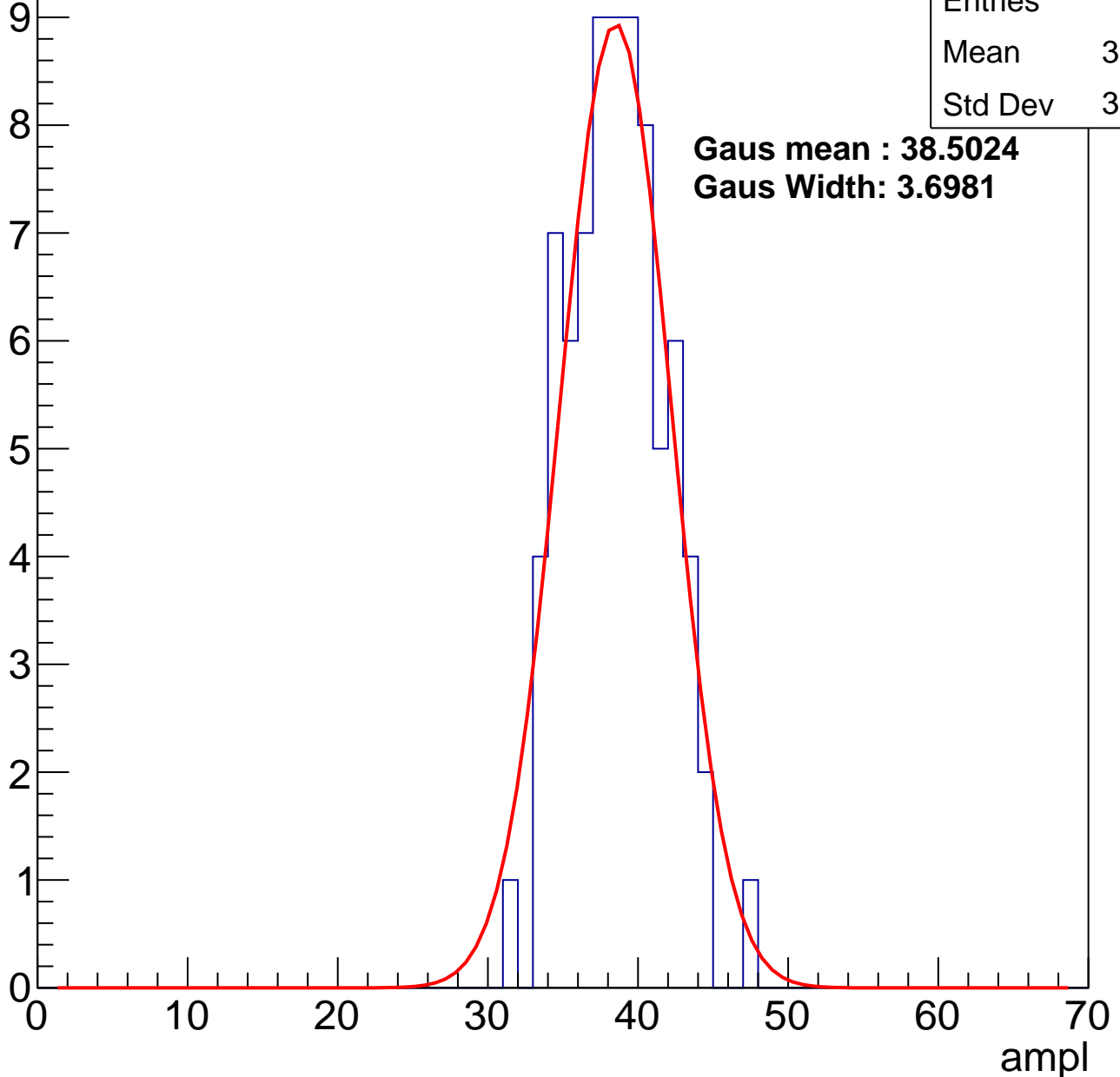
B0L001S, U19-ch51, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

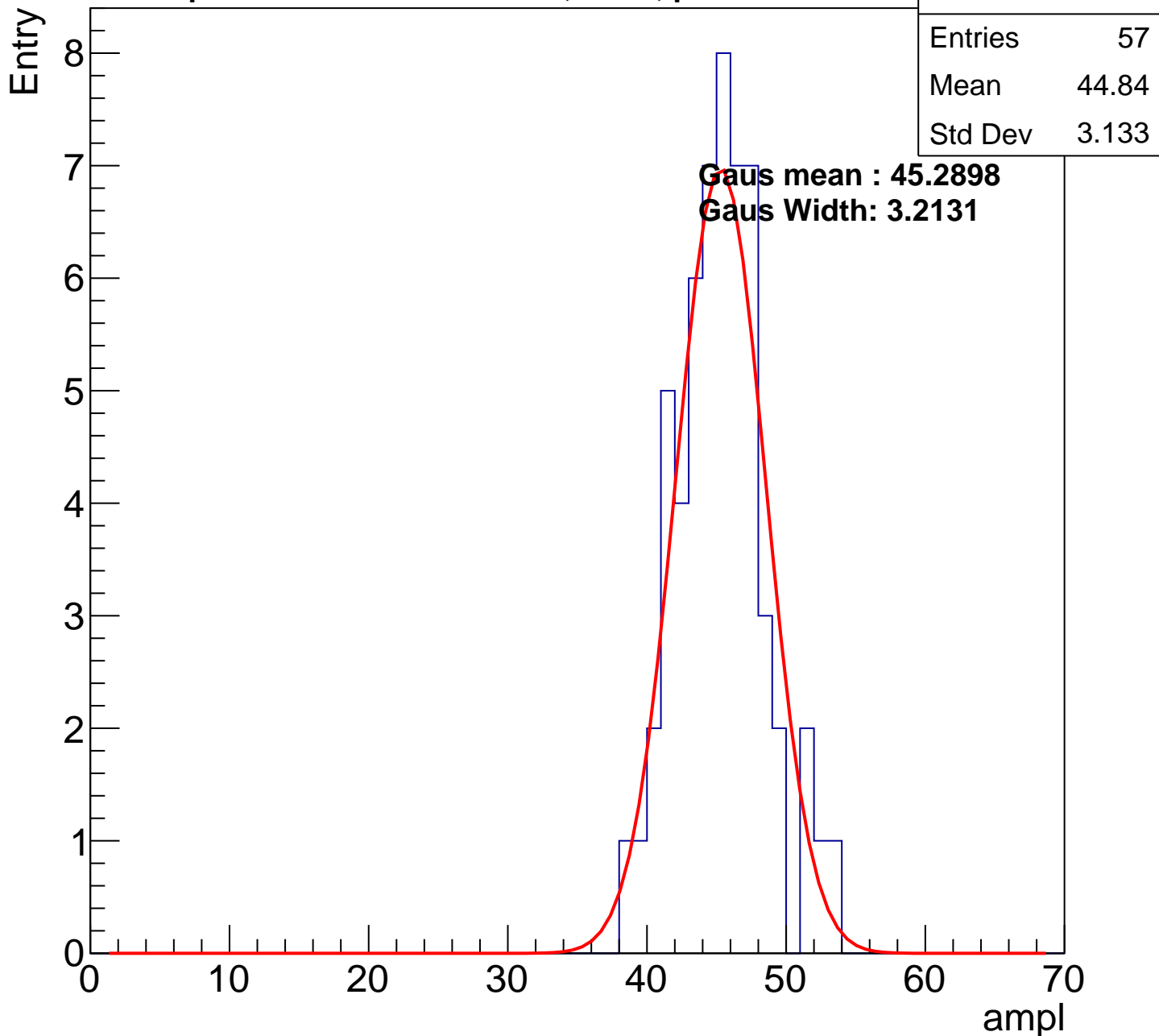
Entries	78
Mean	38.12
Std Dev	3.174

Gaus mean : 38.5024
Gaus Width: 3.6981



B0L001S, U19-ch51, adc2

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U19-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1

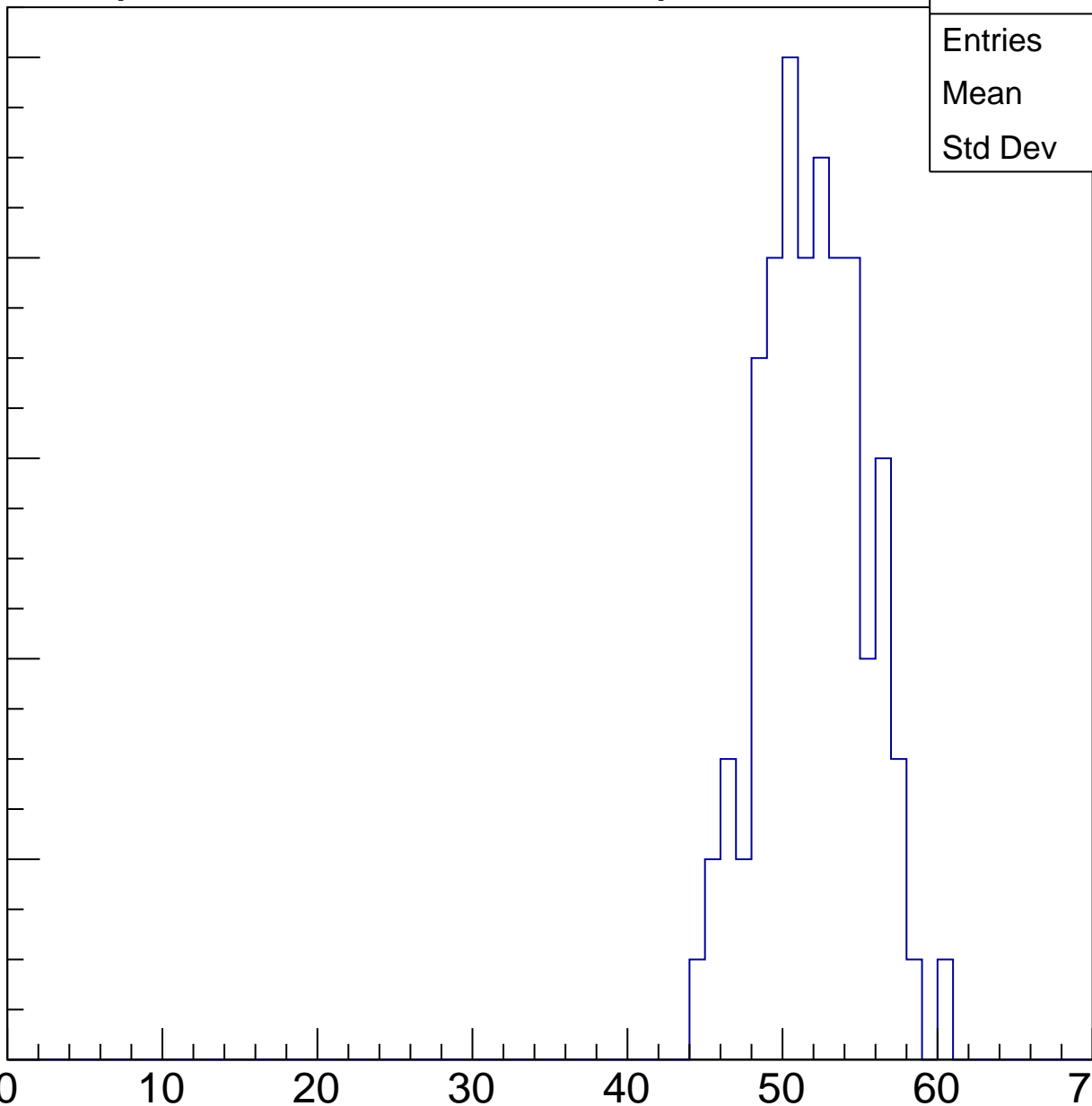
Entries	81
Mean	51.49
Std Dev	3.293

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

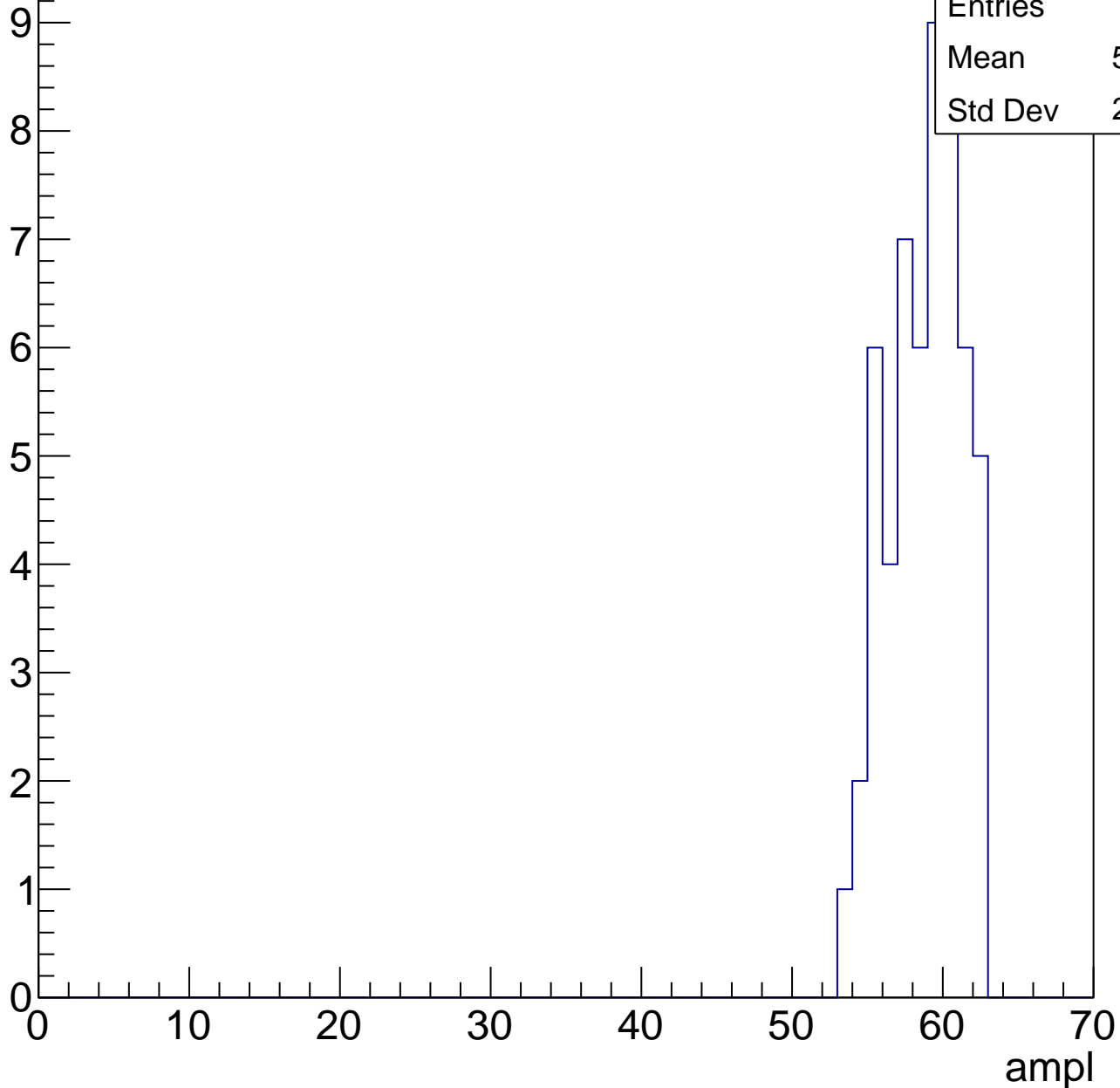
ampl



B0L001S, U19-ch51, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

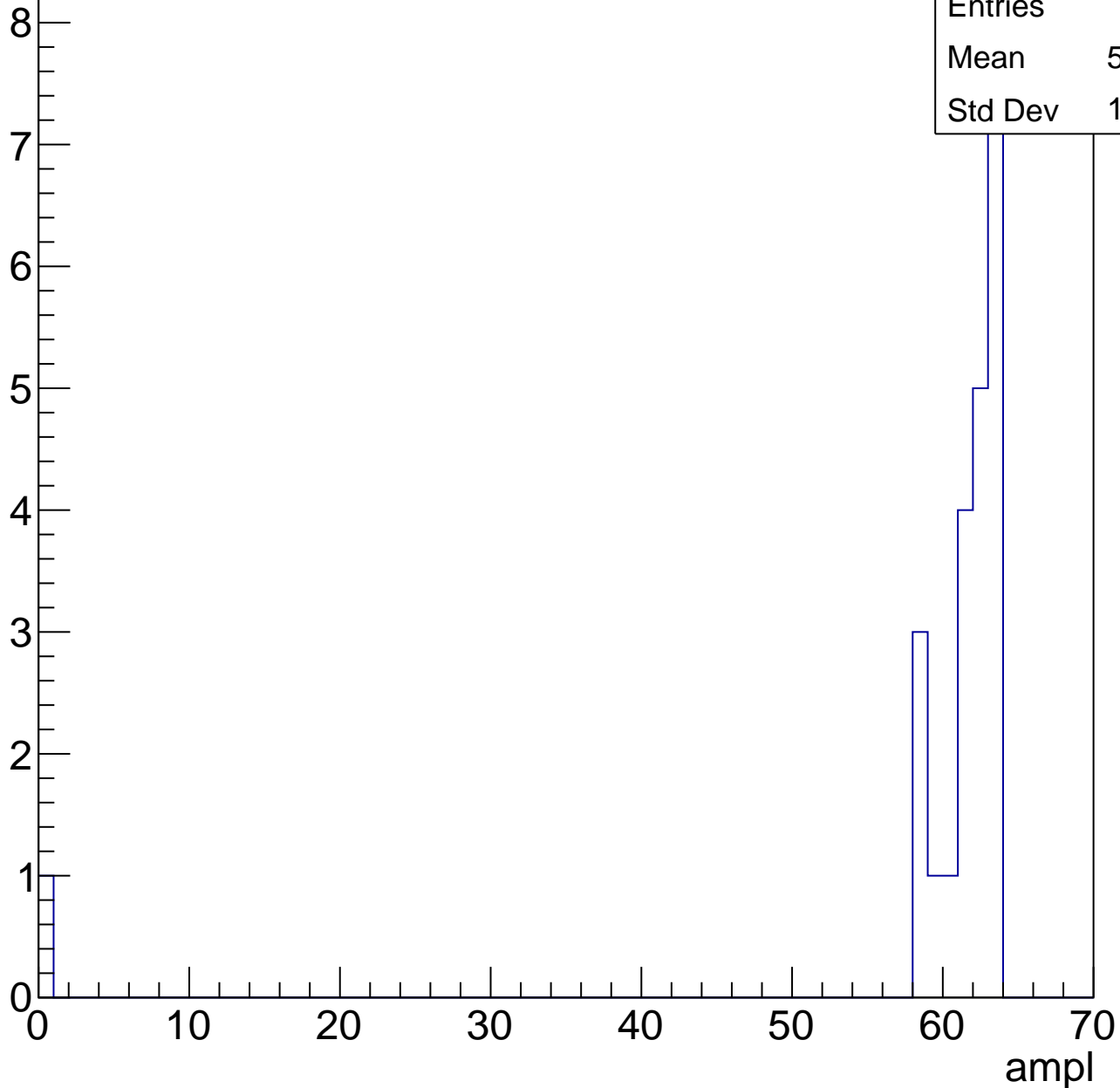


B0L001S, U19-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.74
Std Dev	12.64



B0L001S, U19-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch52, adc0

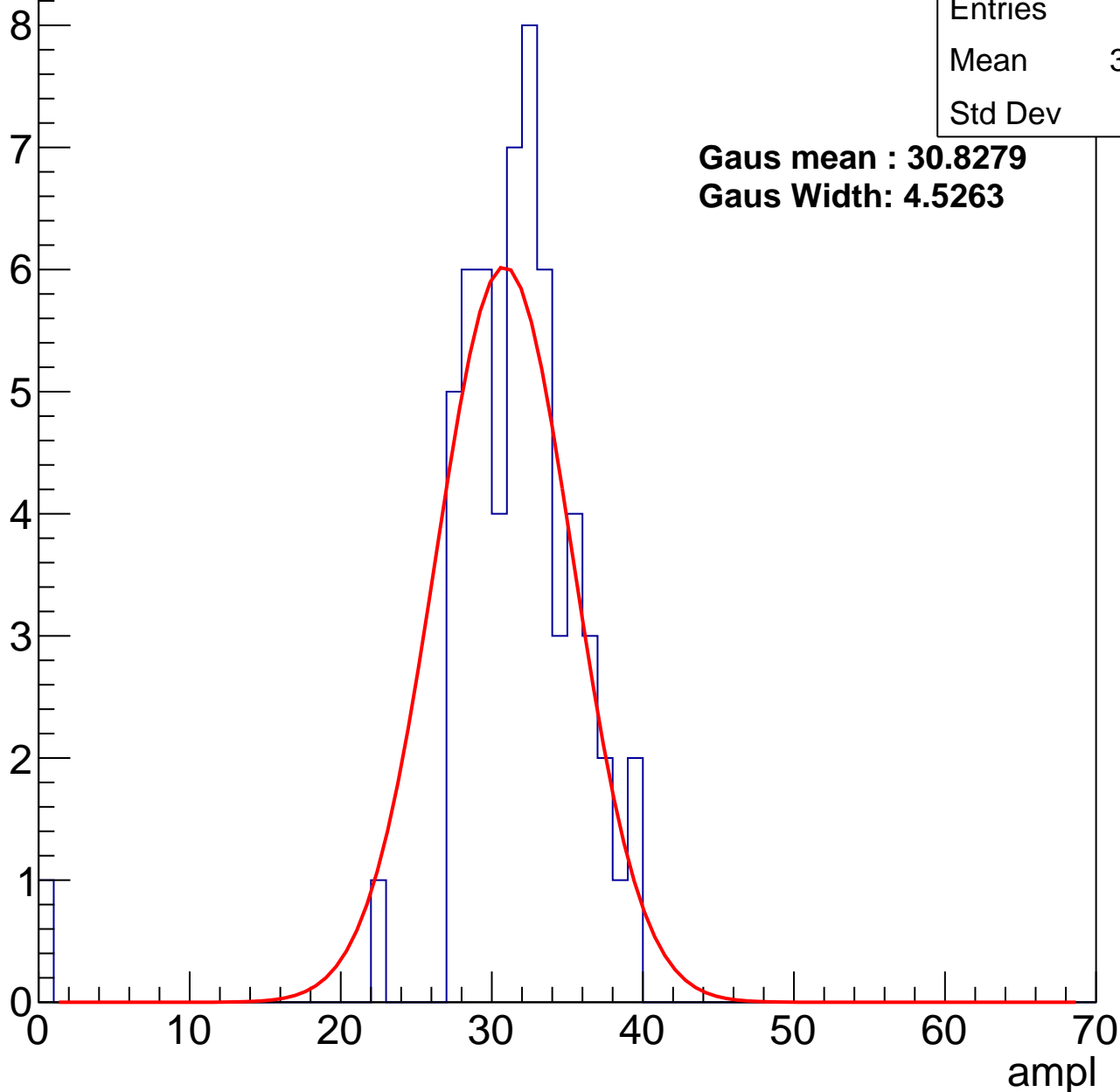
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.02
Std Dev	5.29

Gaus mean : 30.8279

Gaus Width: 4.5263



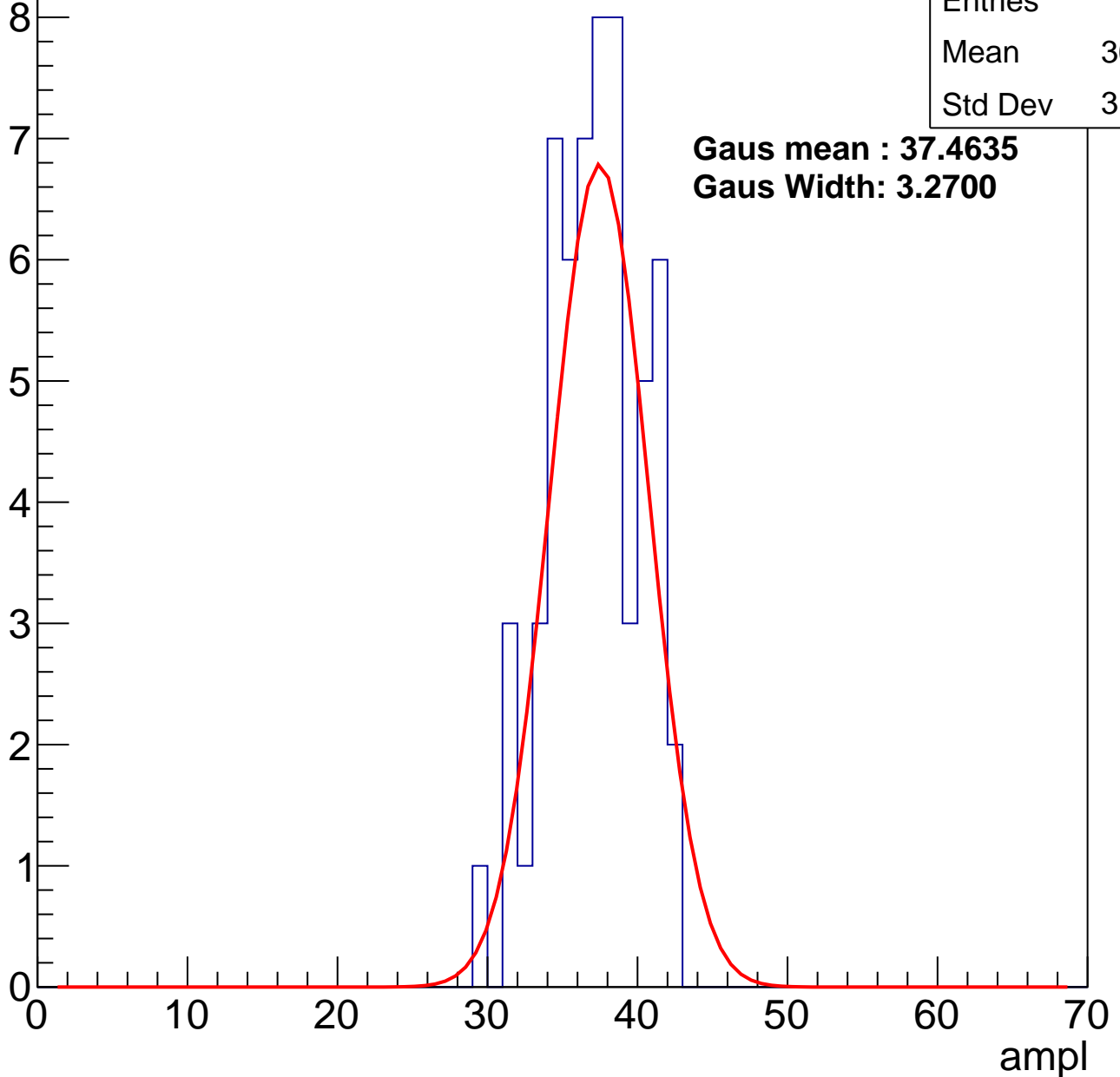
B0L001S, U19-ch52, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	36.67
Std Dev	3.009

Gaus mean : 37.4635
Gaus Width: 3.2700



B0L001S, U19-ch52, adc2

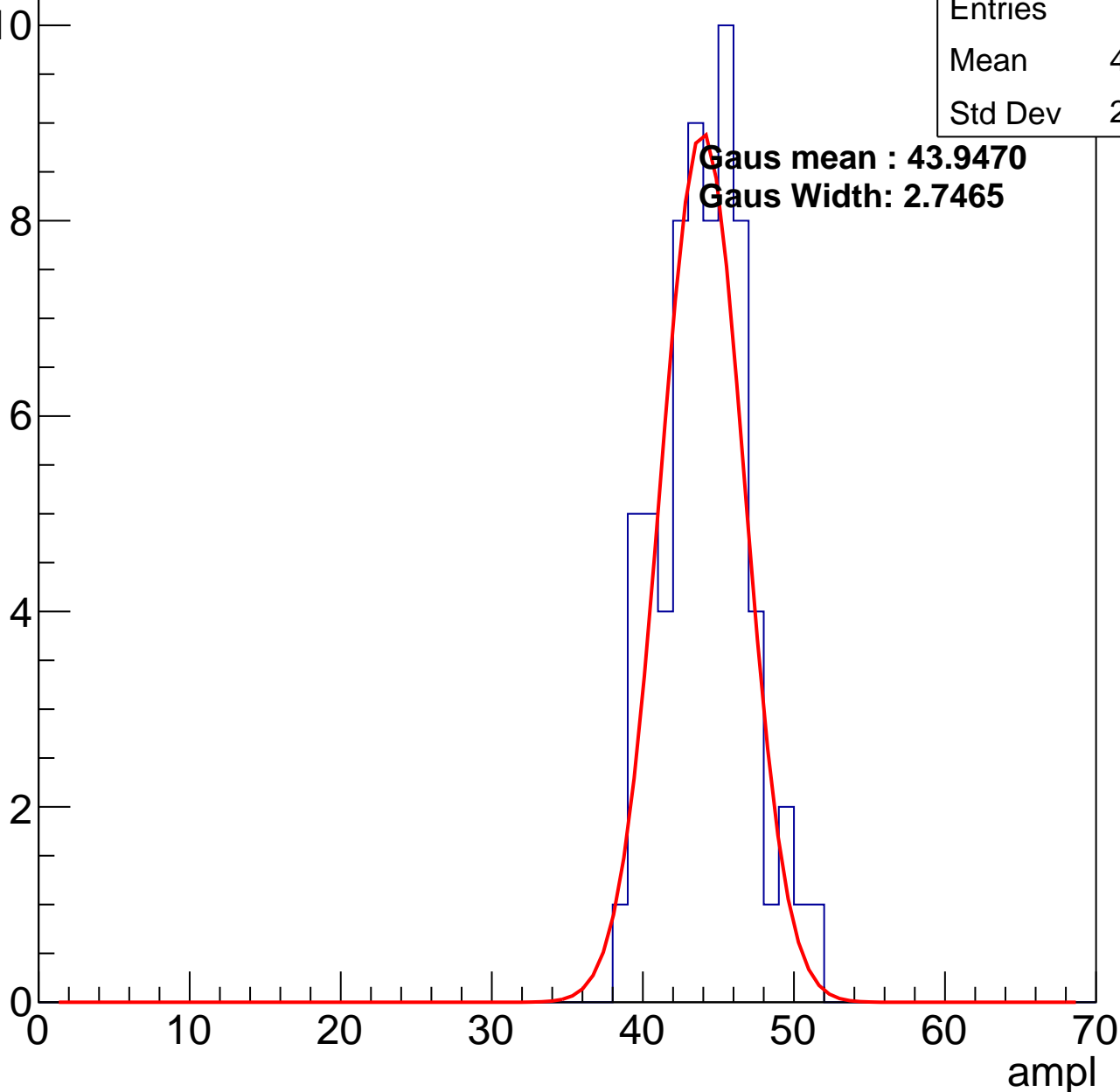
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.66
Std Dev	2.837

Gaus mean : 43.9470

Gaus Width: 2.7465

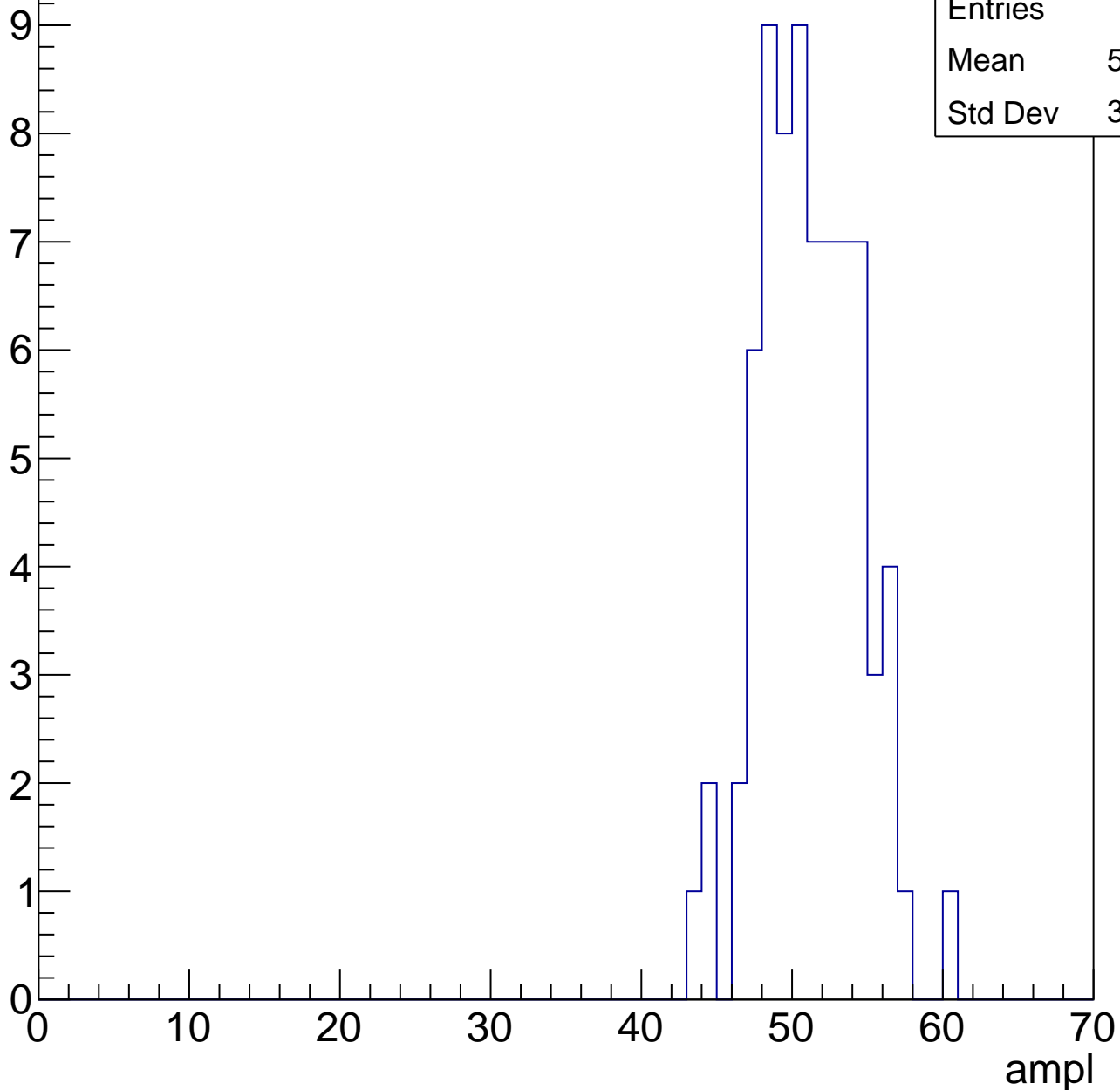


B0L001S, U19-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	50.74
Std Dev	3.263



B0L001S, U19-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries	63
Mean	57.38
Std Dev	3.026

ampl

0

10

20

30

40

50

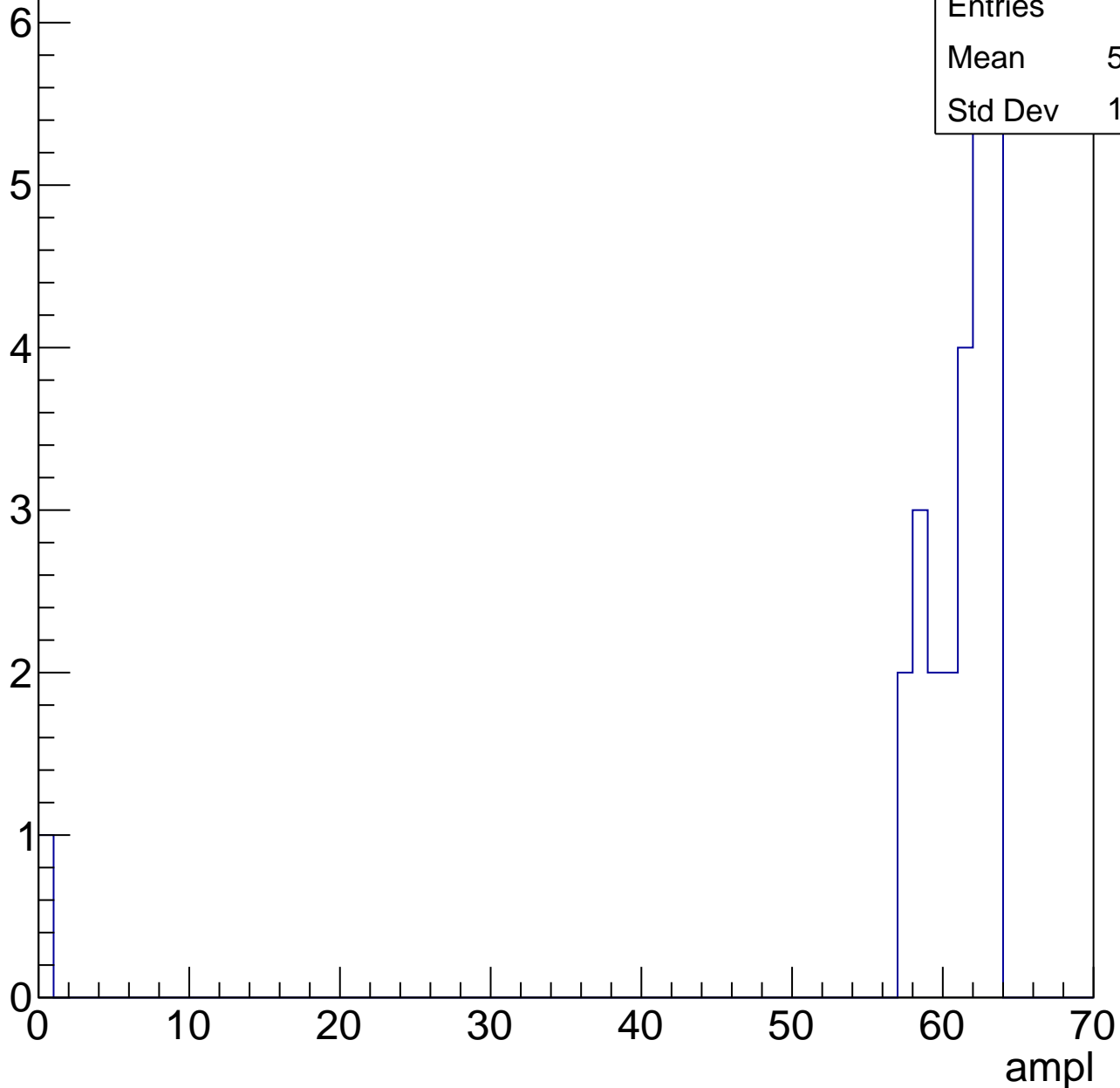
60

70

B0L001S, U19-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

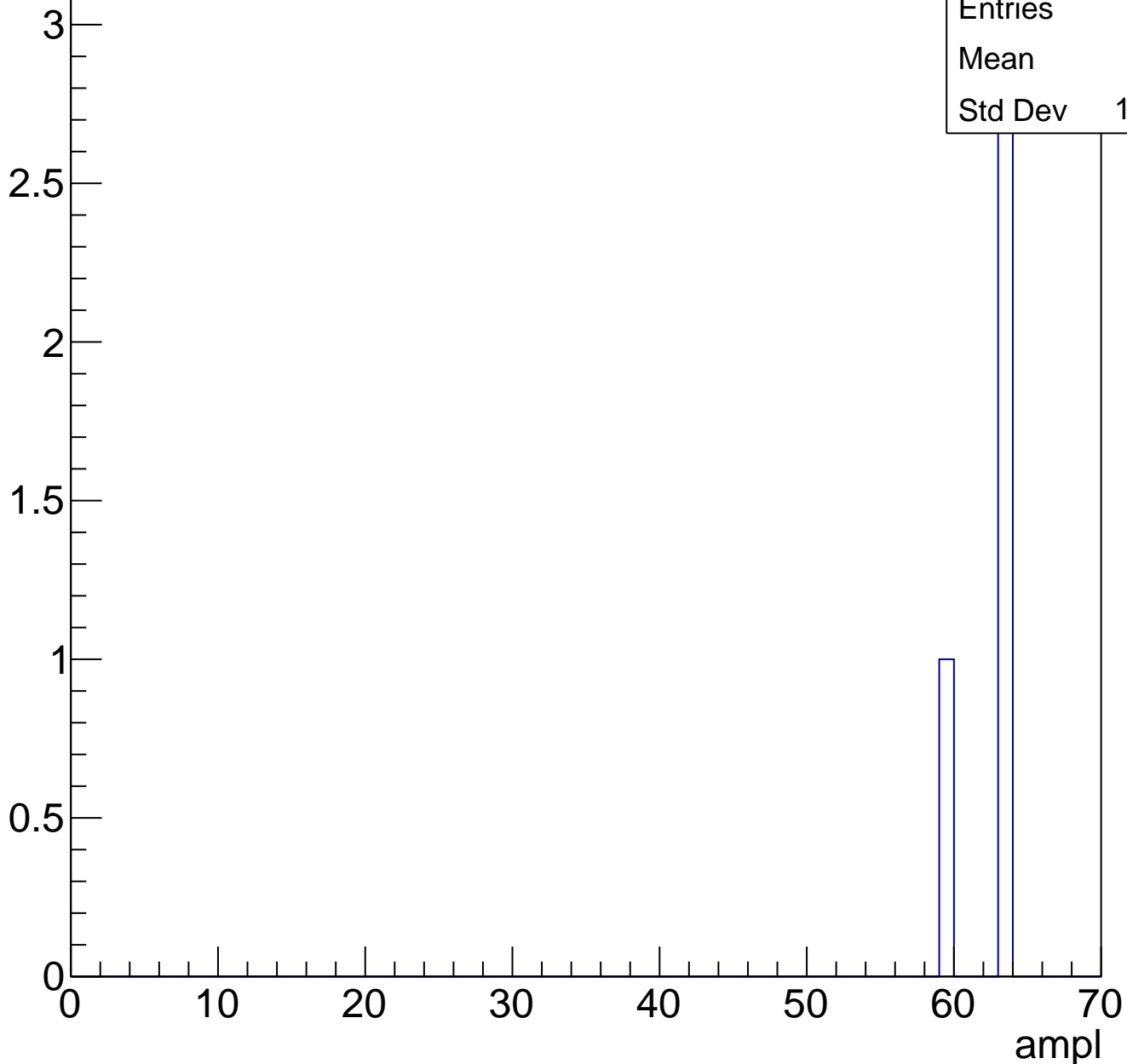
Entry



B0L001S, U19-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U19-ch53, adc0

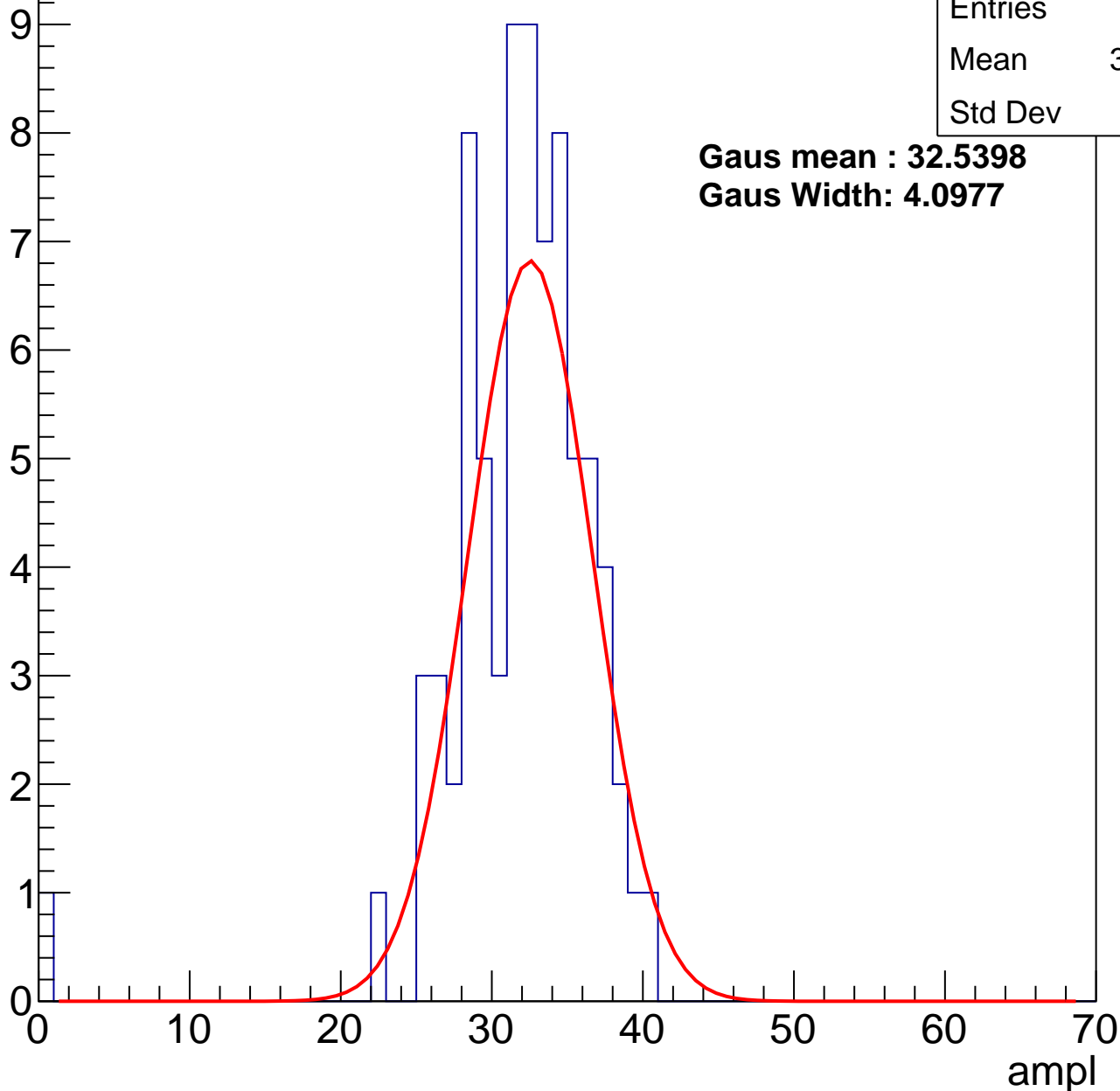
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	31.38
Std Dev	5.15

Gaus mean : 32.5398

Gaus Width: 4.0977



B0L001S, U19-ch53, adc1

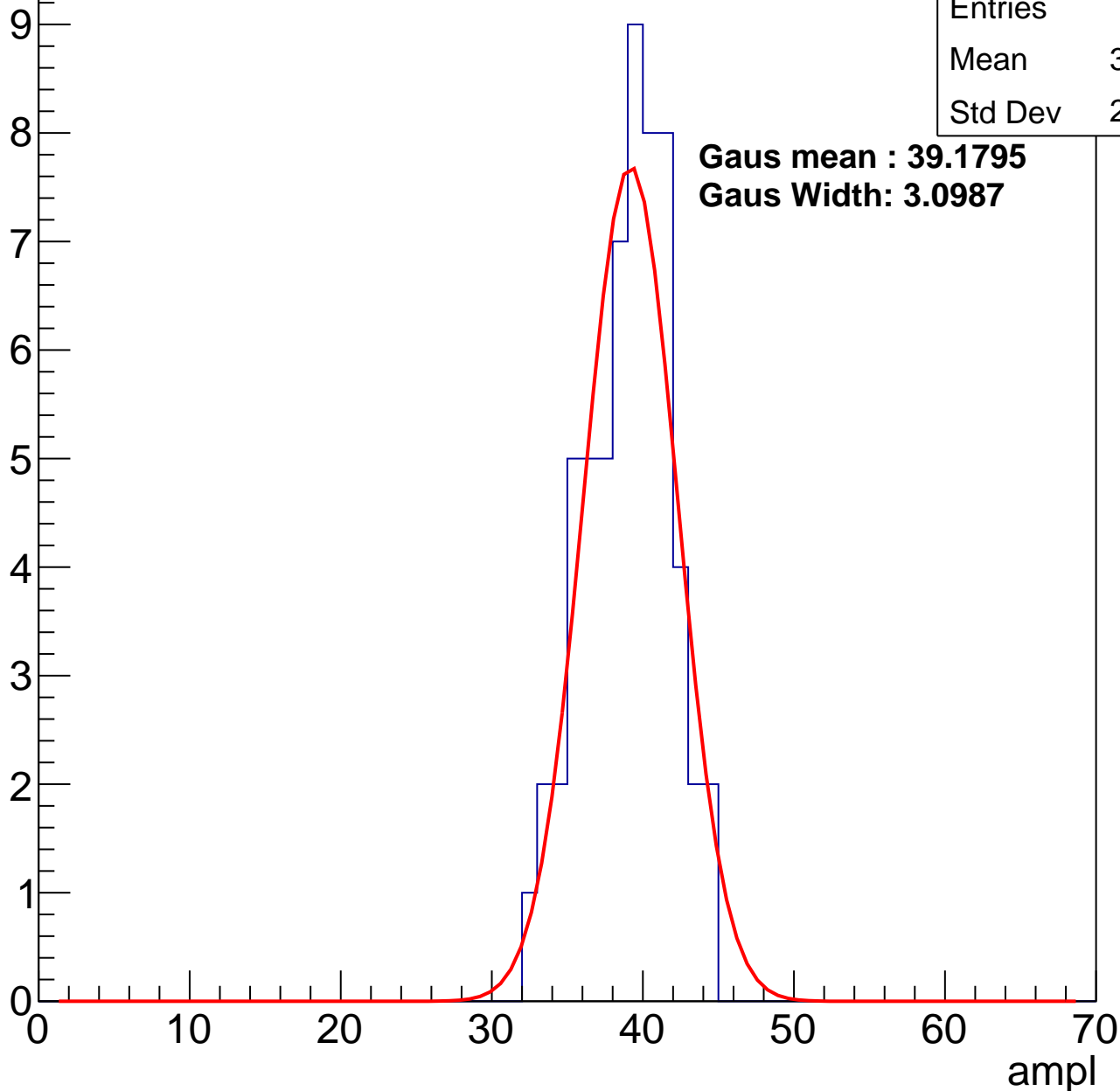
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.55
Std Dev	2.795

Gaus mean : 39.1795

Gaus Width: 3.0987



B0L001S, U19-ch53, adc2

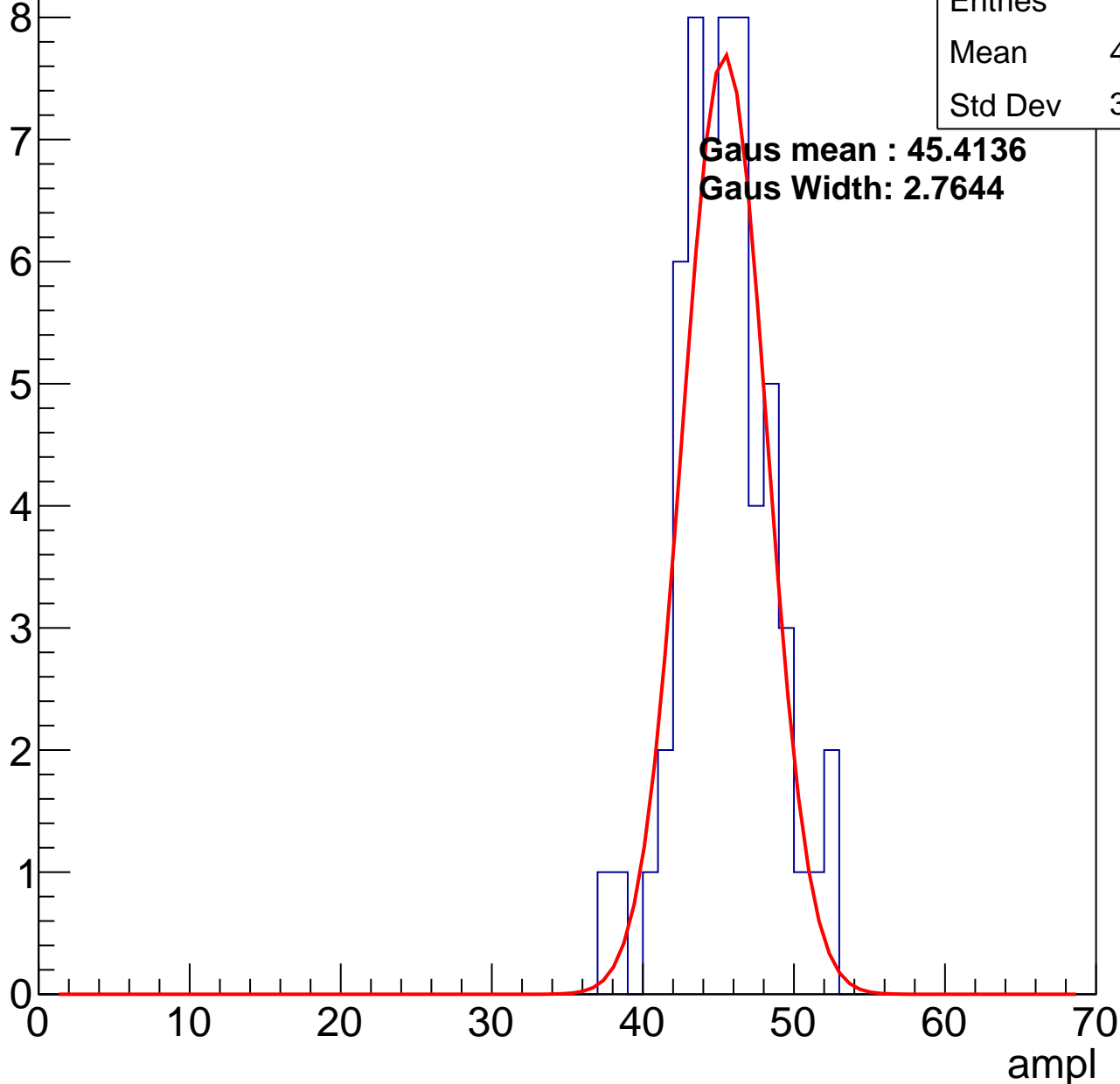
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.98
Std Dev	3.048

Gaus mean : 45.4136

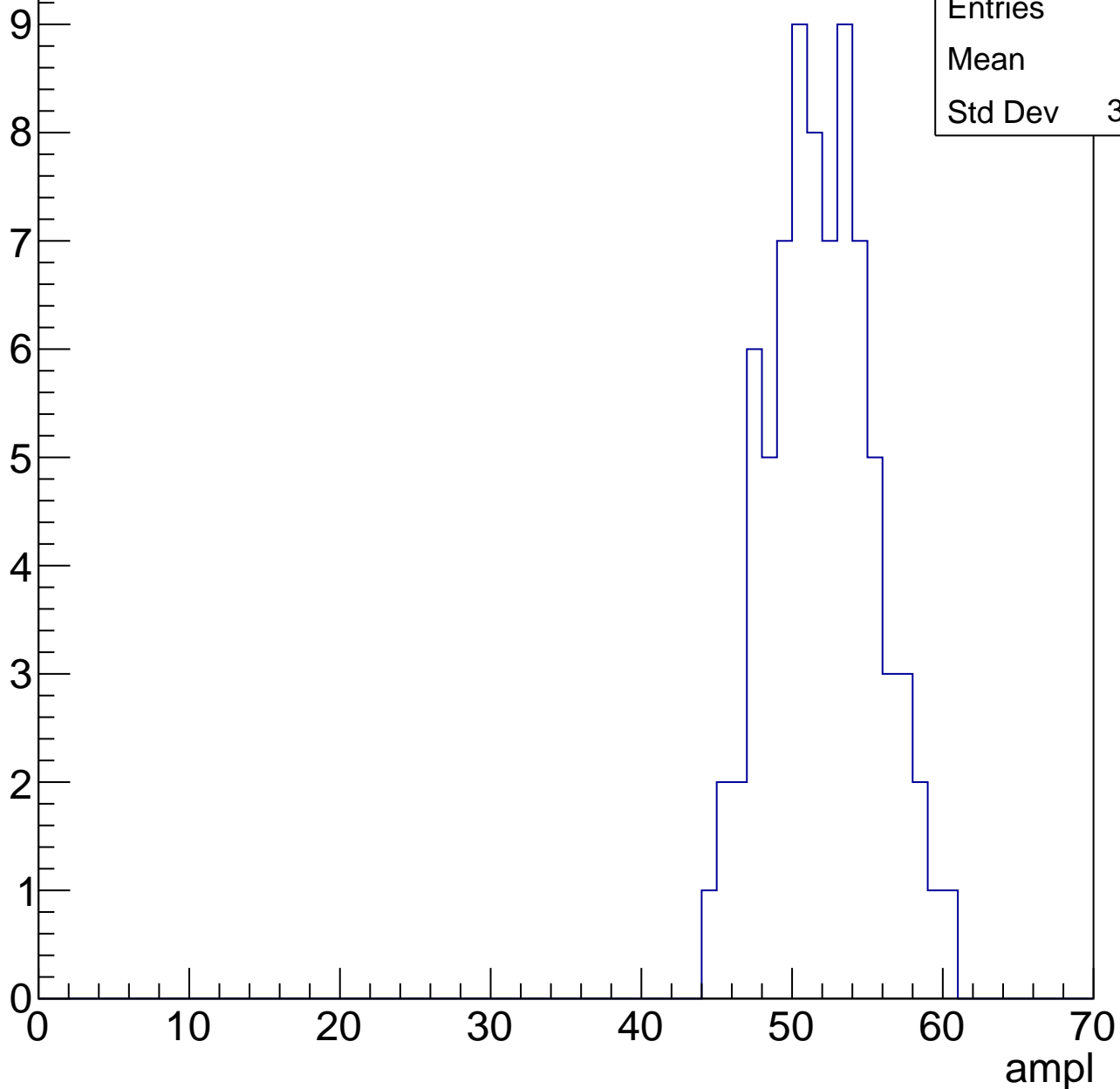
Gaus Width: 2.7644



B0L001S, U19-ch53, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

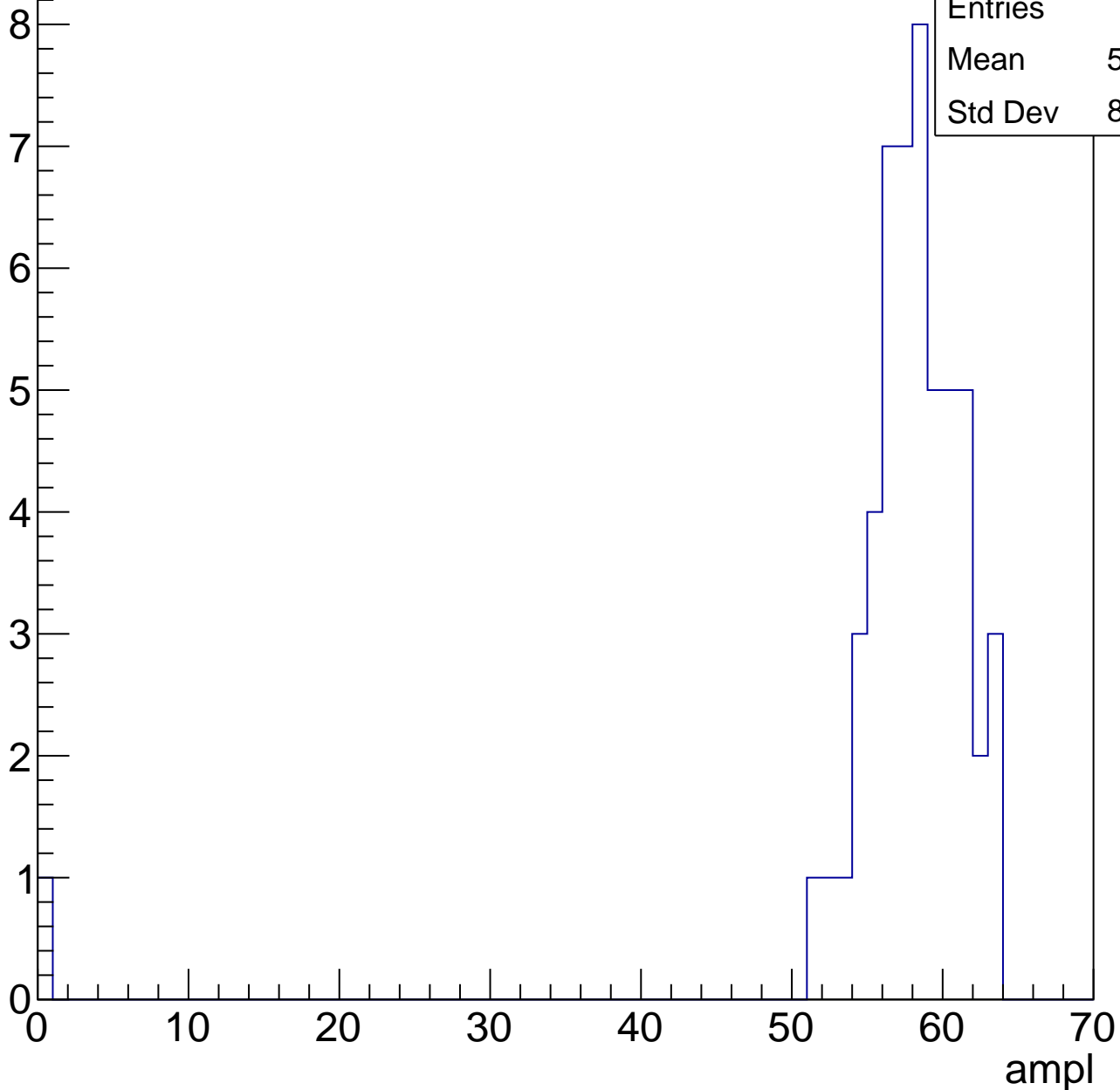


B0L001S, U19-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.72
Std Dev	8.336

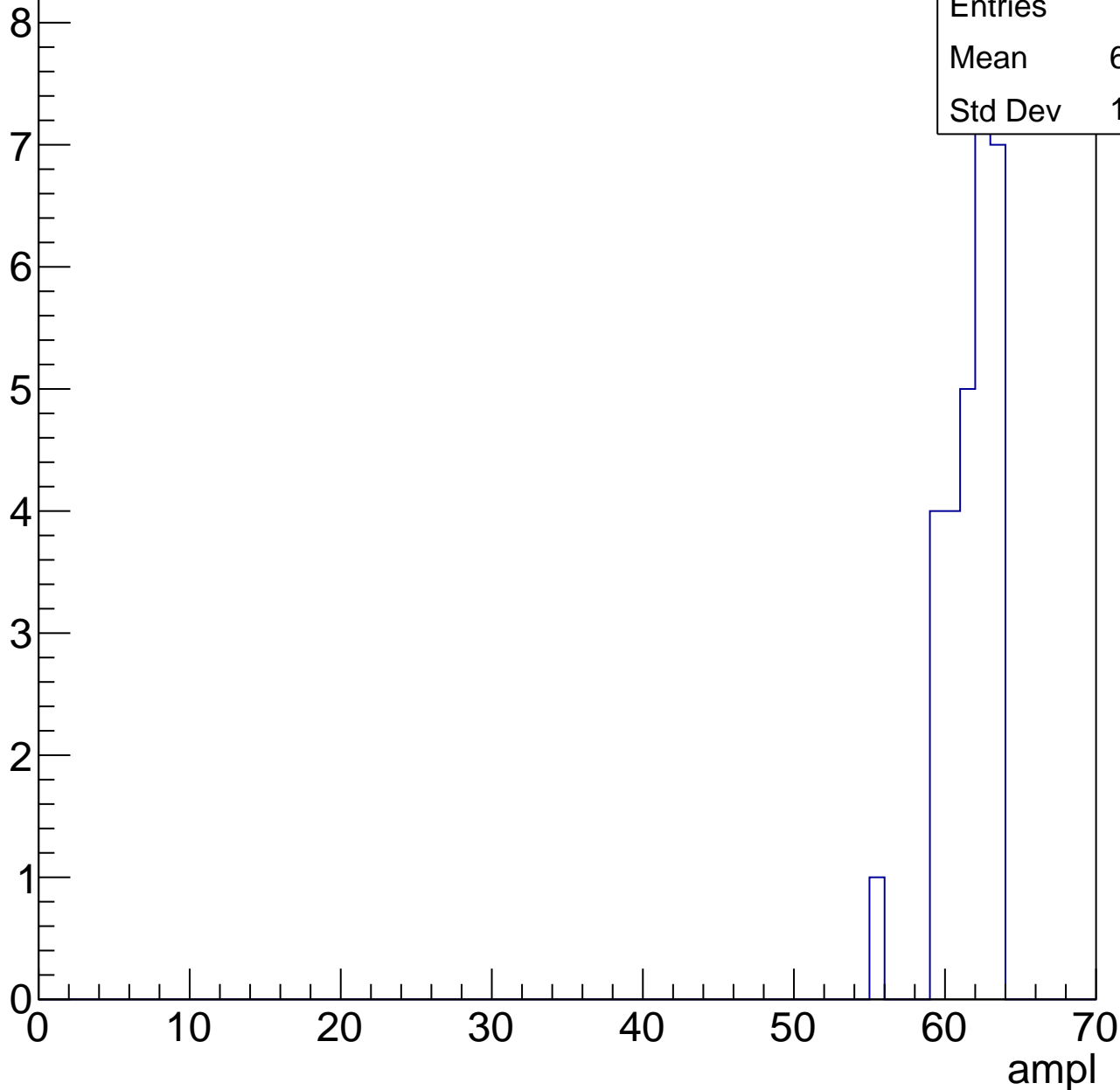


B0L001S, U19-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	61.14
Std Dev	1.776



B0L001S, U19-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch54, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	27.75
Std Dev	5.516

Gaus mean : 28.6564

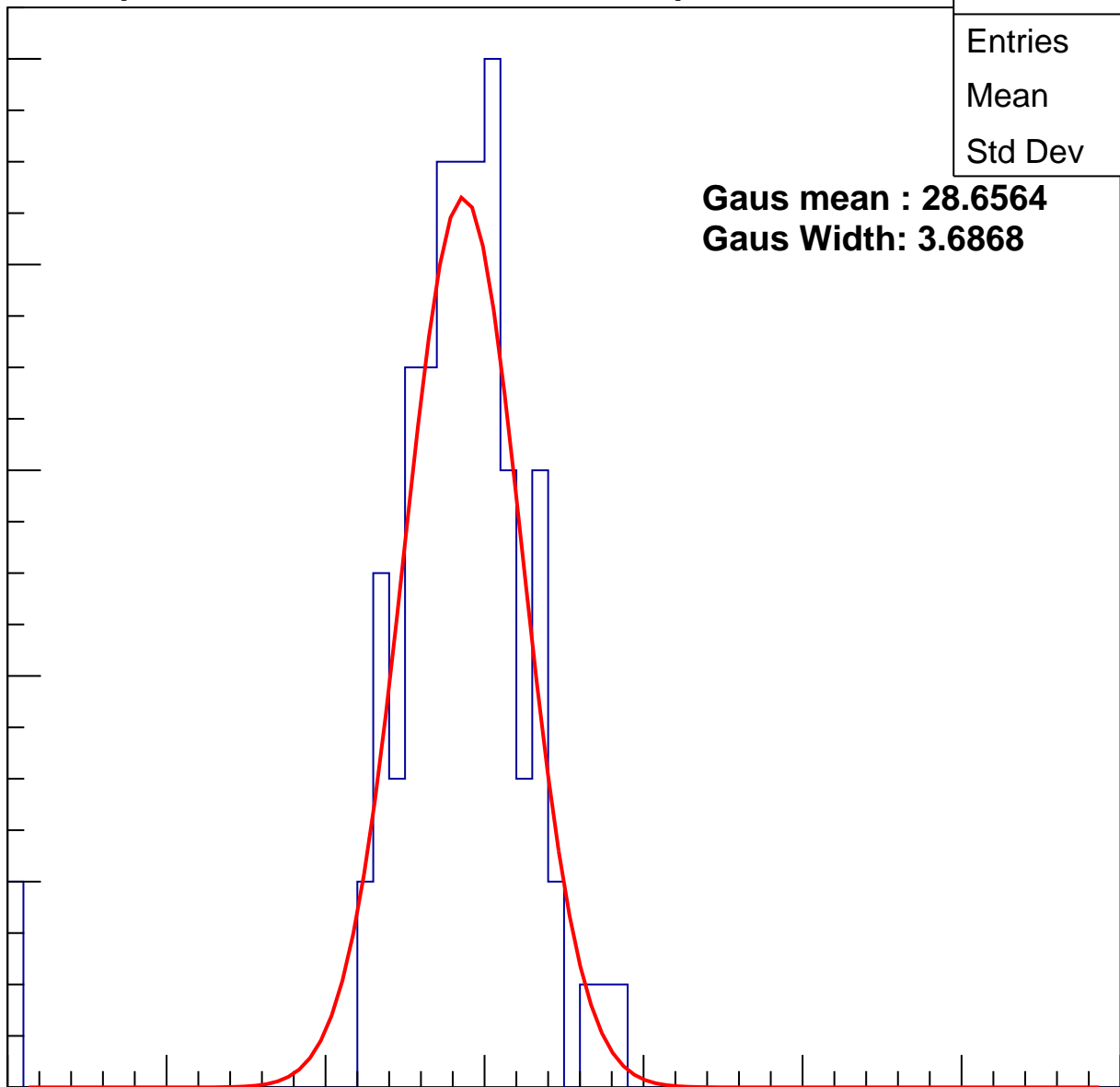
Gaus Width: 3.6868

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch54, adc1

calib_packv5_042523_0143.root, FC#9, port A1

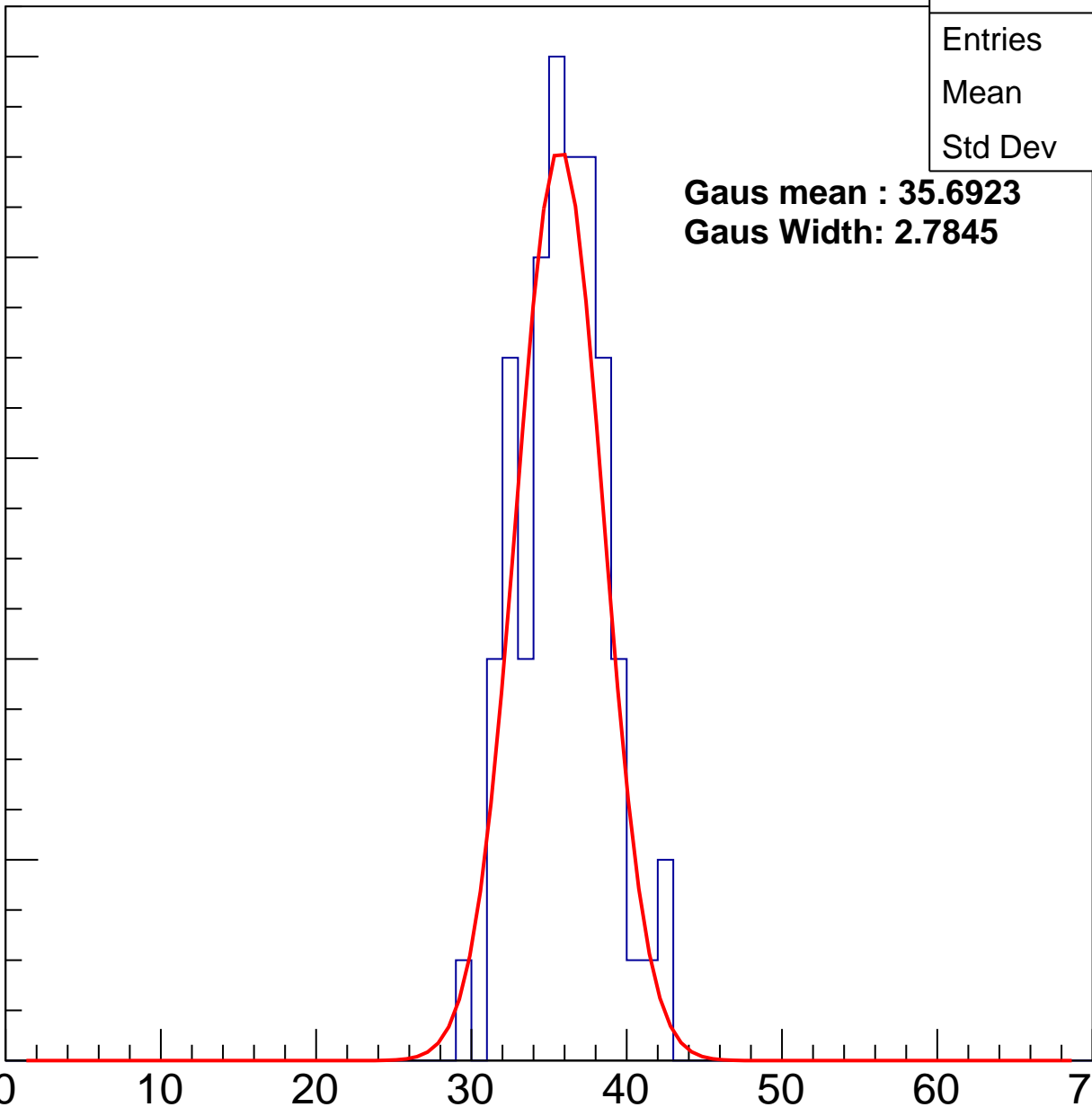
Entries	67
Mean	35.45
Std Dev	2.744

Gaus mean : 35.6923
Gaus Width: 2.7845

Entry

10
8
6
4
2
0

ampl



B0L001S, U19-ch54, adc2

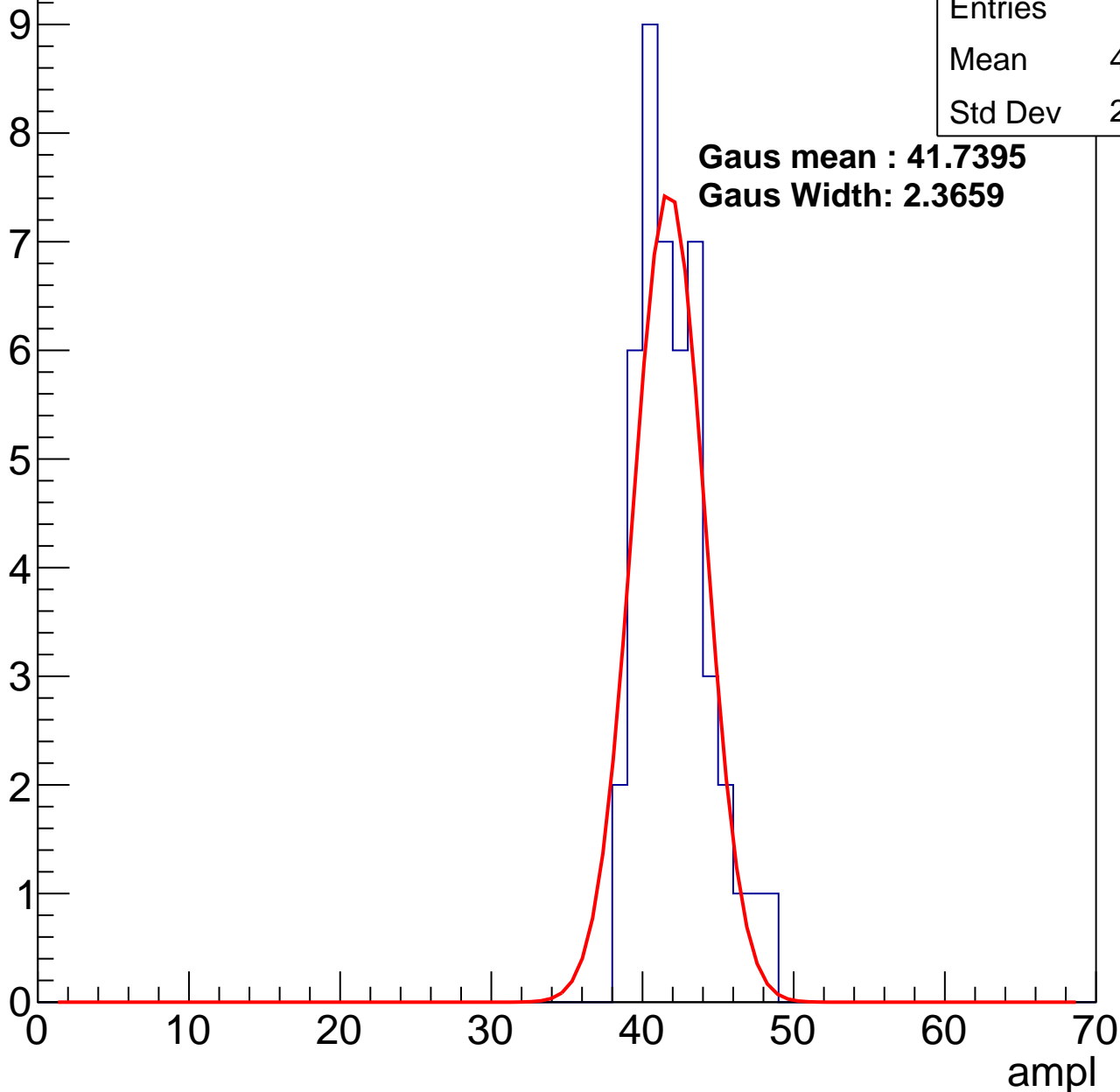
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	41.62
Std Dev	2.293

Gaus mean : 41.7395

Gaus Width: 2.3659

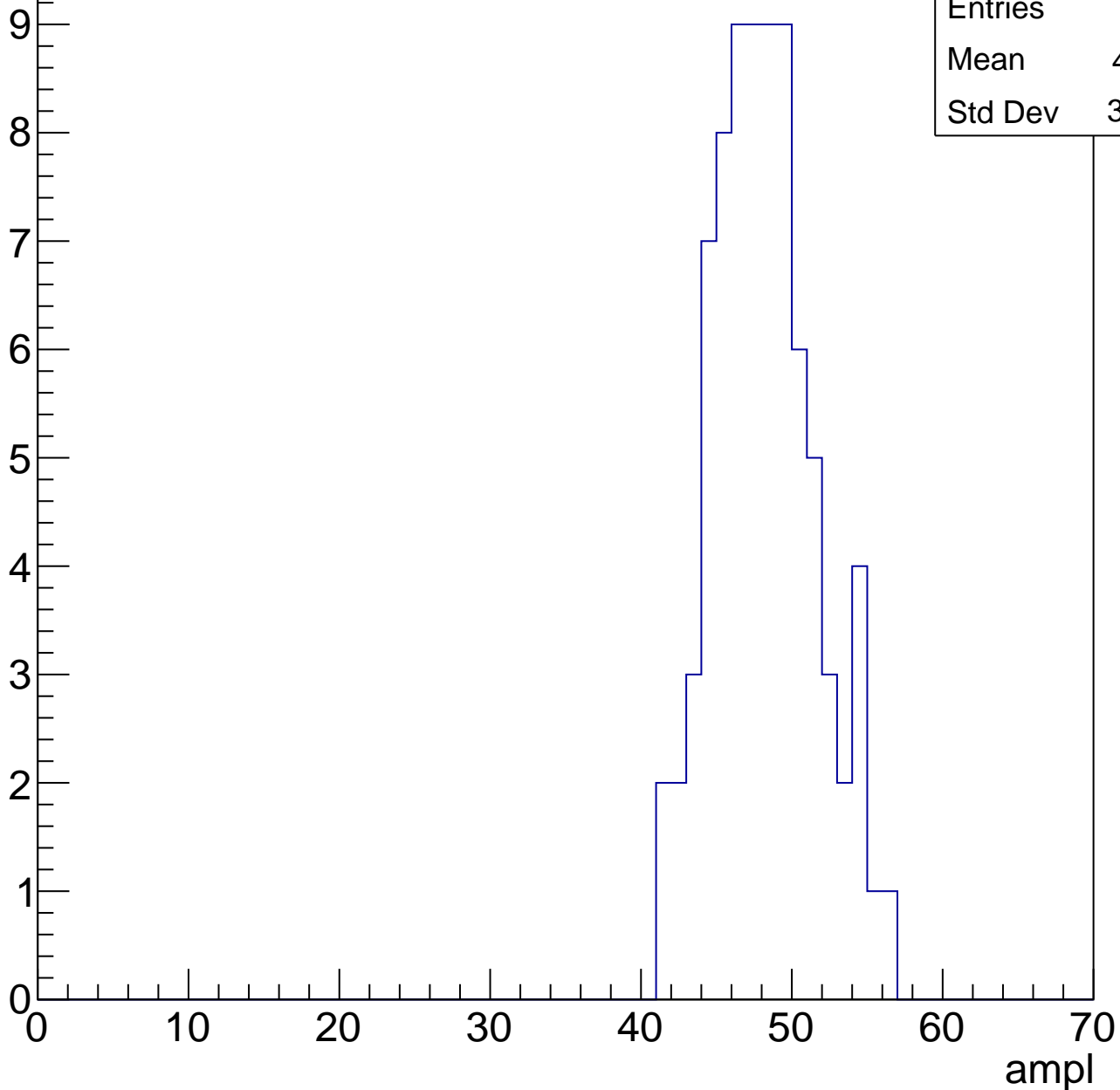


B0L001S, U19-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

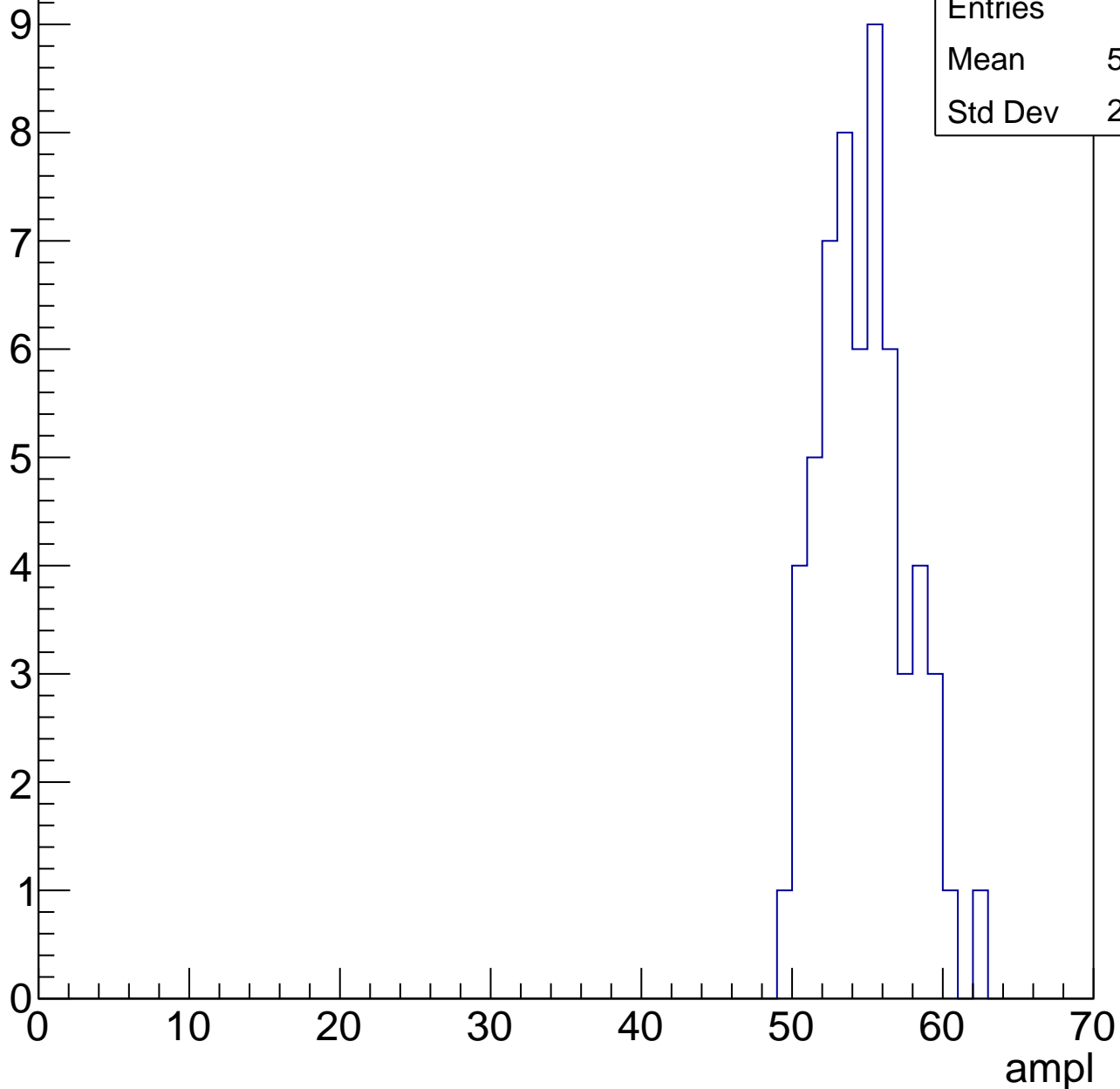
Entries	80
Mean	47.71
Std Dev	3.355



B0L001S, U19-ch54, adc4

calib_packv5_042523_0143.root, FC#9, port A1

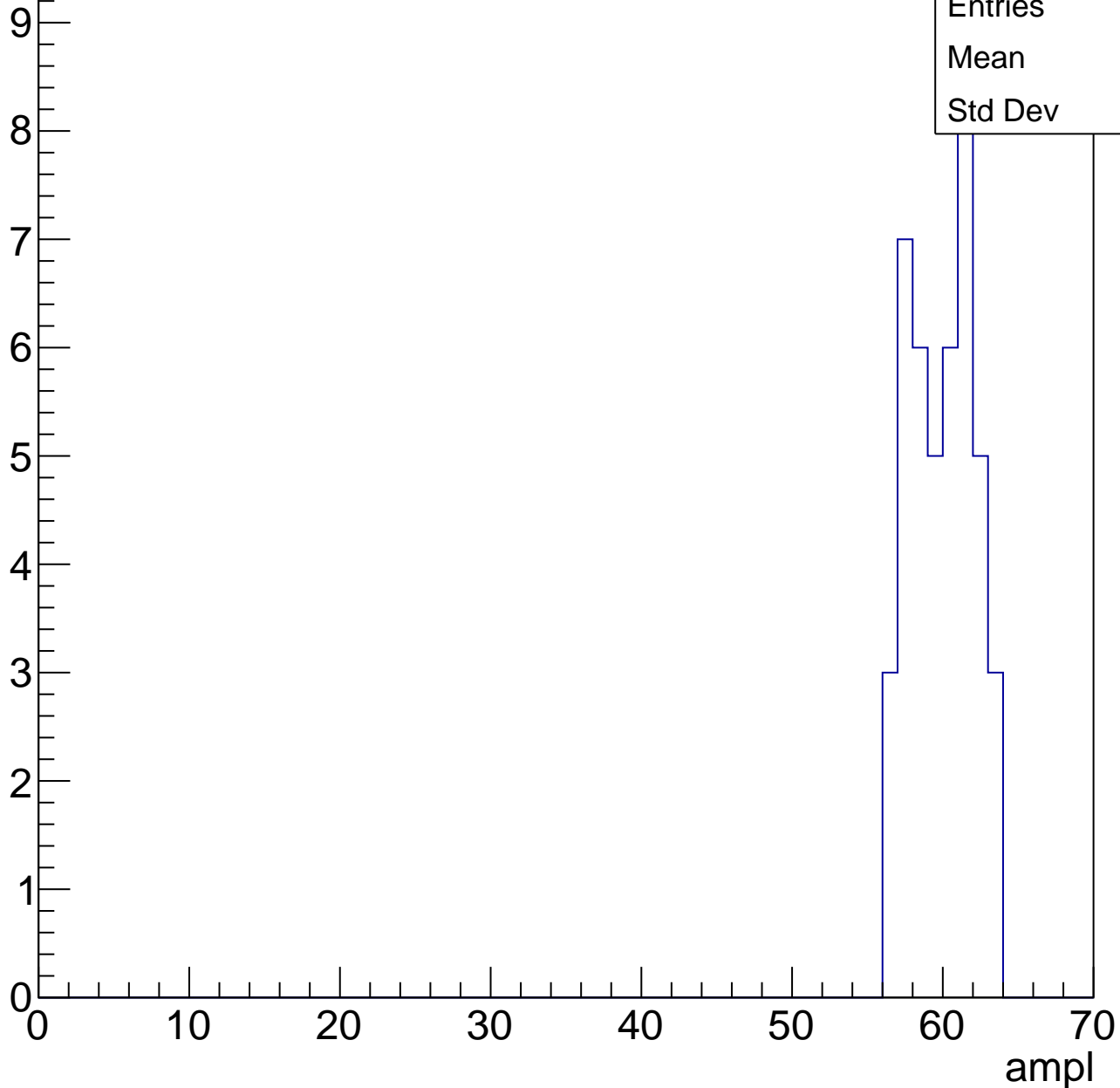
Entry



B0L001S, U19-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

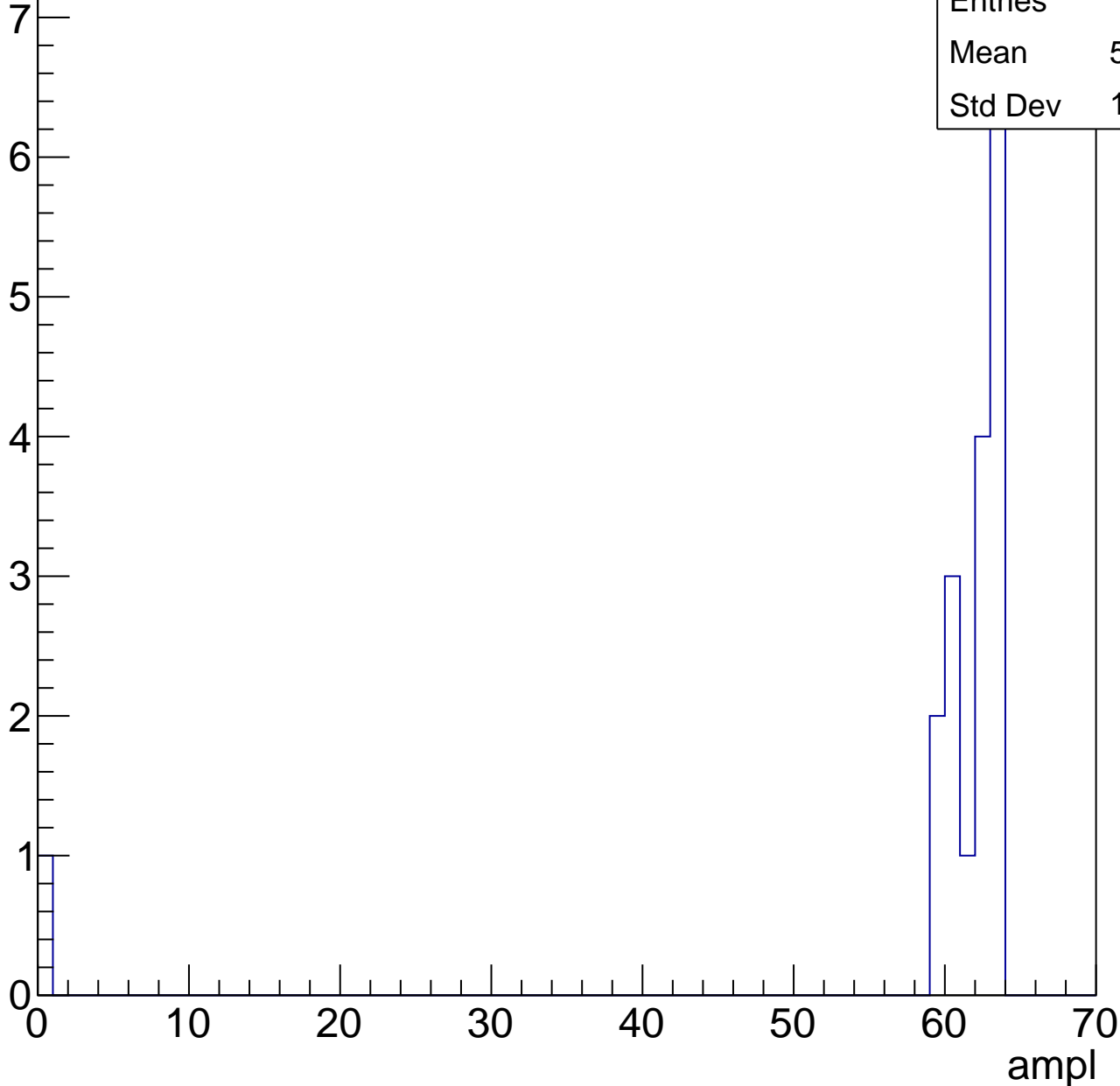


B0L001S, U19-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	18
Mean	58.22
Std Dev	14.19



B0L001S, U19-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch55, adc0

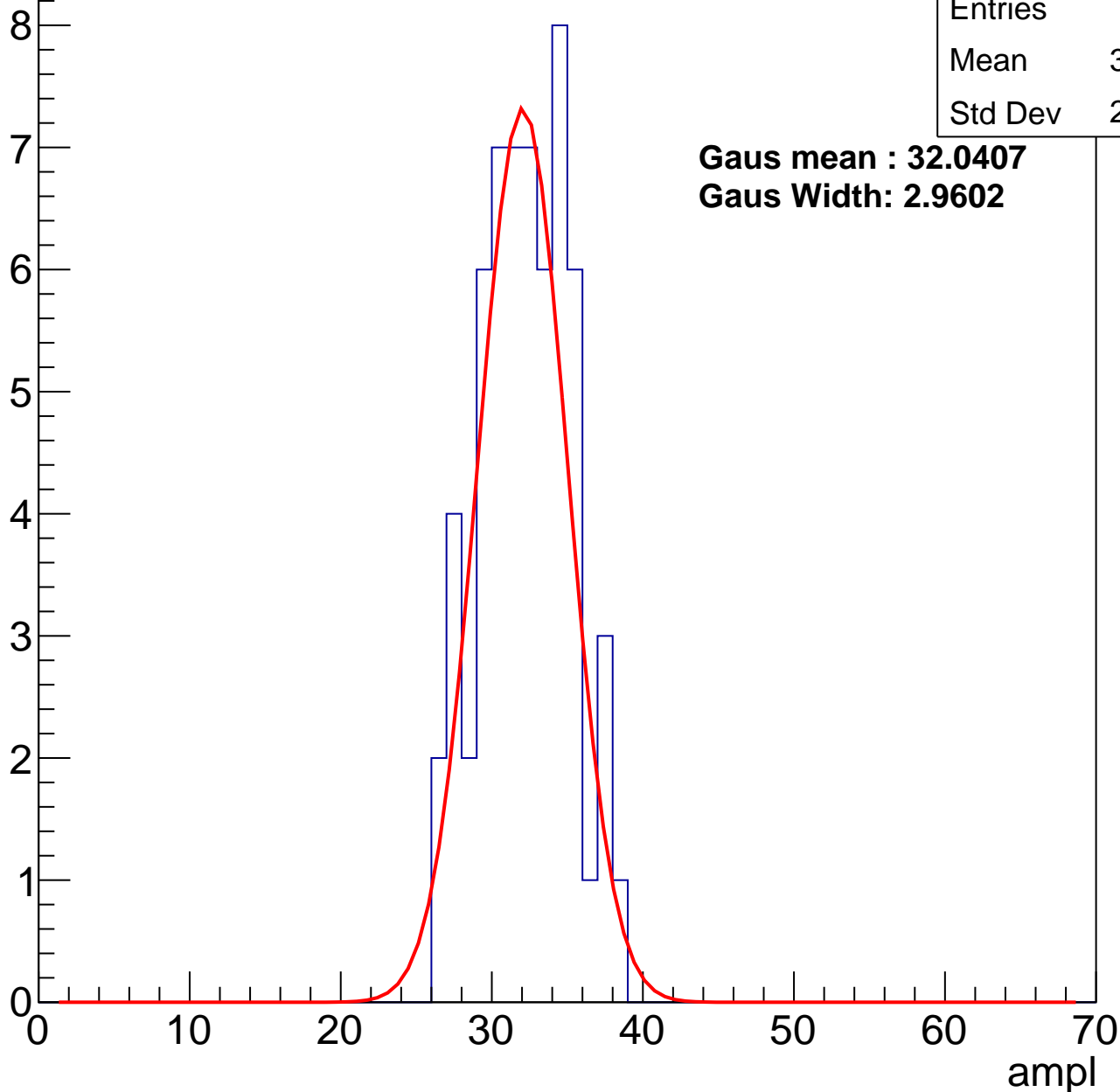
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	31.77
Std Dev	2.912

Gaus mean : 32.0407

Gaus Width: 2.9602



B0L001S, U19-ch55, adc1

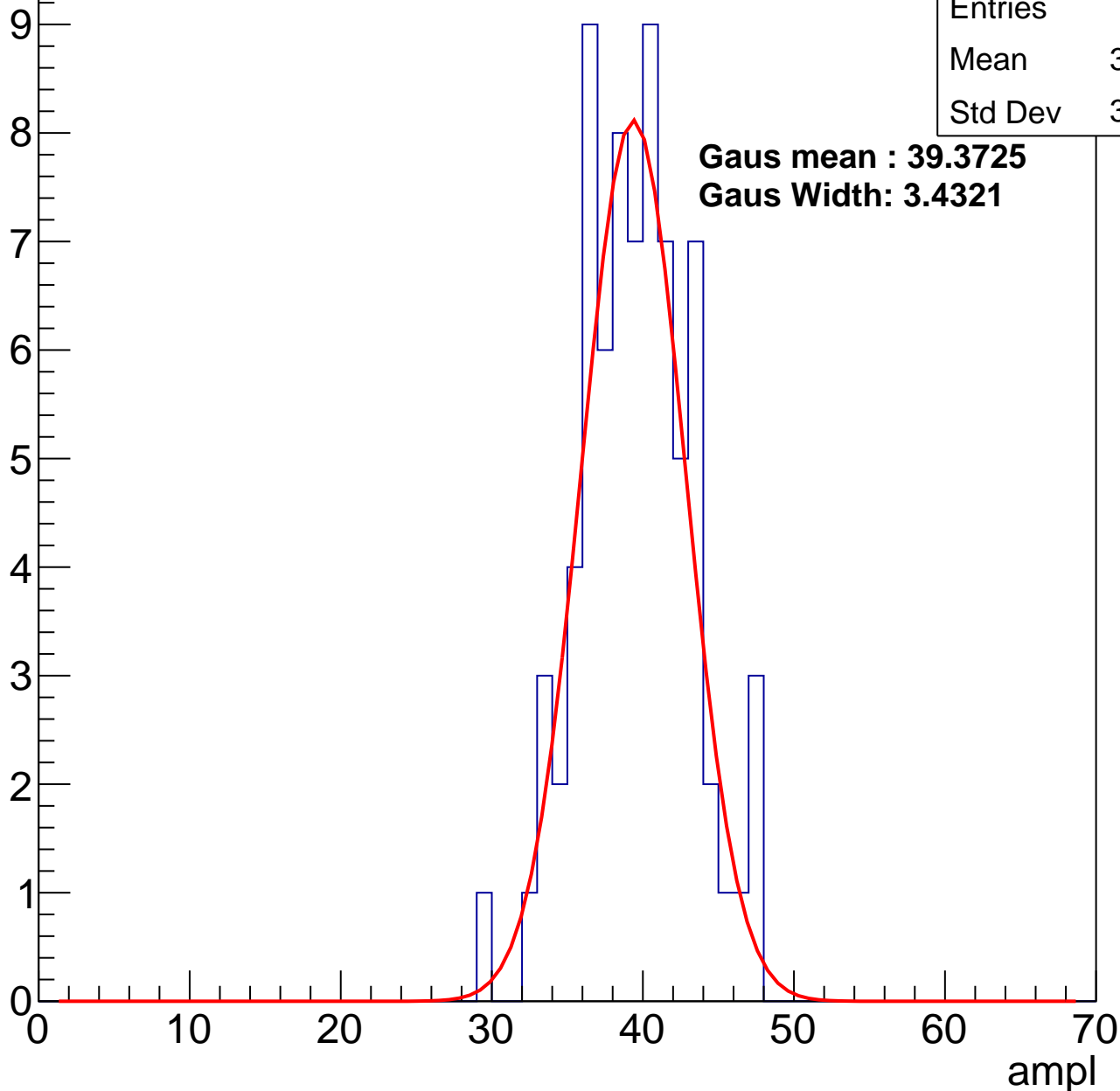
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	39.07
Std Dev	3.636

Gaus mean : 39.3725

Gaus Width: 3.4321



B0L001S, U19-ch55, adc2

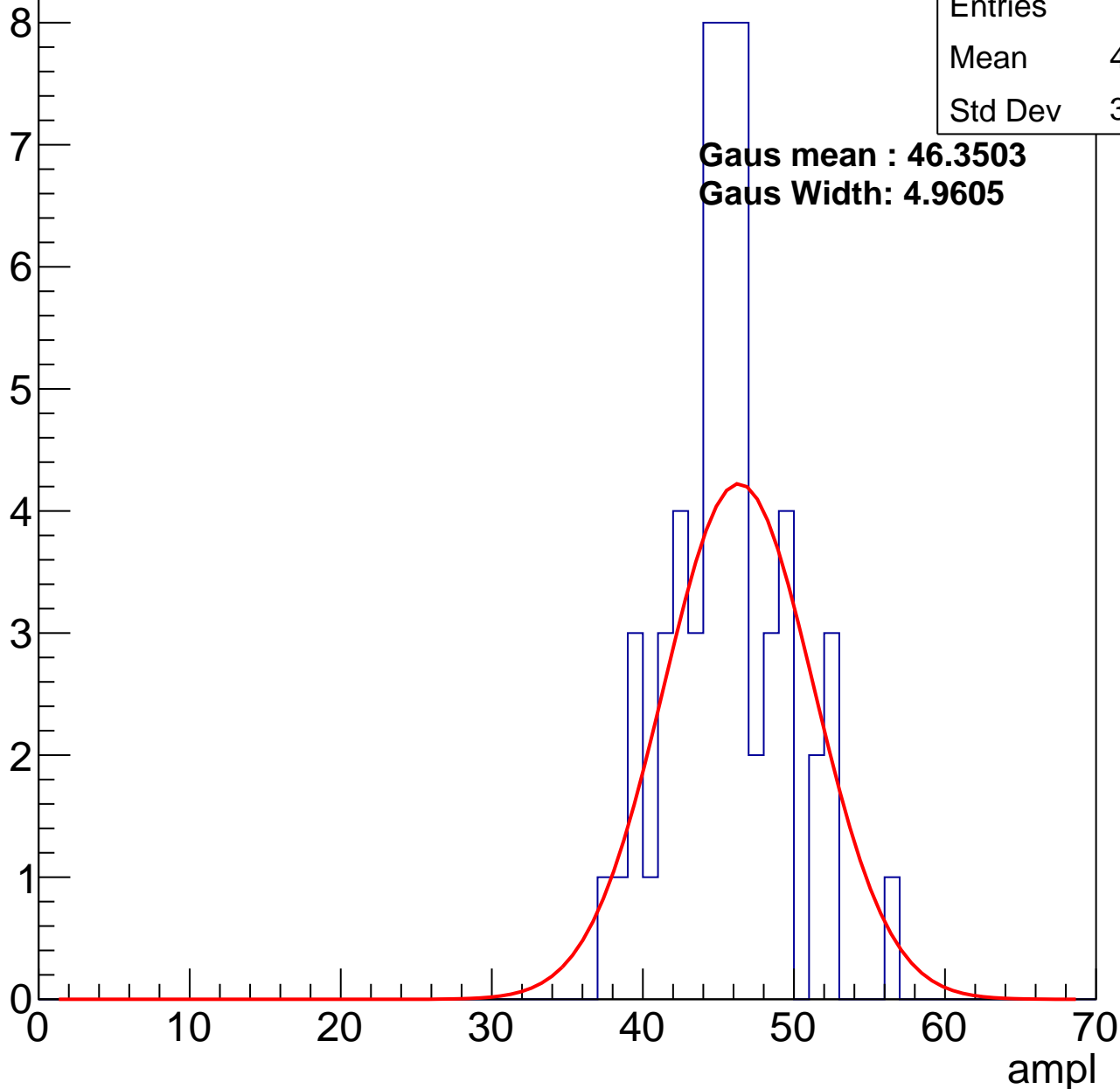
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	45.09
Std Dev	3.806

Gaus mean : 46.3503

Gaus Width: 4.9605



B0L001S, U19-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	51.4
Std Dev	3.467

Entry

10

8

6

4

2

0

0

10

20

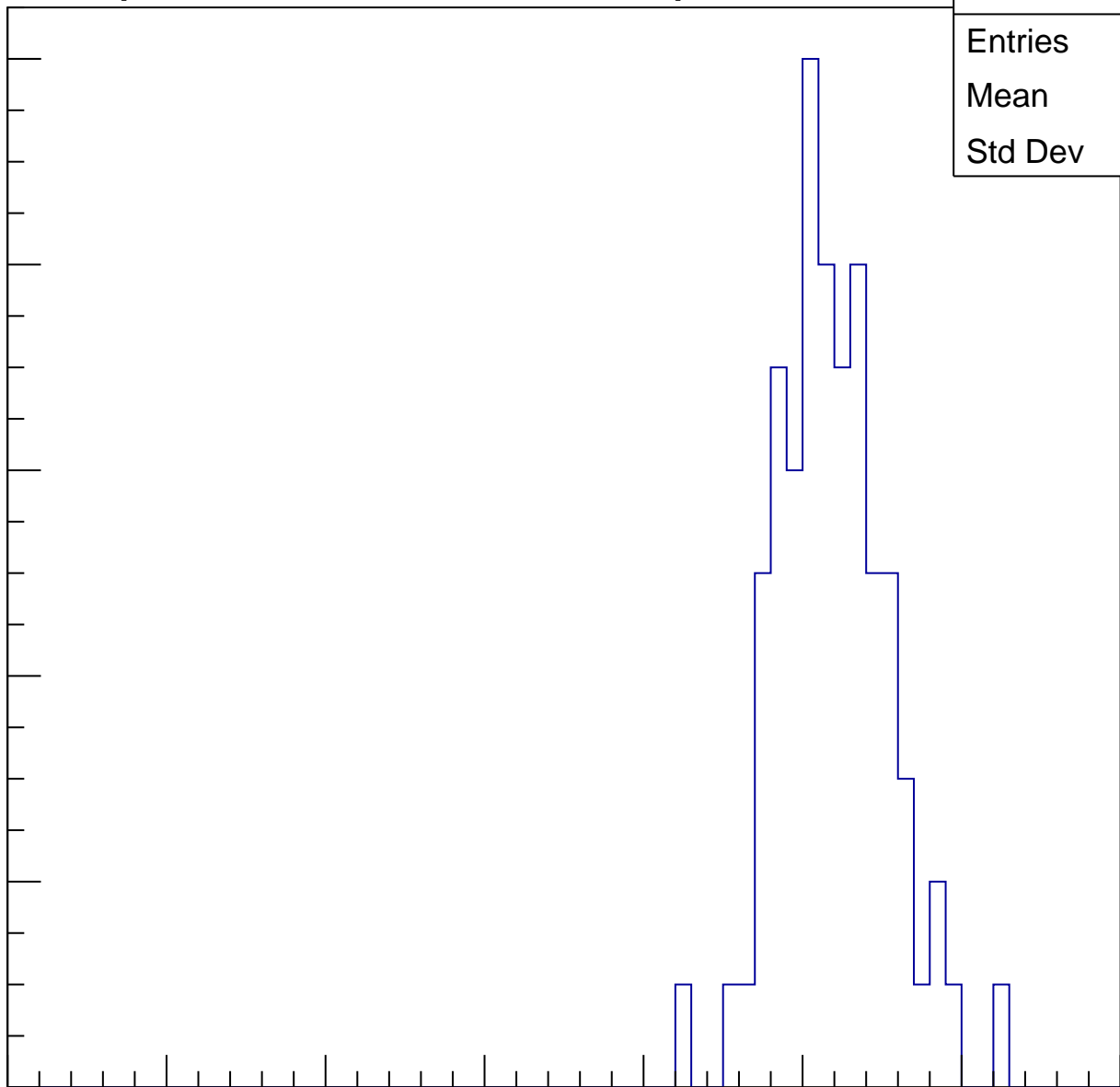
30

40

50

60

ampl



B0L001S, U19-ch55, adc4

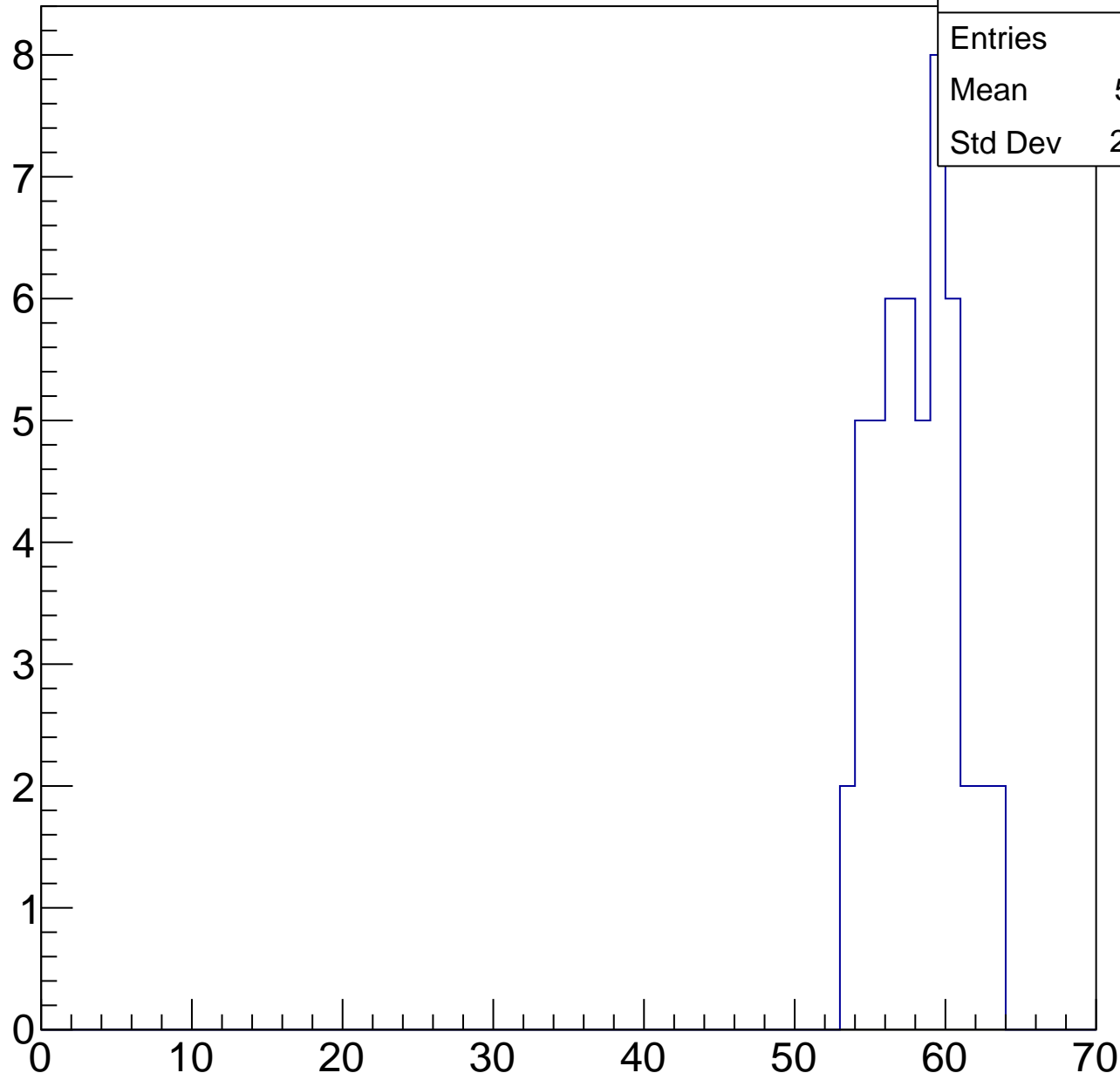
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	57.61
Std Dev	2.594

ampl

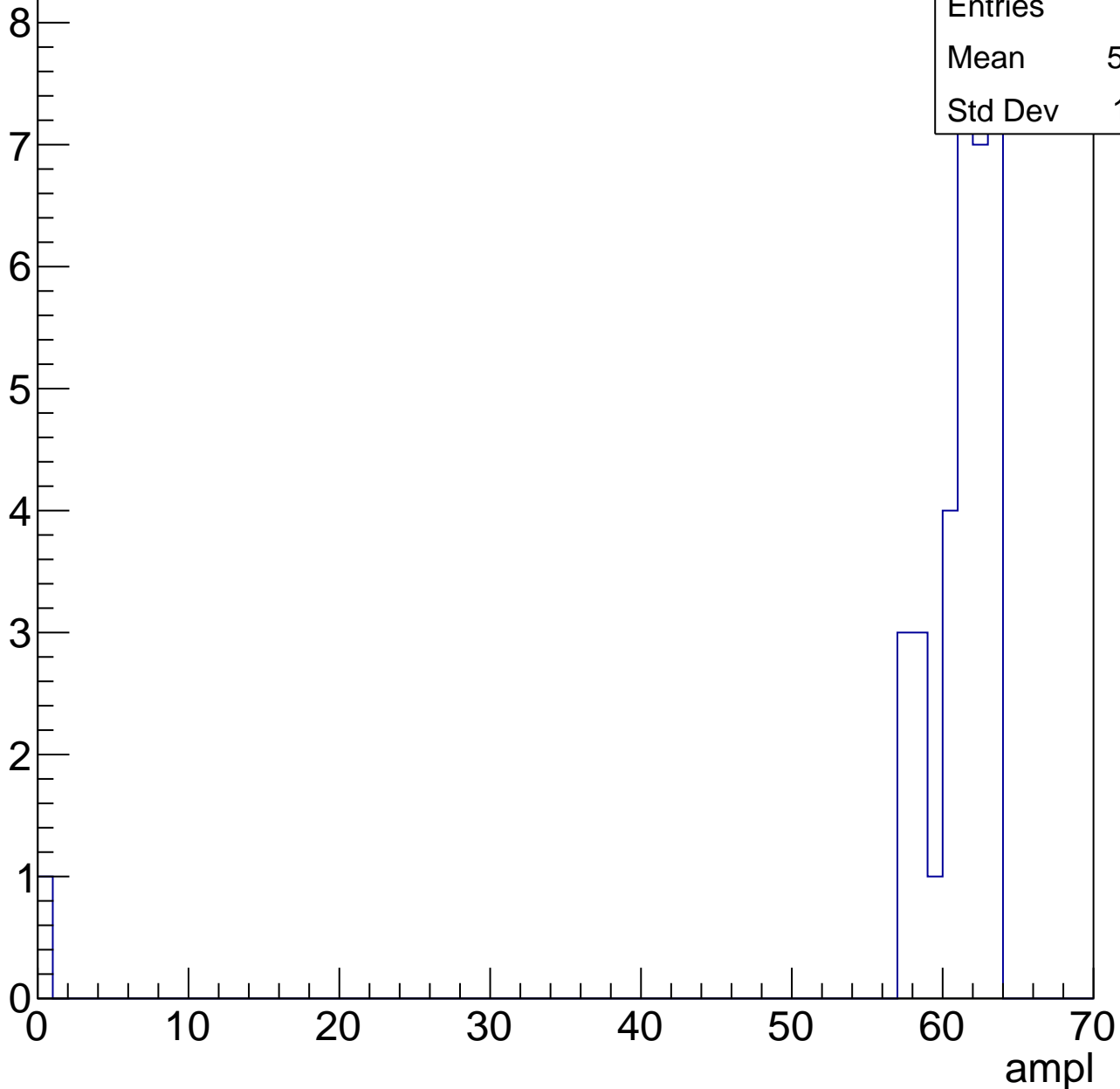


B0L001S, U19-ch55, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	59.14
Std Dev	10.31



B0L001S, U19-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

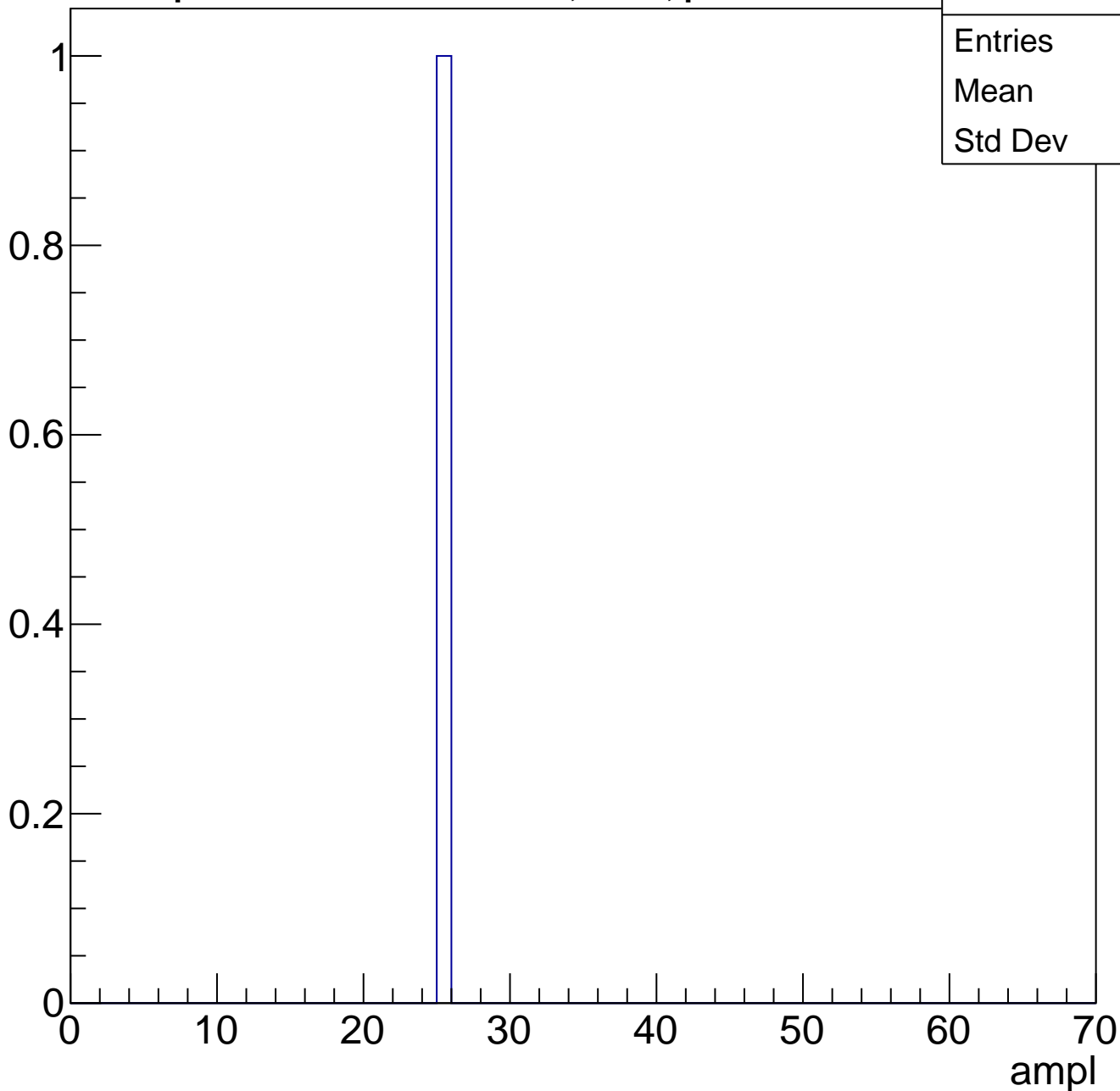


Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch56, adc0

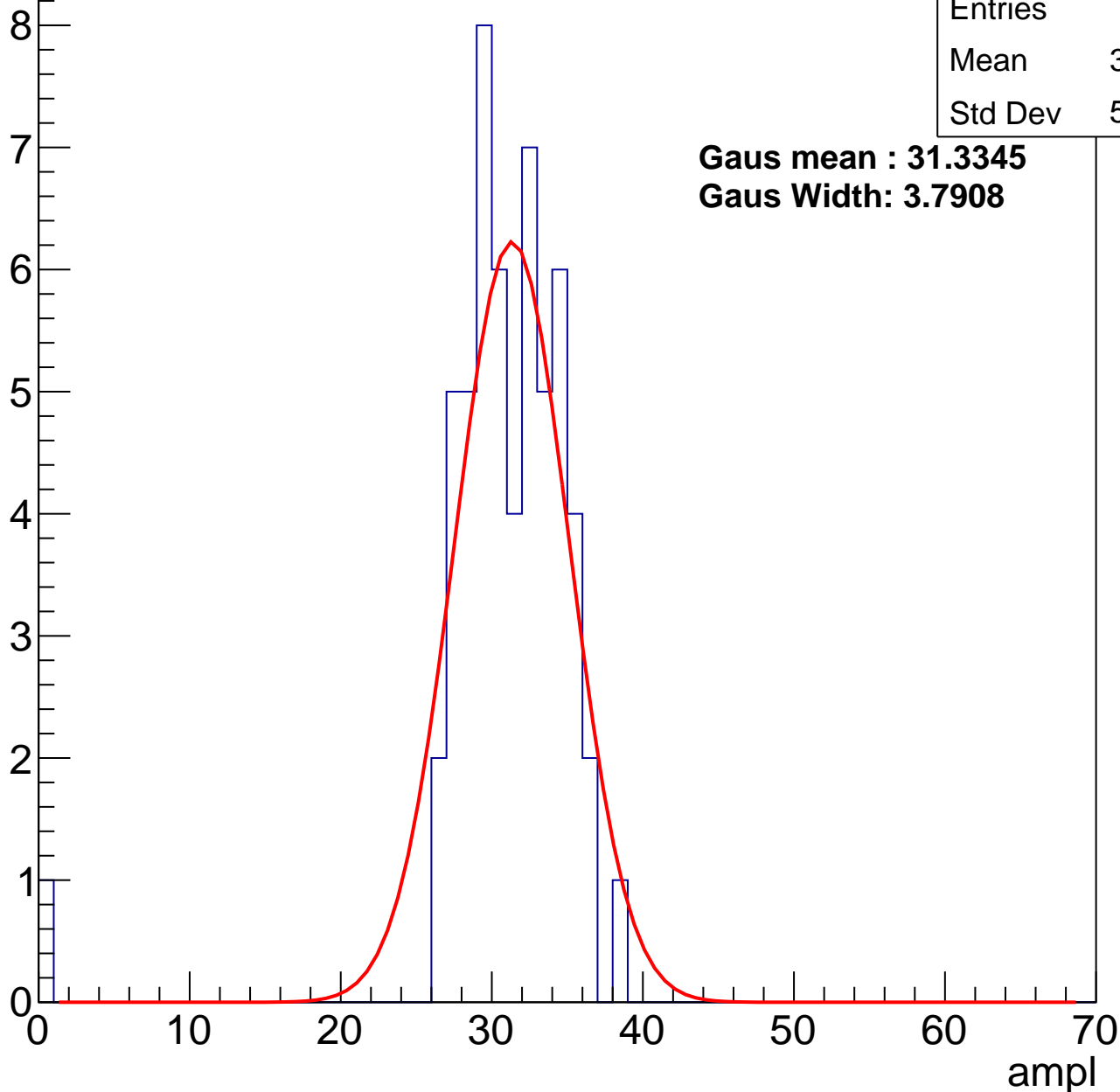
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	30.46
Std Dev	5.003

Gaus mean : 31.3345

Gaus Width: 3.7908



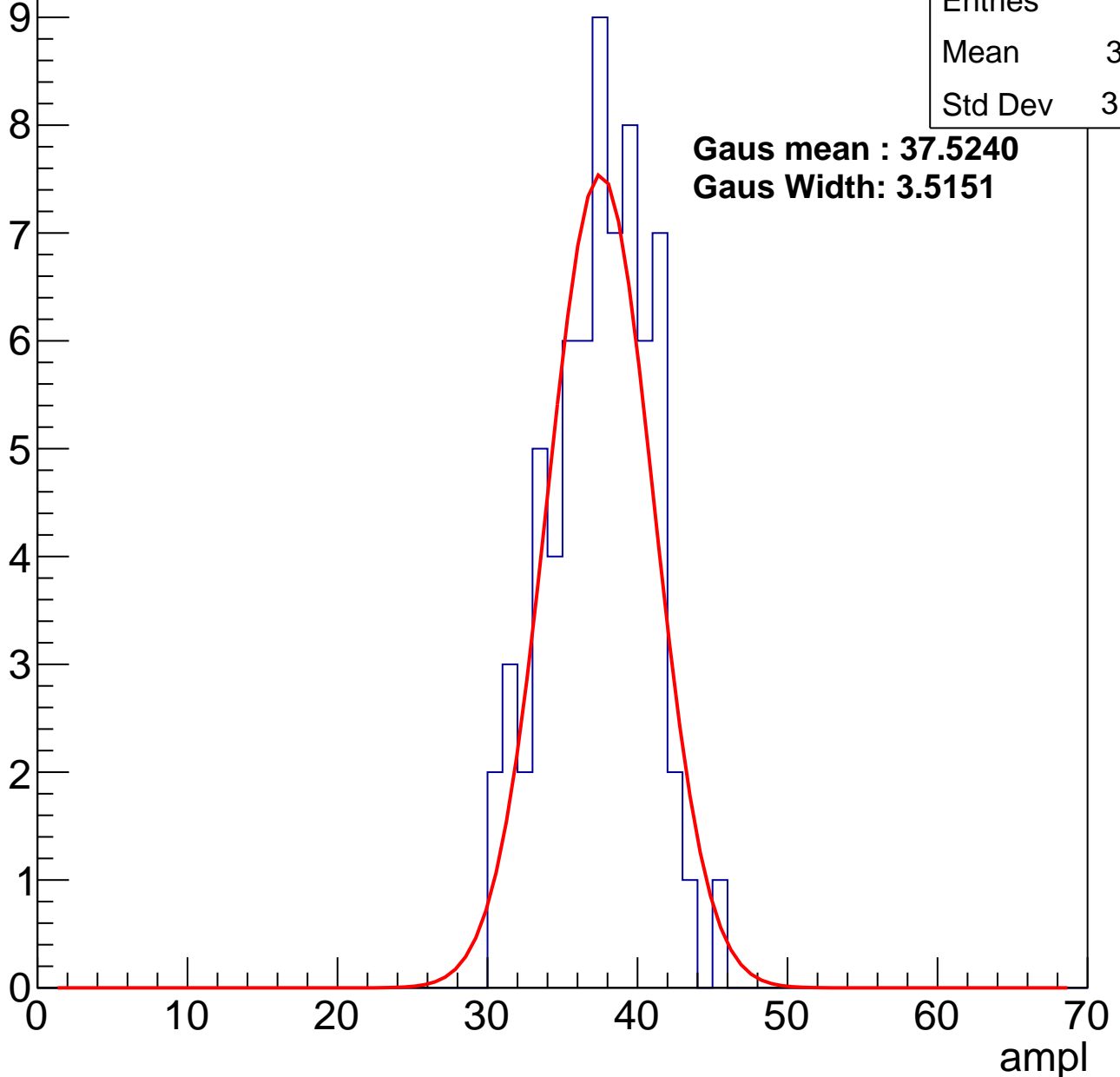
B0L001S, U19-ch56, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.01
Std Dev	3.312

Gaus mean : 37.5240
Gaus Width: 3.5151



B0L001S, U19-ch56, adc2

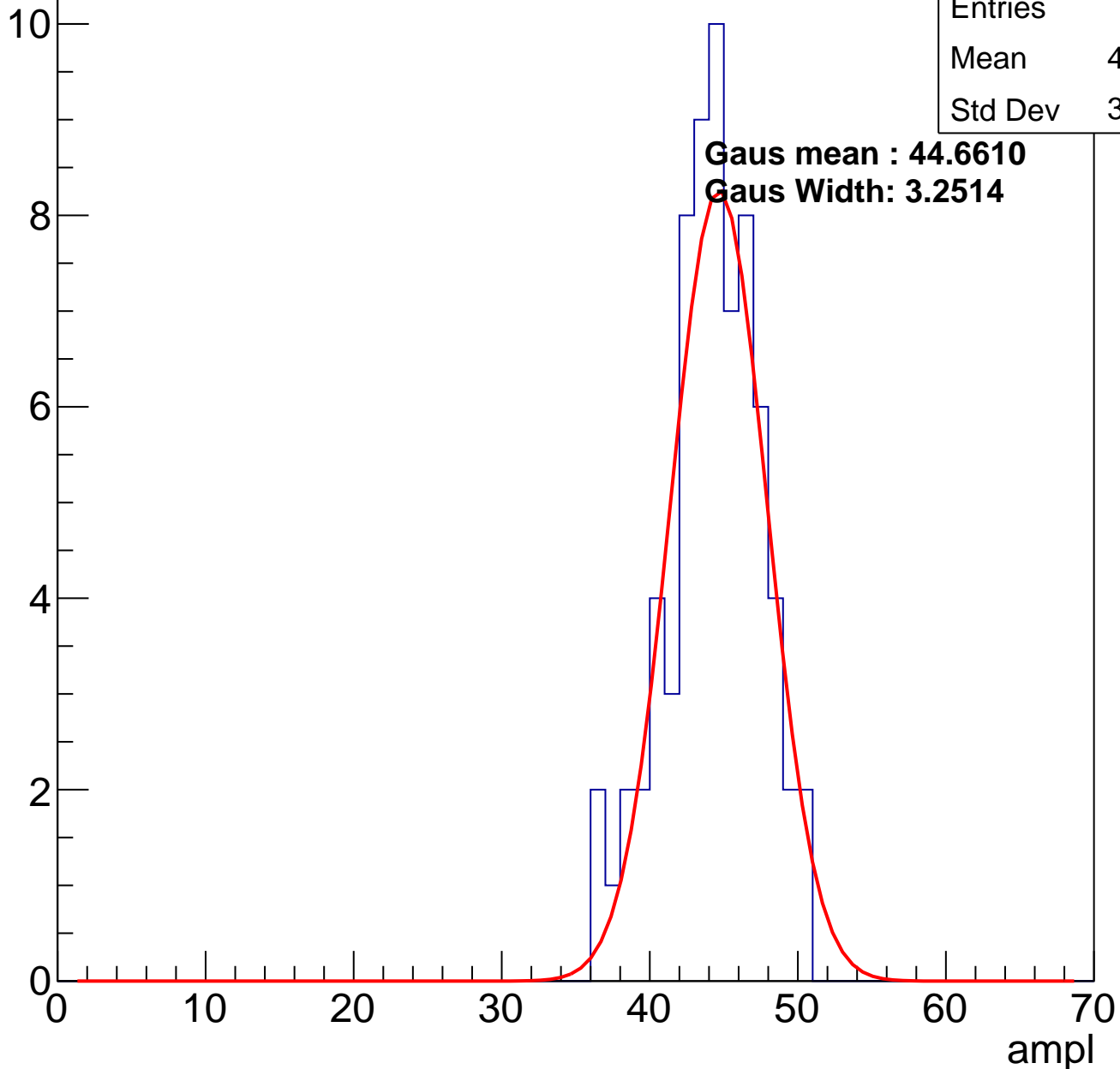
calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	43.77
Std Dev	3.177

Gaus mean : 44.6610

Gaus Width: 3.2514

Entry

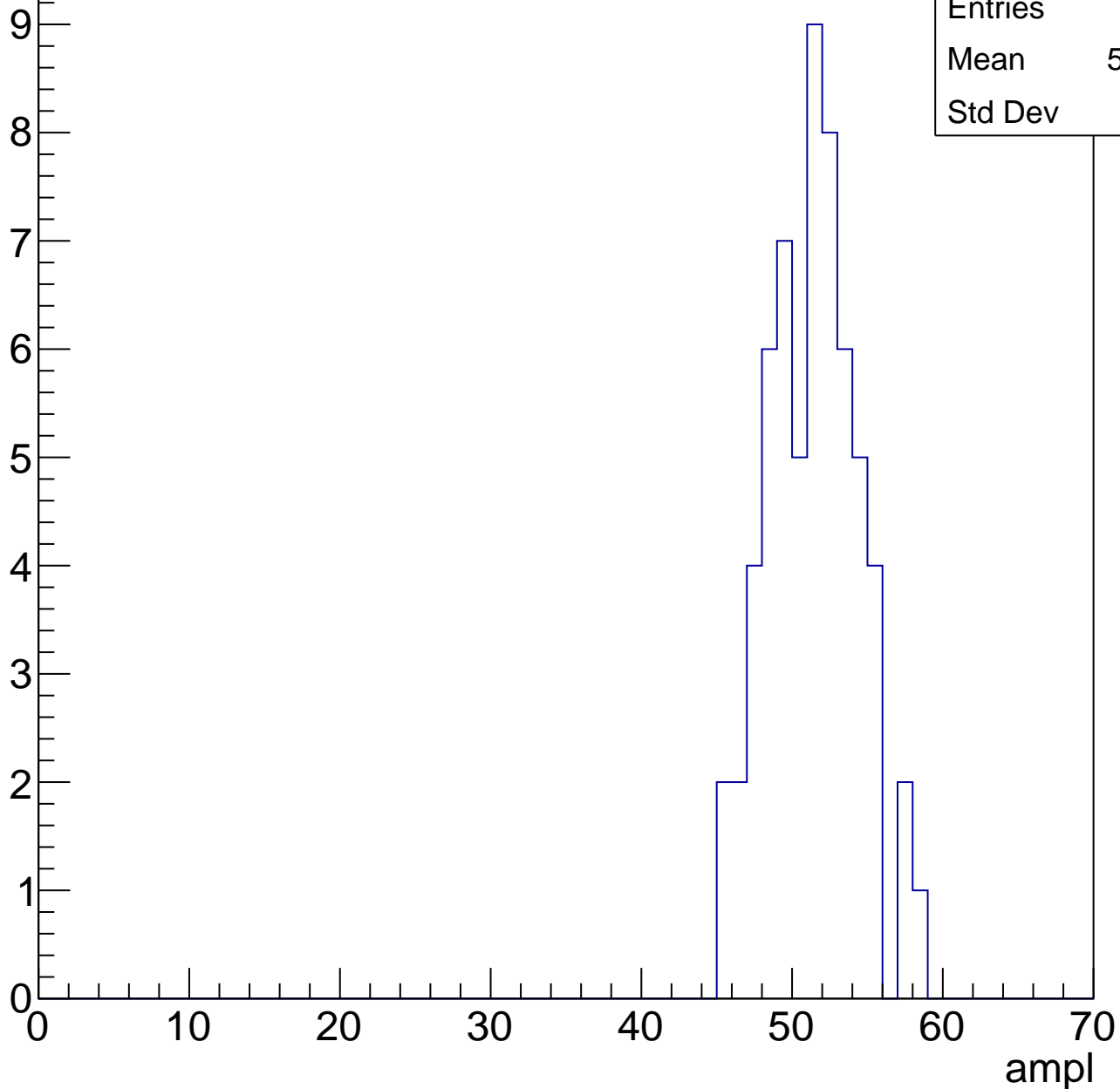


B0L001S, U19-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.92
Std Dev	2.96



B0L001S, U19-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

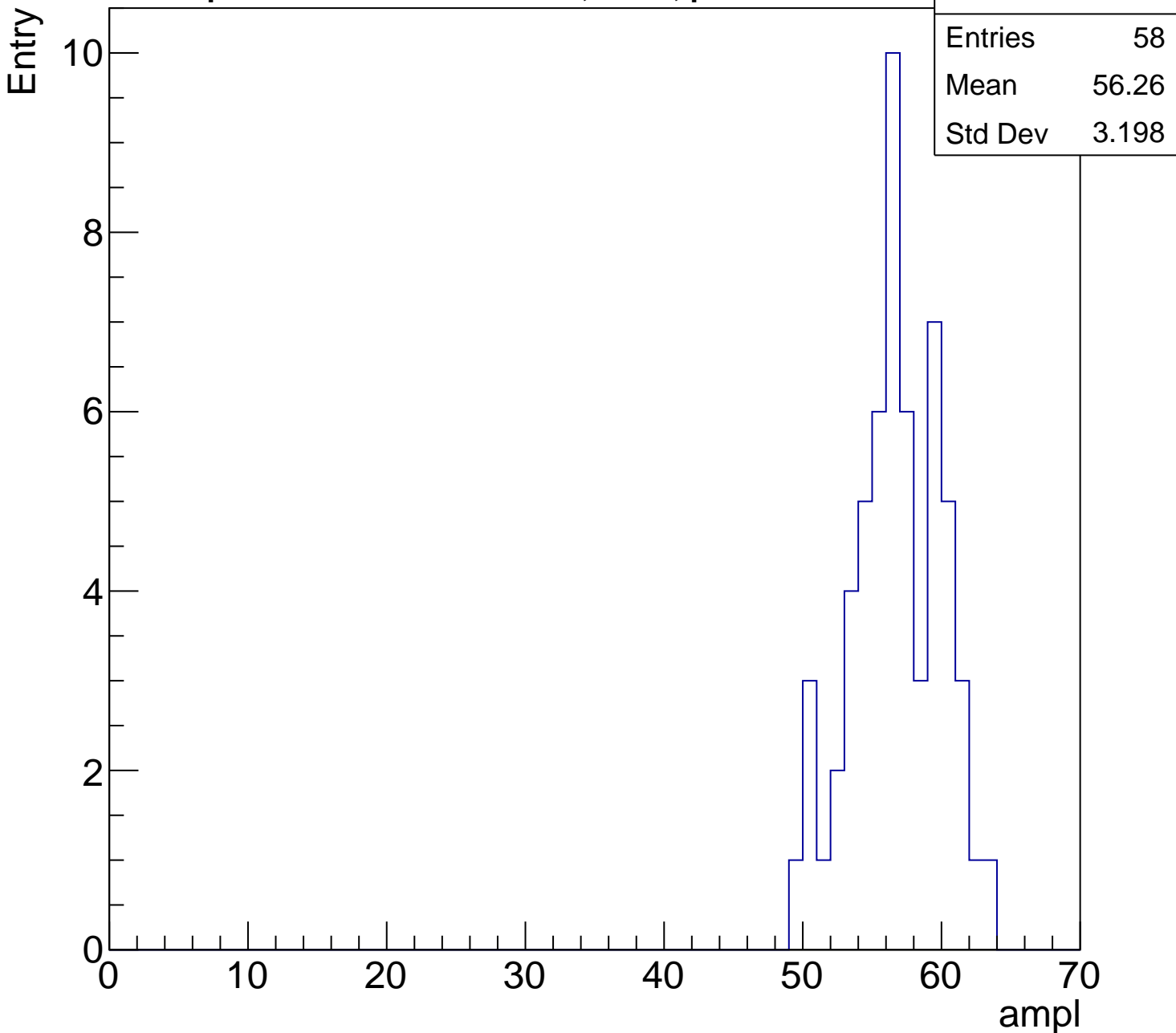
Entries	58
Mean	56.26
Std Dev	3.198

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

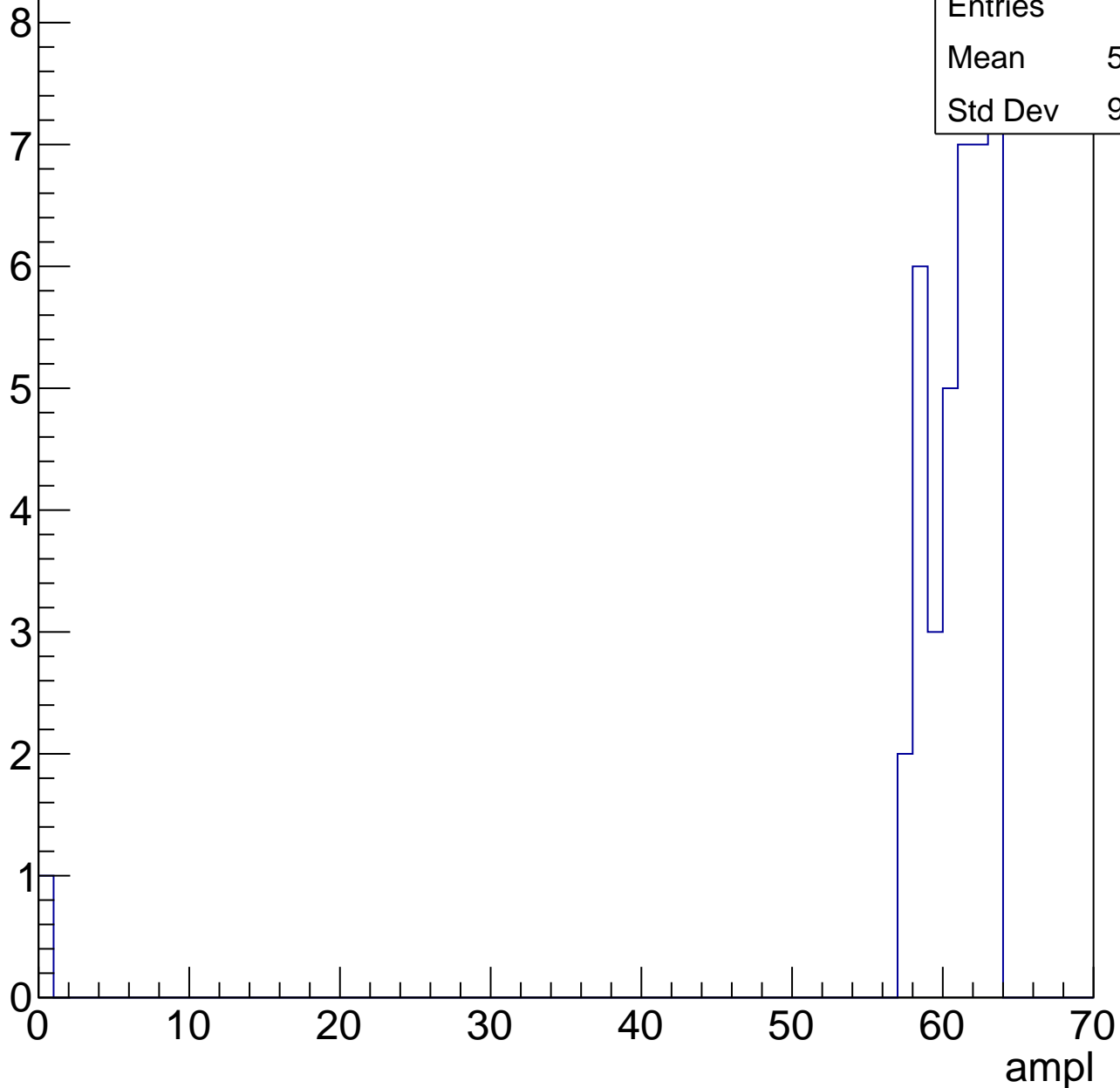


B0L001S, U19-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	59.08
Std Dev	9.765



B0L001S, U19-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B0L001S, U19-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch57, adc0

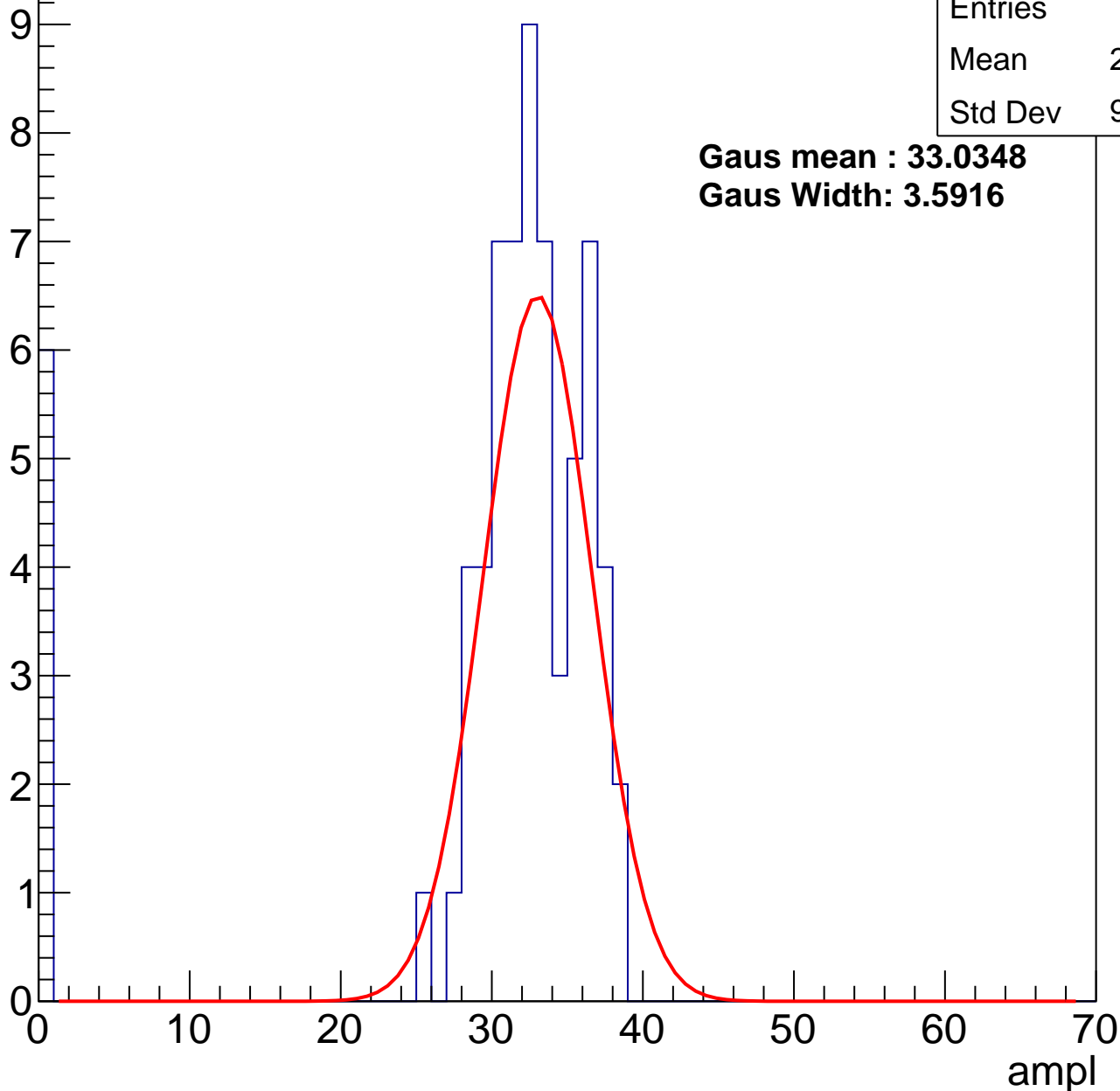
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	29.54
Std Dev	9.693

Gaus mean : 33.0348

Gaus Width: 3.5916



B0L001S, U19-ch57, adc1

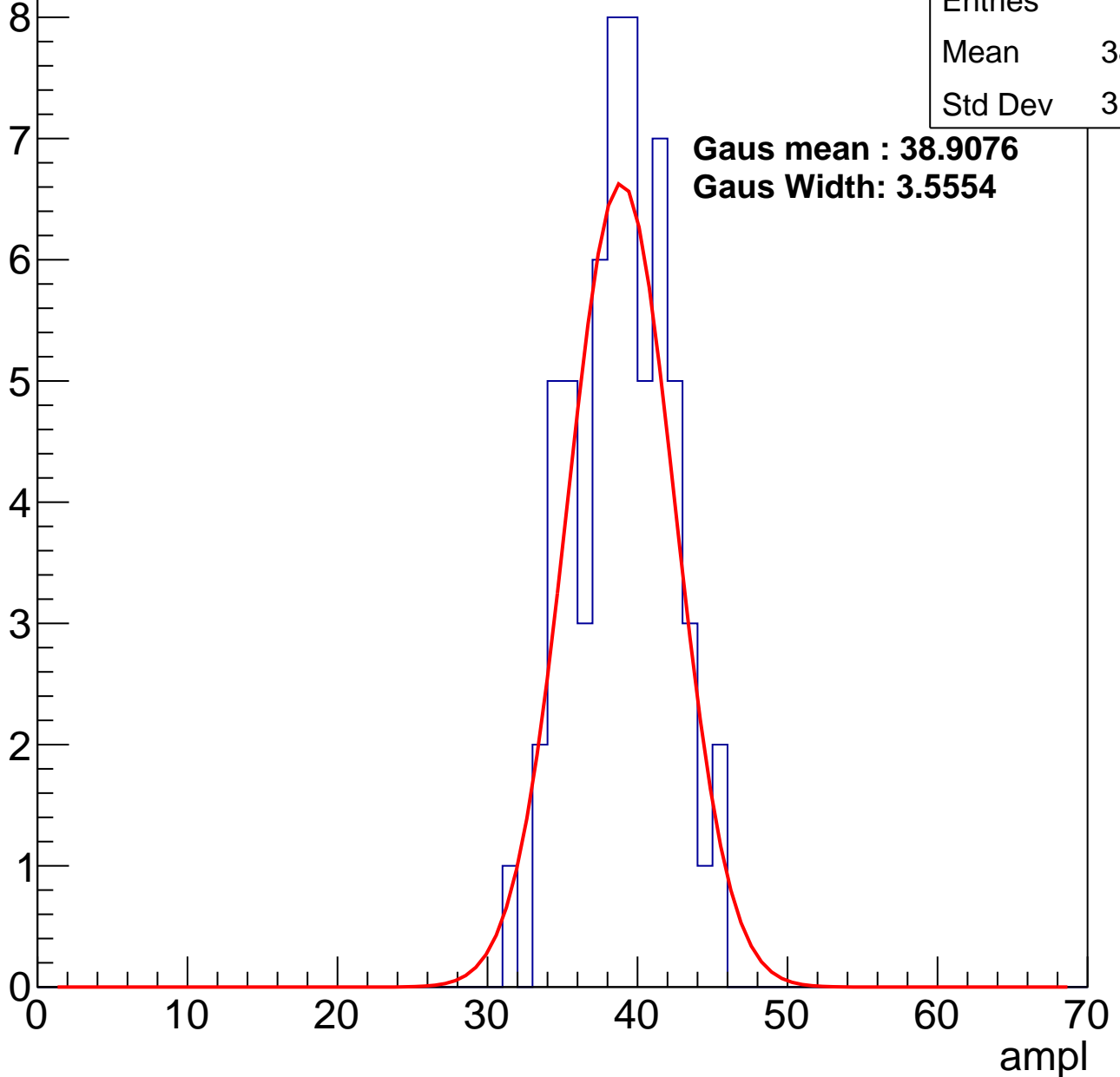
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	38.49
Std Dev	3.155

Gaus mean : 38.9076

Gaus Width: 3.5554



B0L001S, U19-ch57, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

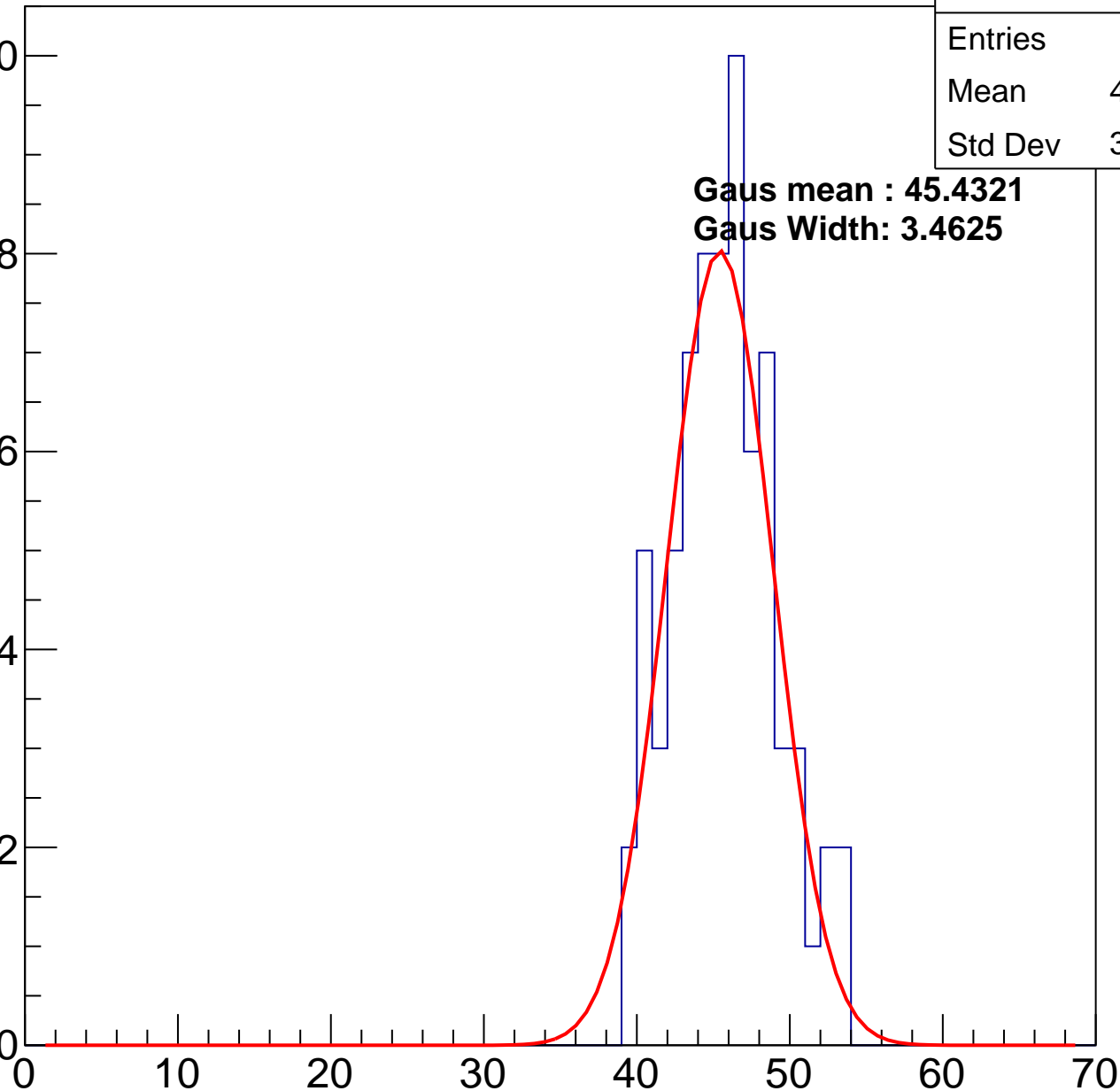
Entries	72
Mean	45.28
Std Dev	3.338

Gaus mean : 45.4321

Gaus Width: 3.4625

10
8
6
4
2
0

ampl

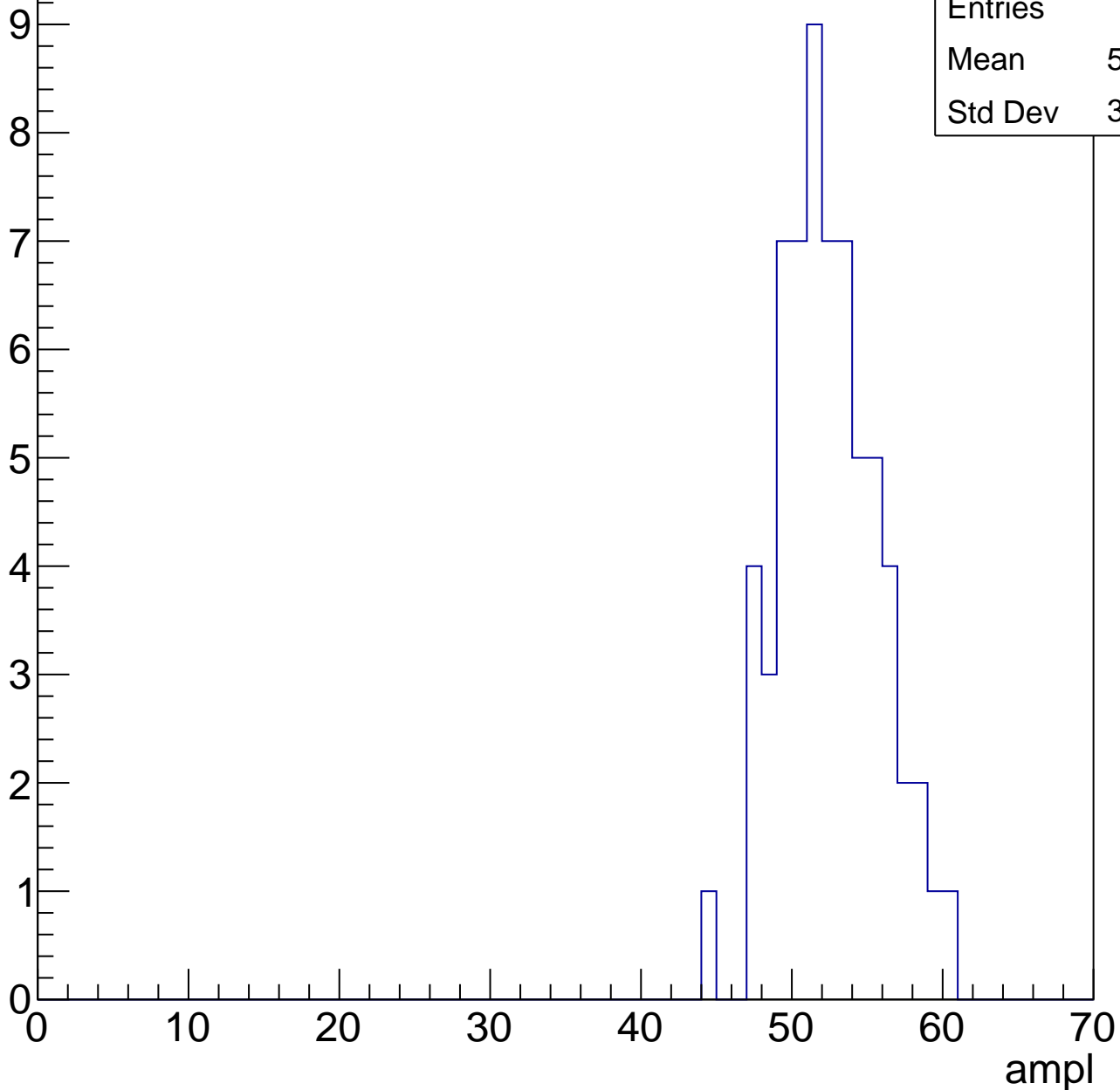


B0L001S, U19-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

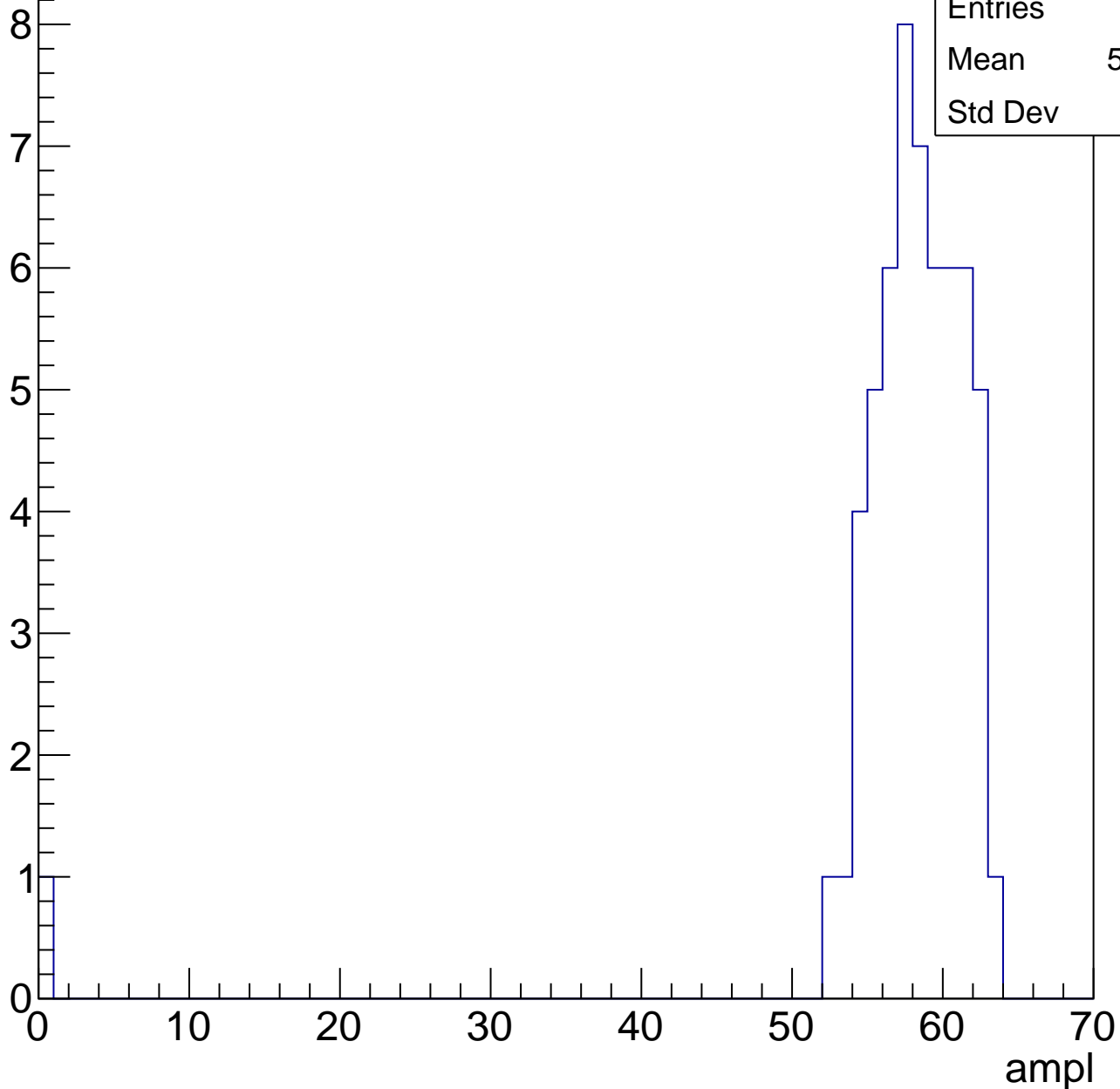
Entries	65
Mean	52.02
Std Dev	3.242



B0L001S, U19-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

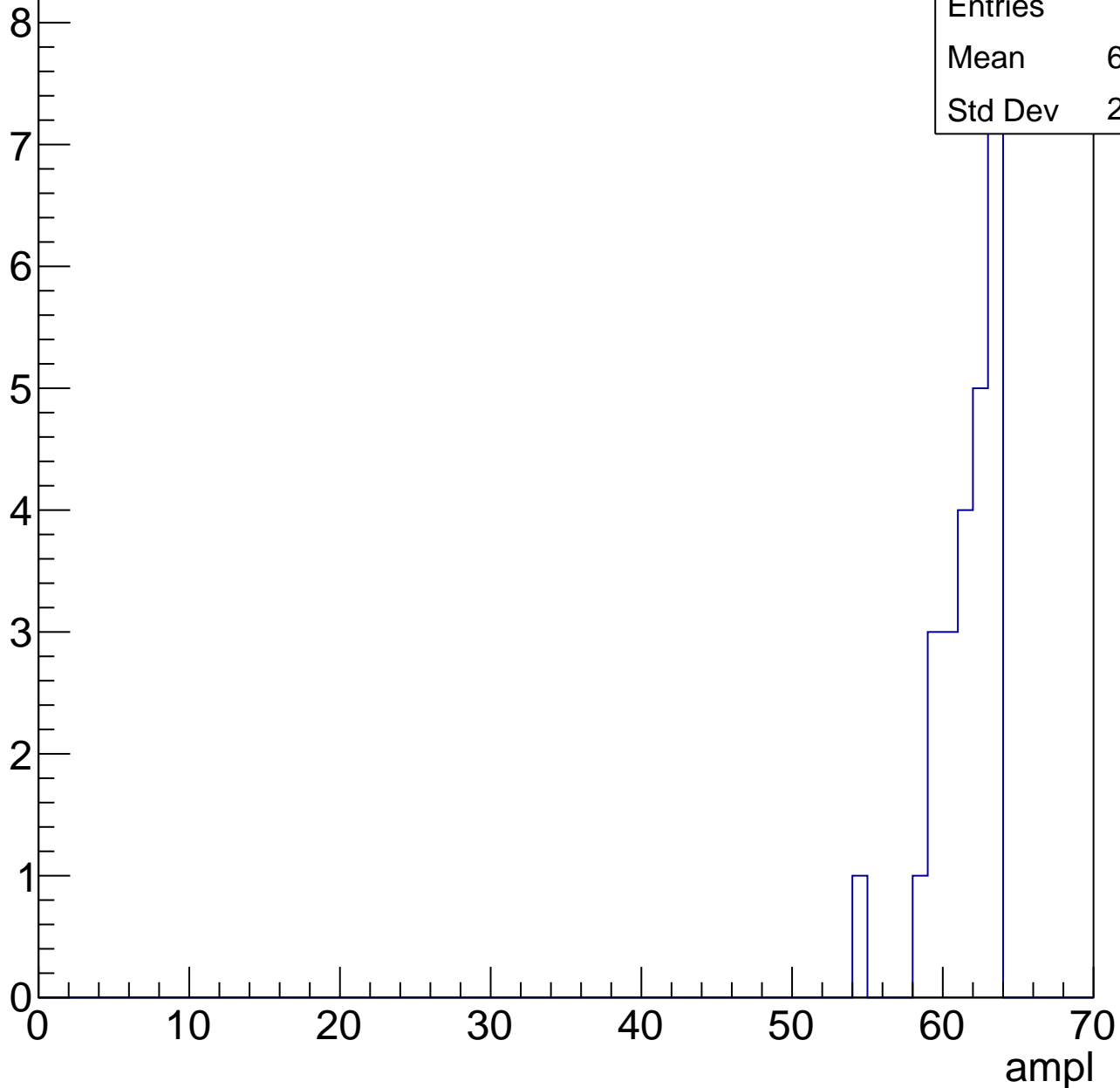


B0L001S, U19-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	61.08
Std Dev	2.096



B0L001S, U19-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch58, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	28.59
Std Dev	4.496

Gaus mean : 29.3746

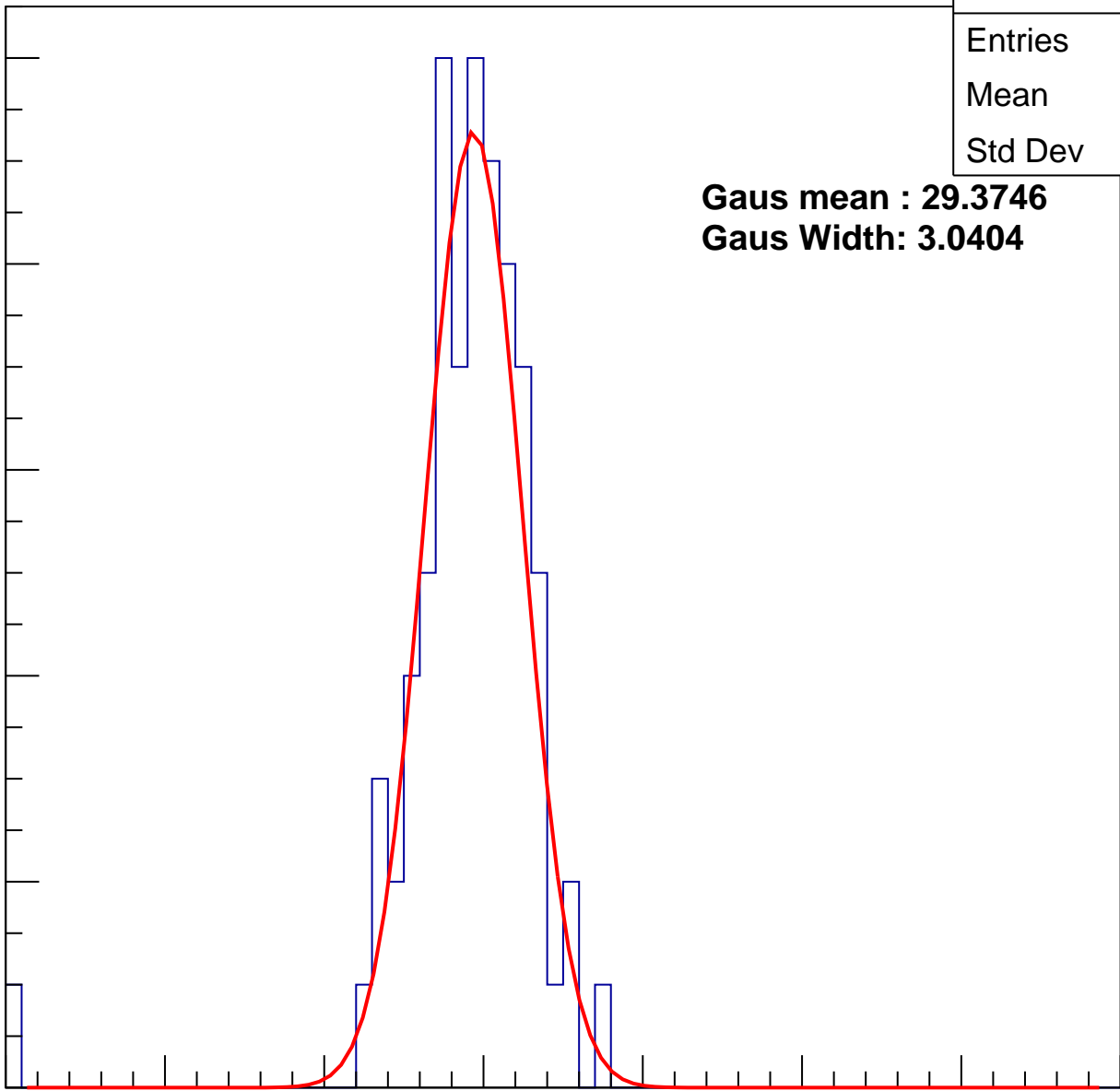
Gaus Width: 3.0404

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



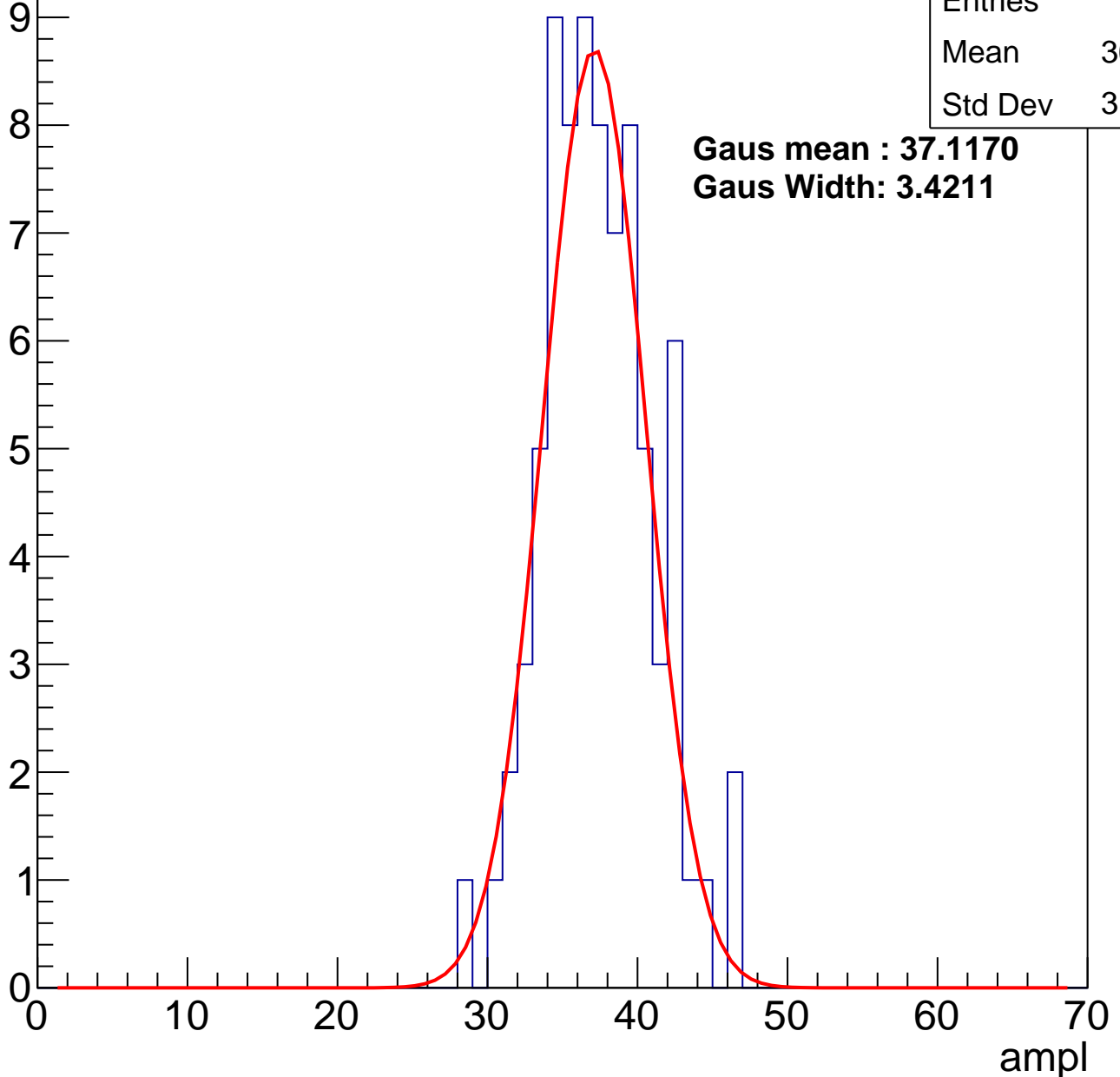
B0L001S, U19-ch58, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	36.95
Std Dev	3.572

Gaus mean : 37.1170
Gaus Width: 3.4211



B0L001S, U19-ch58, adc2

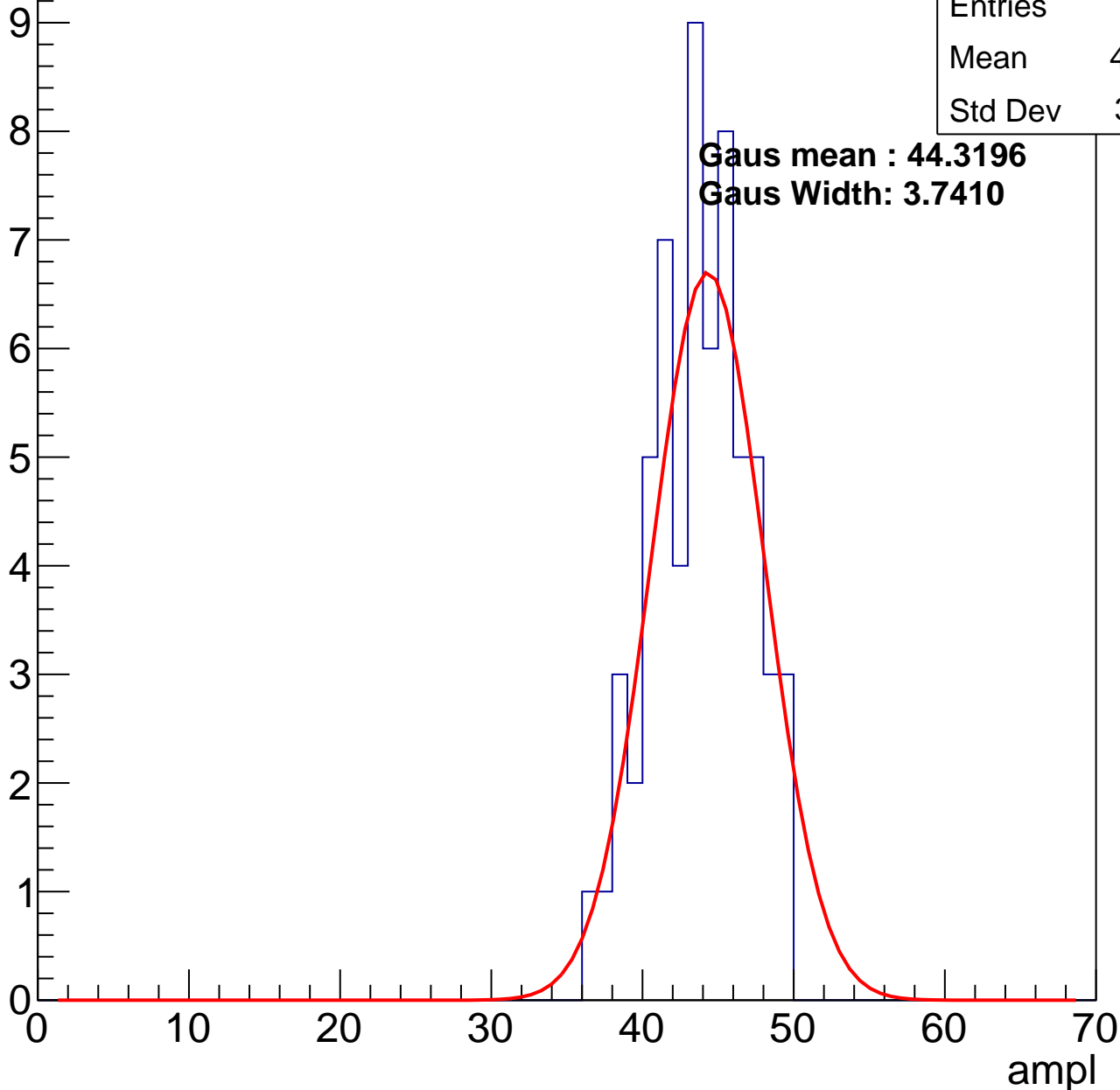
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.34
Std Dev	3.131

Gaus mean : 44.3196

Gaus Width: 3.7410

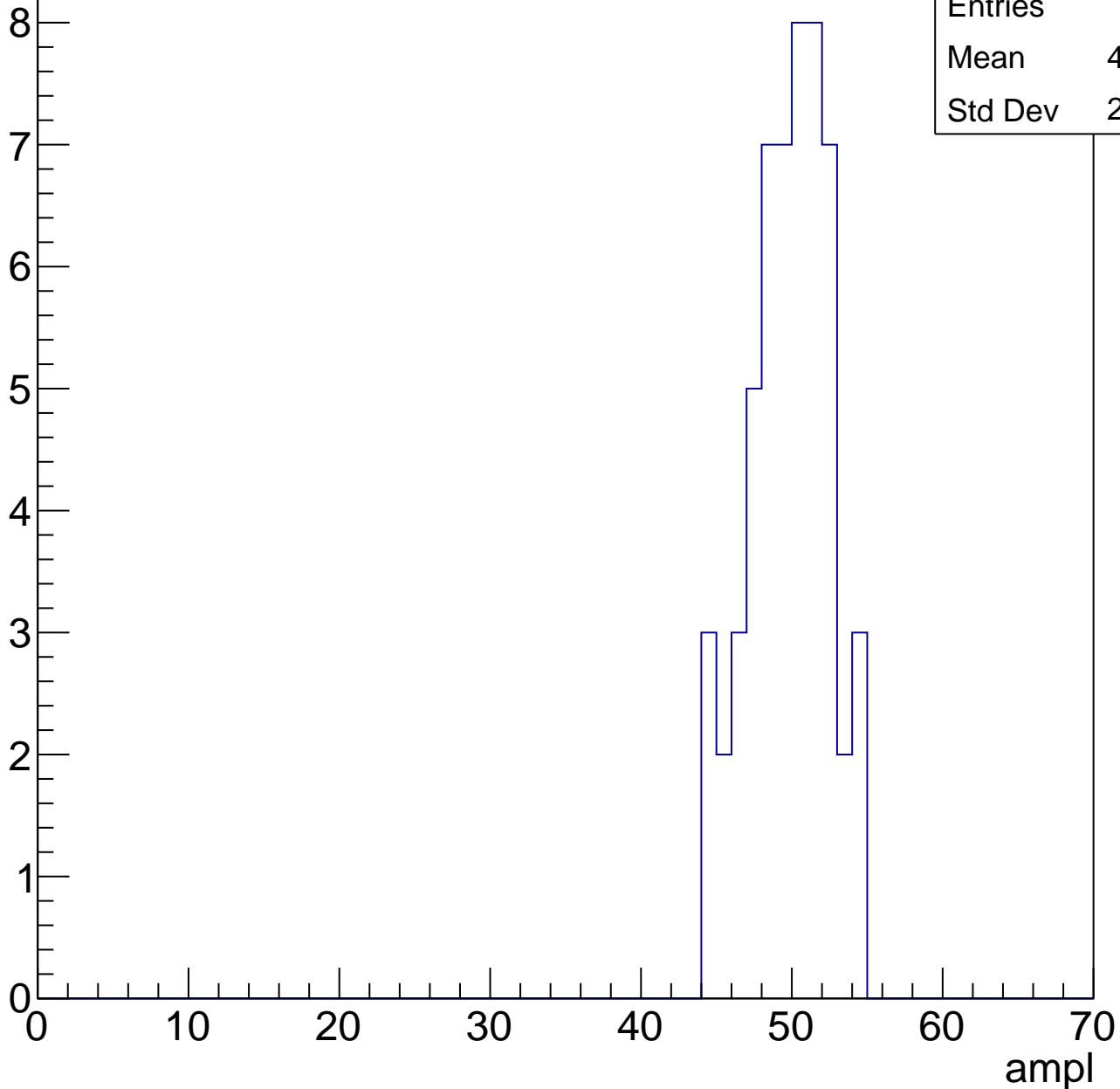


B0L001S, U19-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

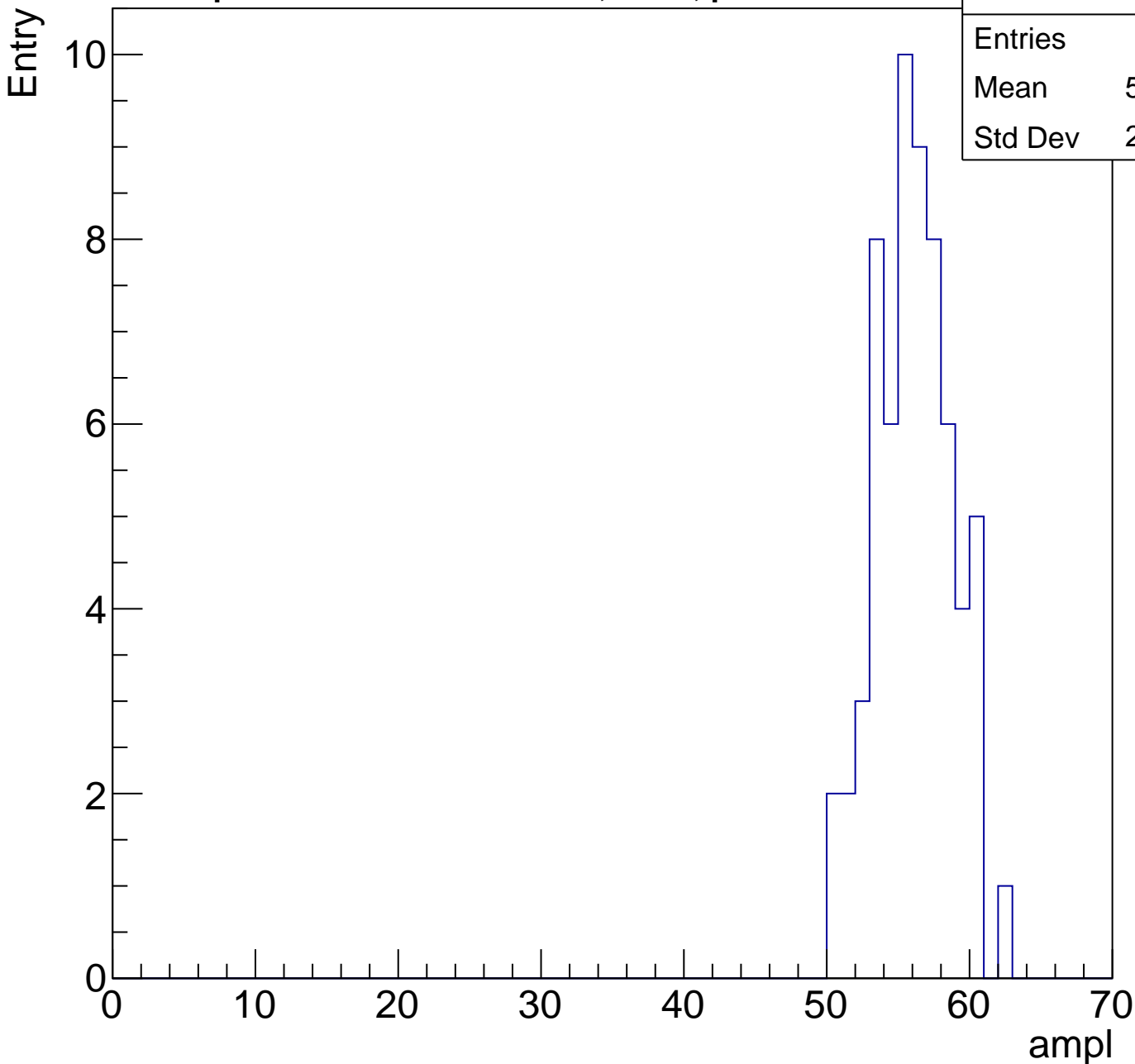
Entries	55
Mean	49.35
Std Dev	2.574



B0L001S, U19-ch58, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	55.66
Std Dev	2.659

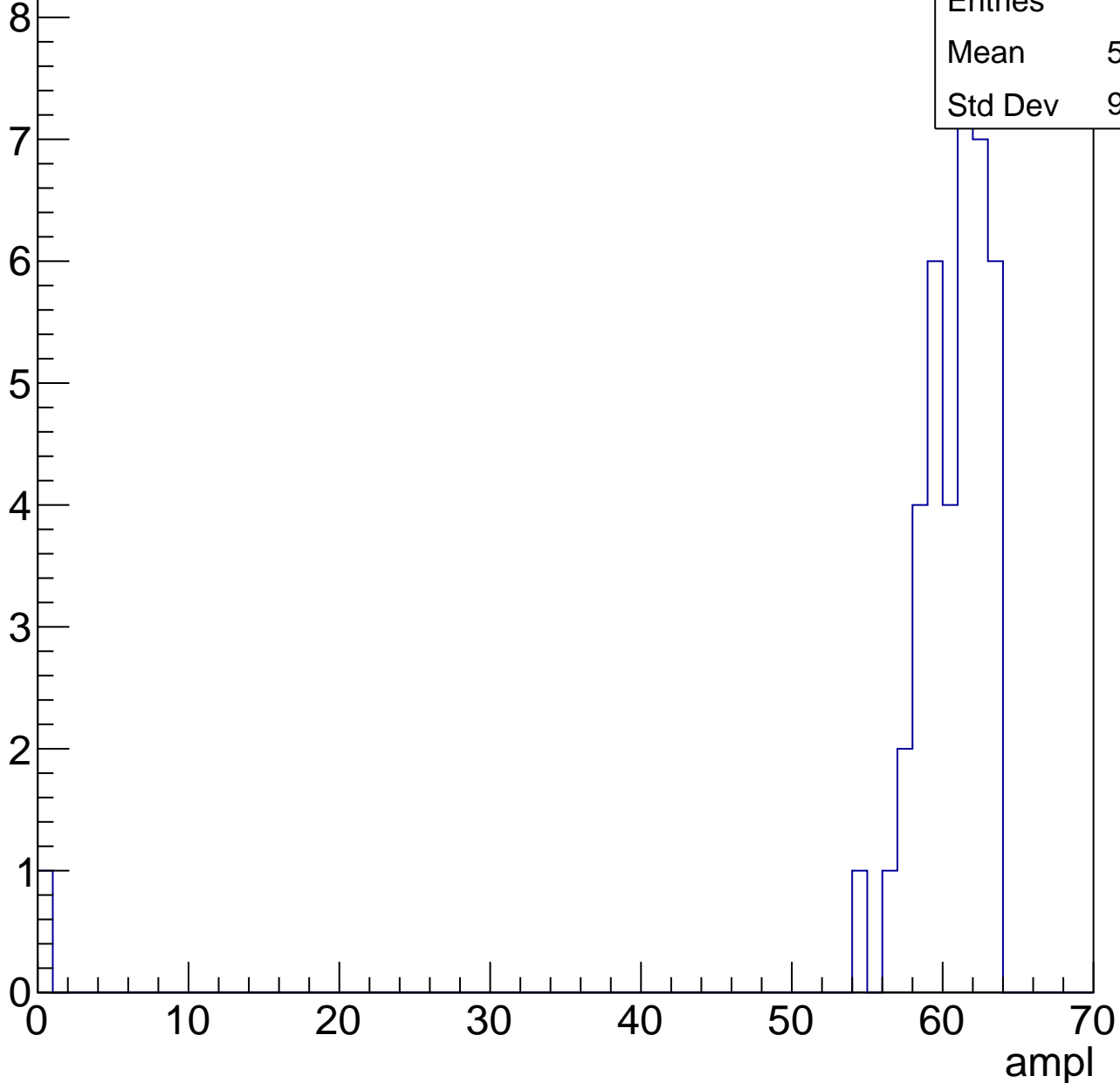


B0L001S, U19-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

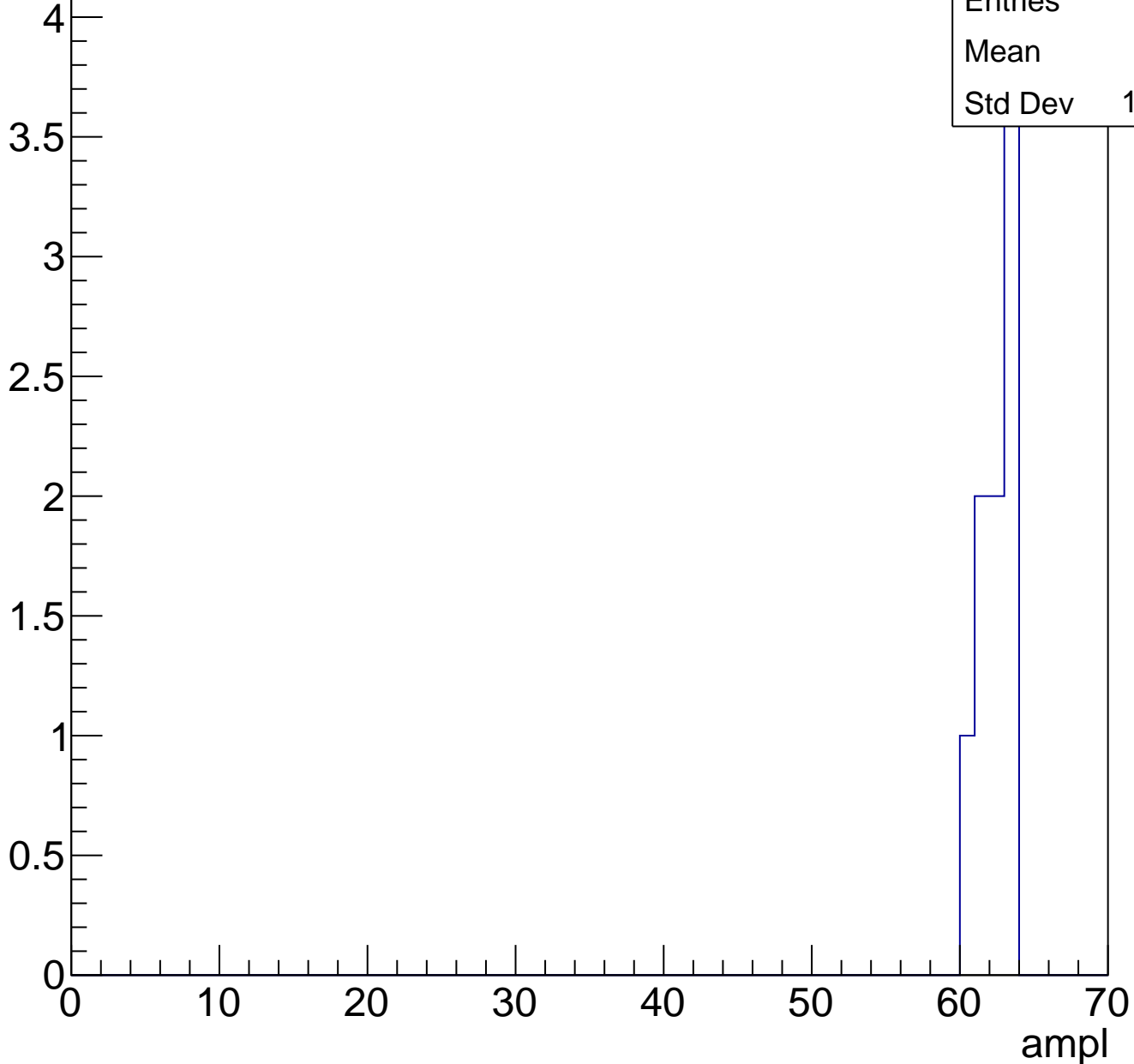
Entries	40
Mean	58.75
Std Dev	9.643



B0L001S, U19-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch59, adc0

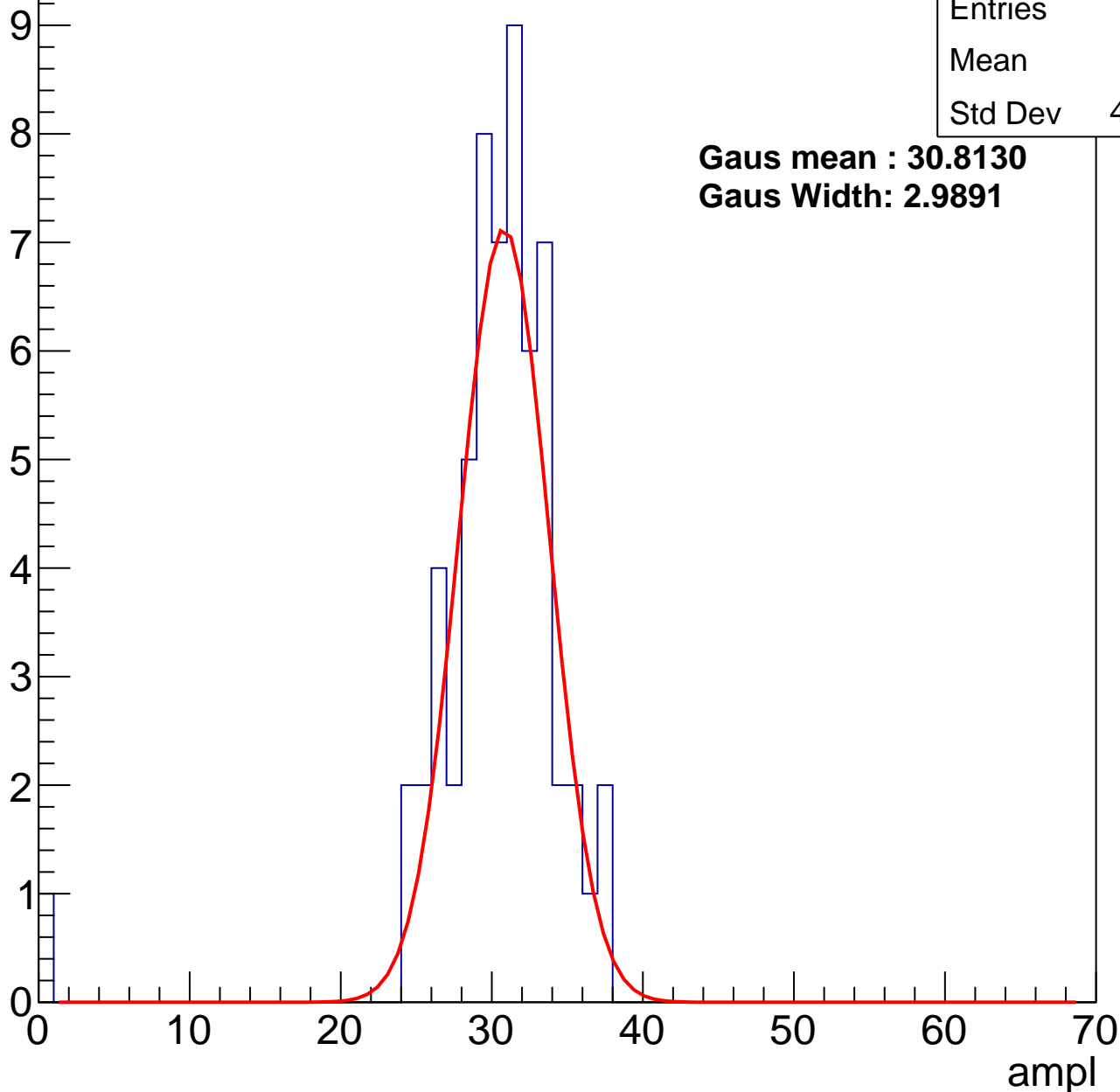
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.8
Std Dev	4.902

Gaus mean : 30.8130

Gaus Width: 2.9891

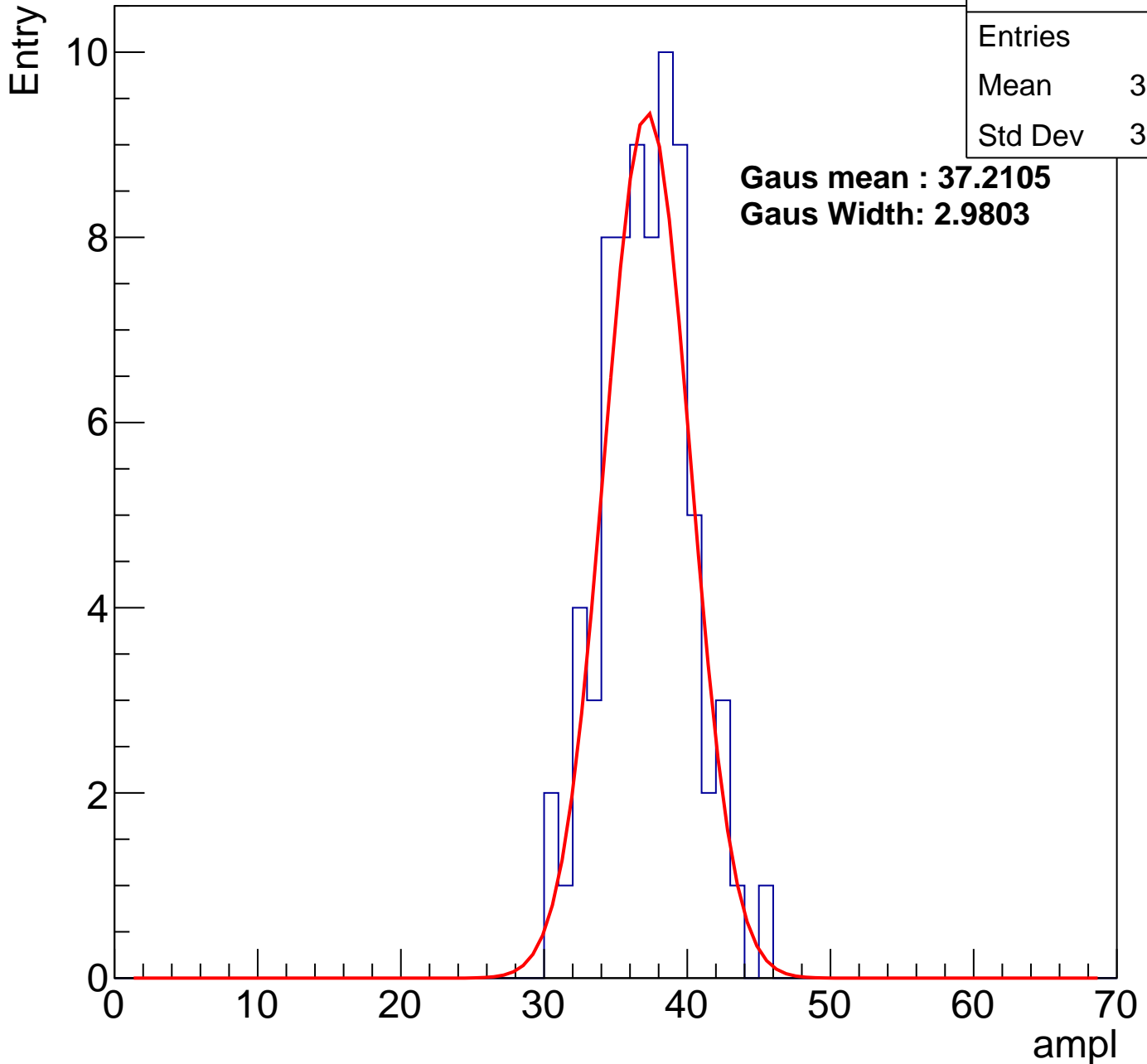


B0L001S, U19-ch59, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	36.72
Std Dev	3.047

Gaus mean : 37.2105
Gaus Width: 2.9803



B0L001S, U19-ch59, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.98
Std Dev	2.85

Gaus mean : 44.4504

Gaus Width: 2.8809

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

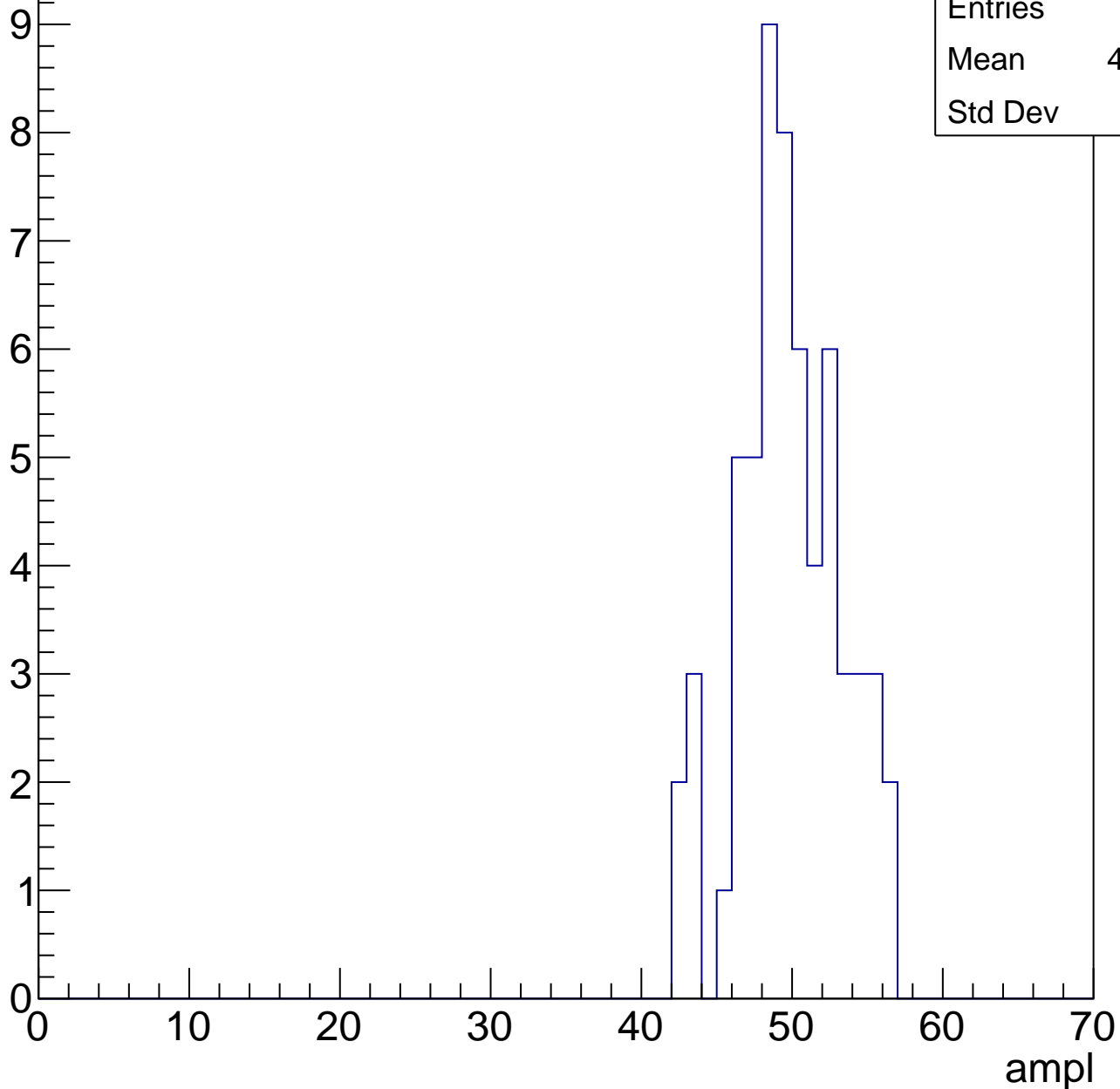
8

10

B0L001S, U19-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

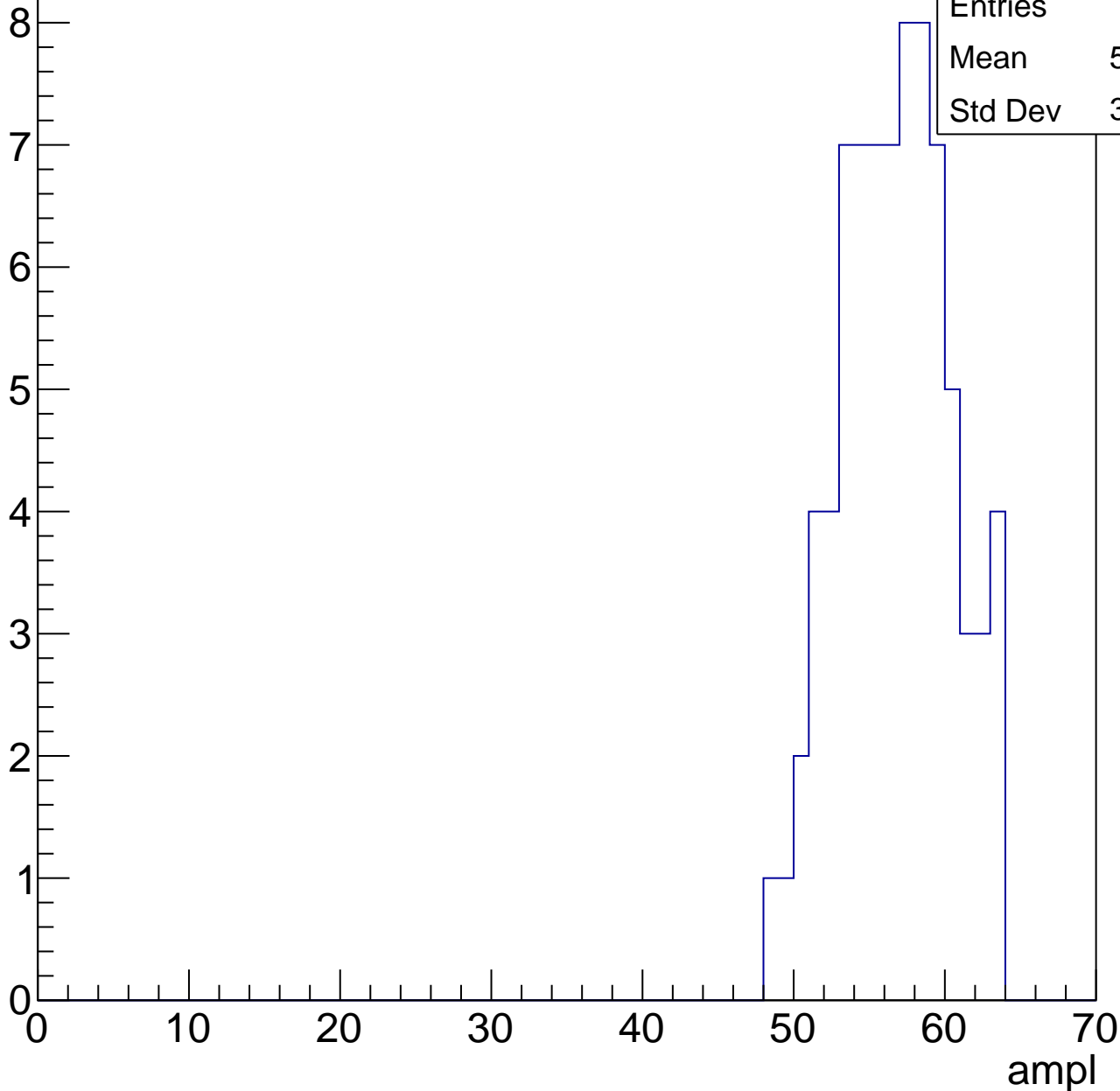


B0L001S, U19-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	56.27
Std Dev	3.587

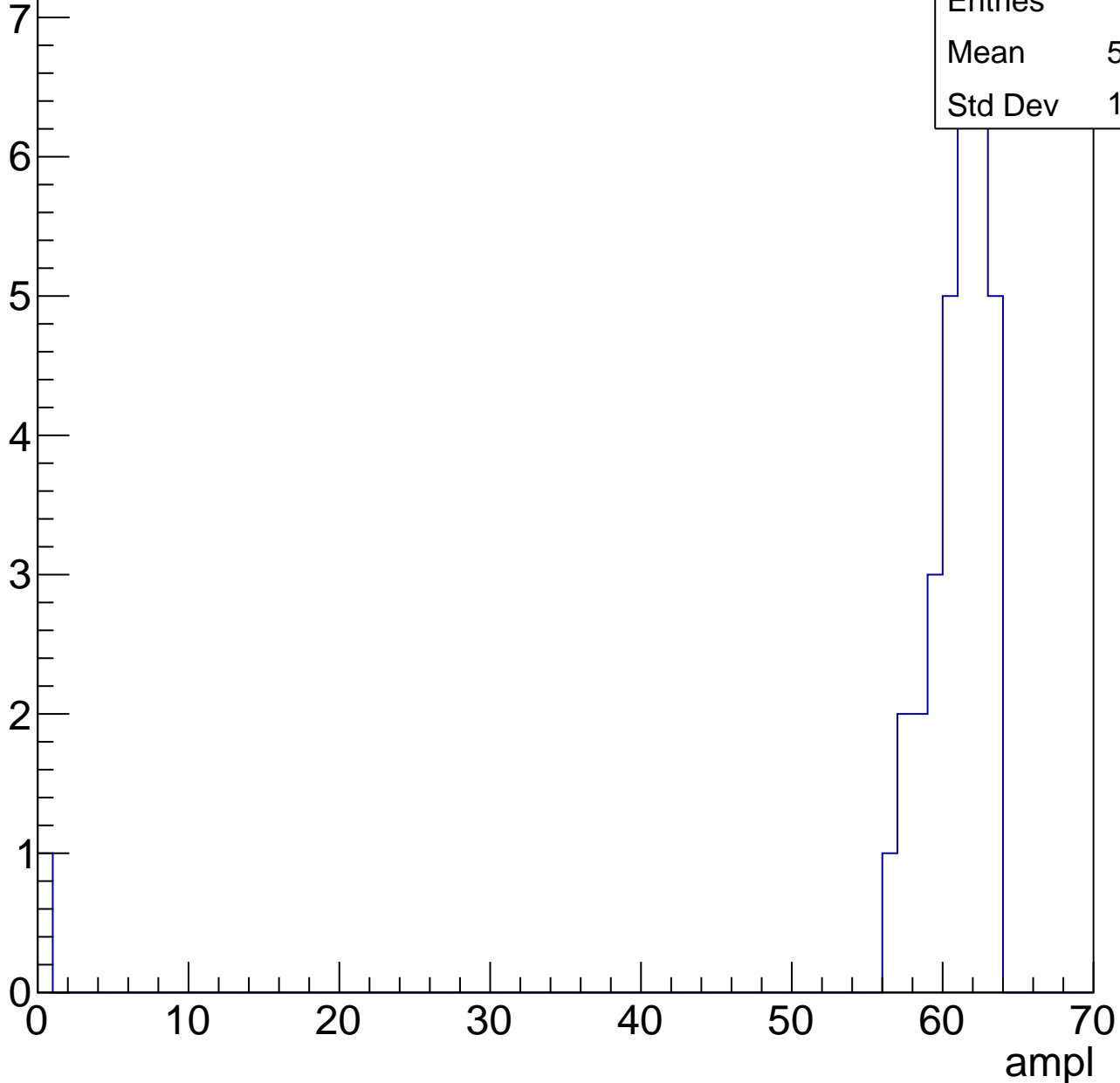


B0L001S, U19-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.76
Std Dev	10.55



B0L001S, U19-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch60, adc0

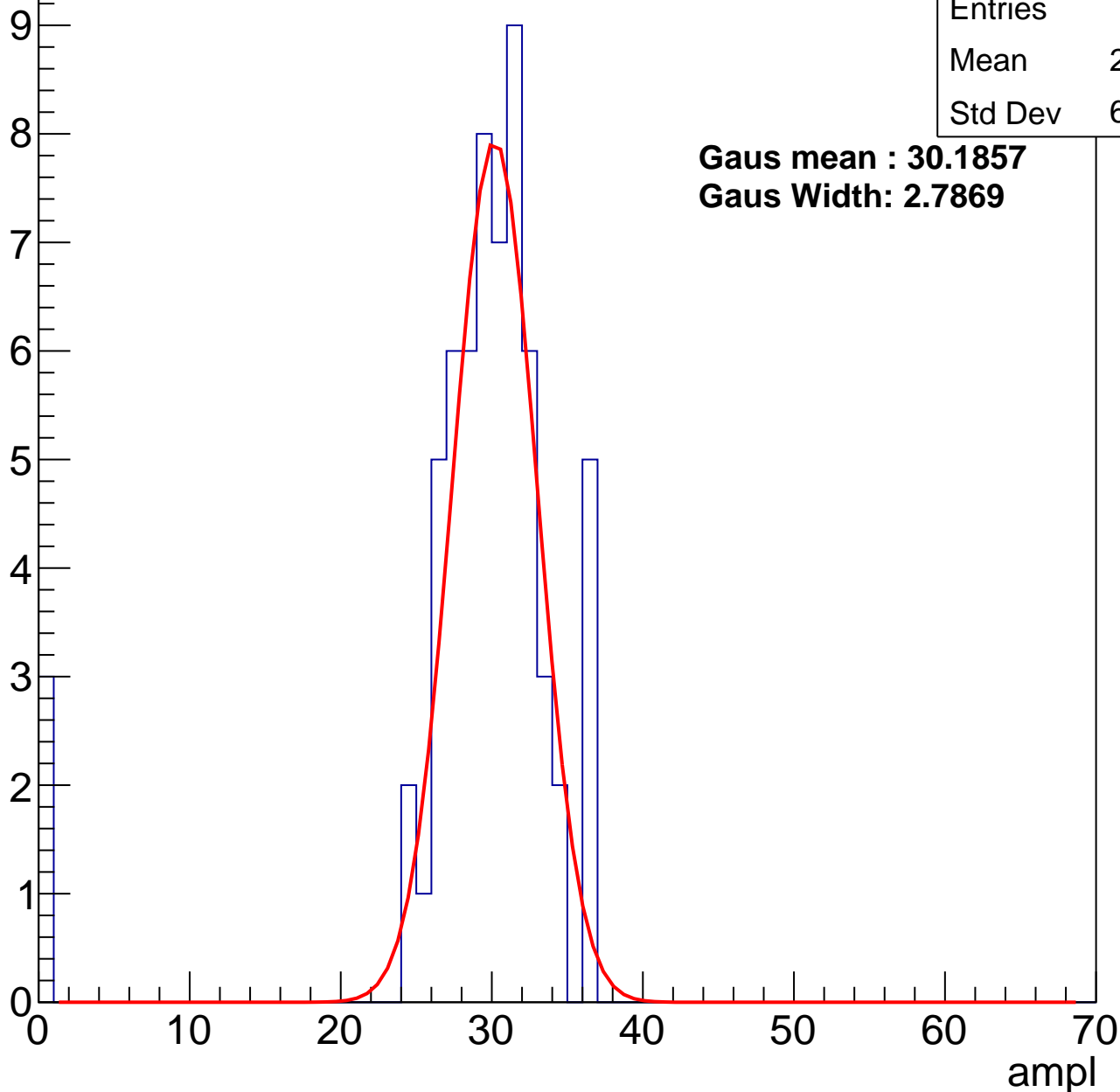
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	28.46
Std Dev	6.998

Gaus mean : 30.1857

Gaus Width: 2.7869



B0L001S, U19-ch60, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	36.74
Std Dev	3.356

Gaus mean : 36.5562

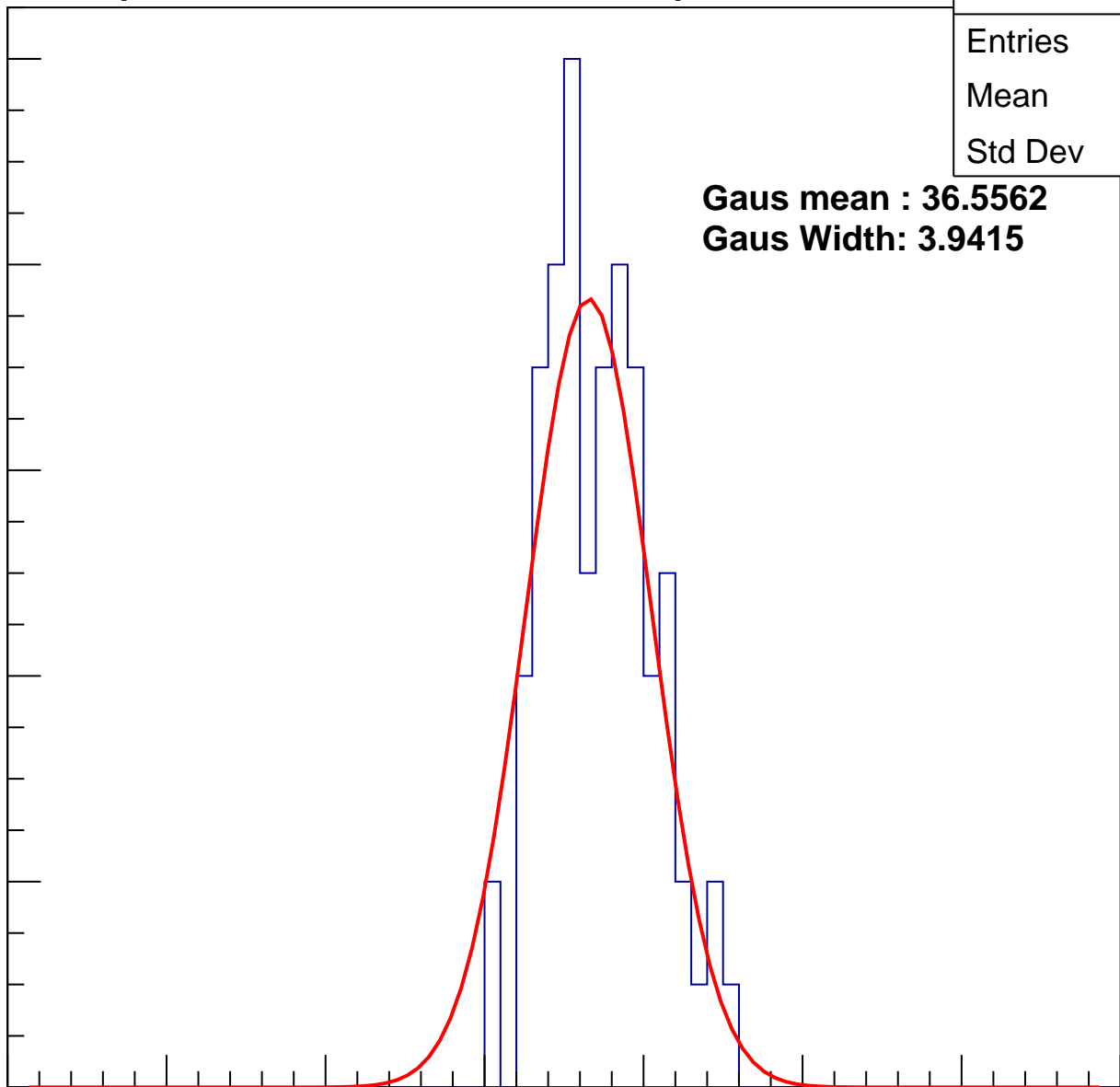
Gaus Width: 3.9415

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch60, adc2

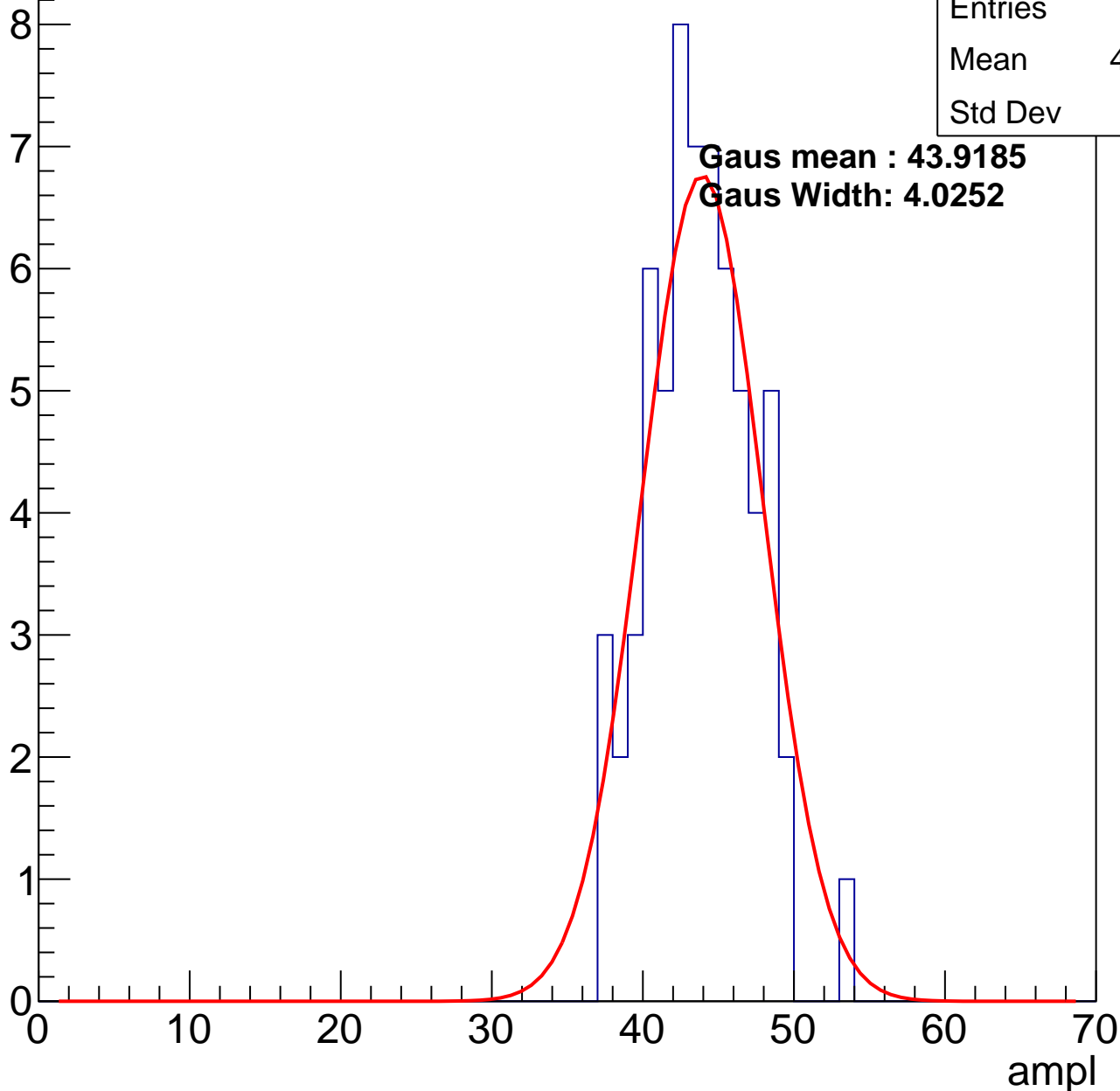
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43.33
Std Dev	3.35

Gaus mean : 43.9185

Gaus Width: 4.0252



B0L001S, U19-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	50.44
Std Dev	3.417

Entry

10

8

6

4

2

0

0

10

20

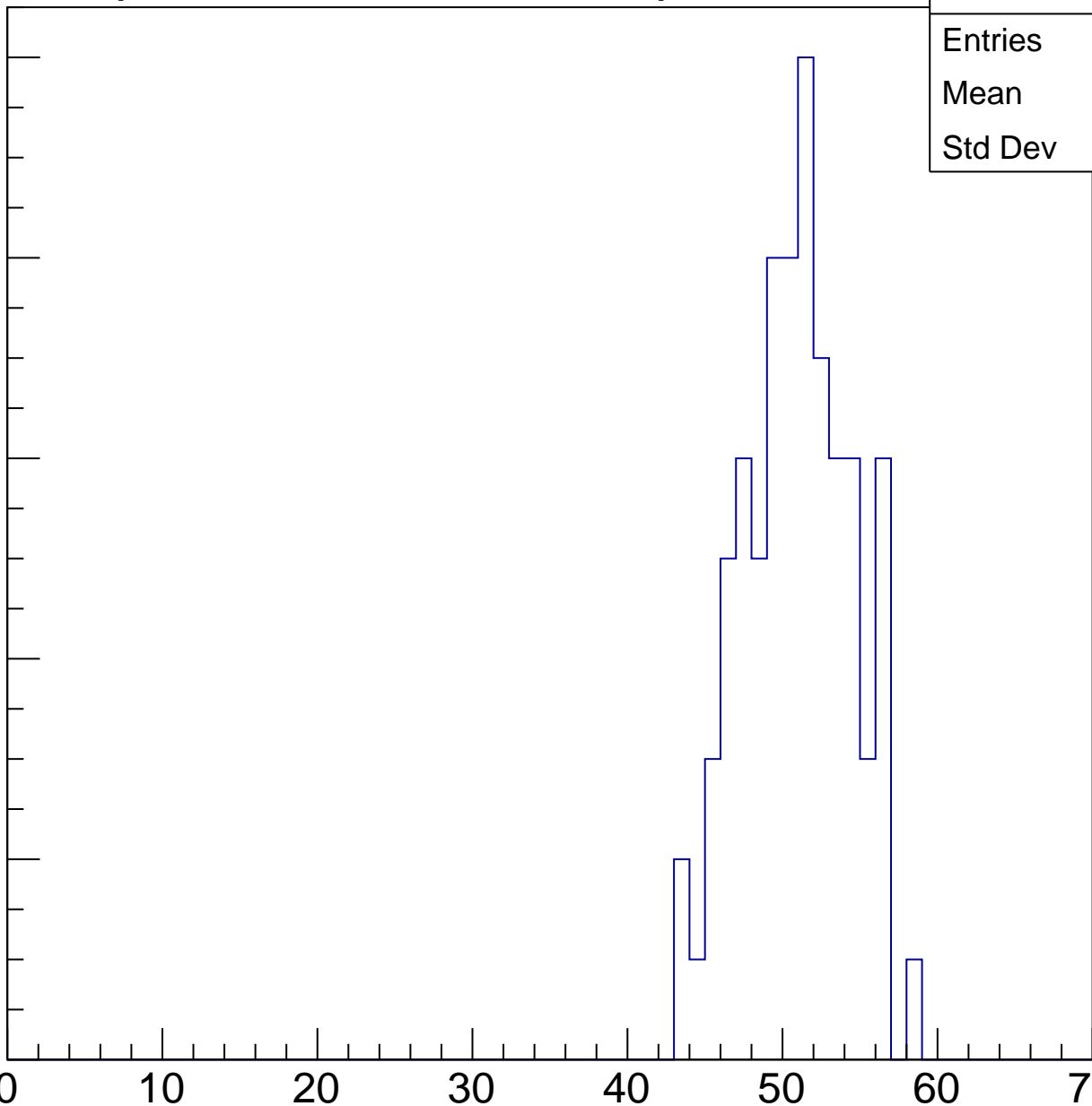
30

40

50

60

ampl

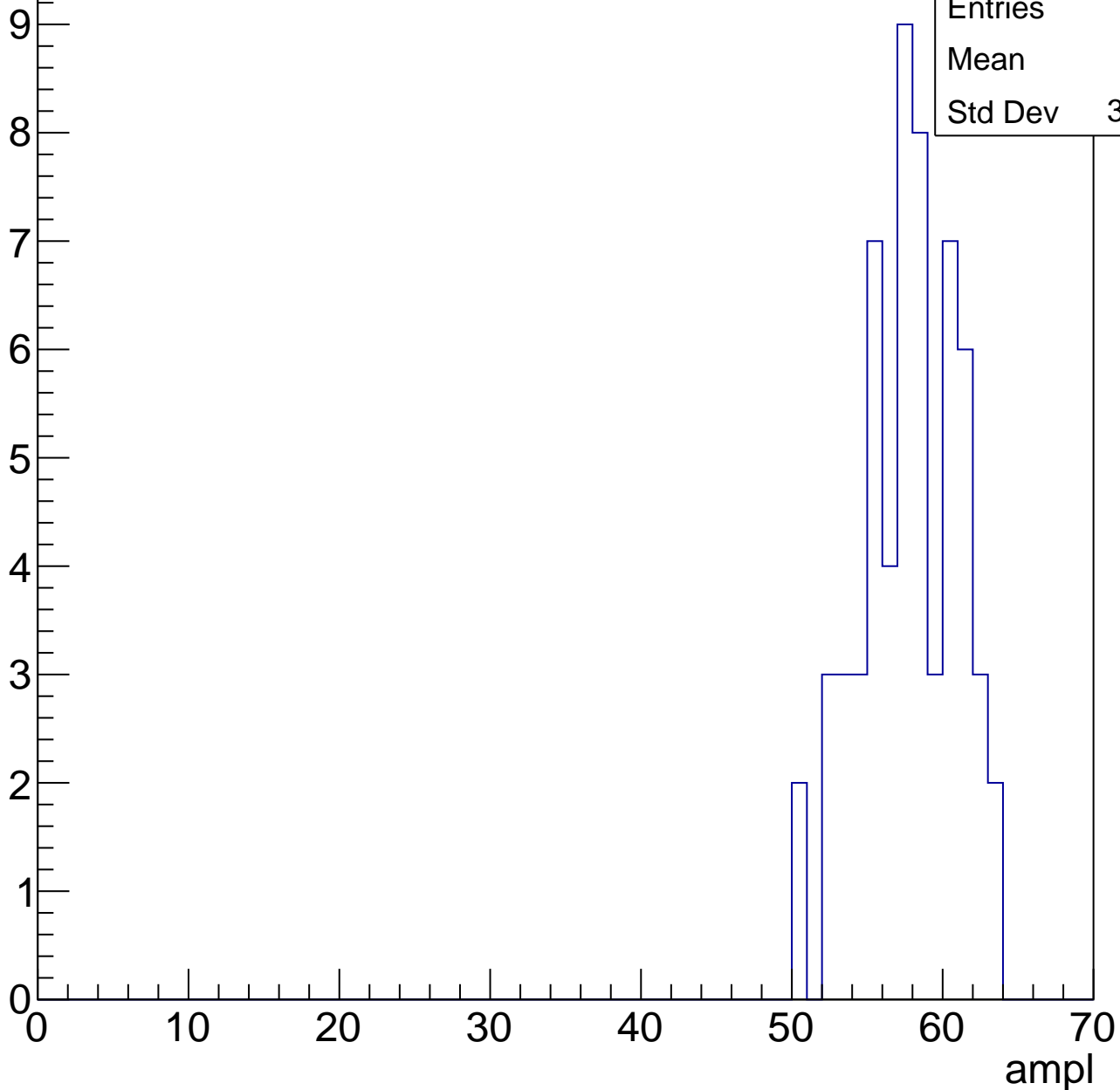


B0L001S, U19-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

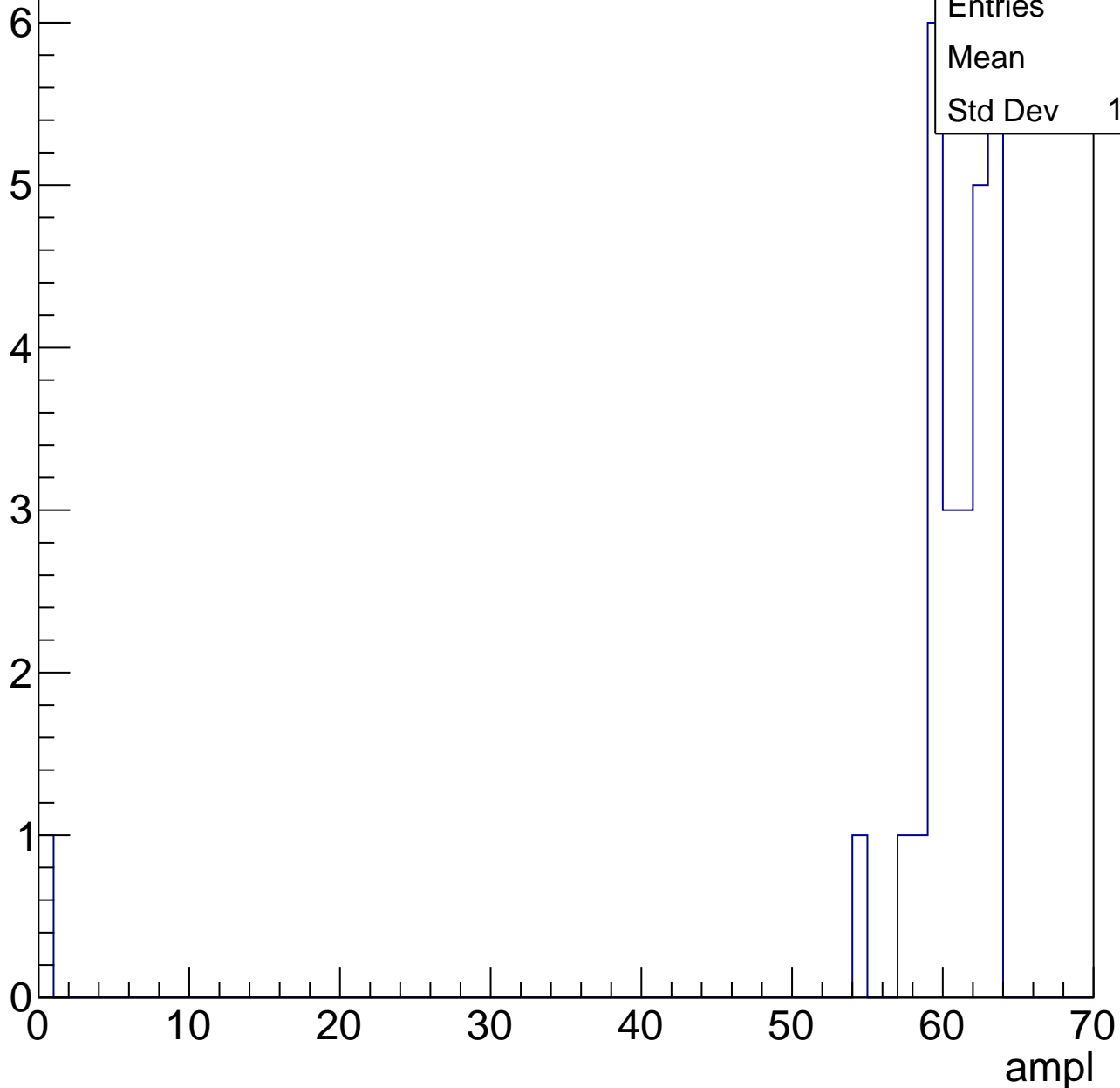
Entries	60
Mean	57.3
Std Dev	3.164



B0L001S, U19-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

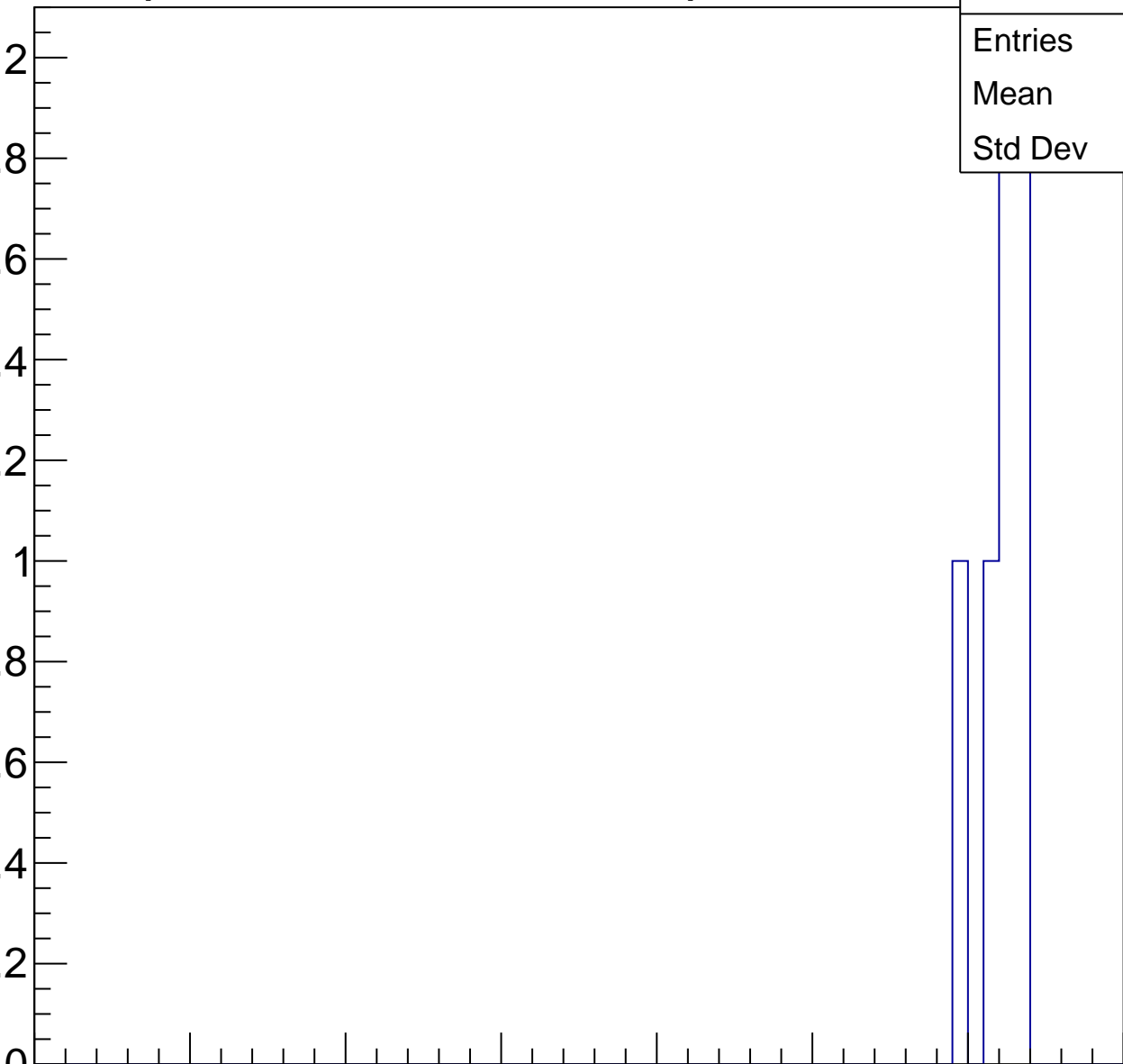
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.374

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch61, adc0

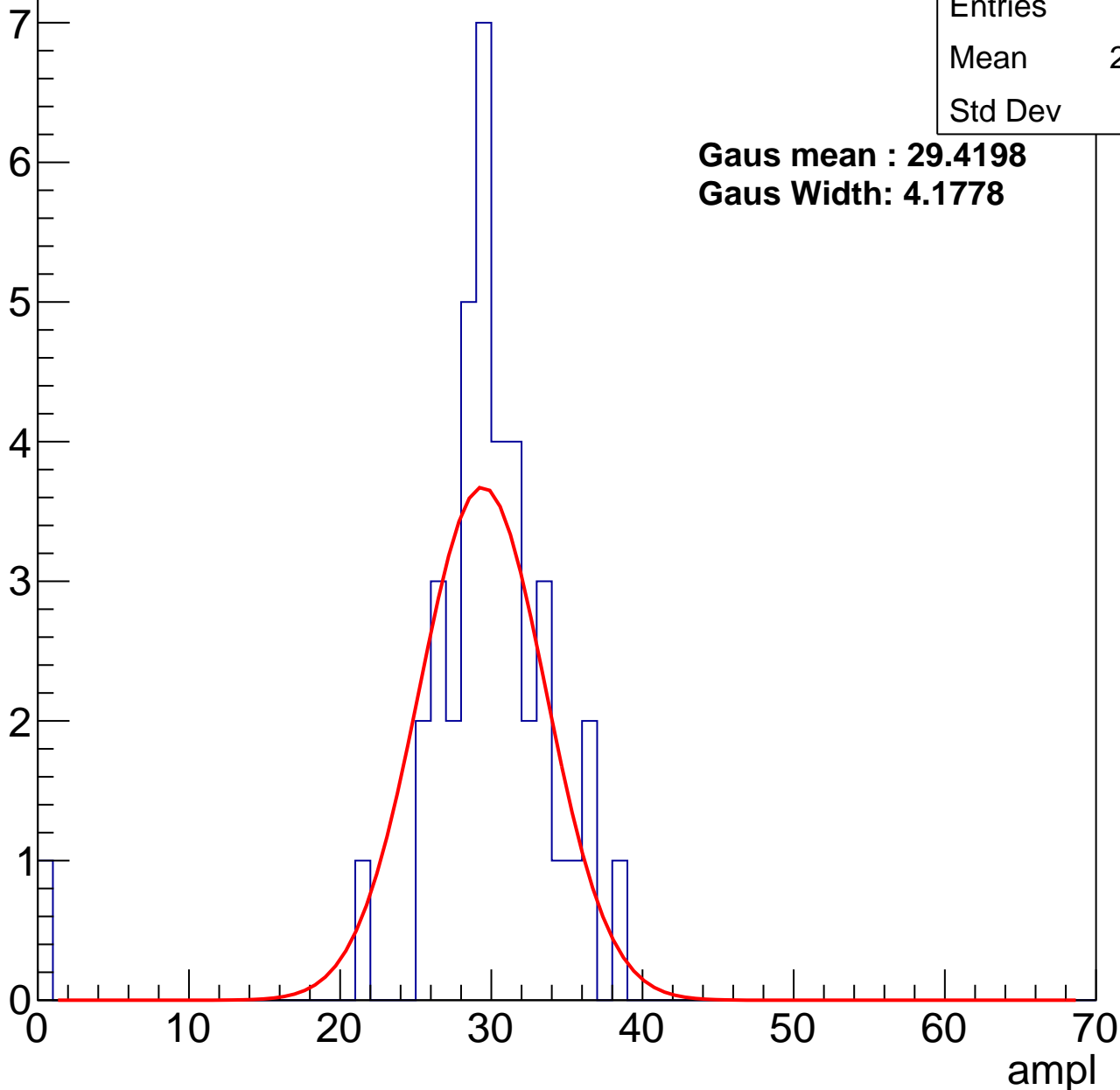
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	29.03
Std Dev	5.78

Gaus mean : 29.4198

Gaus Width: 4.1778

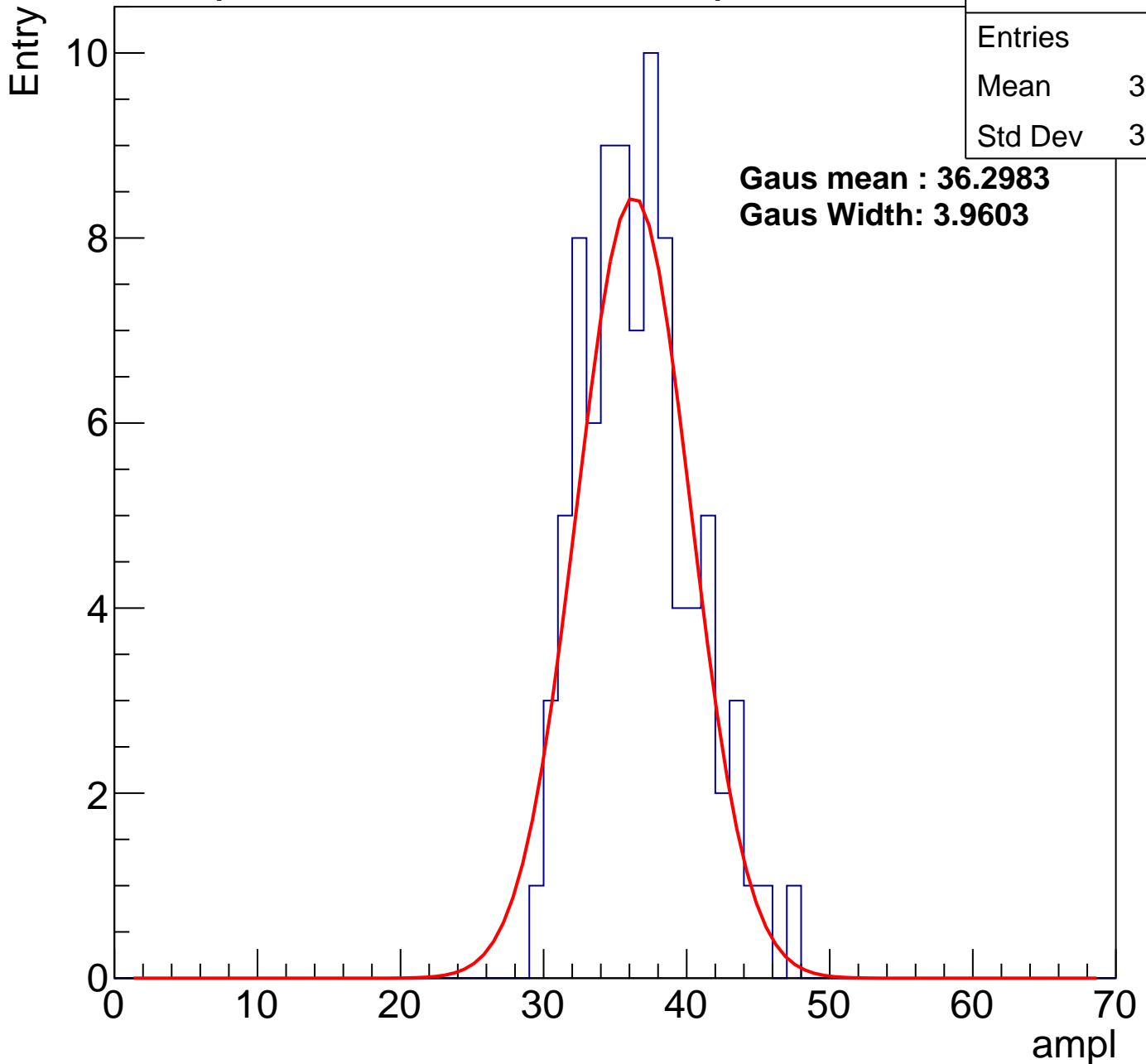


B0L001S, U19-ch61, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	36.15
Std Dev	3.804

Gaus mean : 36.2983
Gaus Width: 3.9603



B0L001S, U19-ch61, adc2

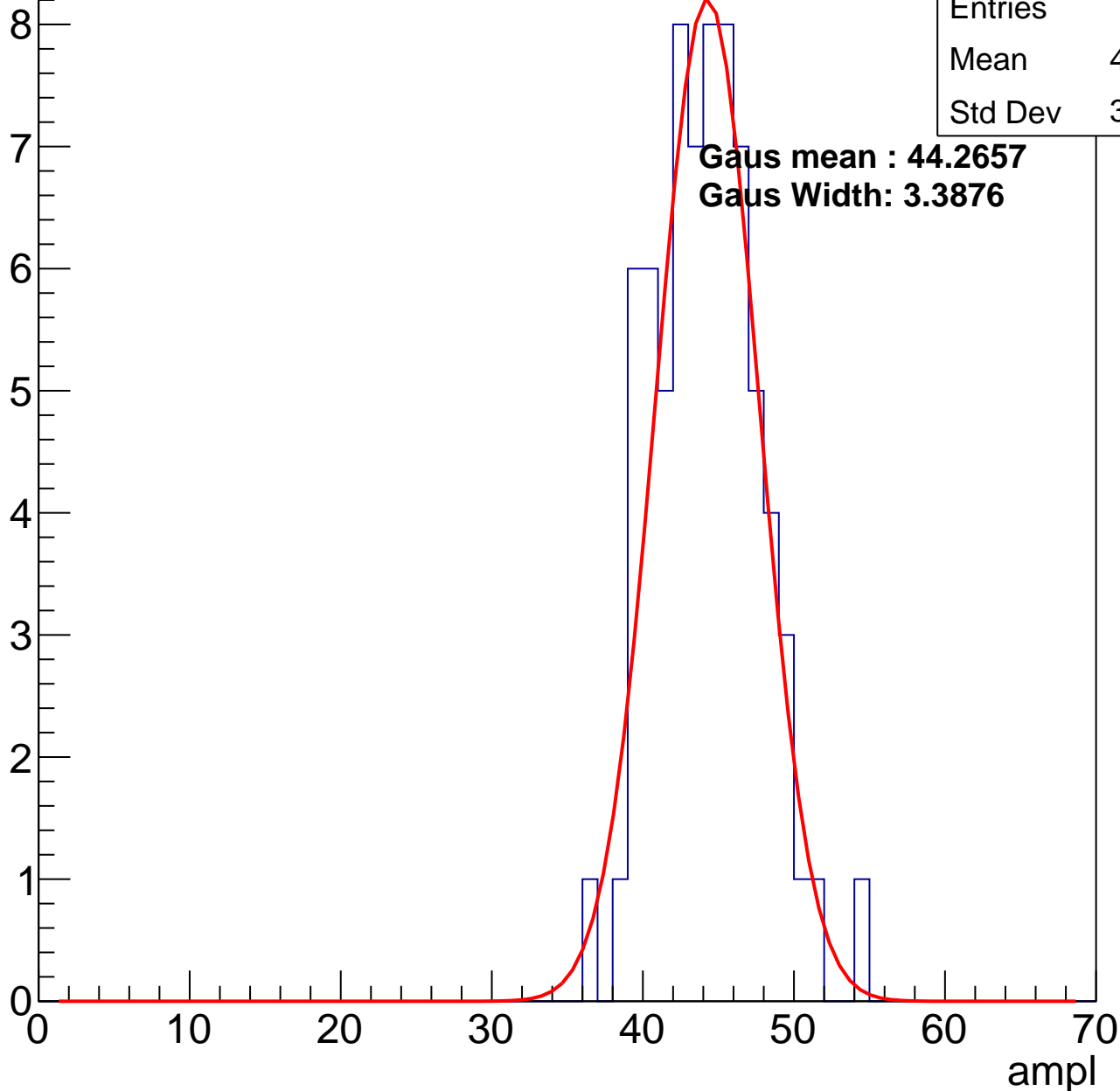
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.79
Std Dev	3.399

Gaus mean : 44.2657

Gaus Width: 3.3876

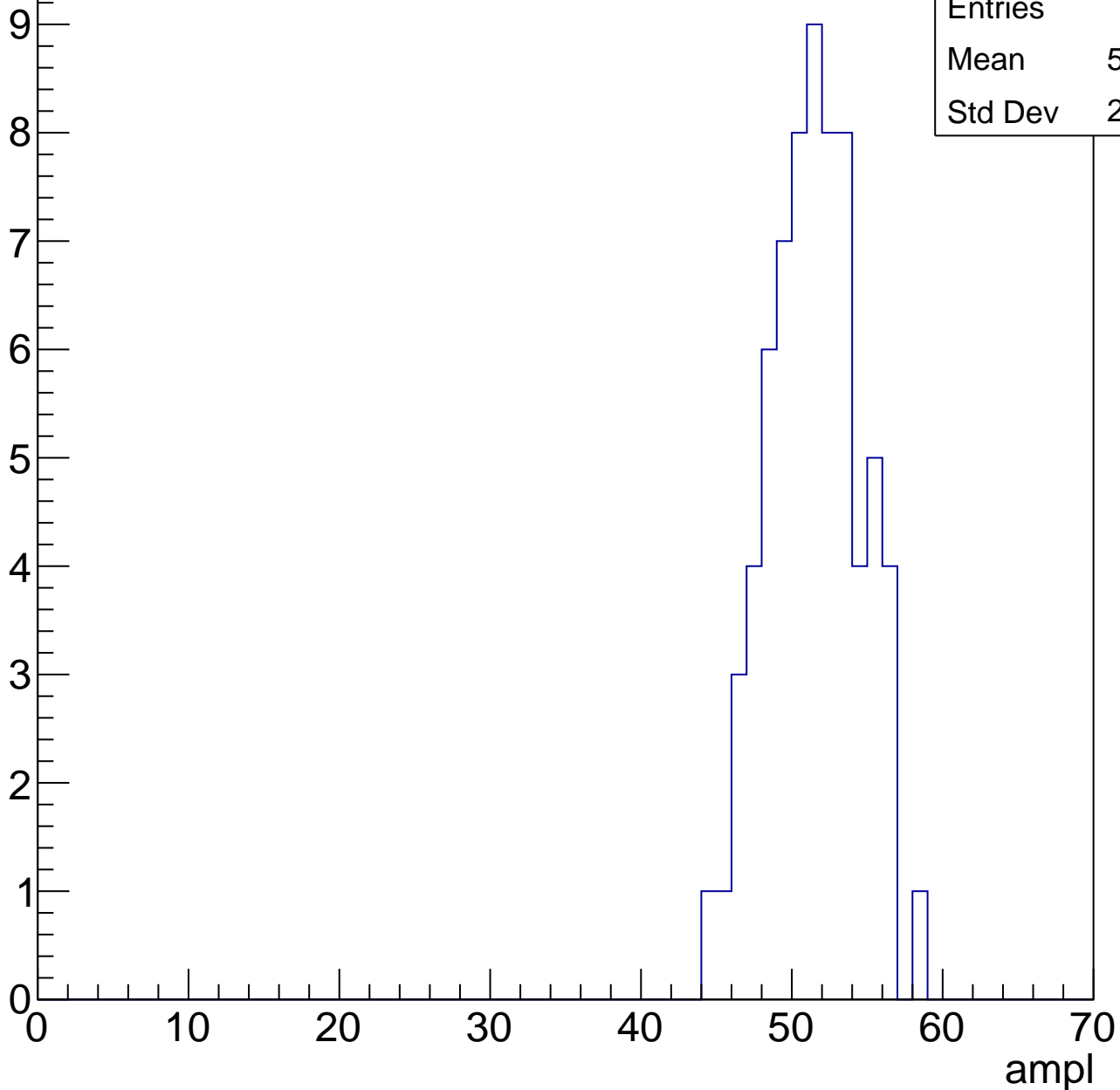


B0L001S, U19-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.99
Std Dev	2.995

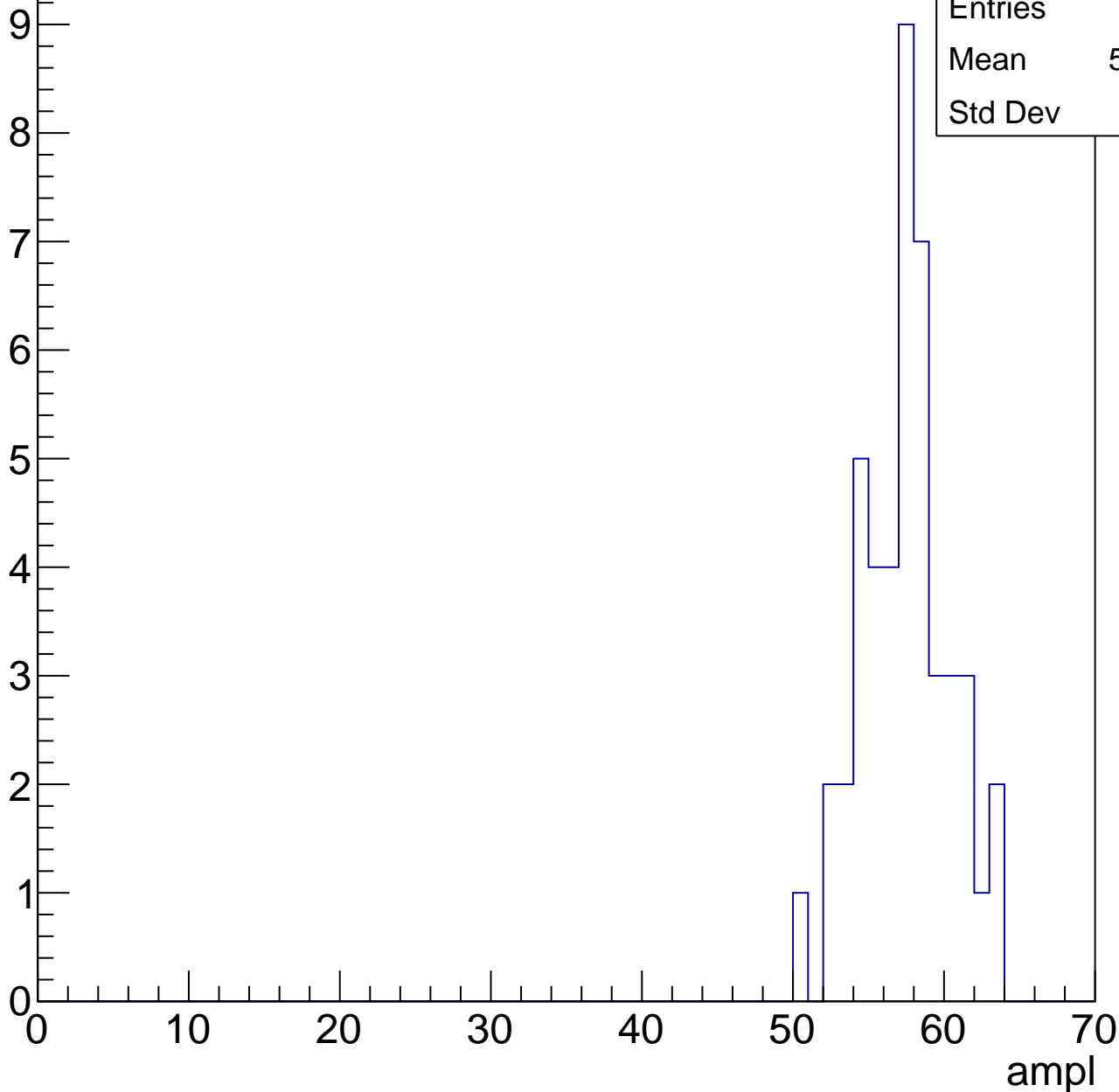


B0L001S, U19-ch61, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	56.98
Std Dev	2.9

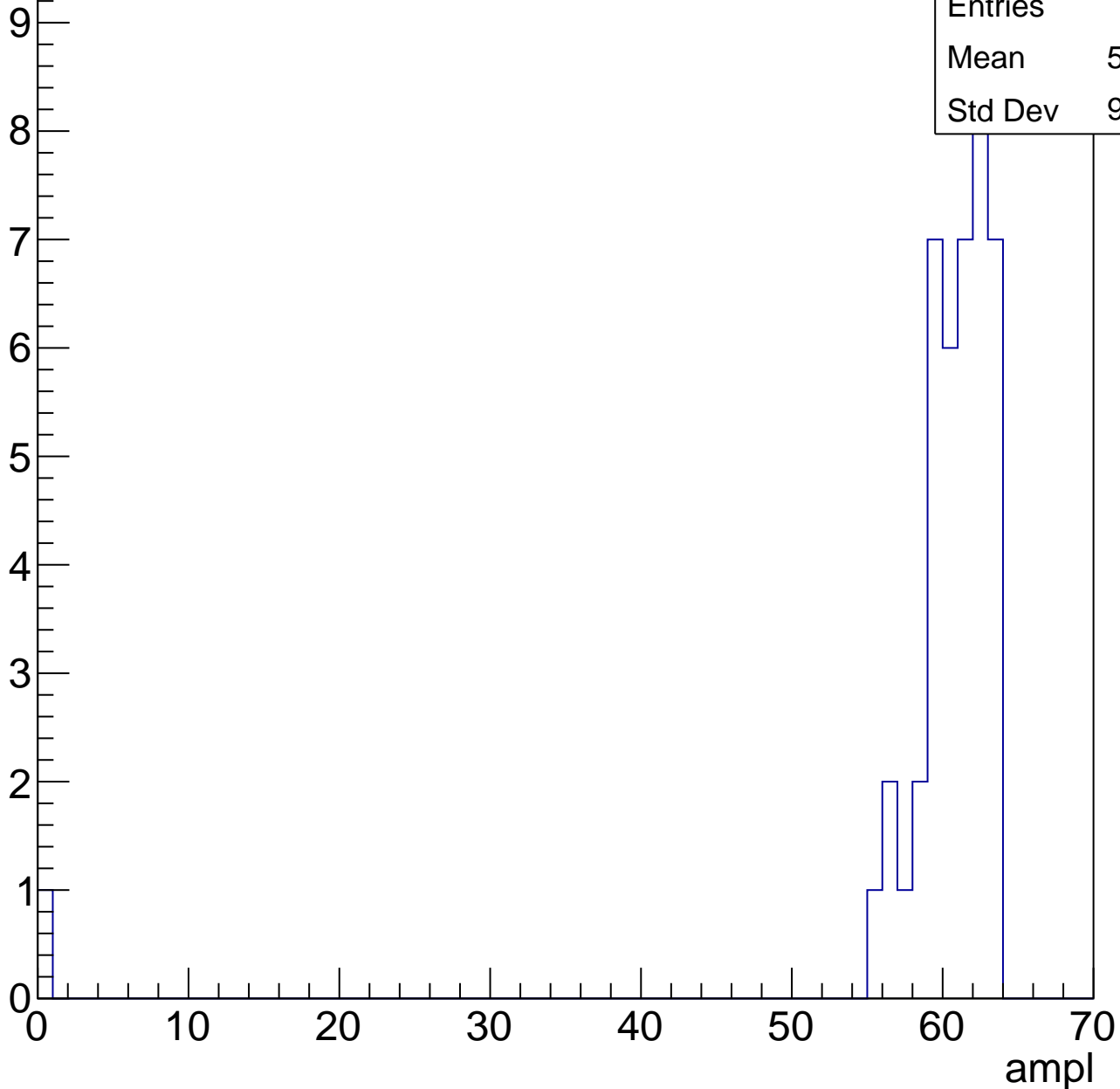


B0L001S, U19-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	59.05
Std Dev	9.336



B0L001S, U19-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

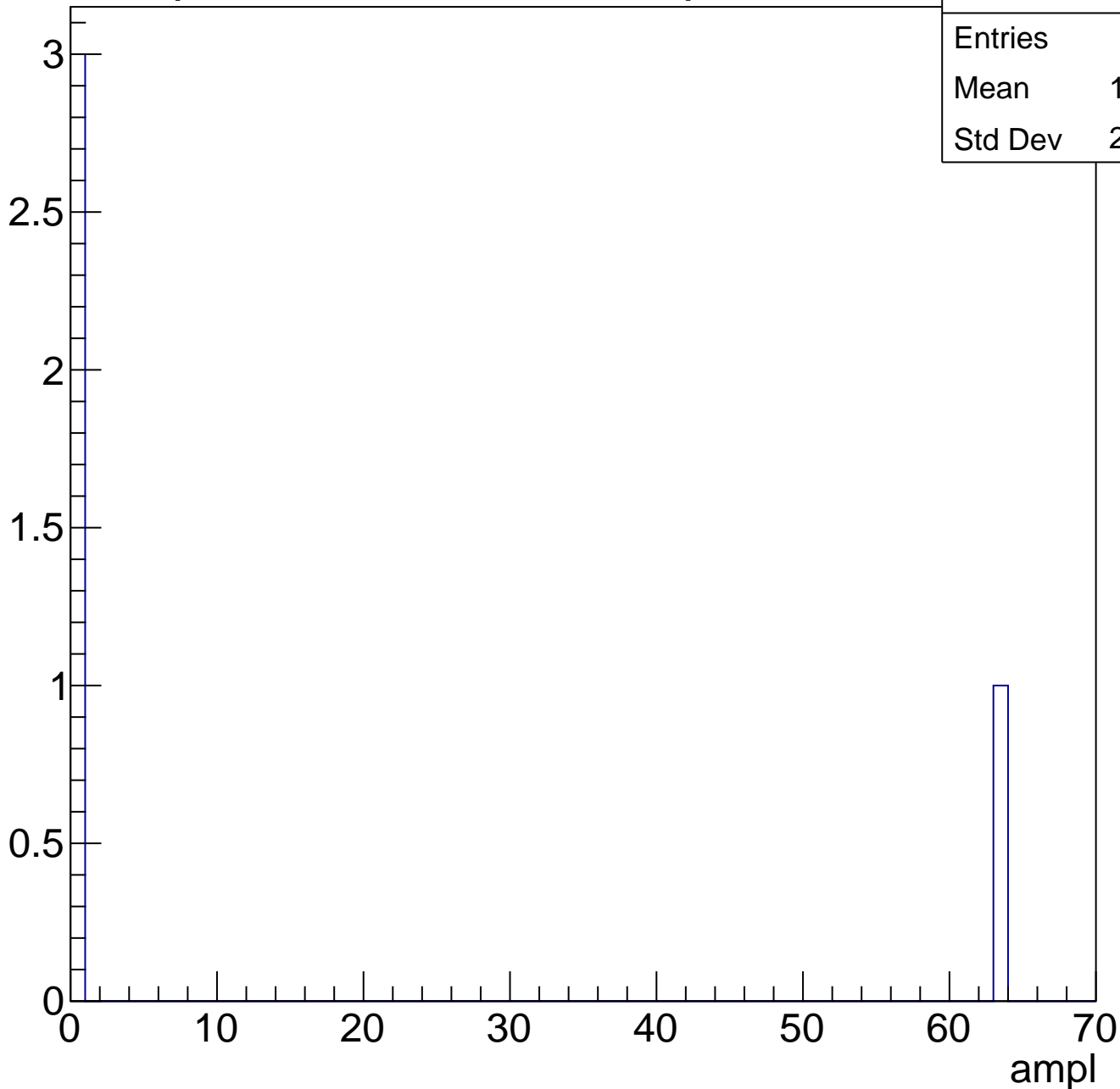
Entry



B0L001S, U19-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch62, adc0

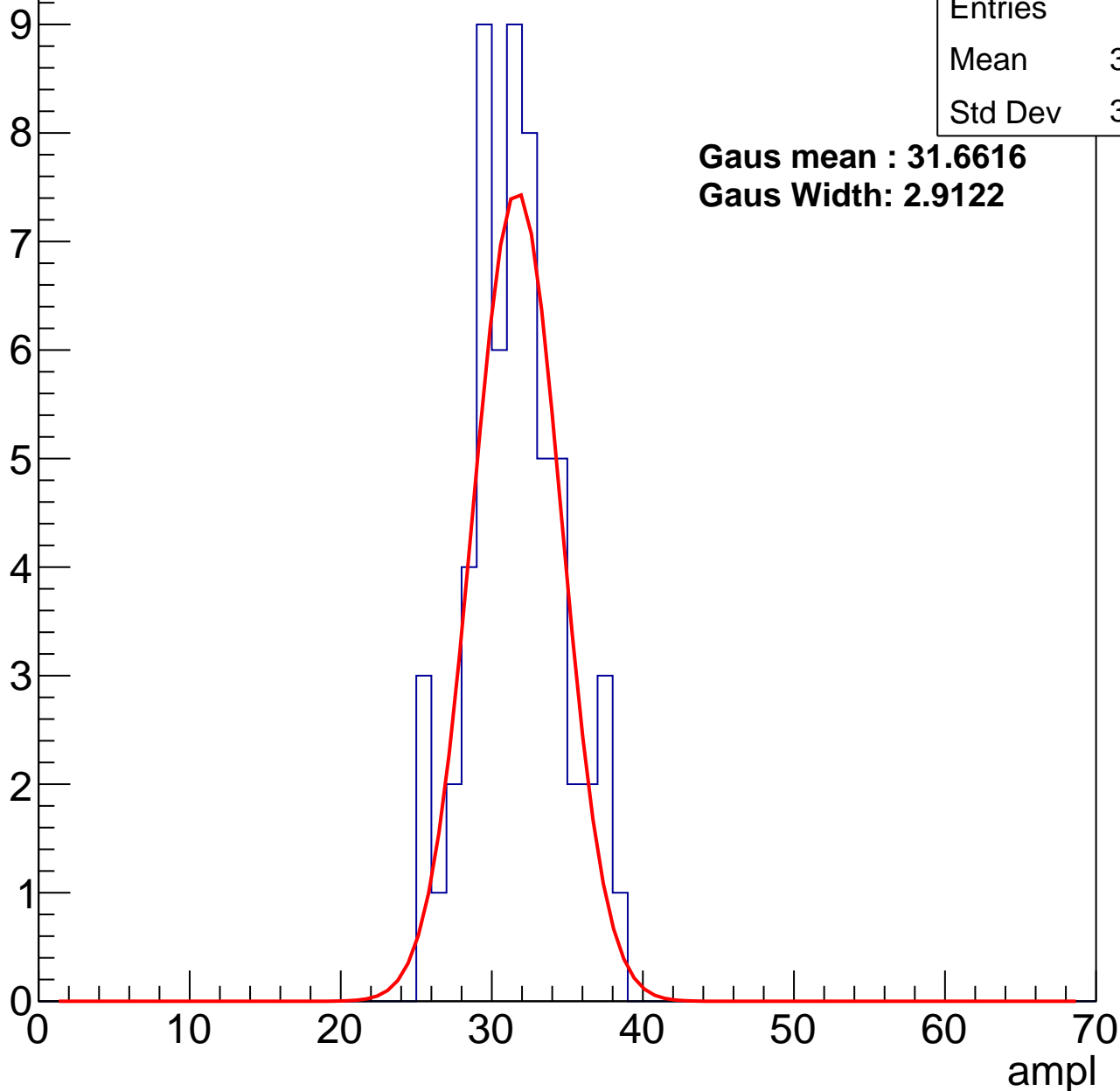
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	31.15
Std Dev	3.038

Gaus mean : 31.6616

Gaus Width: 2.9122



B0L001S, U19-ch62, adc1

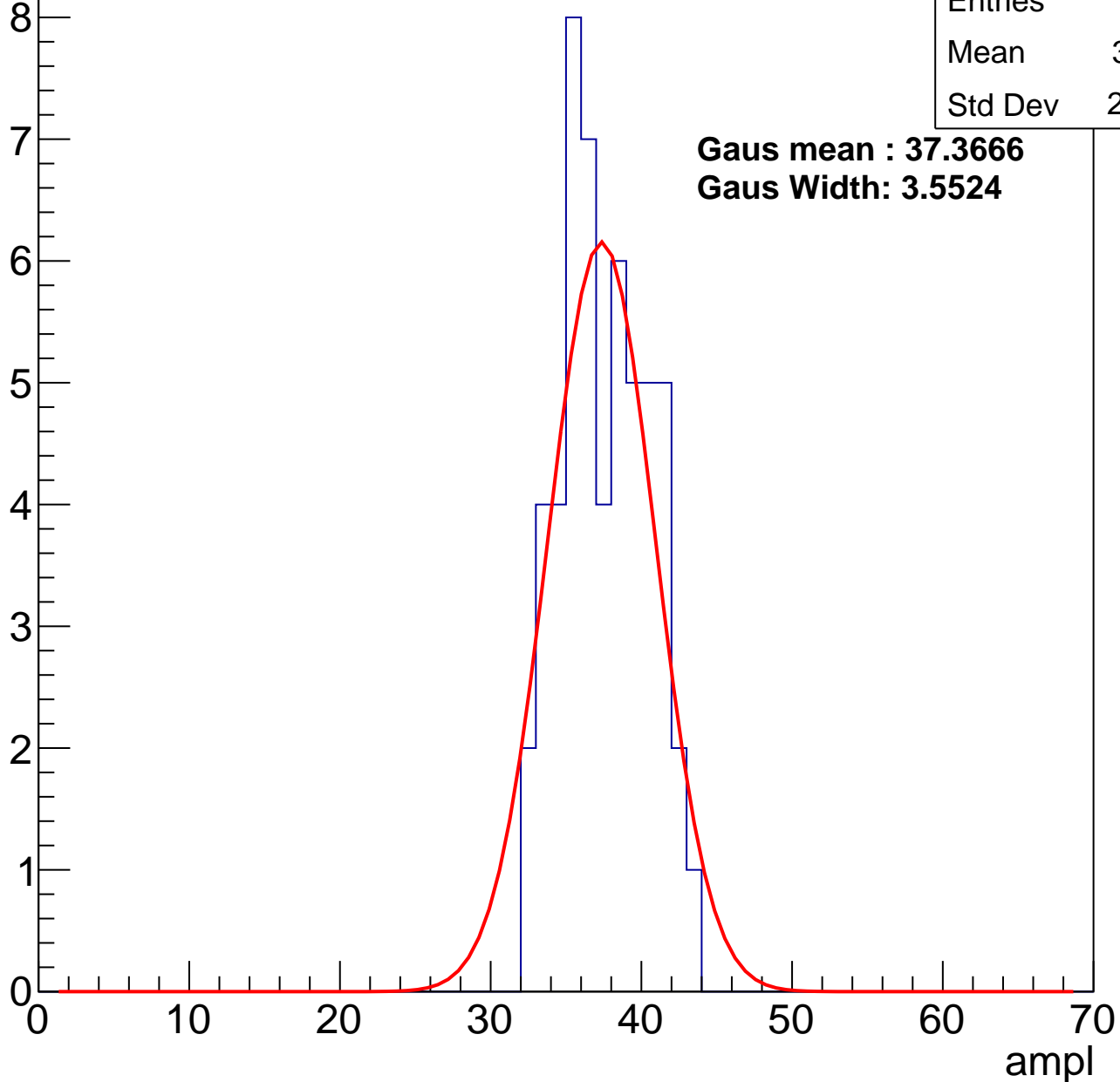
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	37.11
Std Dev	2.833

Gaus mean : 37.3666

Gaus Width: 3.5524



B0L001S, U19-ch62, adc2

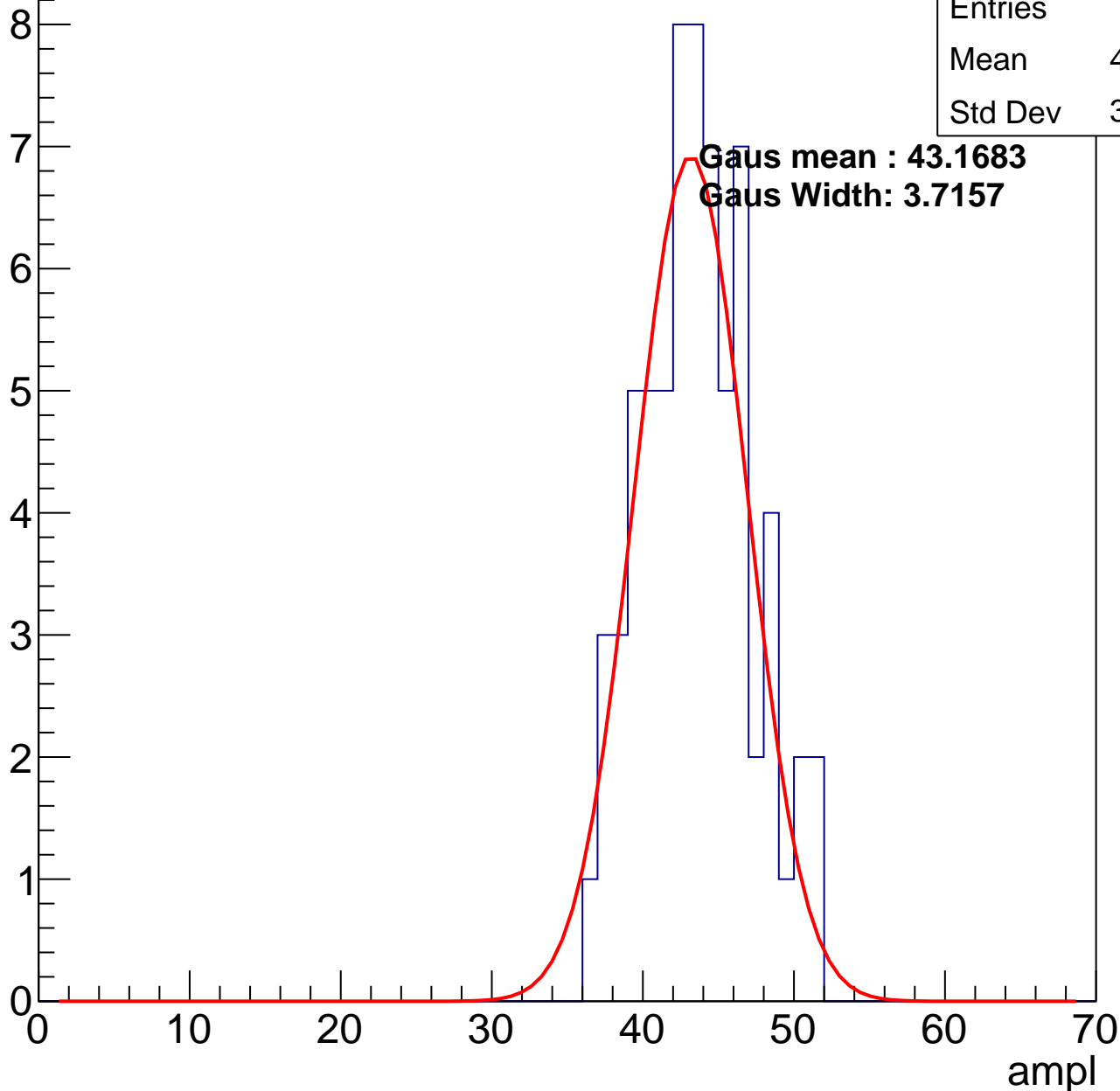
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.13
Std Dev	3.572

Gaus mean : 43.1683

Gaus Width: 3.7157



B0L001S, U19-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

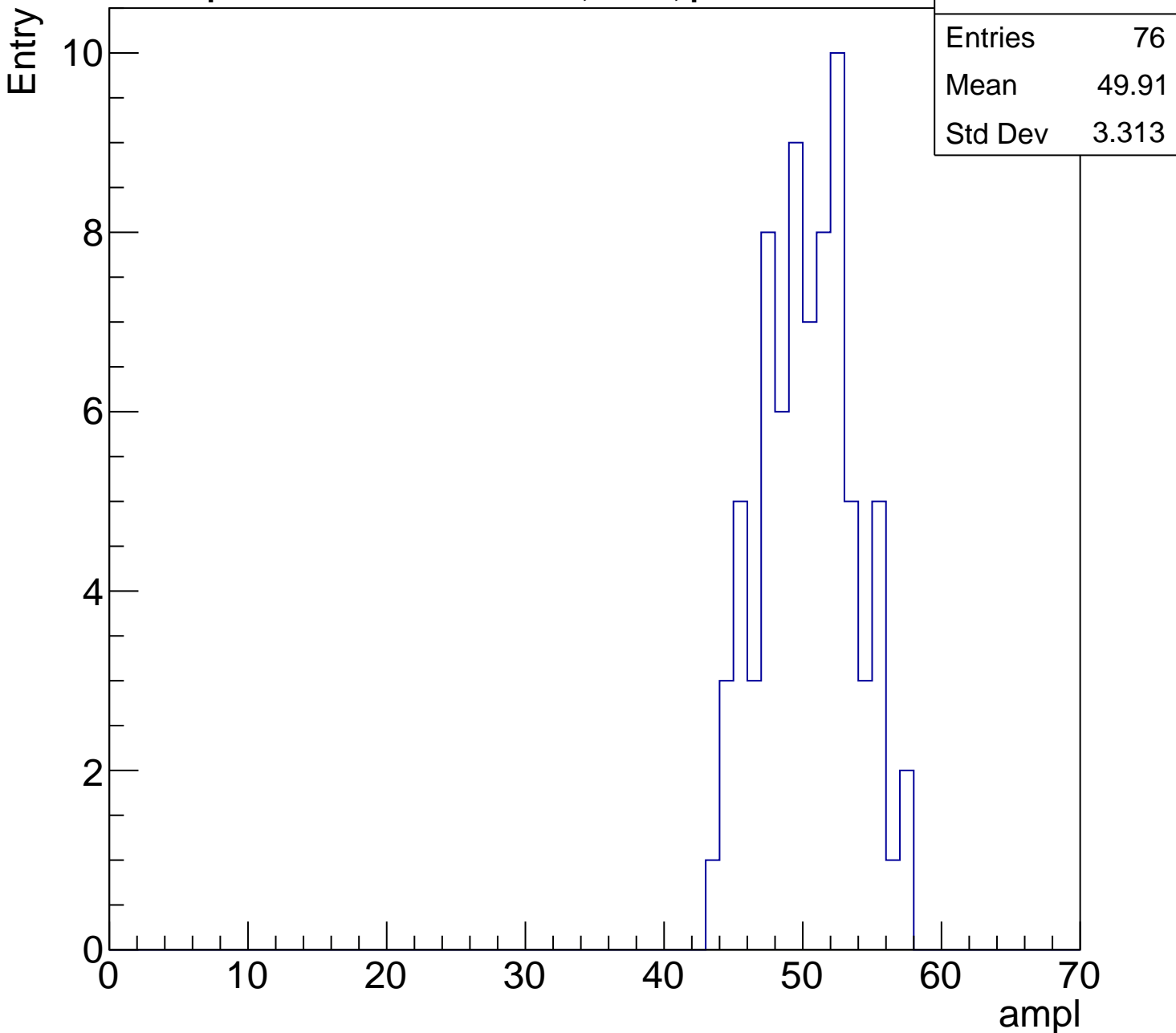
Entries	76
Mean	49.91
Std Dev	3.313

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

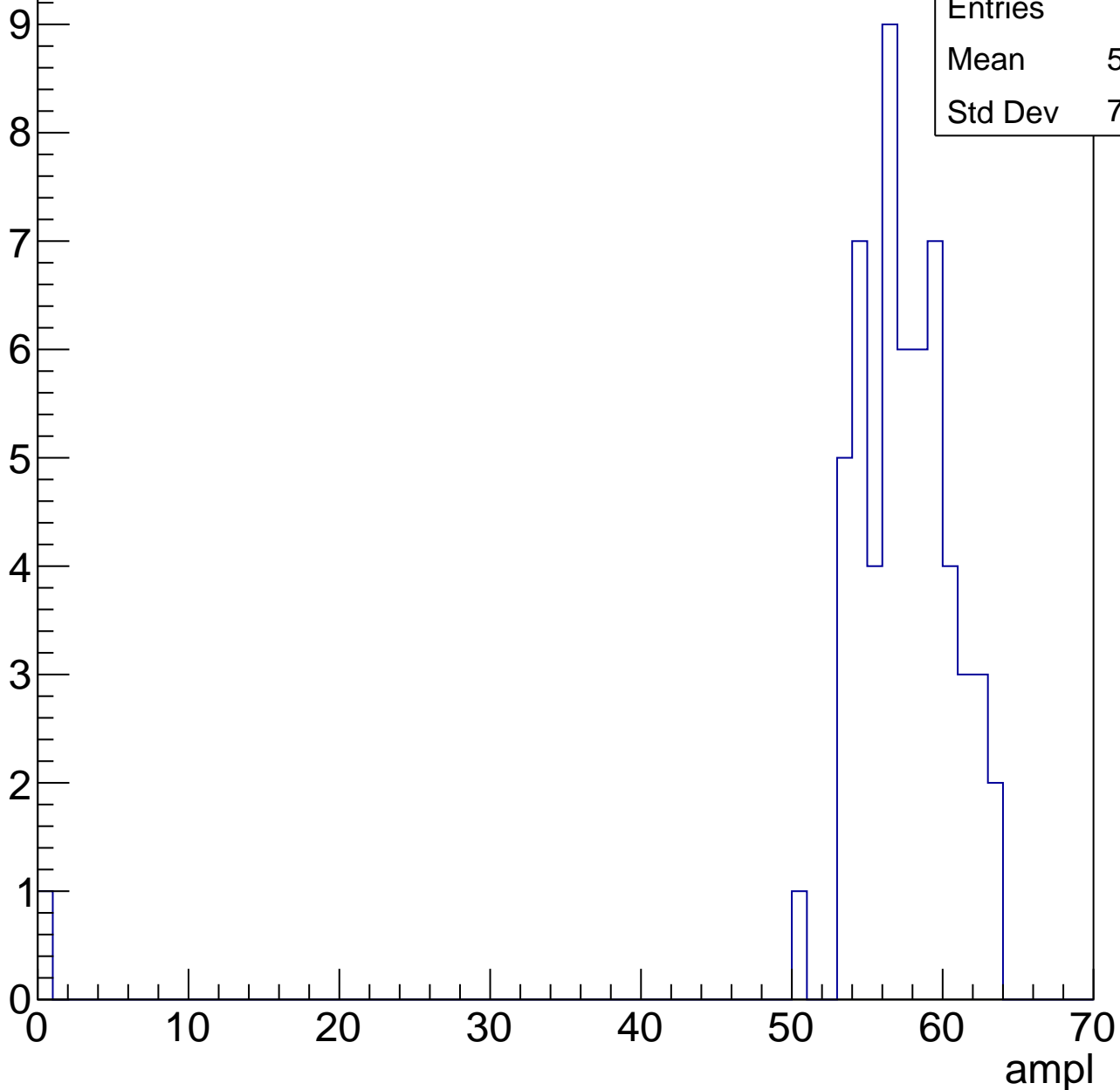


B0L001S, U19-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.12
Std Dev	7.972

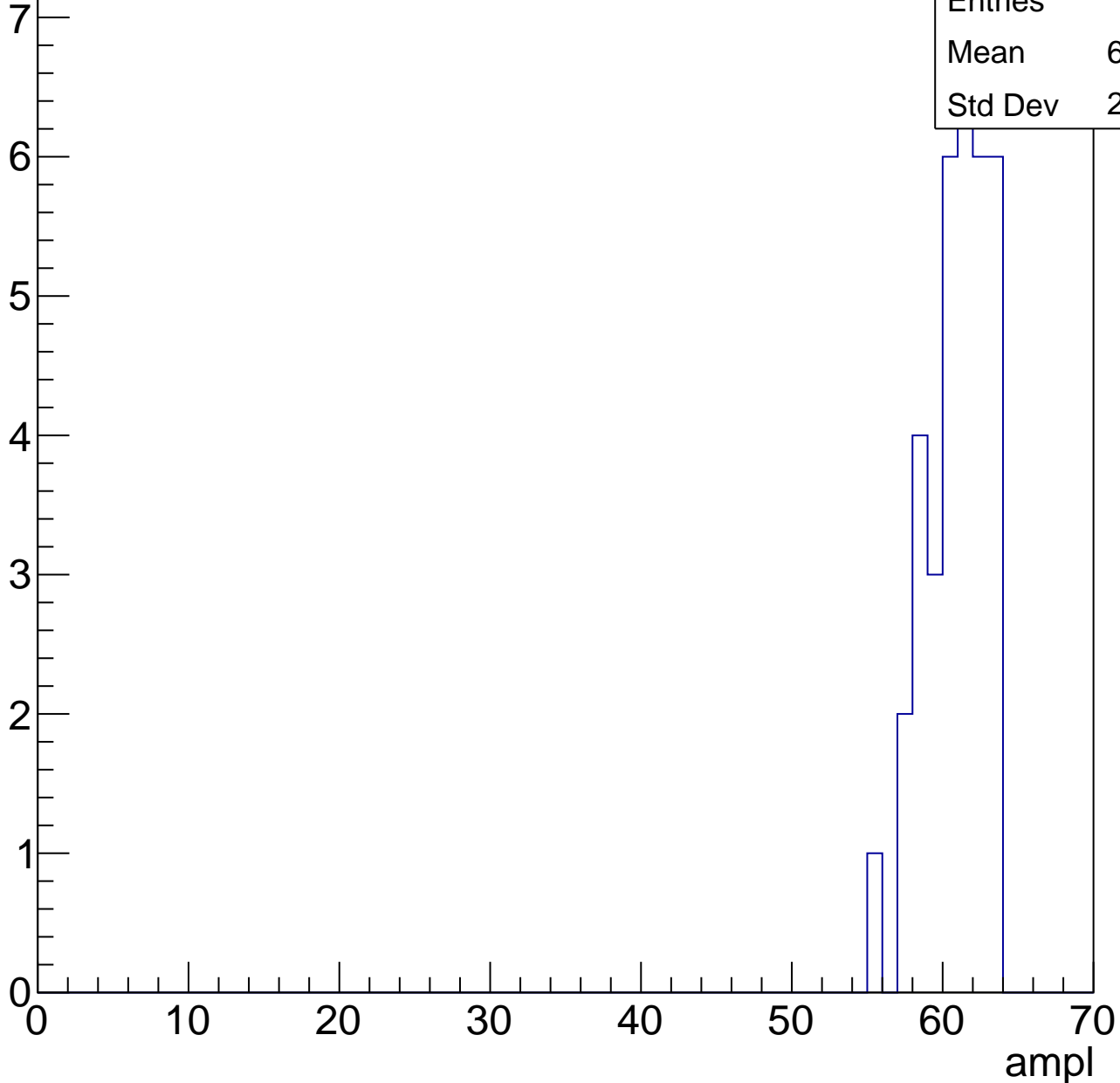


B0L001S, U19-ch62, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	60.43
Std Dev	2.004



B0L001S, U19-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch63, adc0

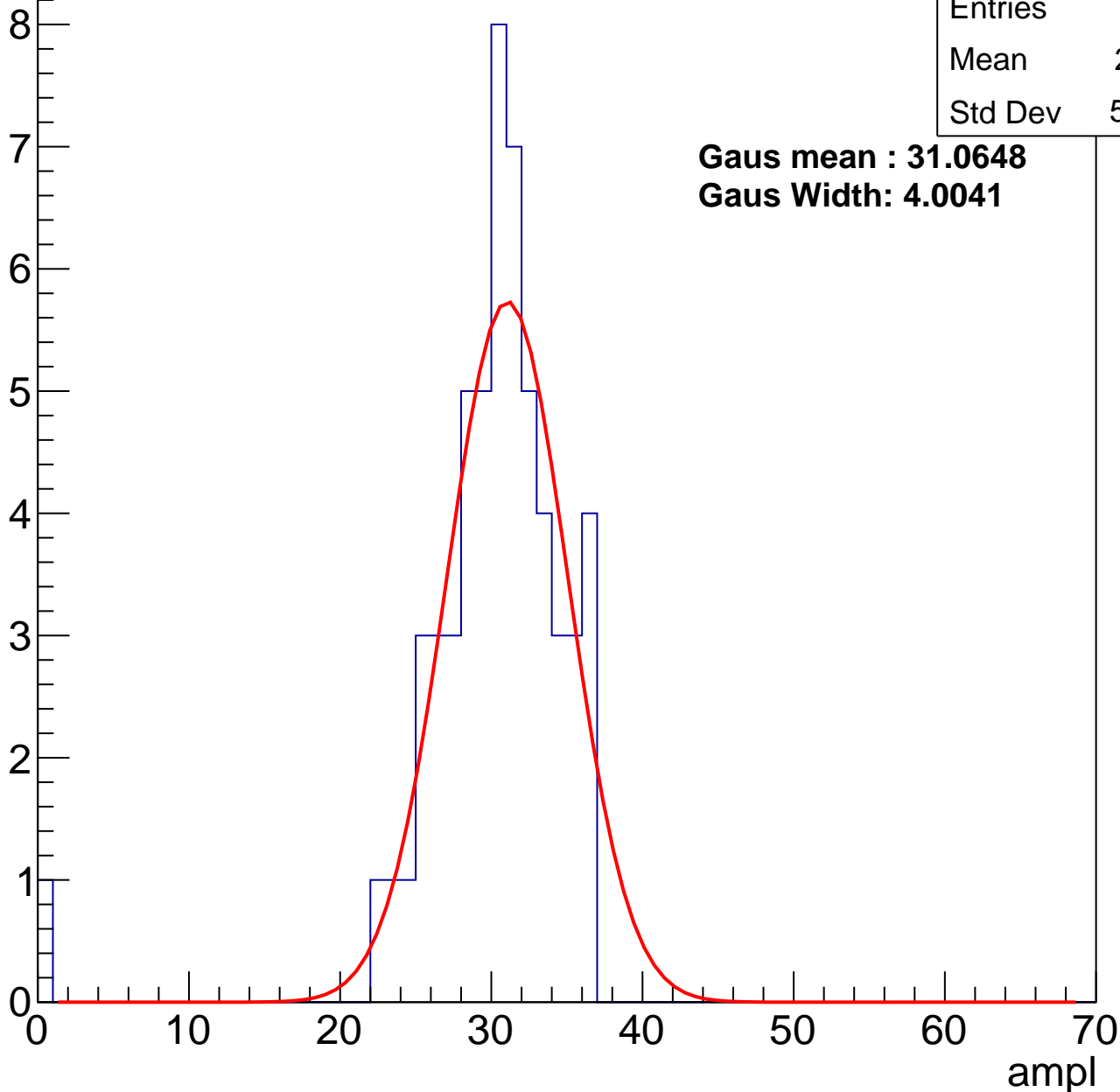
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	29.61
Std Dev	5.207

Gaus mean : 31.0648

Gaus Width: 4.0041



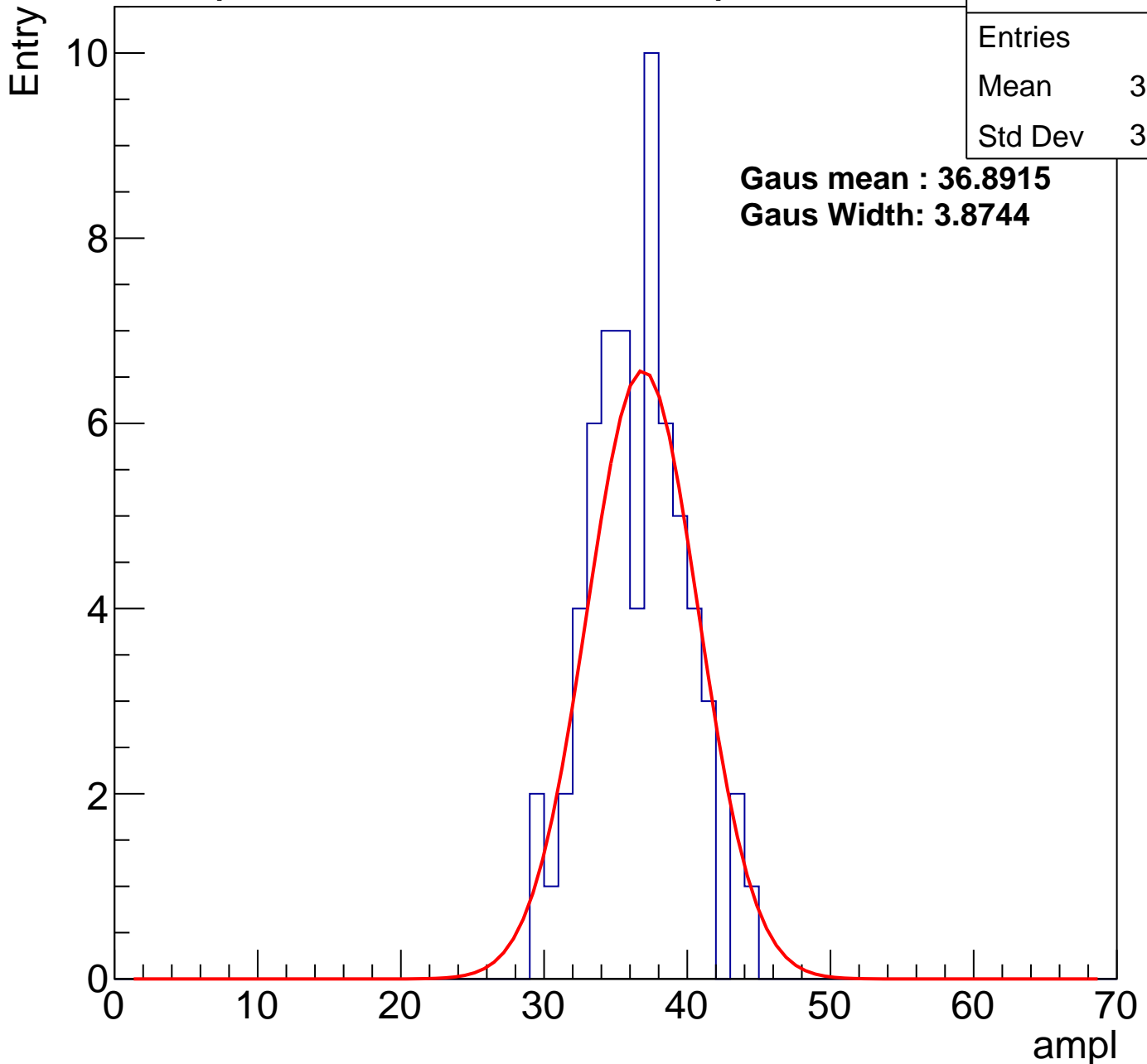
B0L001S, U19-ch63, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	36.08
Std Dev	3.346

Gaus mean : 36.8915

Gaus Width: 3.8744



B0L001S, U19-ch63, adc2

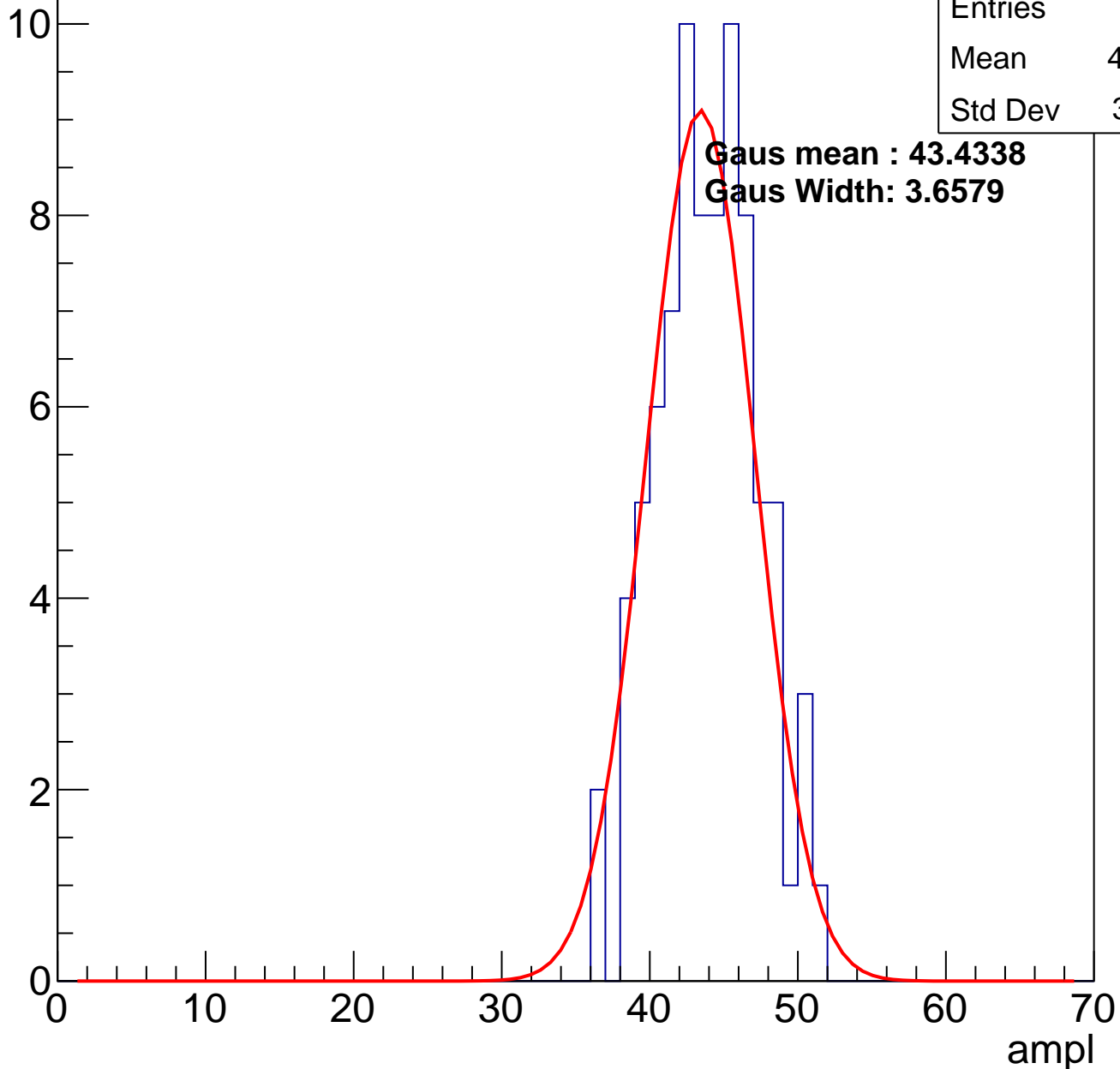
calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	43.43
Std Dev	3.341

Gaus mean : 43.4338

Gaus Width: 3.6579

Entry

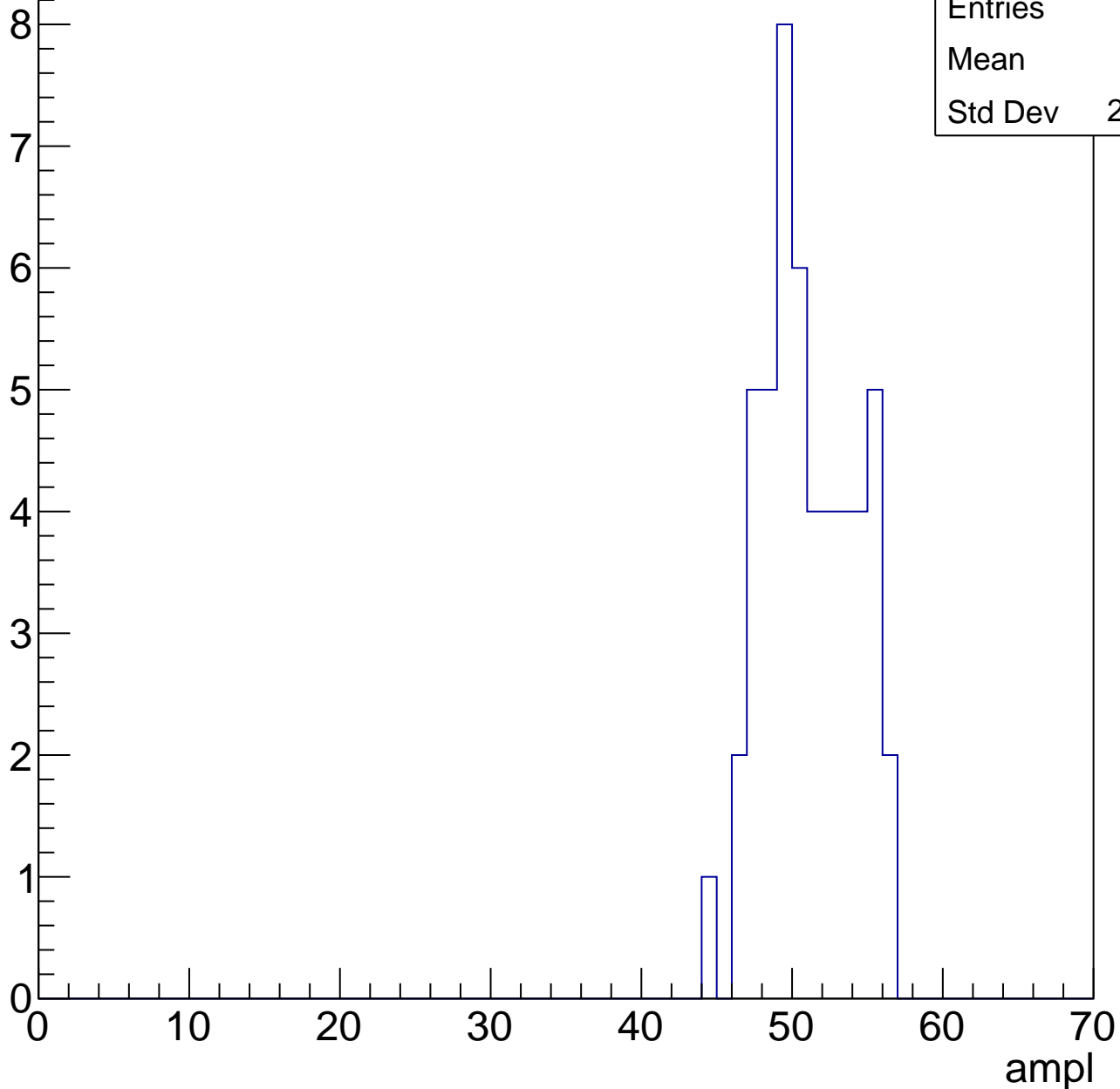


B0L001S, U19-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

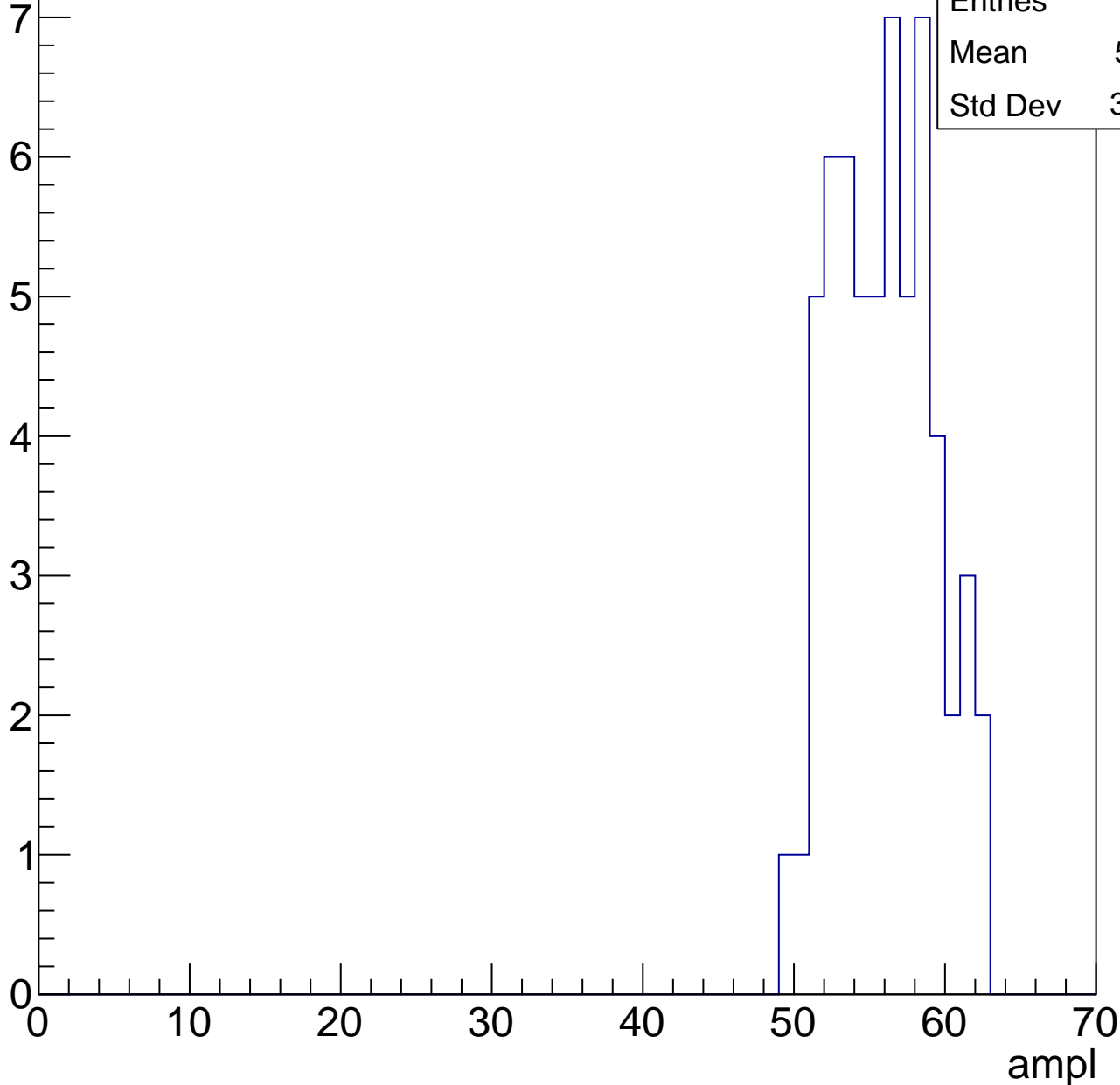
Entries	50
Mean	50.6
Std Dev	2.966



B0L001S, U19-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

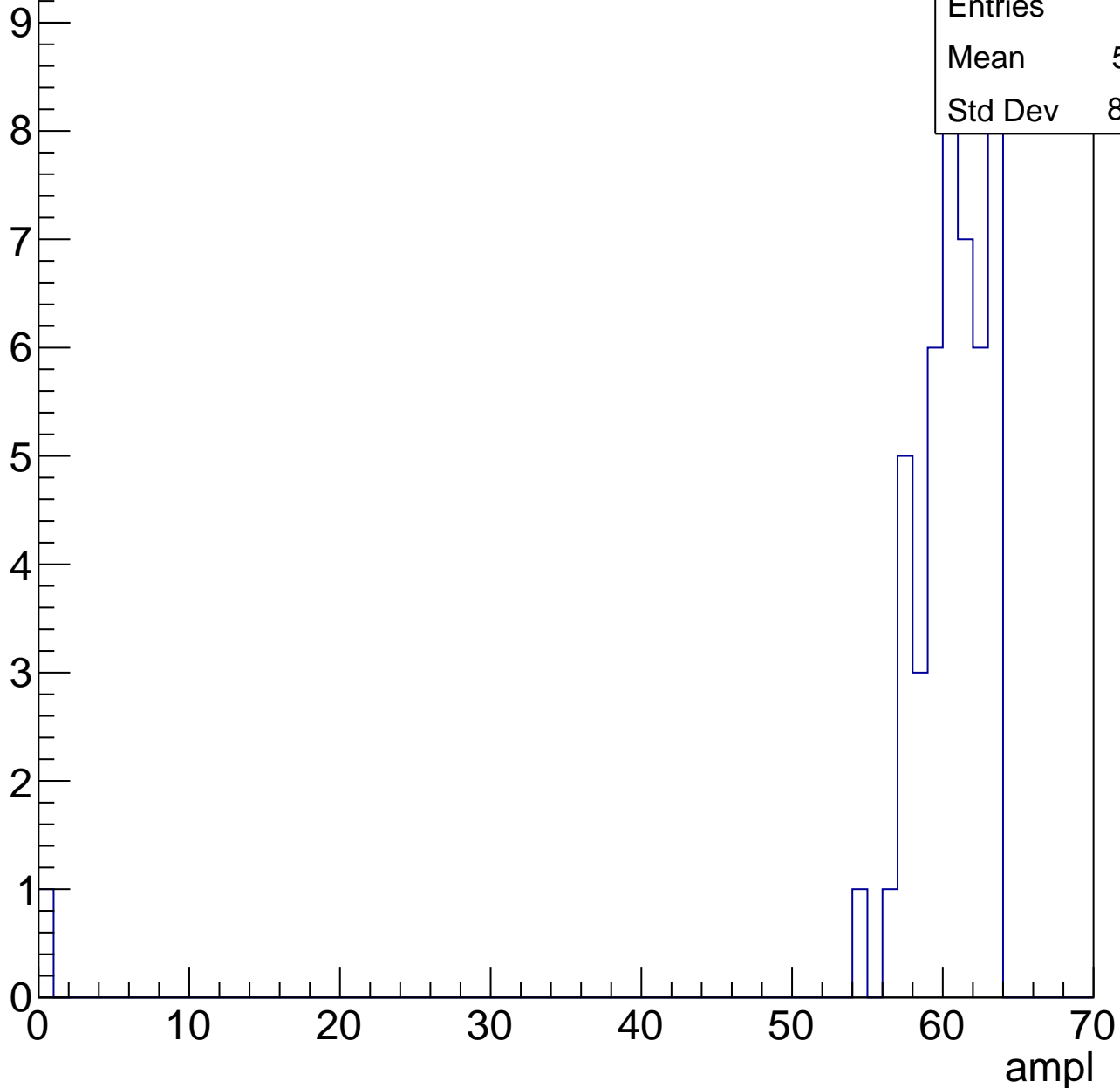


Entries	59
Mean	55.51
Std Dev	3.238

B0L001S, U19-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch63, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch64, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	29.87
Std Dev	5.956

Gaus mean : 31.3372

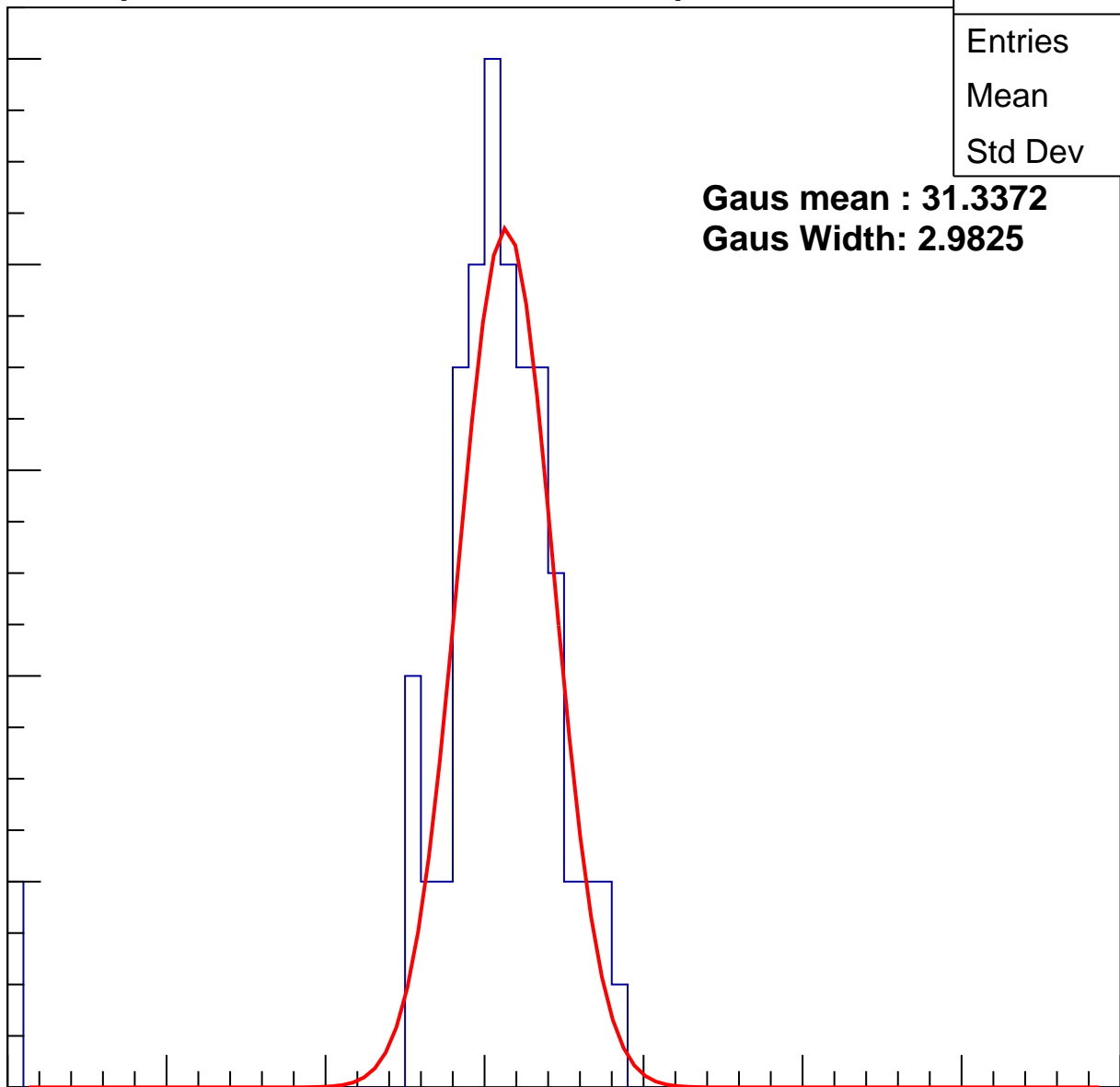
Gaus Width: 2.9825

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch64, adc1

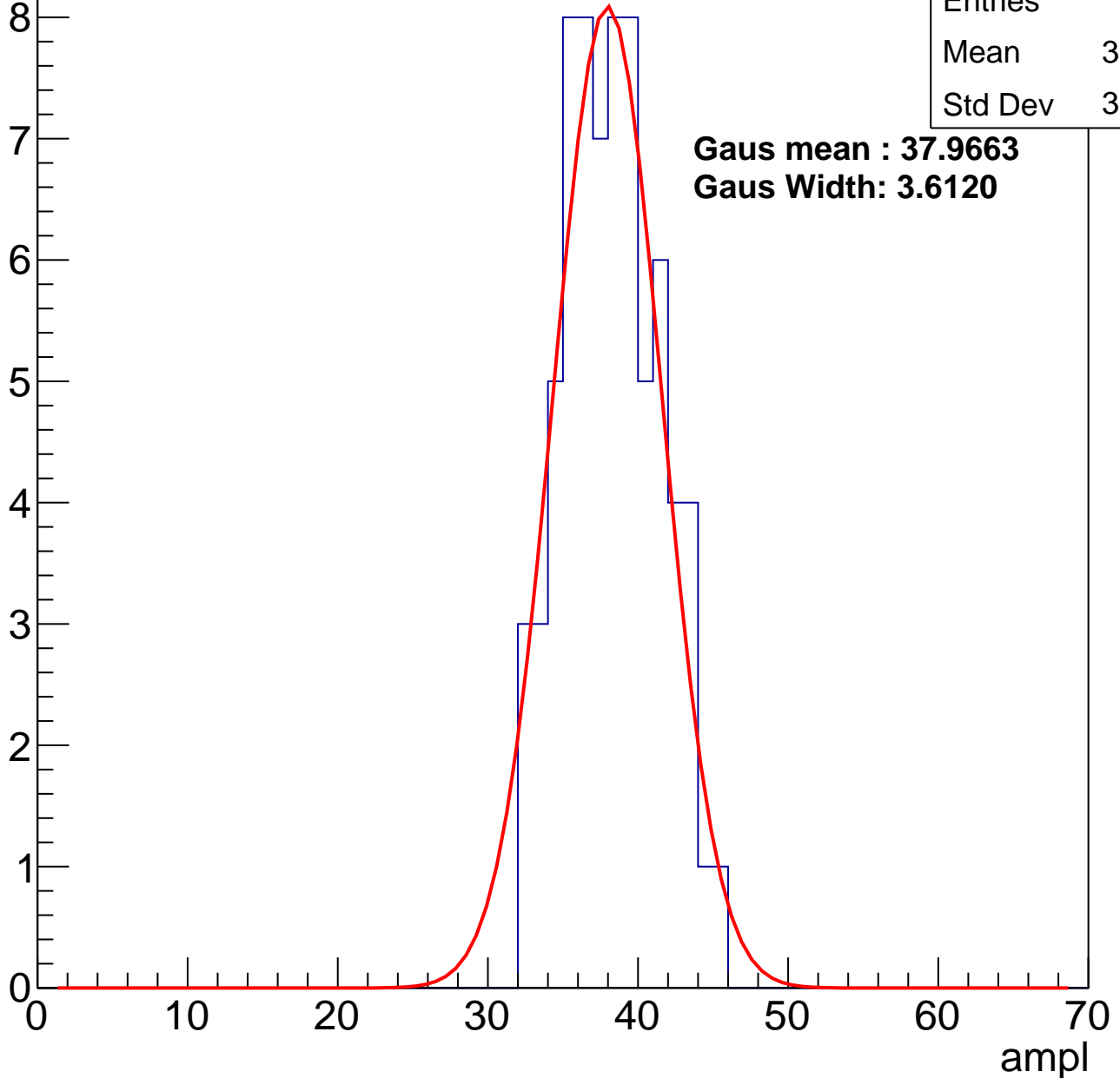
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.79
Std Dev	3.144

Gaus mean : 37.9663

Gaus Width: 3.6120



B0L001S, U19-ch64, adc2

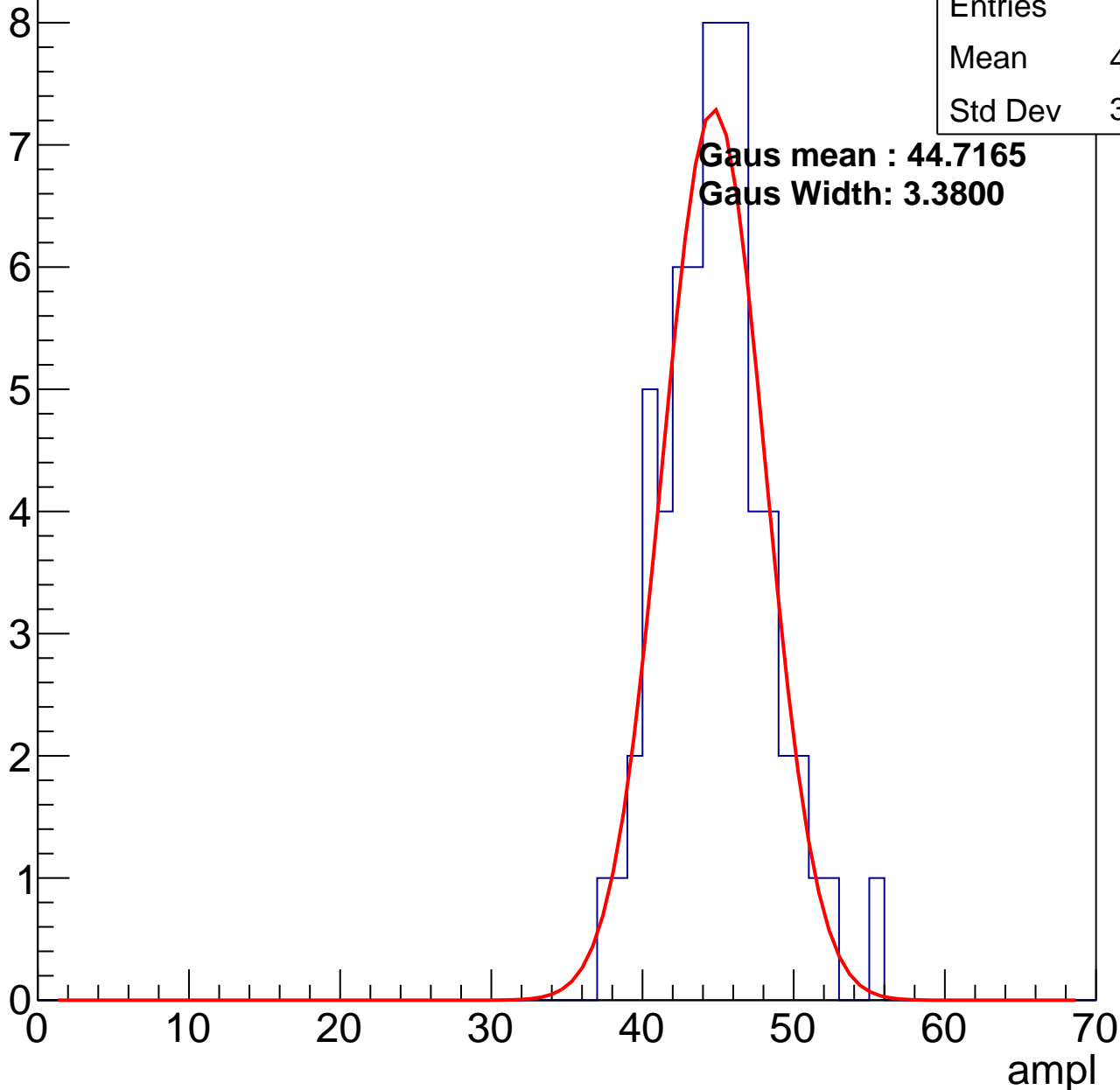
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	44.42
Std Dev	3.459

Gaus mean : 44.7165

Gaus Width: 3.3800

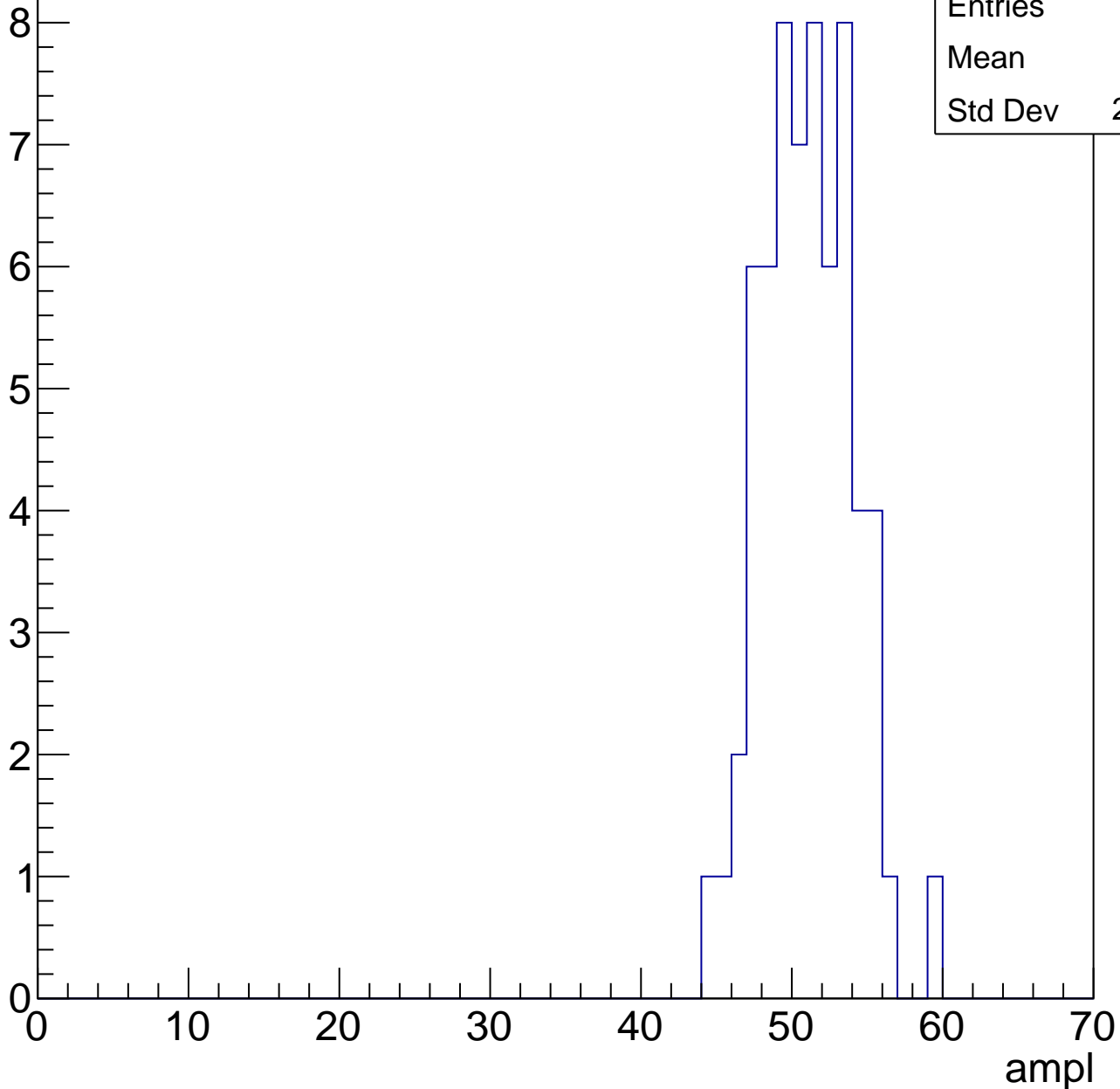


B0L001S, U19-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

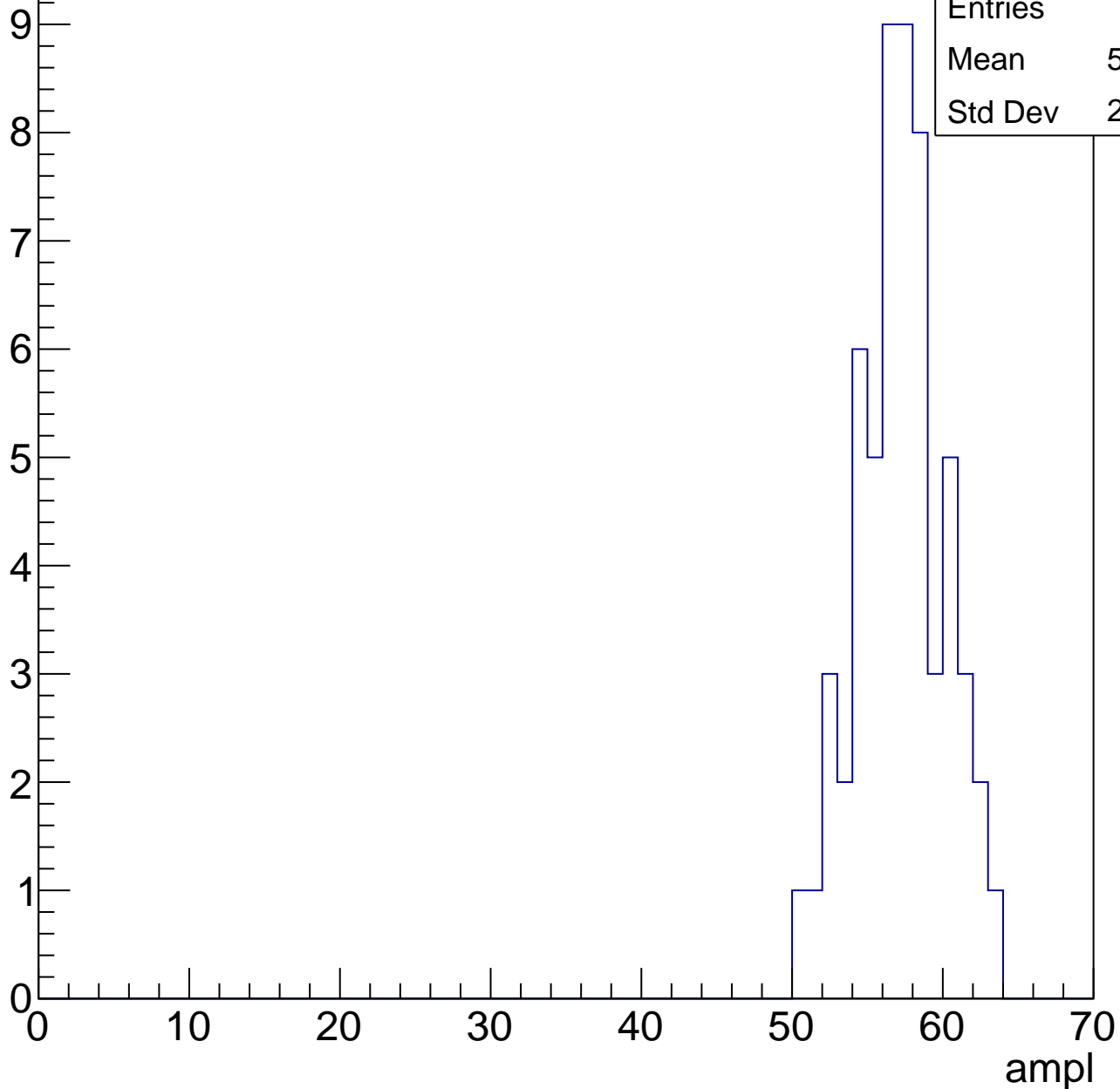
Entries	63
Mean	50.6
Std Dev	2.931



B0L001S, U19-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



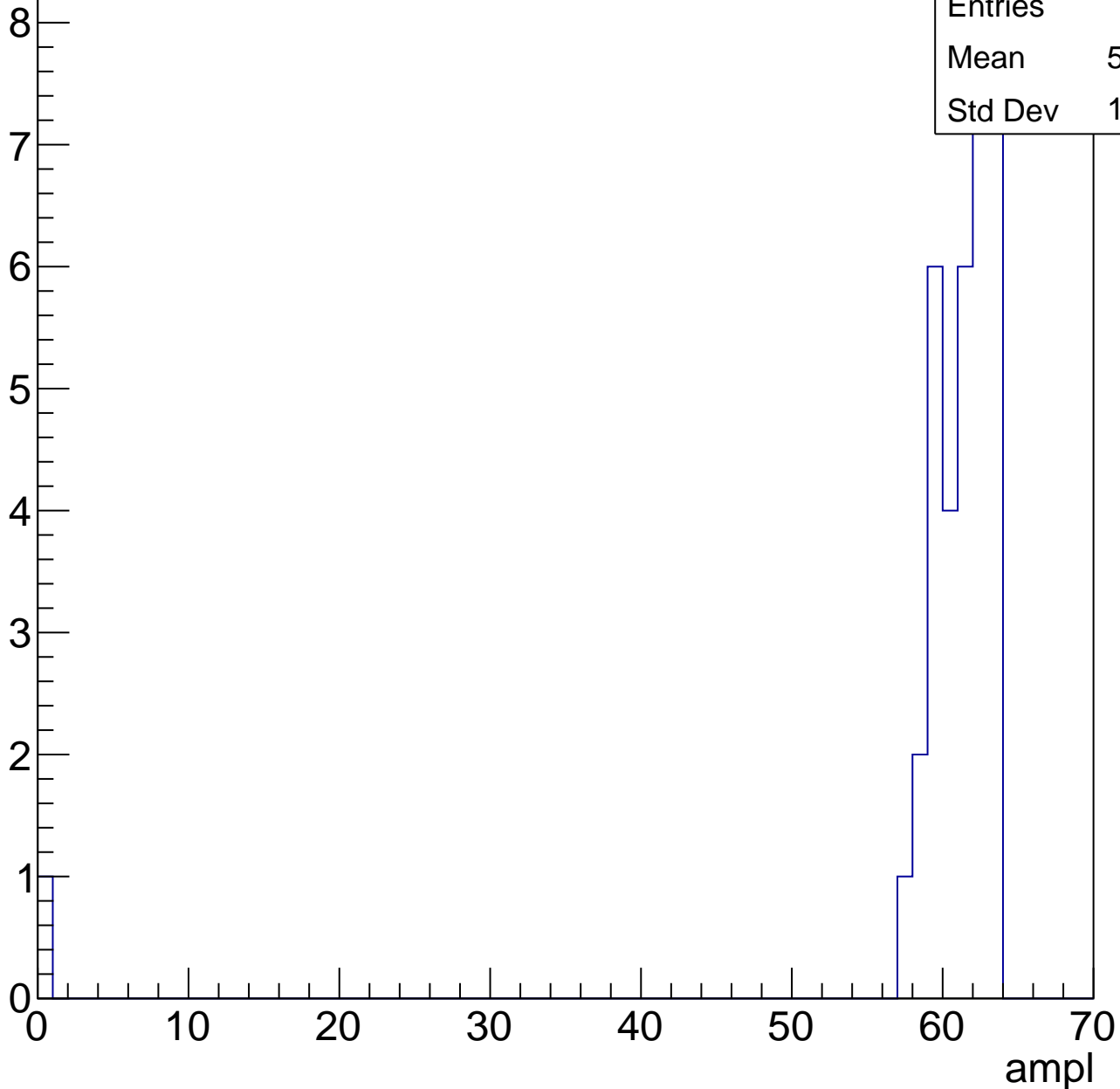
Entries	58
Mean	56.72
Std Dev	2.845

B0L001S, U19-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59.25
Std Dev	10.16



B0L001S, U19-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

61.33

Std Dev

1.247

B0L001S, U19-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch65, adc0

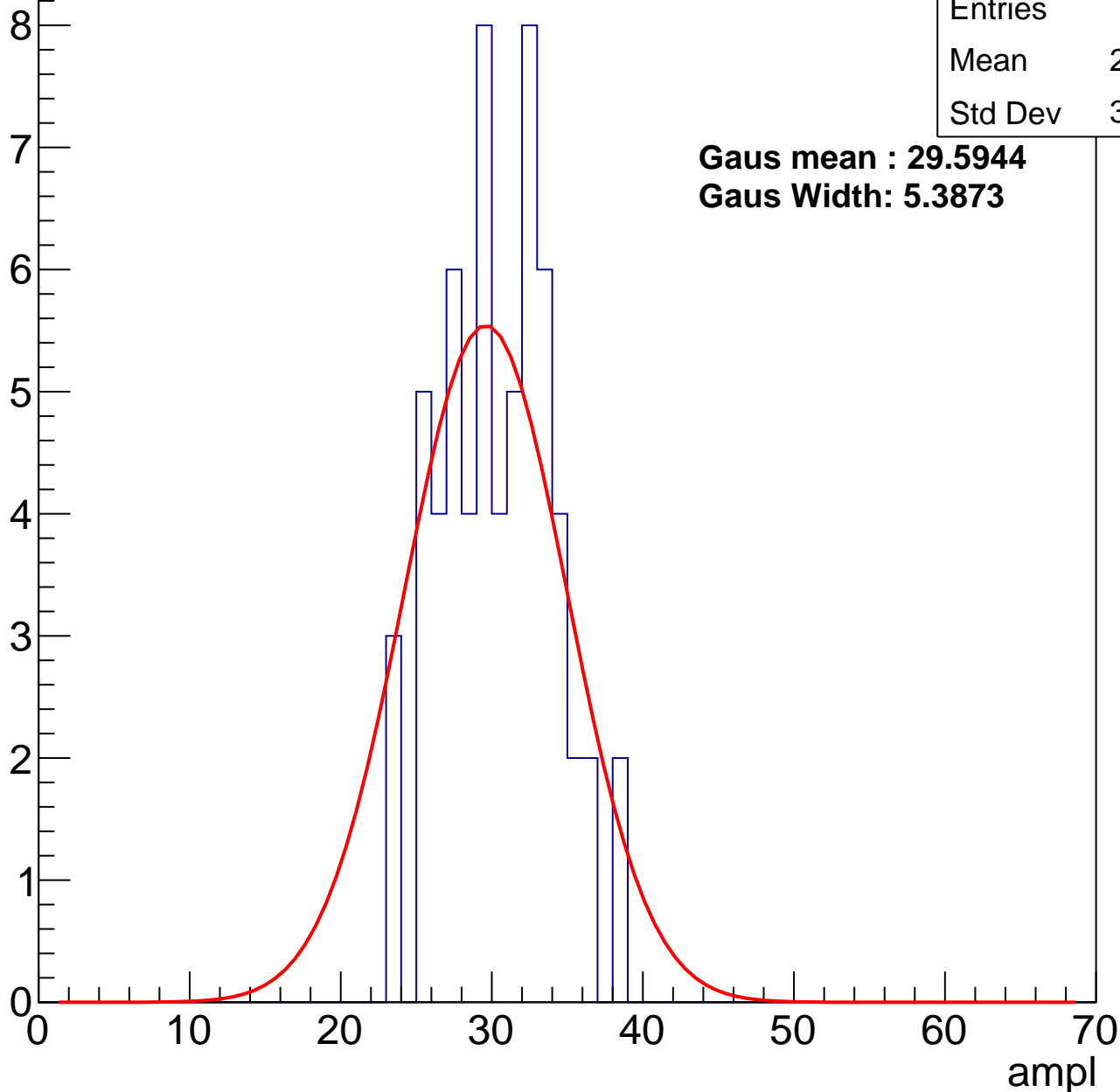
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.95
Std Dev	3.605

Gaus mean : 29.5944

Gaus Width: 5.3873



B0L001S, U19-ch65, adc1

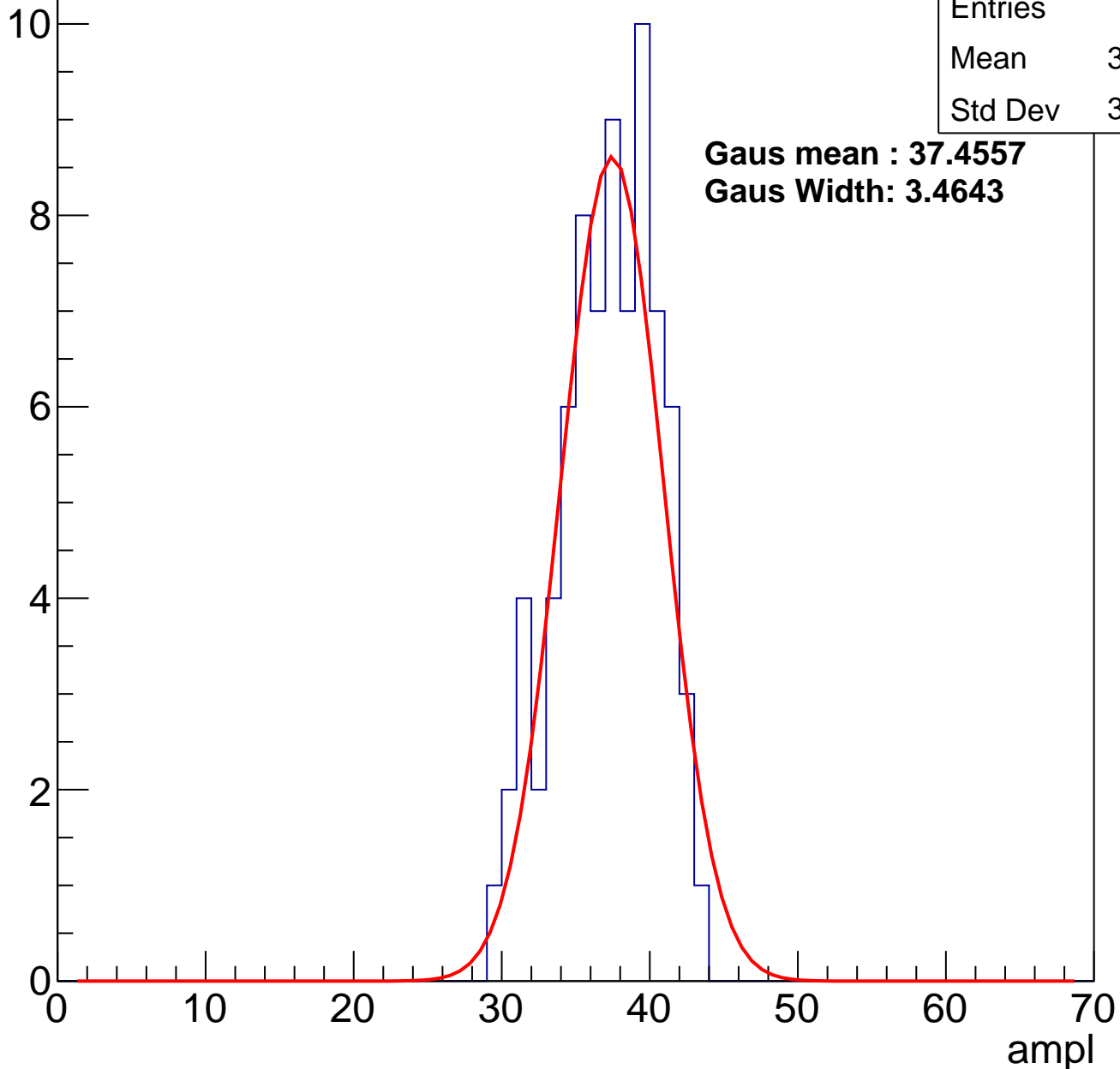
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	36.74
Std Dev	3.273

Gaus mean : 37.4557

Gaus Width: 3.4643

Entry



B0L001S, U19-ch65, adc2

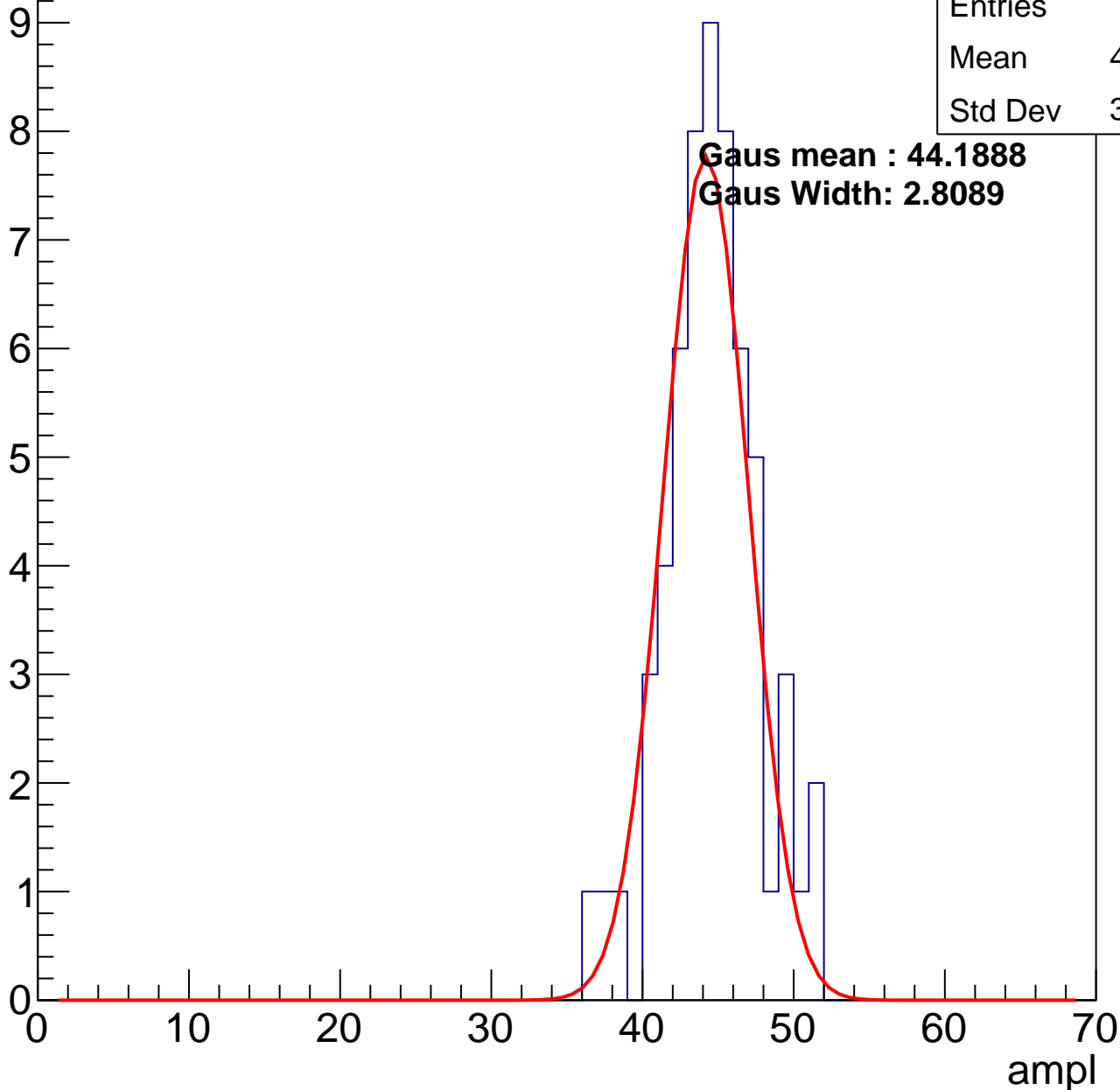
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.15
Std Dev	3.096

Gaus mean : 44.1888

Gaus Width: 2.8089

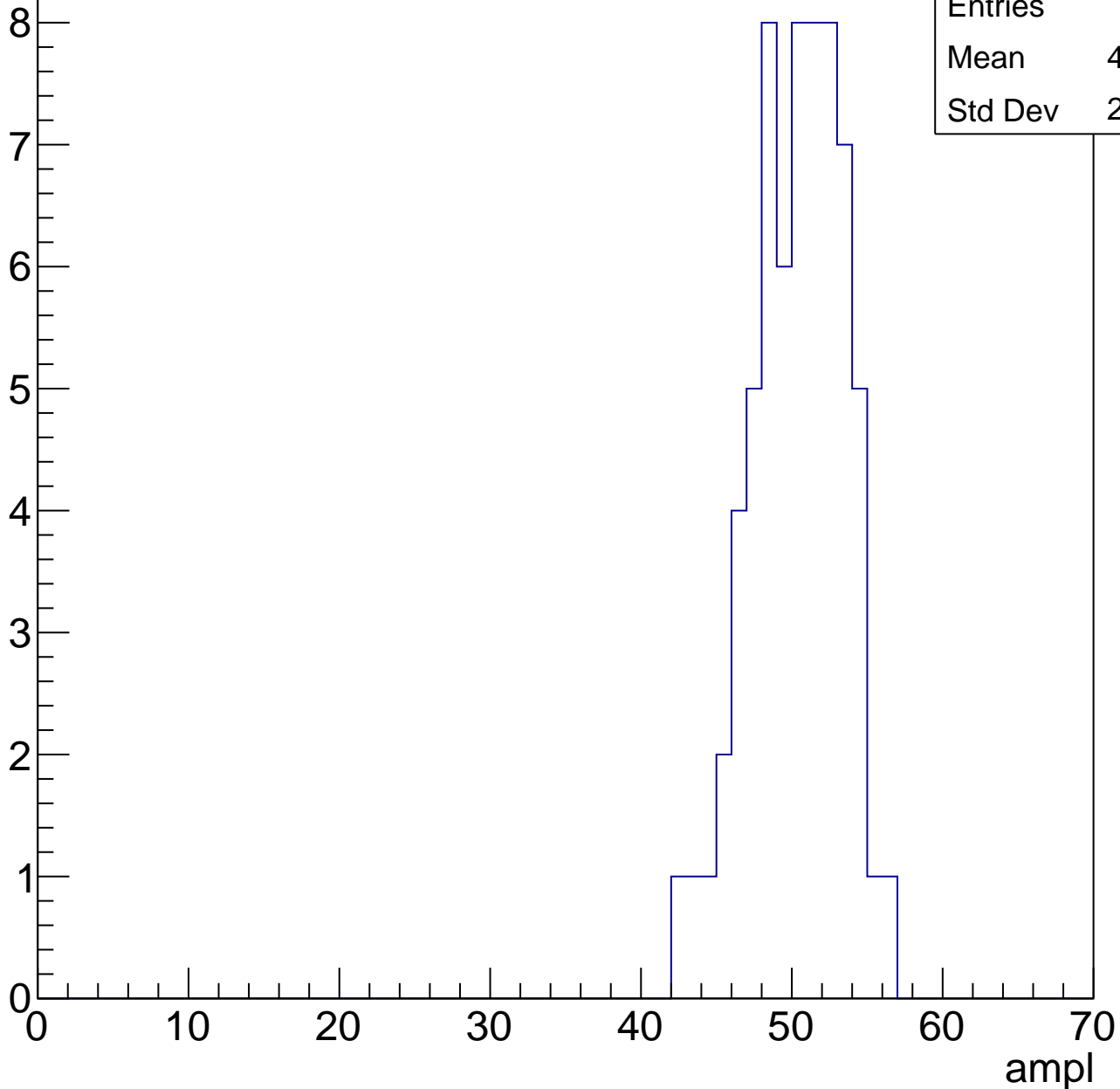


B0L001S, U19-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1

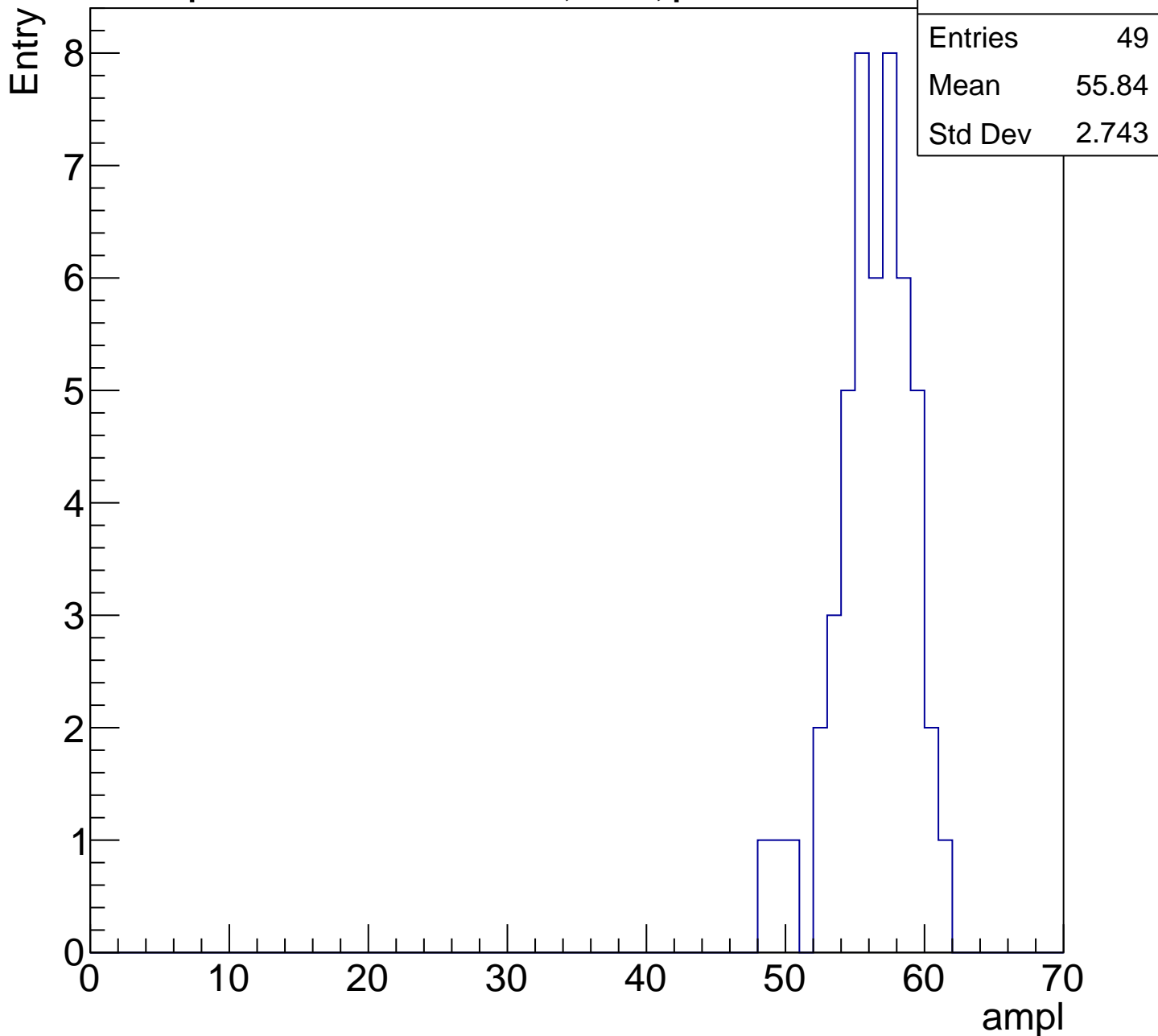
Entry

Entries	66
Mean	49.88
Std Dev	2.987



B0L001S, U19-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U19-ch65, adc5

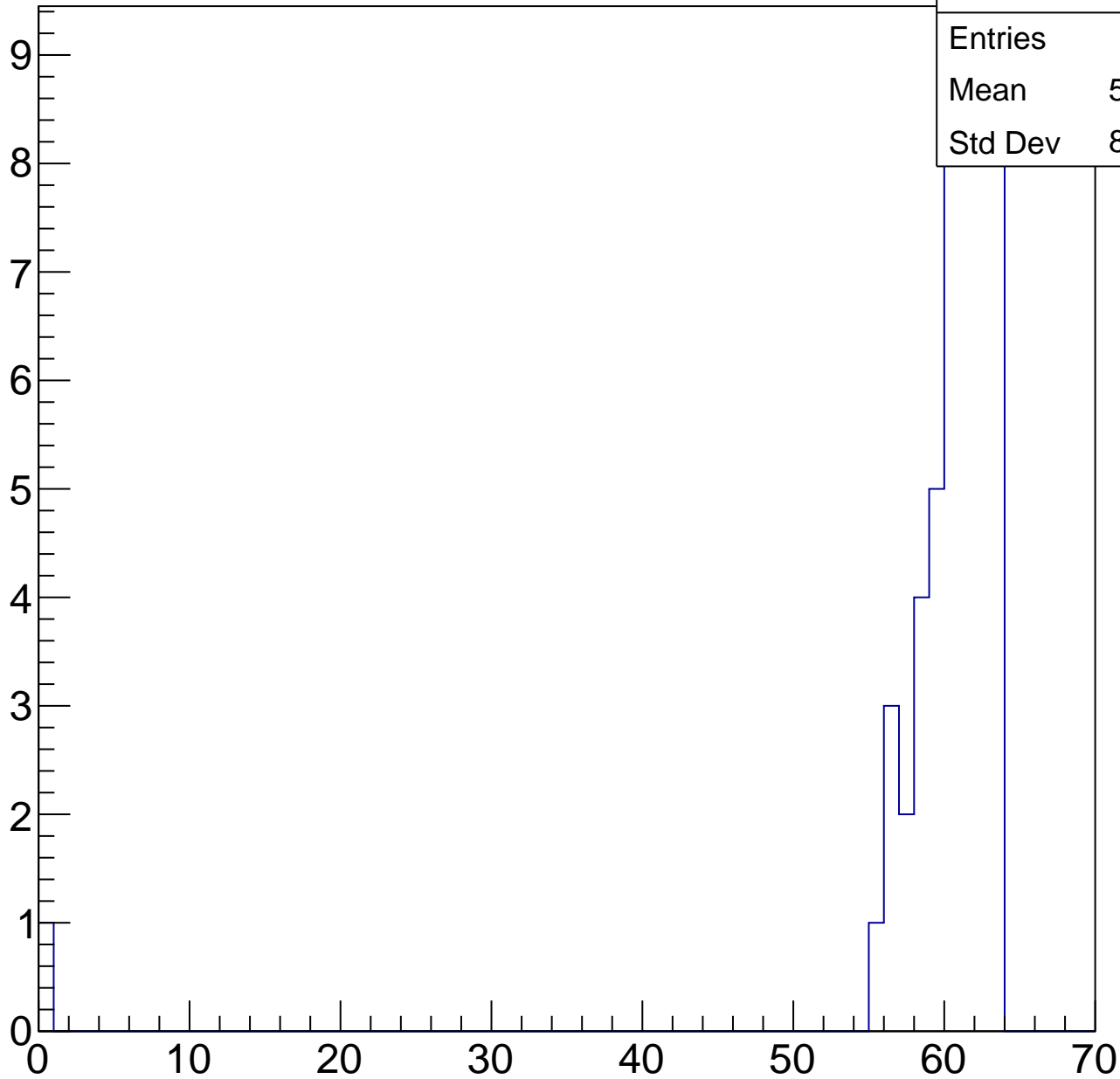
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.12
Std Dev	8.712

ampl



B0L001S, U19-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U19-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch66, adc0

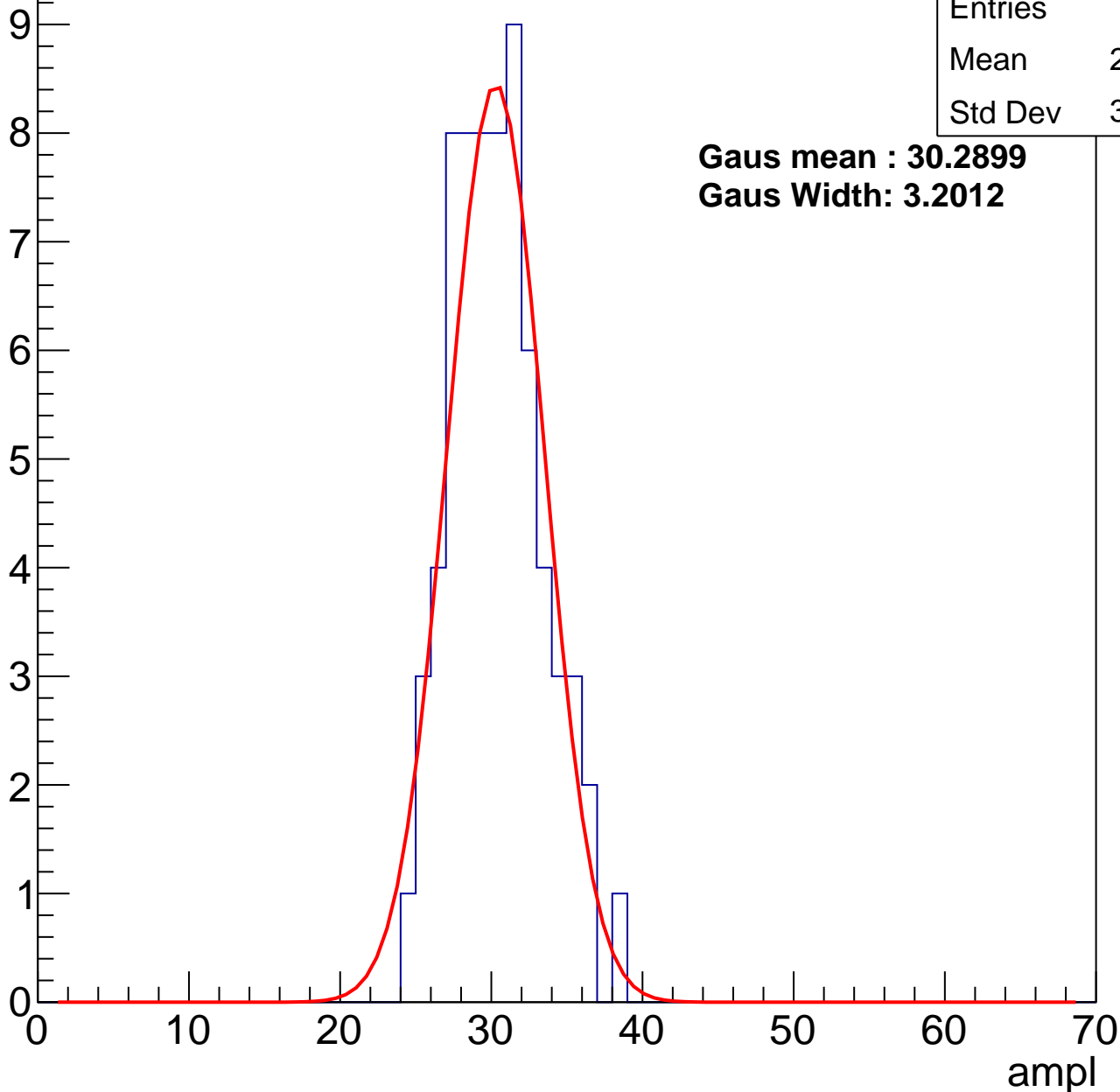
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	29.93
Std Dev	3.006

Gaus mean : 30.2899

Gaus Width: 3.2012



B0L001S, U19-ch66, adc1

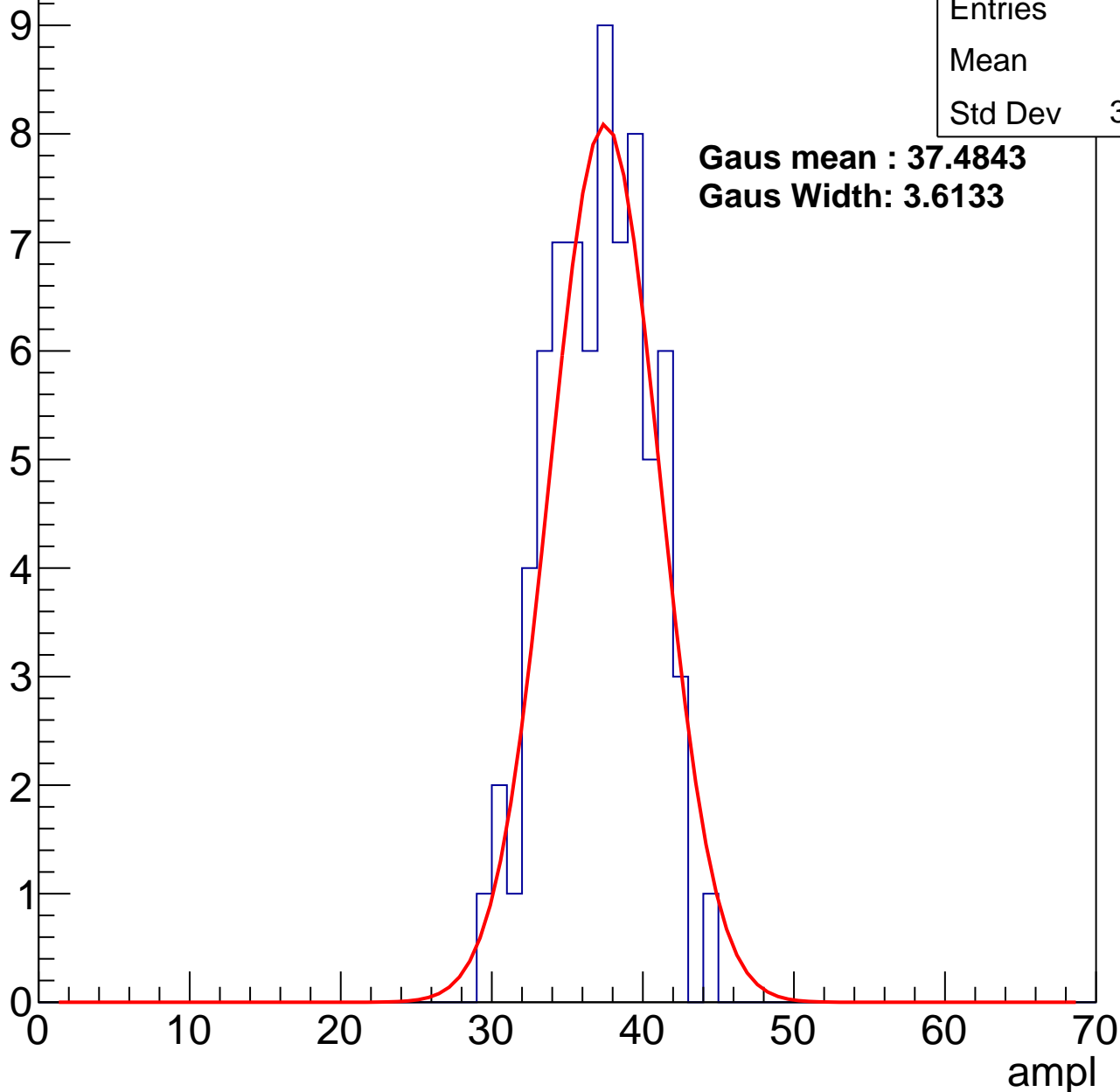
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	36.6
Std Dev	3.276

Gaus mean : 37.4843

Gaus Width: 3.6133



B0L001S, U19-ch66, adc2

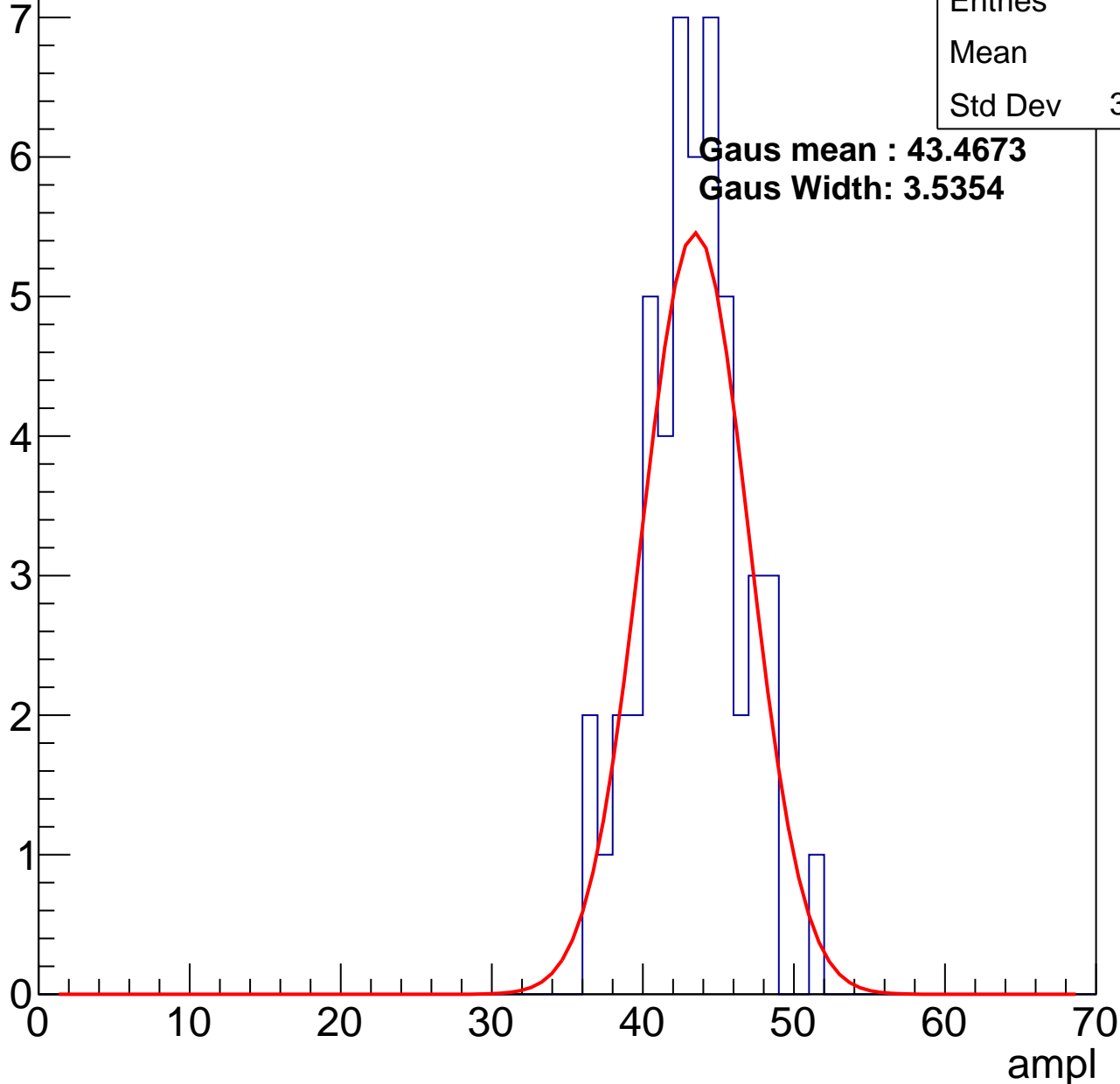
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	42.8
Std Dev	3.206

Gaus mean : 43.4673

Gaus Width: 3.5354

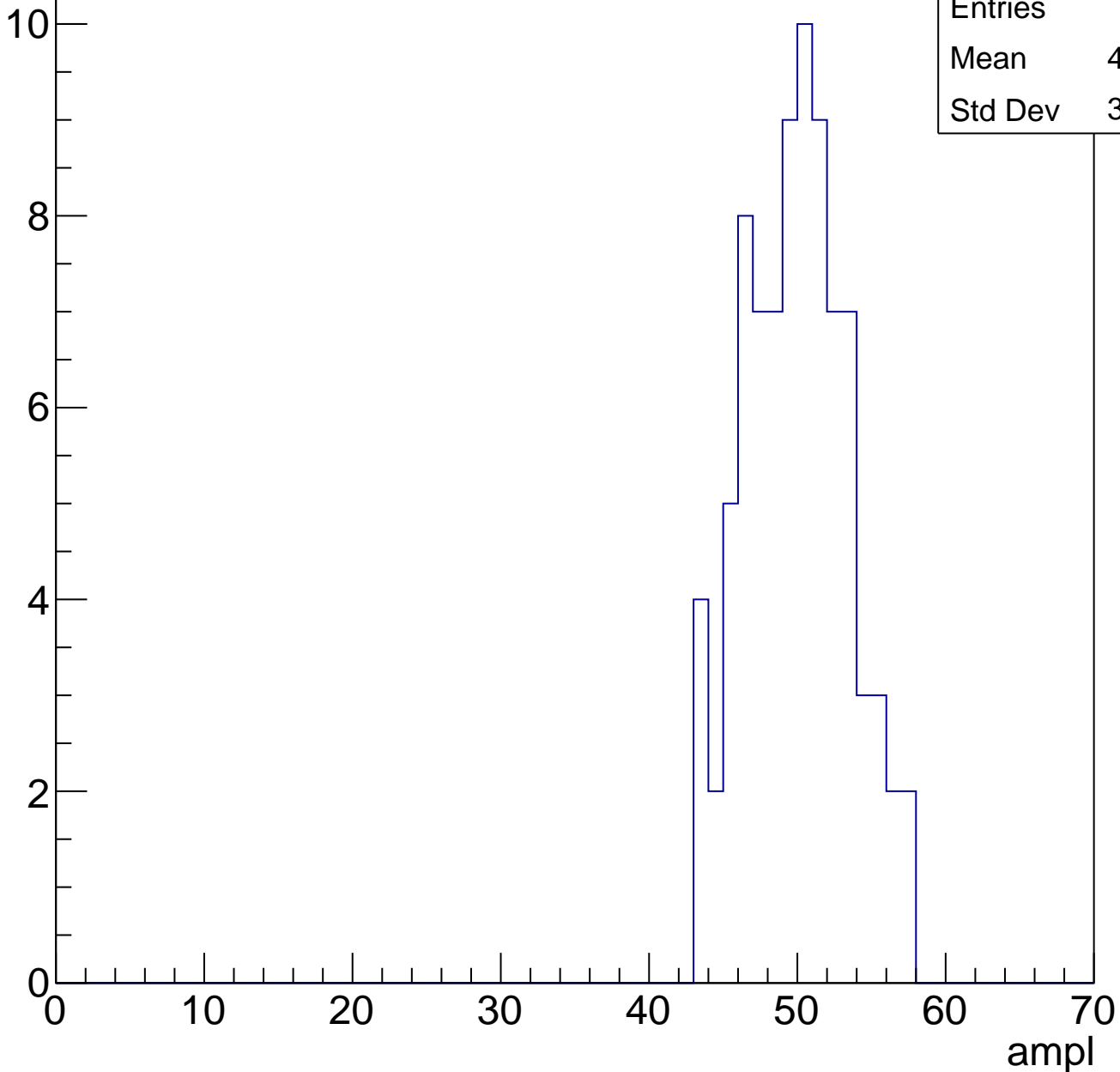


B0L001S, U19-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	49.48
Std Dev	3.415

Entry

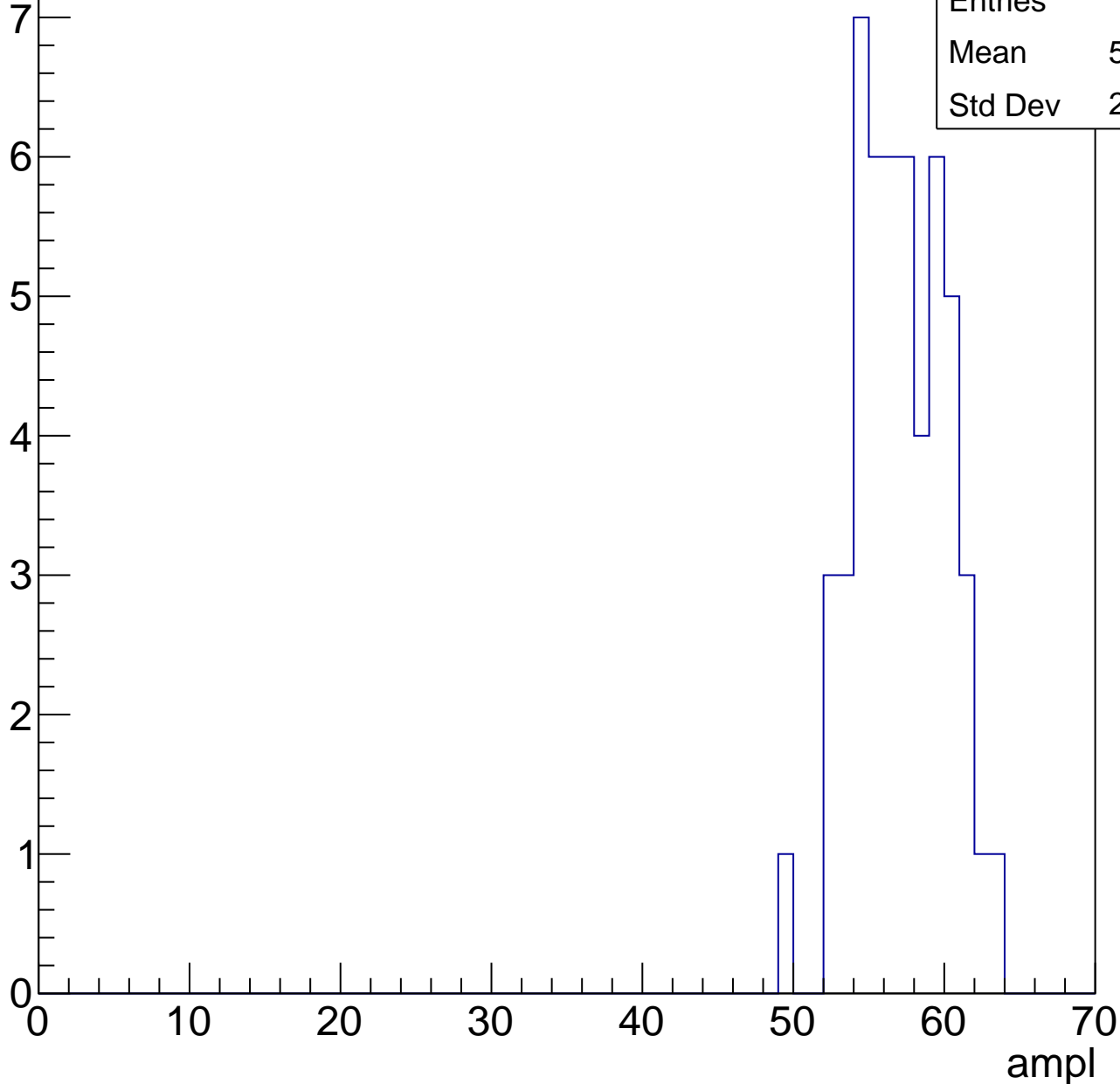


B0L001S, U19-ch66, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.62
Std Dev	2.956

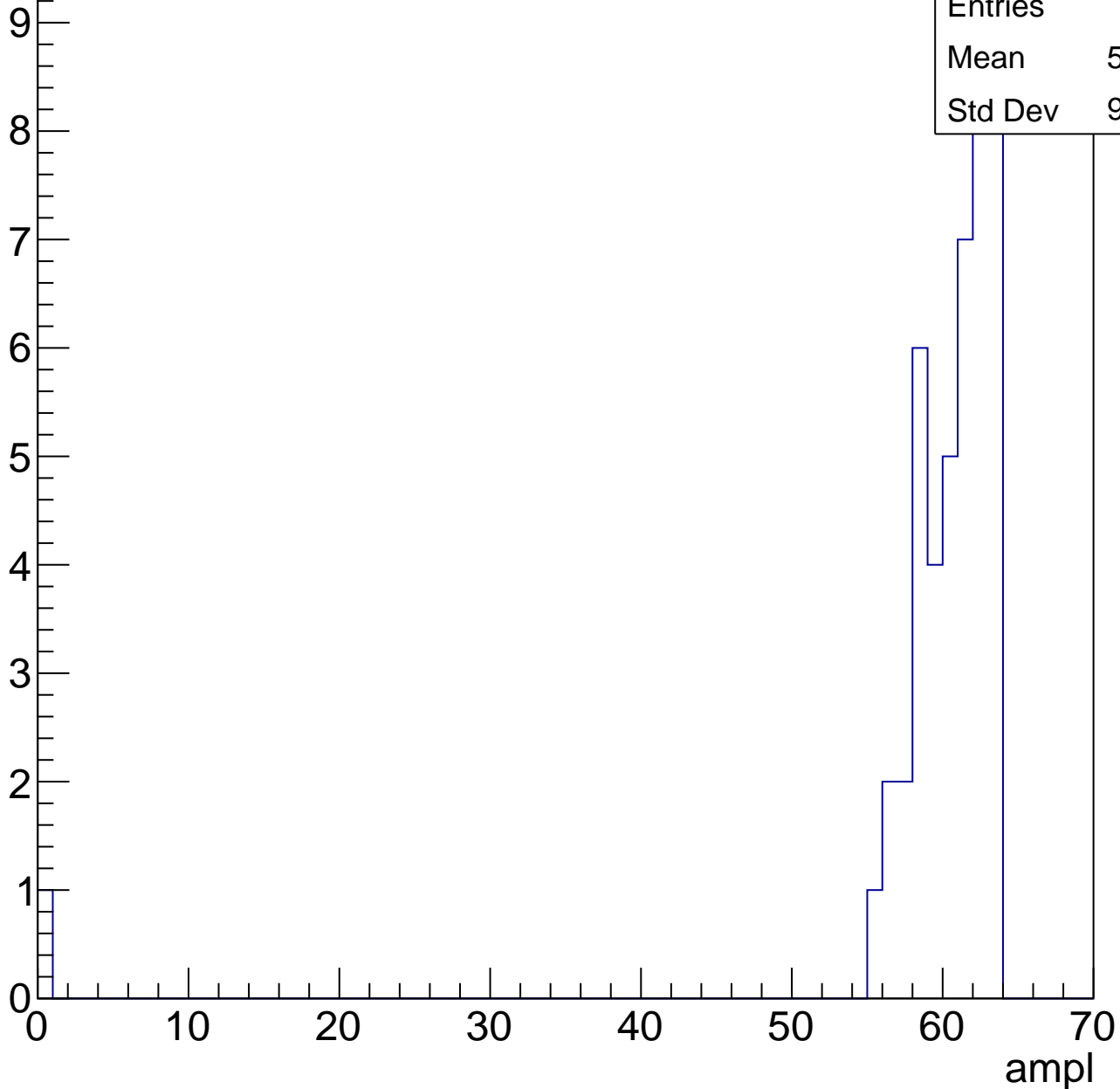


B0L001S, U19-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.98
Std Dev	9.154



B0L001S, U19-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch67, adc0

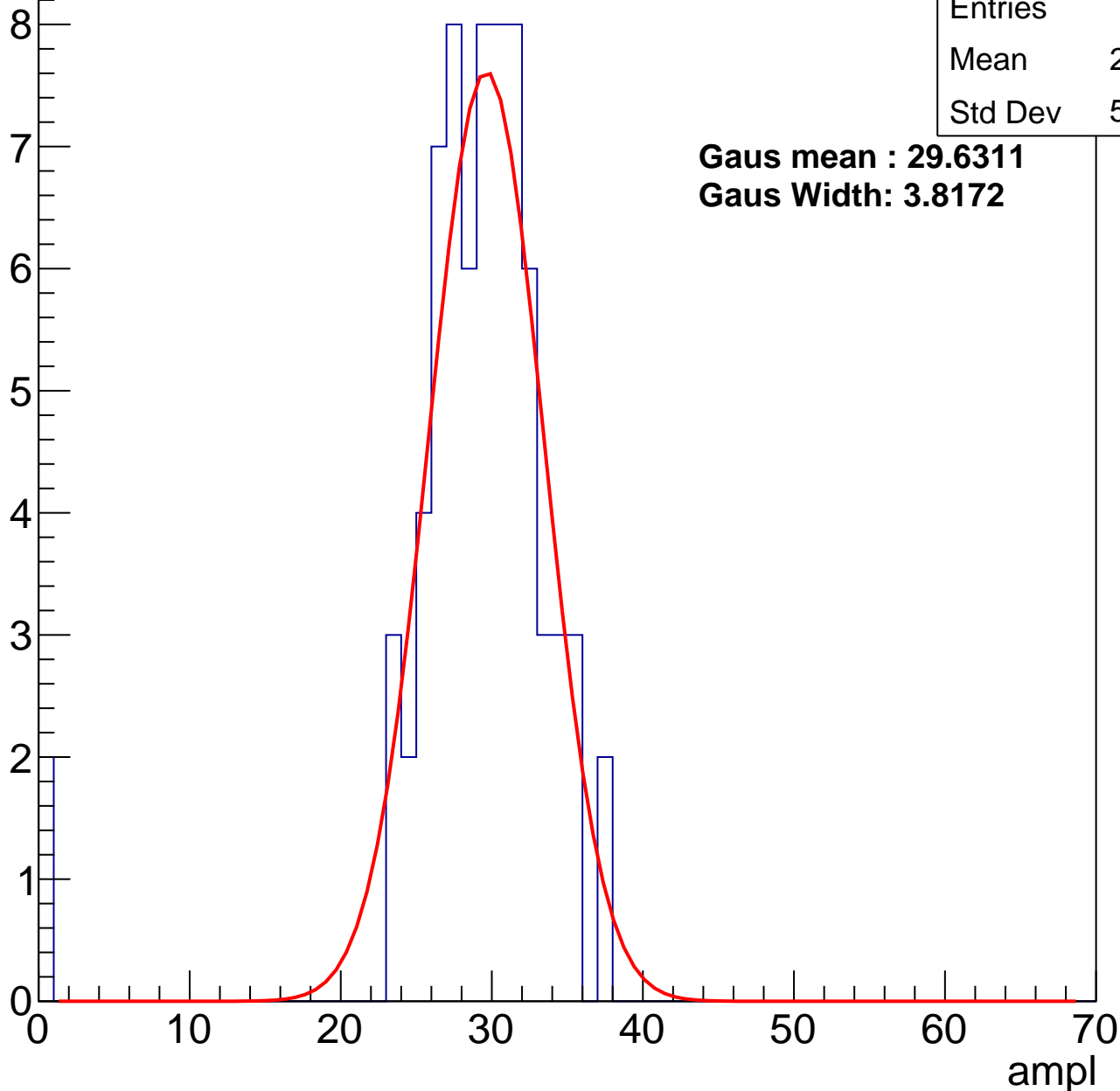
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	28.42
Std Dev	5.774

Gaus mean : 29.6311

Gaus Width: 3.8172



B0L001S, U19-ch67, adc1

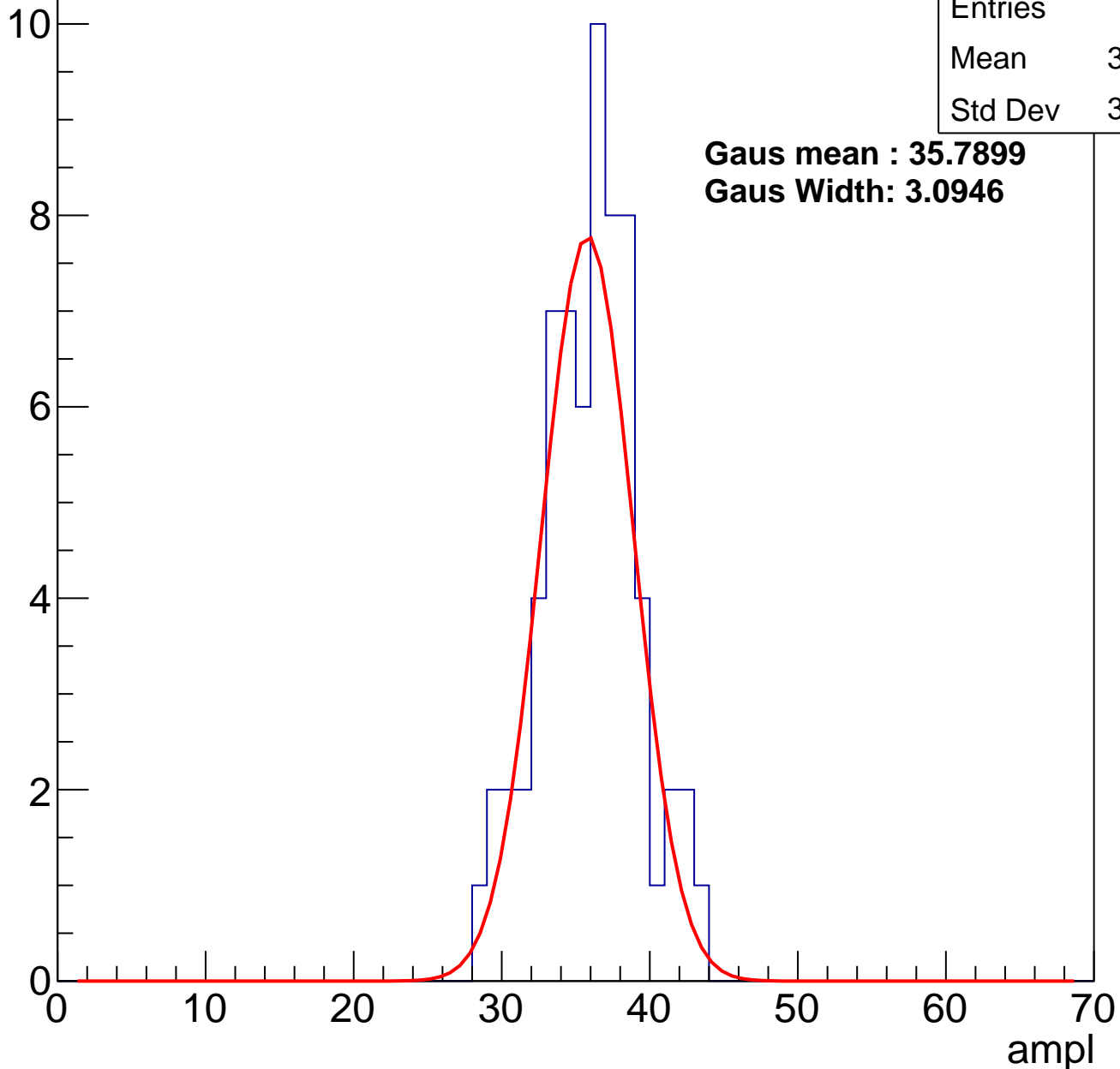
calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	35.52
Std Dev	3.215

Gaus mean : 35.7899

Gaus Width: 3.0946

Entry



B0L001S, U19-ch67, adc2

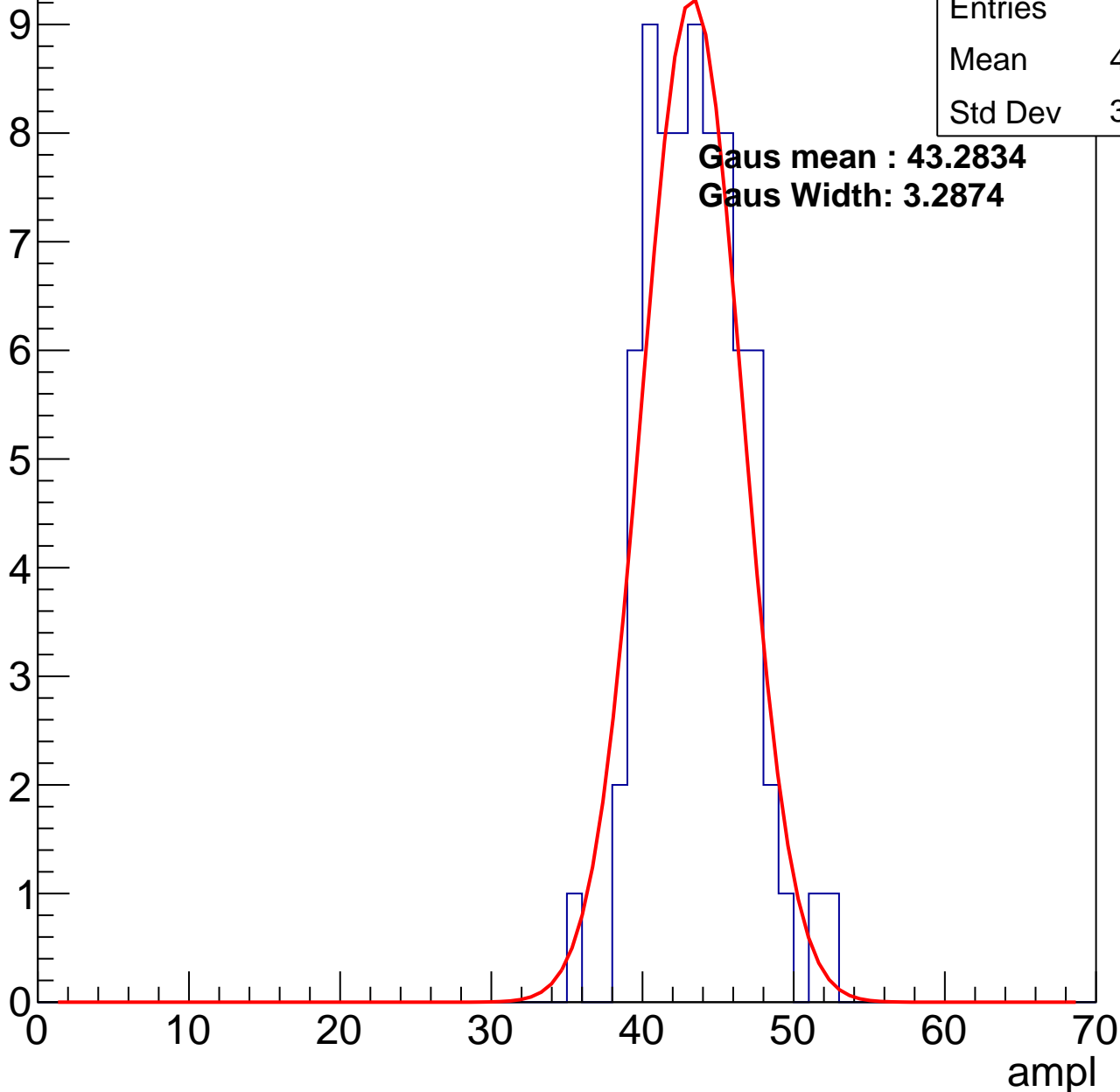
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	43.08
Std Dev	3.145

Gaus mean : 43.2834

Gaus Width: 3.2874

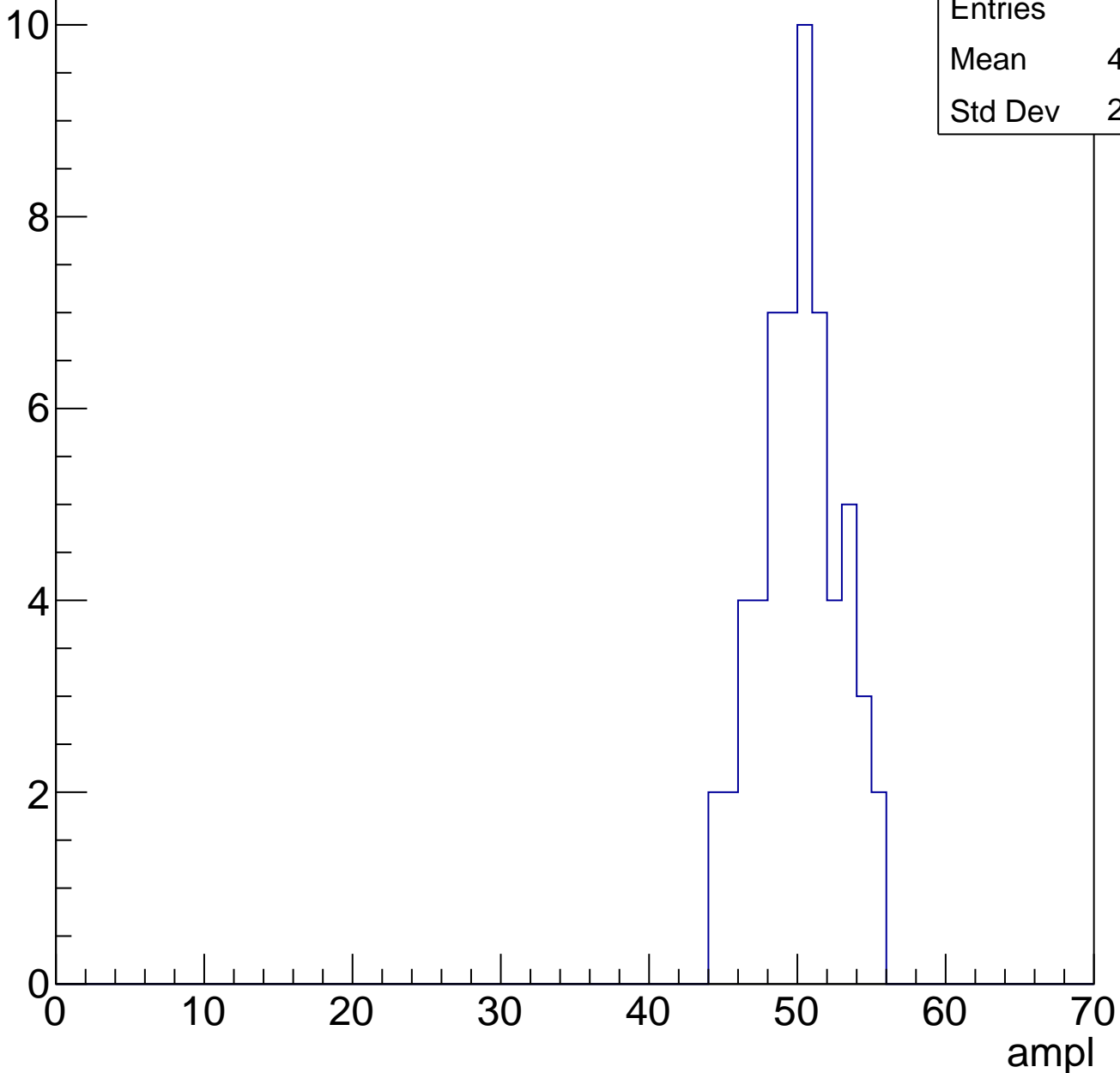


B0L001S, U19-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	49.67
Std Dev	2.704

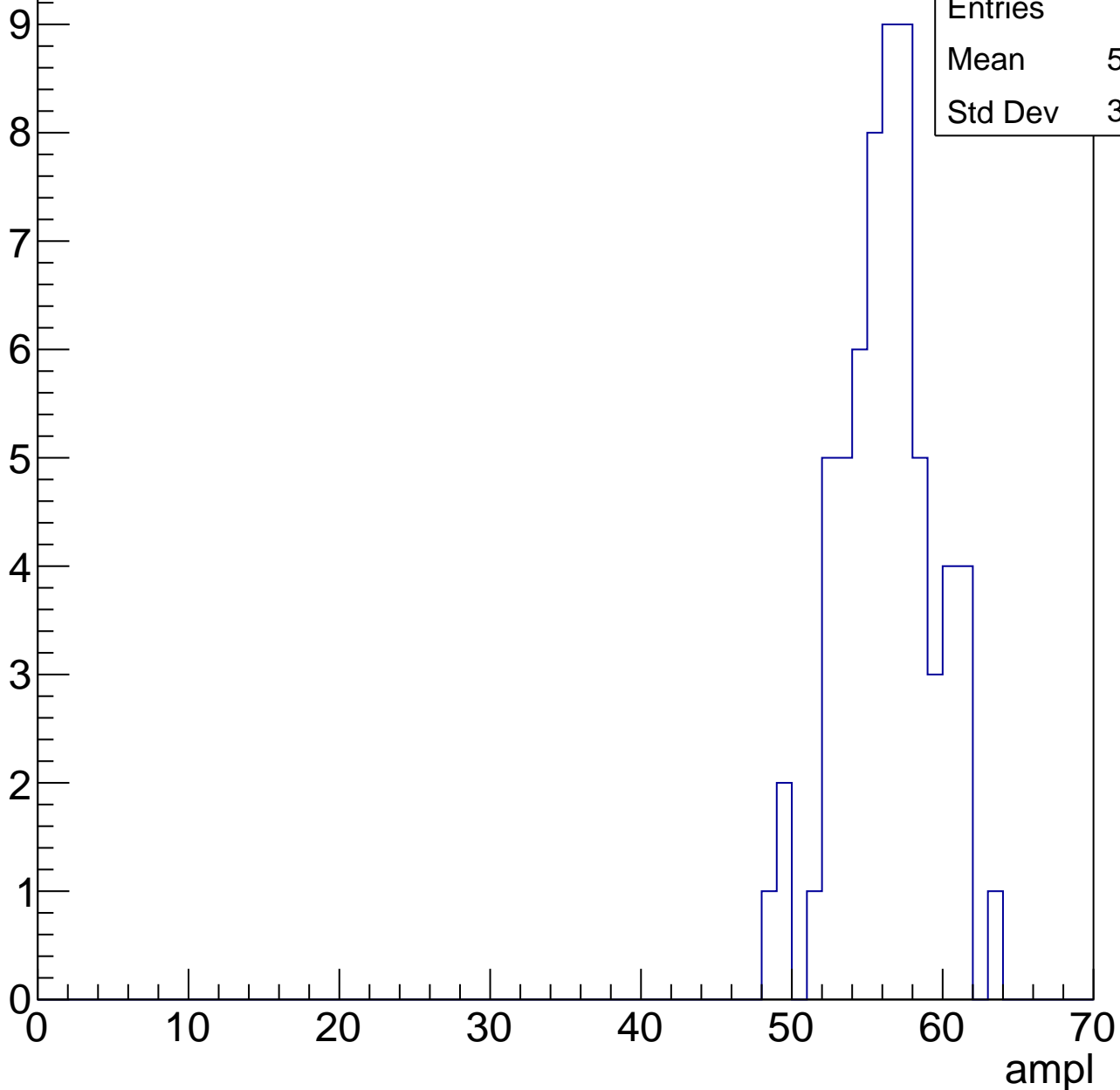


B0L001S, U19-ch67, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	55.83
Std Dev	3.114

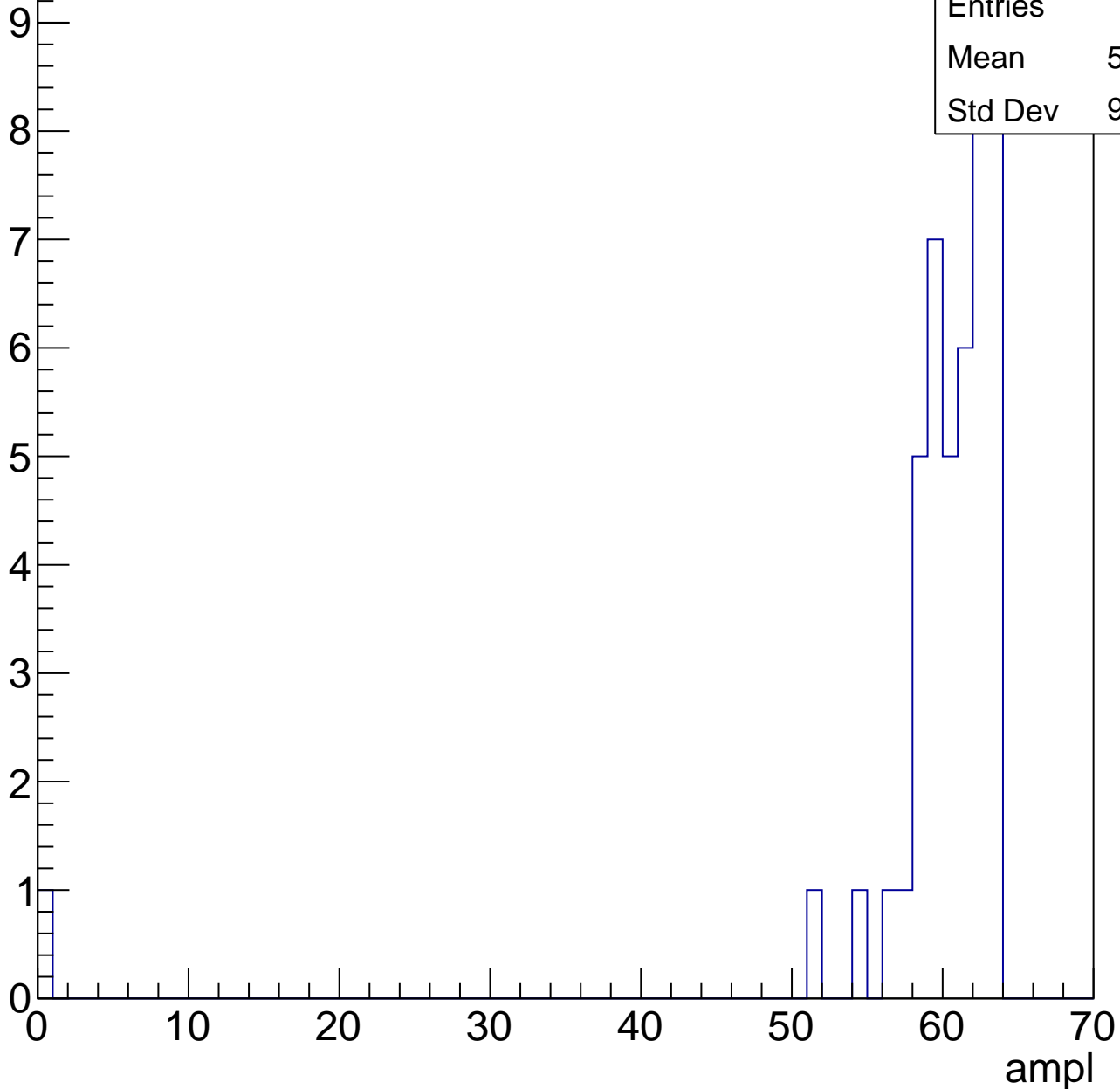


B0L001S, U19-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

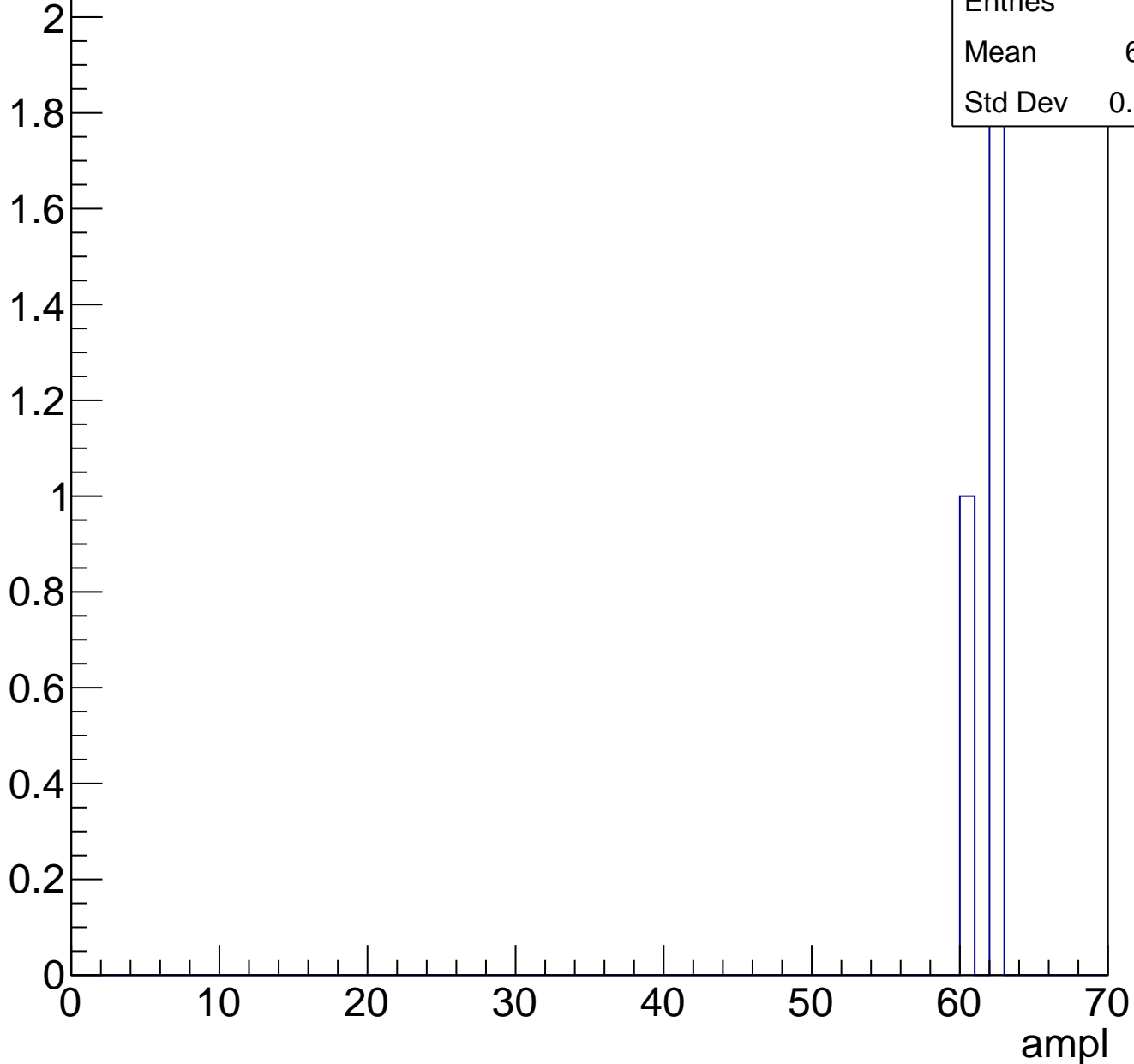
Entries	45
Mean	58.89
Std Dev	9.229



B0L001S, U19-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U19-ch68, adc0

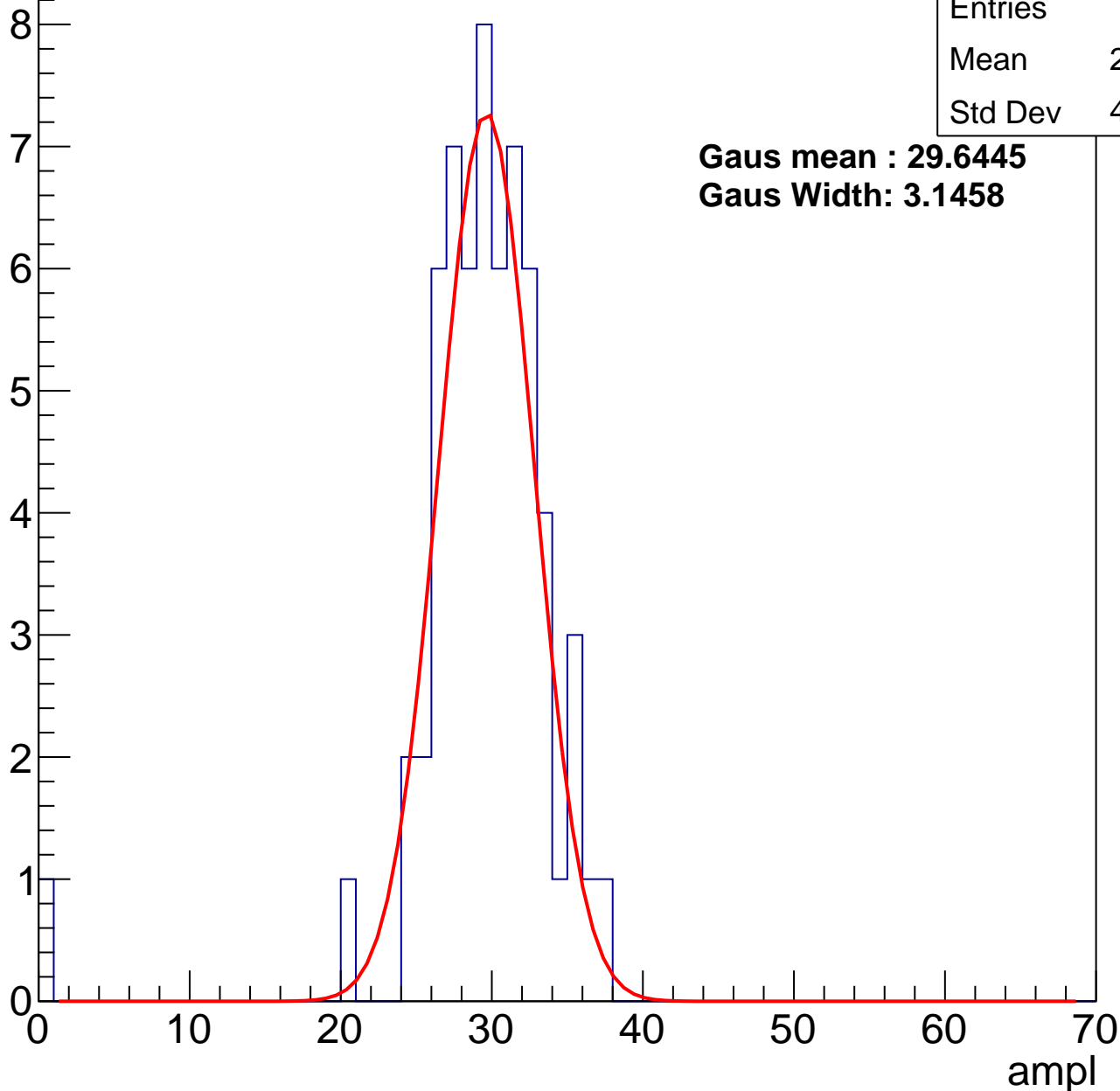
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	28.97
Std Dev	4.902

Gaus mean : 29.6445

Gaus Width: 3.1458



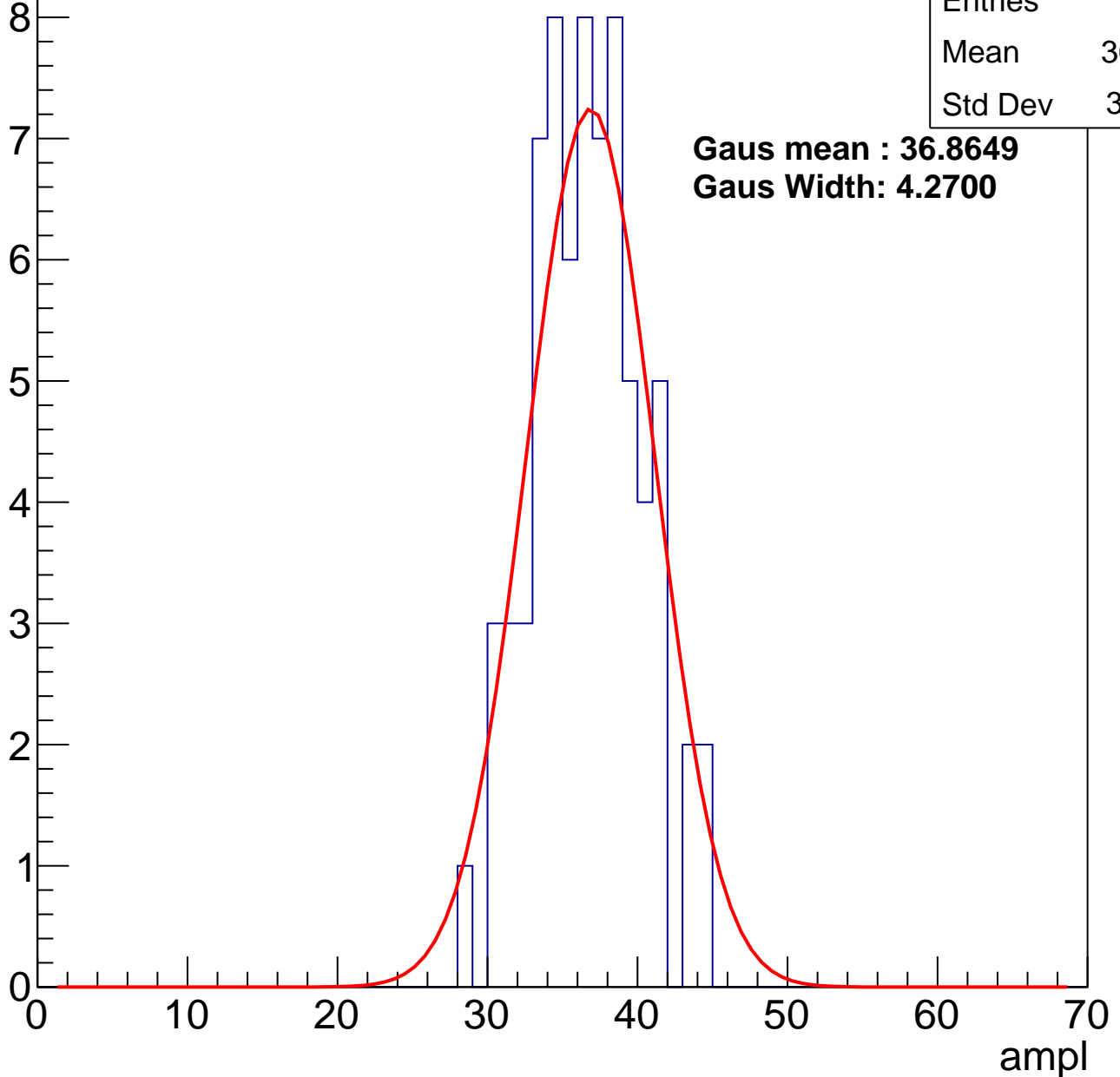
B0L001S, U19-ch68, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.18
Std Dev	3.521

Gaus mean : 36.8649
Gaus Width: 4.2700



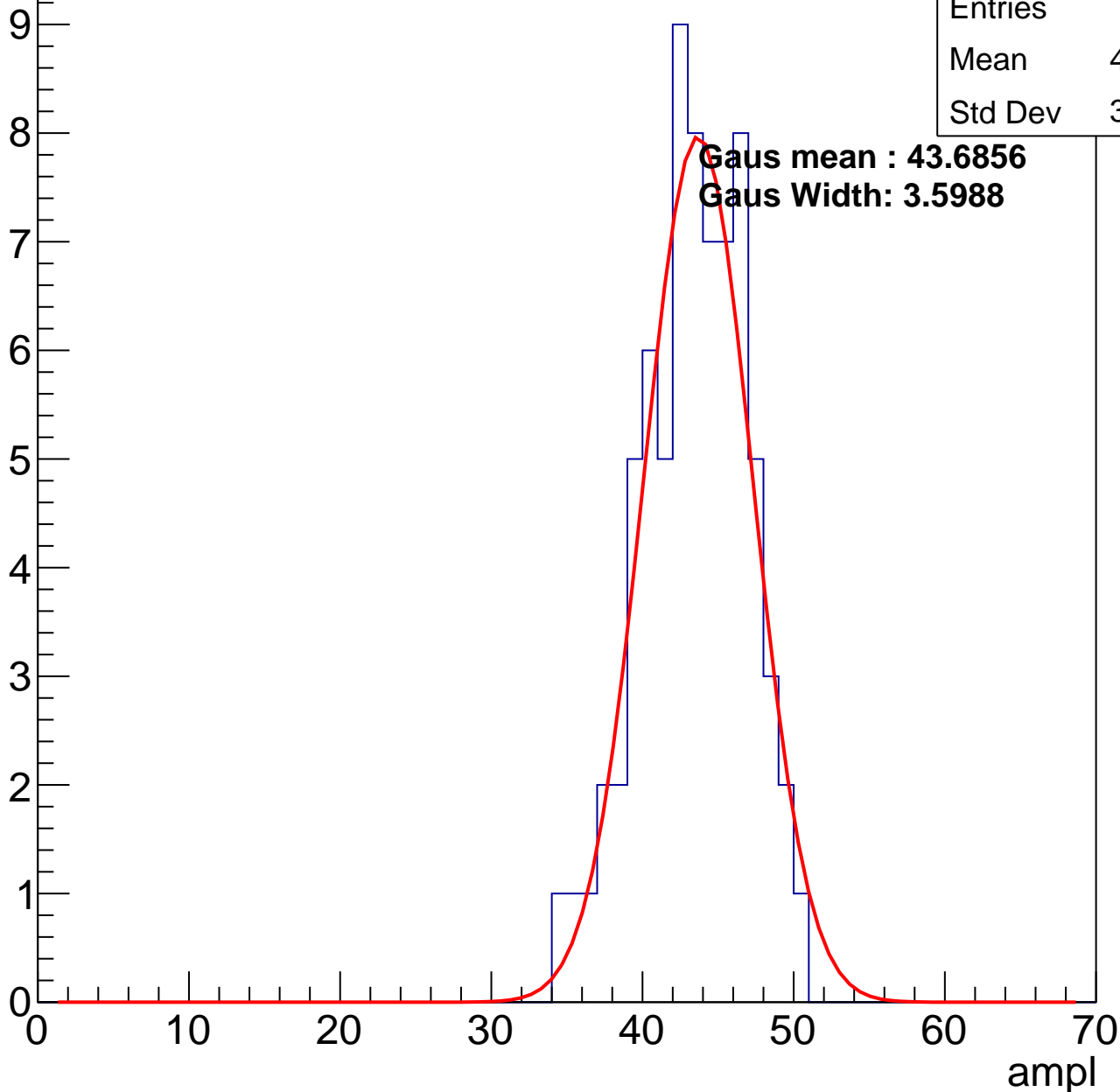
B0L001S, U19-ch68, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	42.95
Std Dev	3.436

Gaus mean : 43.6856
Gaus Width: 3.5988

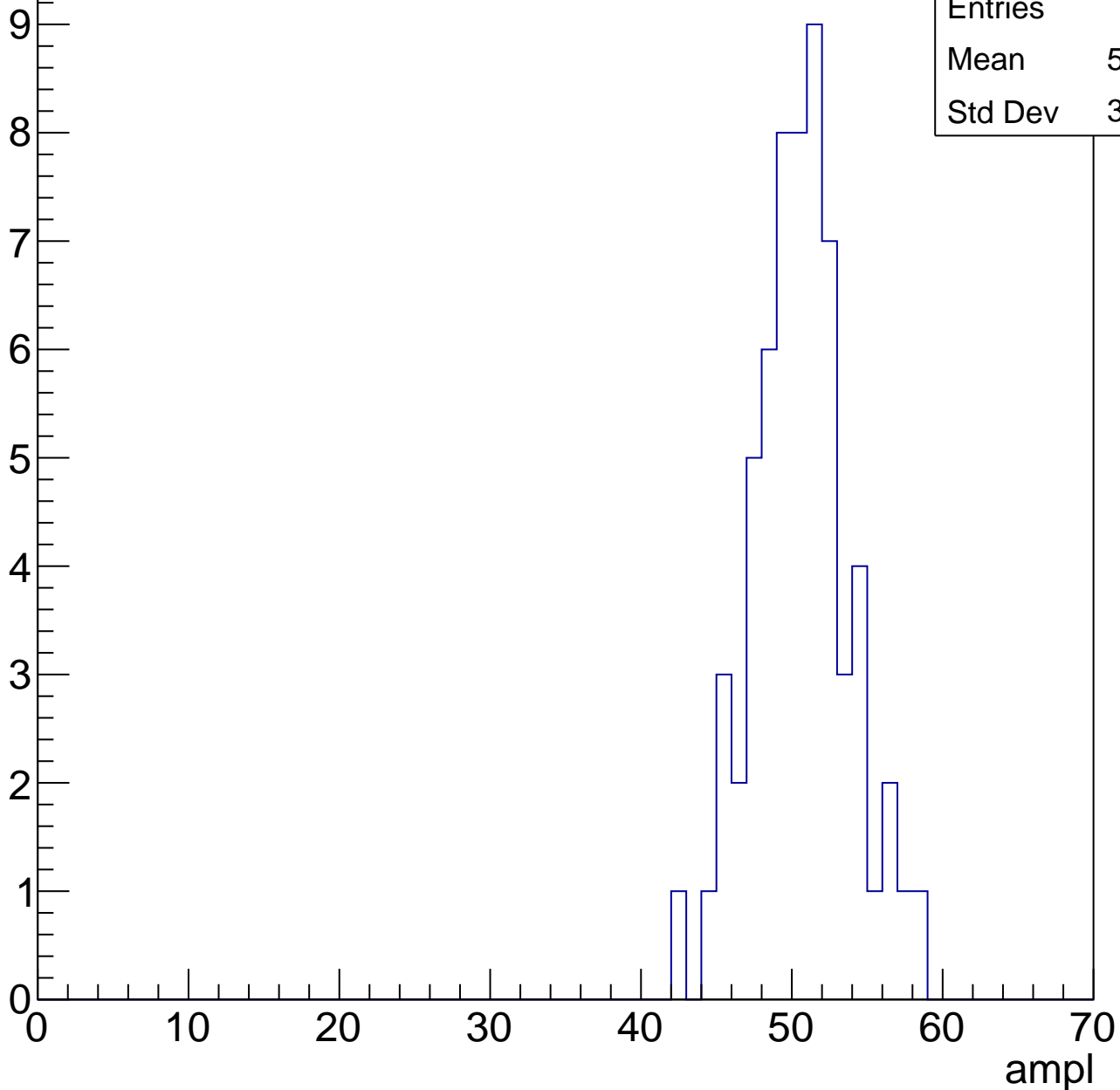


B0L001S, U19-ch68, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.13
Std Dev	3.165

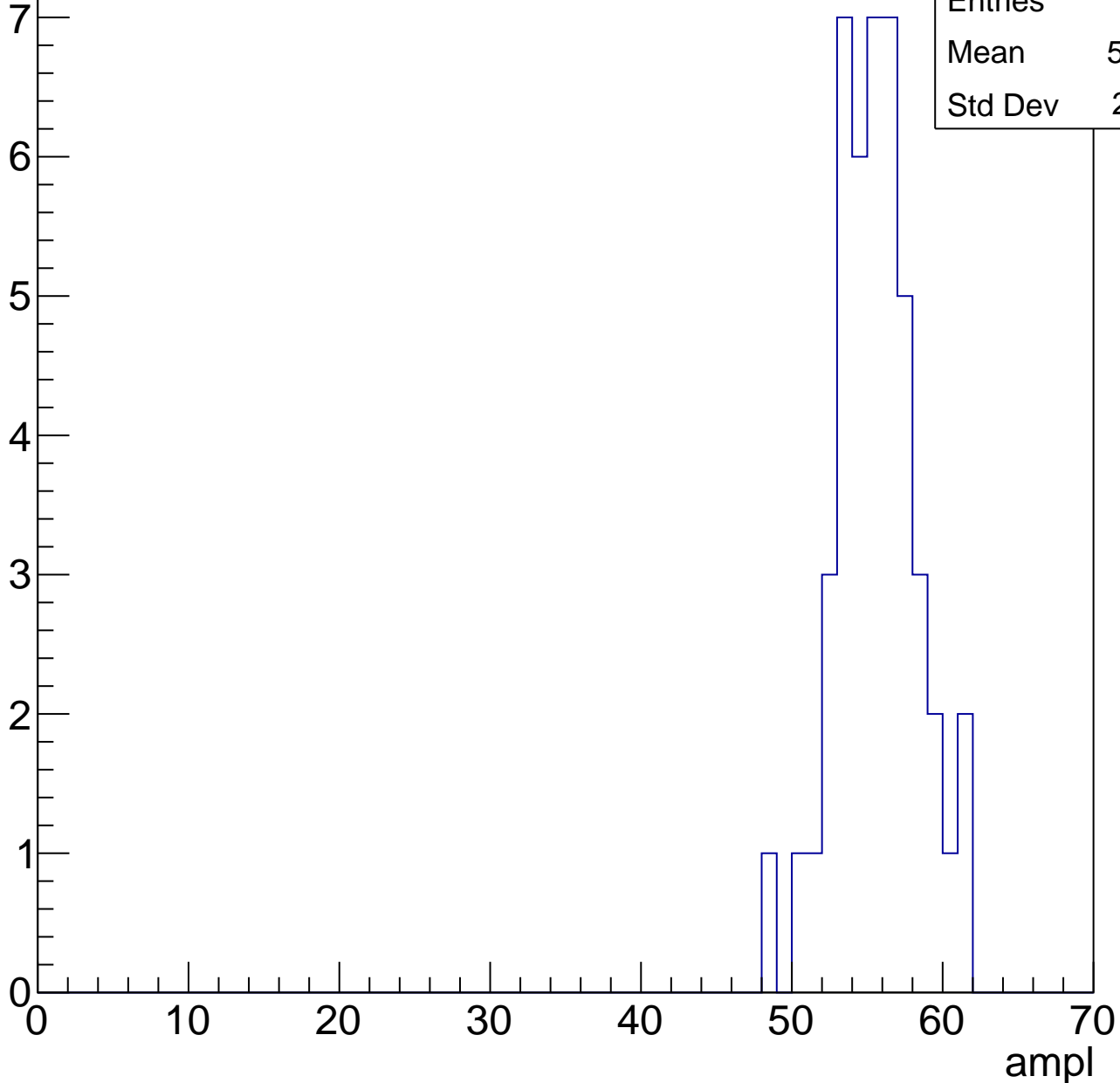


B0L001S, U19-ch68, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

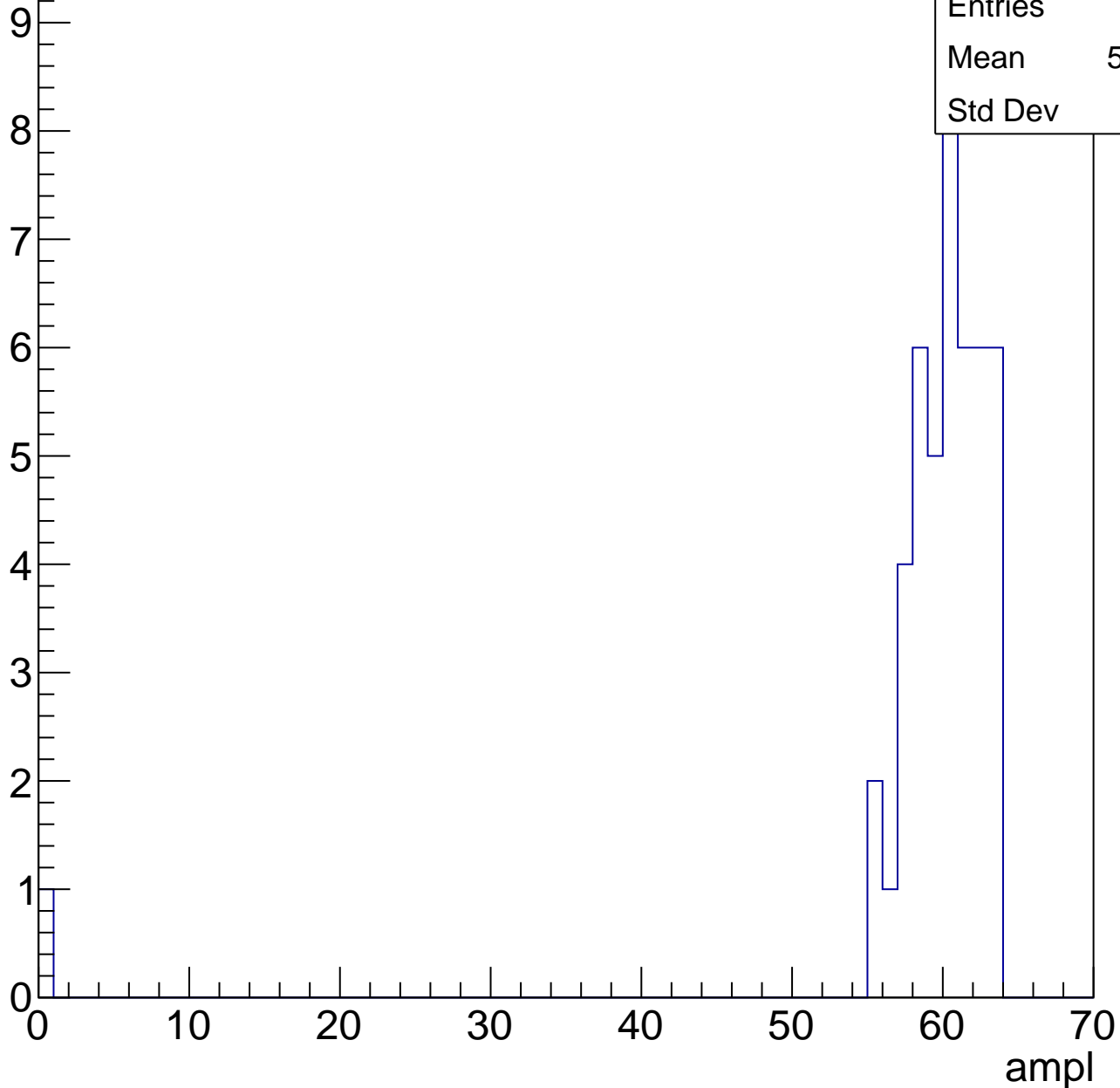
Entries	46
Mean	55.13
Std Dev	2.691



B0L001S, U19-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1

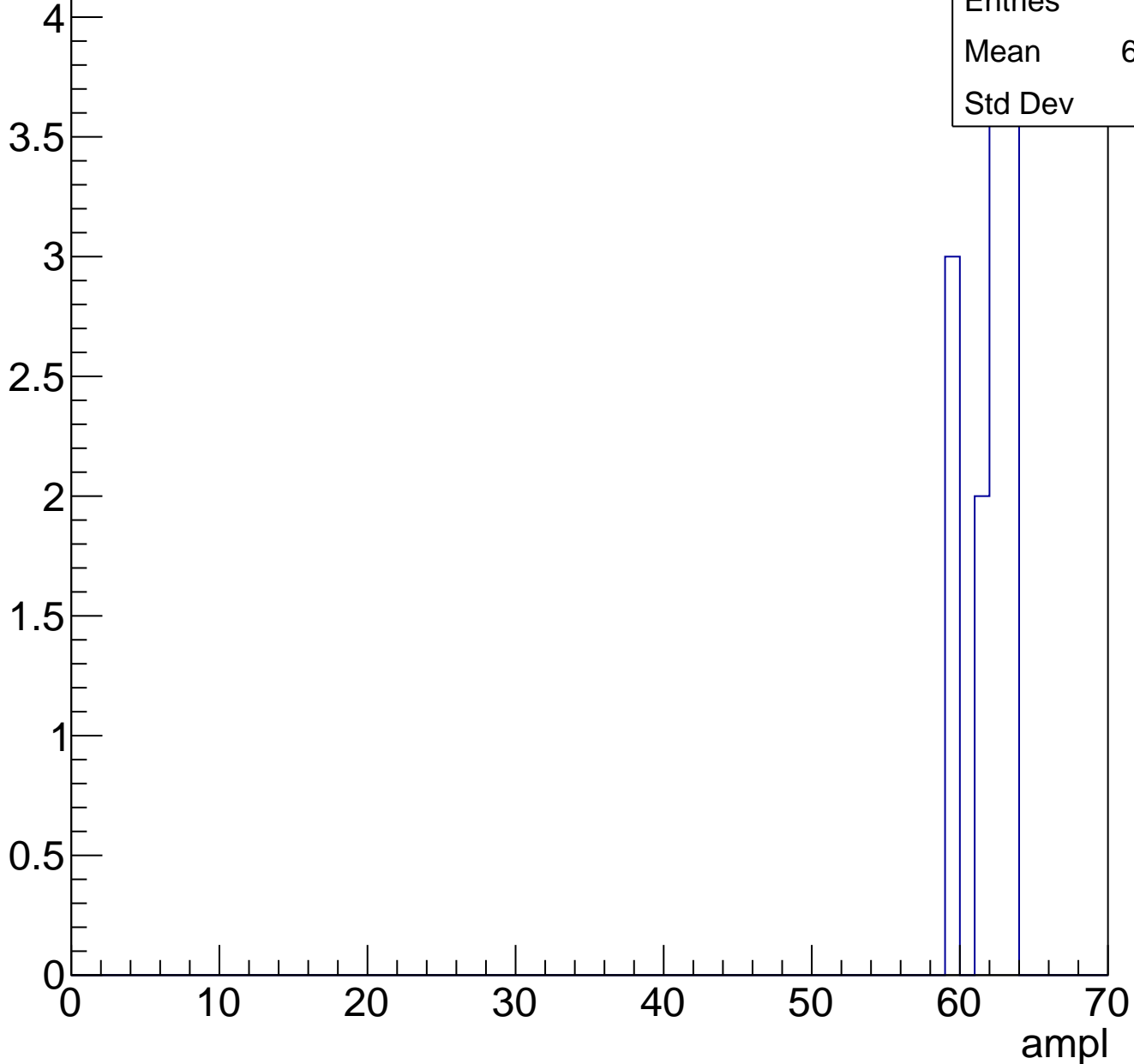
Entry



B0L001S, U19-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch69, adc0

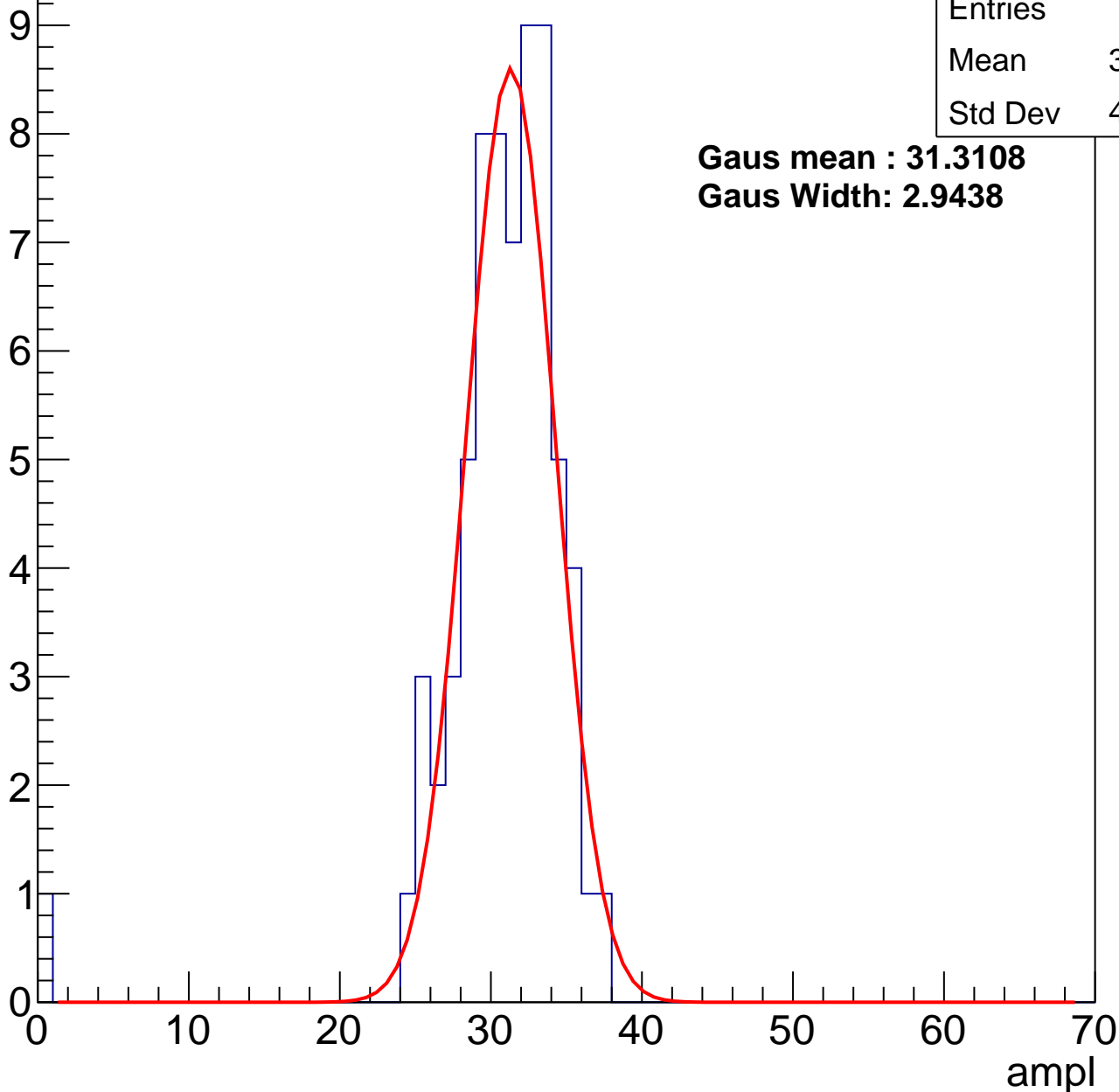
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.28
Std Dev	4.703

Gaus mean : 31.3108

Gaus Width: 2.9438



B0L001S, U19-ch69, adc1

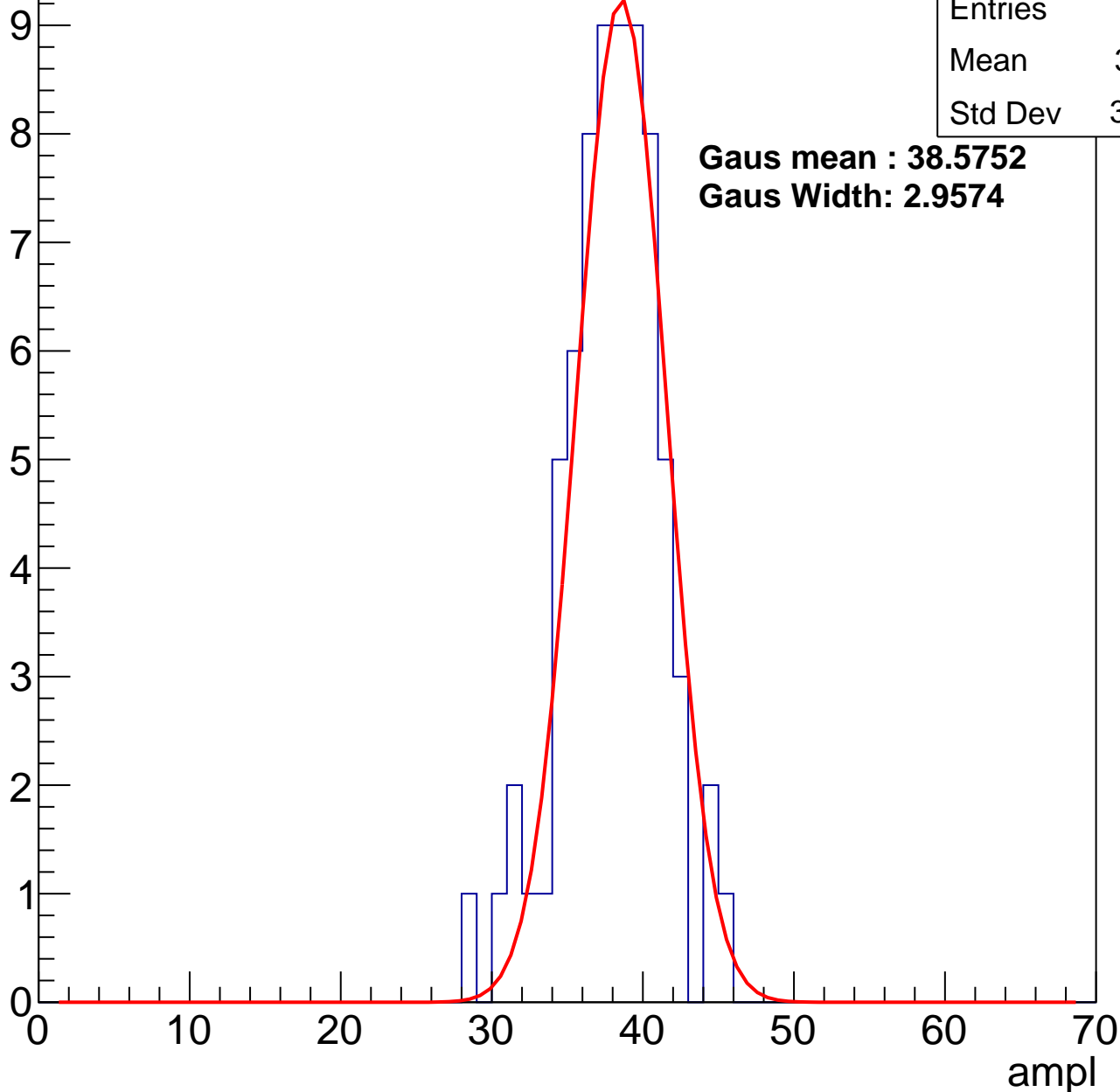
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.51
Std Dev	3.219

Gaus mean : 38.5752

Gaus Width: 2.9574



B0L001S, U19-ch69, adc2

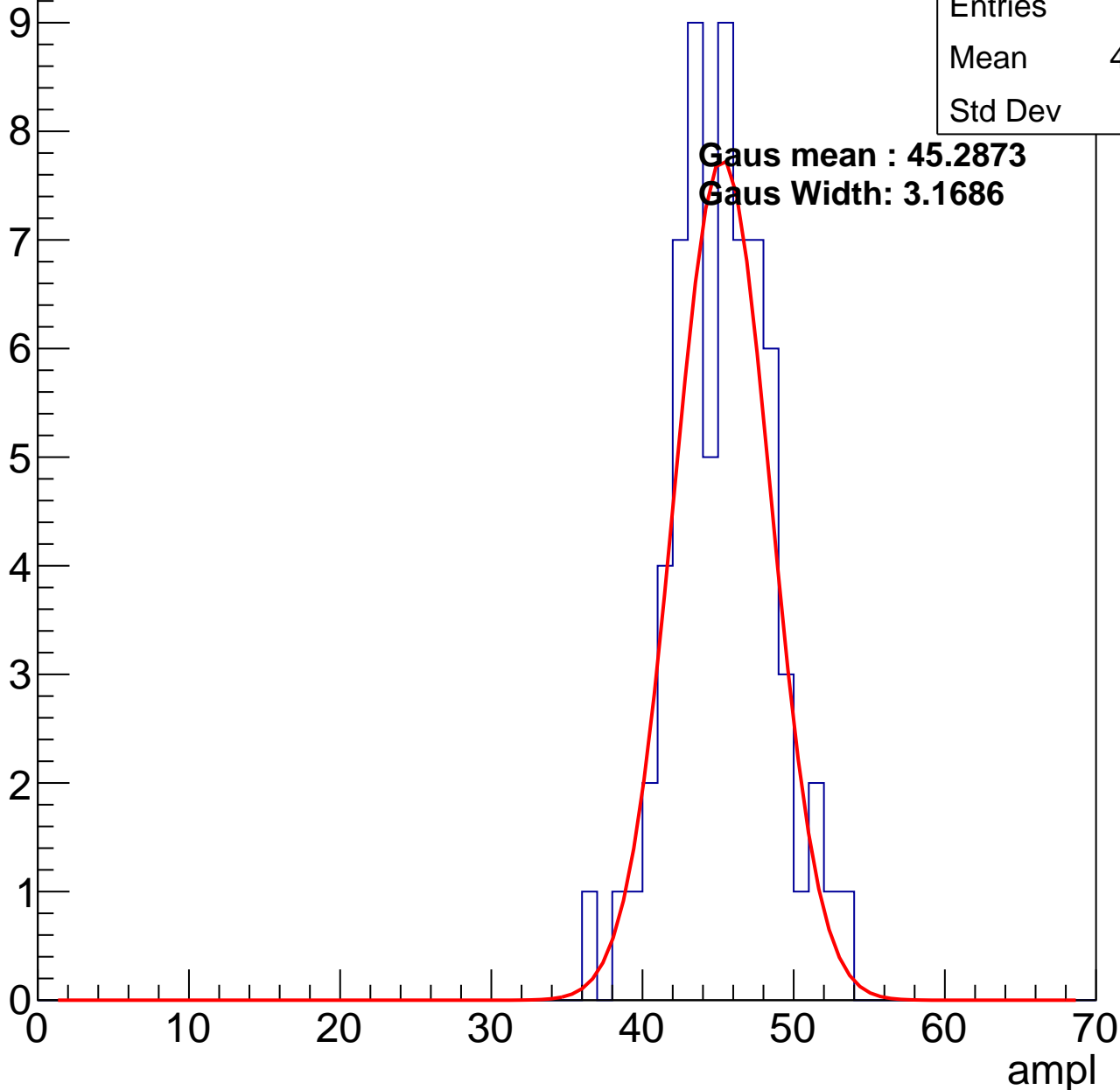
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.87
Std Dev	3.3

Gaus mean : 45.2873

Gaus Width: 3.1686

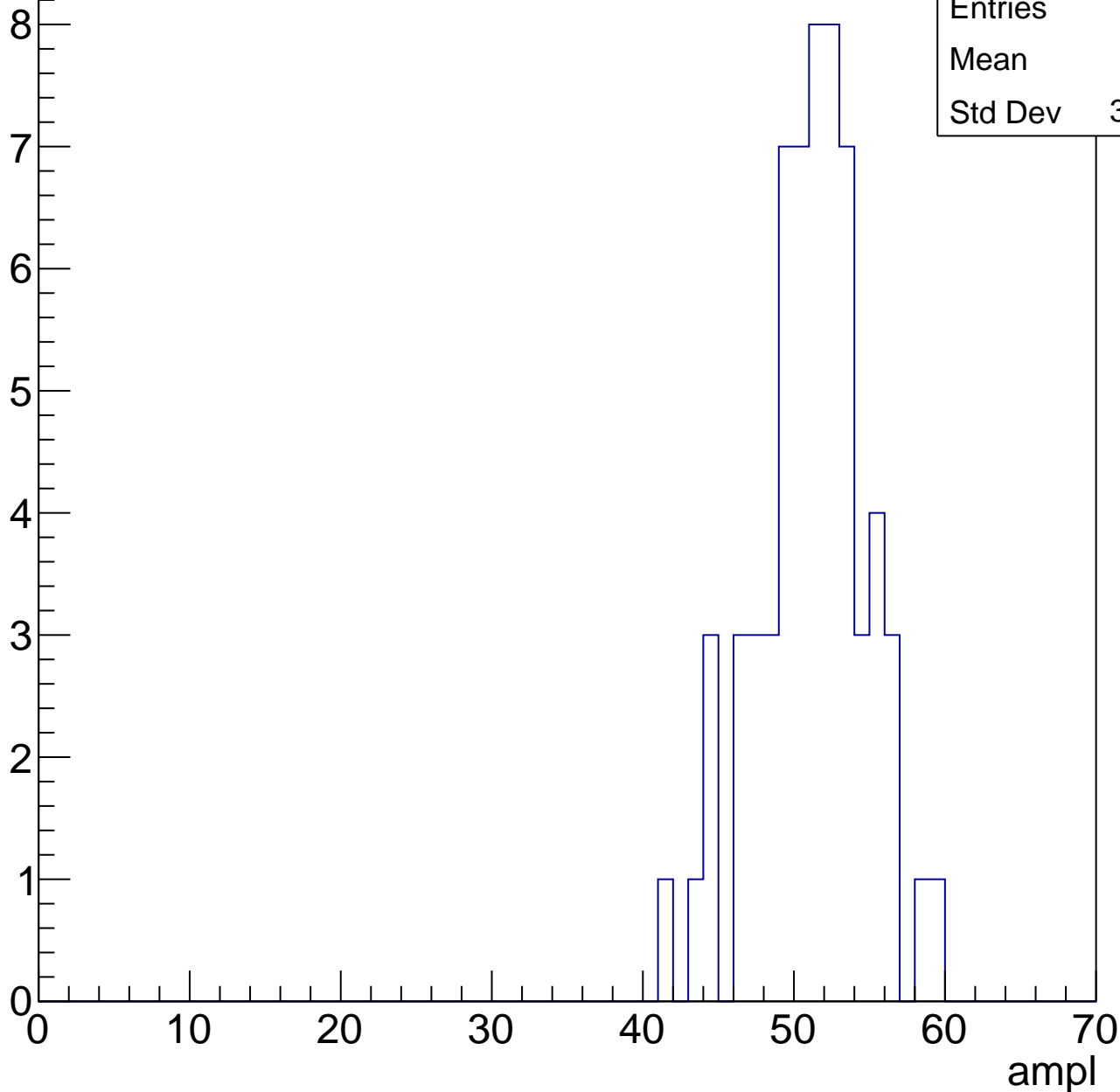


B0L001S, U19-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	50.7
Std Dev	3.575



B0L001S, U19-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	57
Mean	56.77
Std Dev	3.14

Entry

10

8

6

4

2

0

0

10

20

30

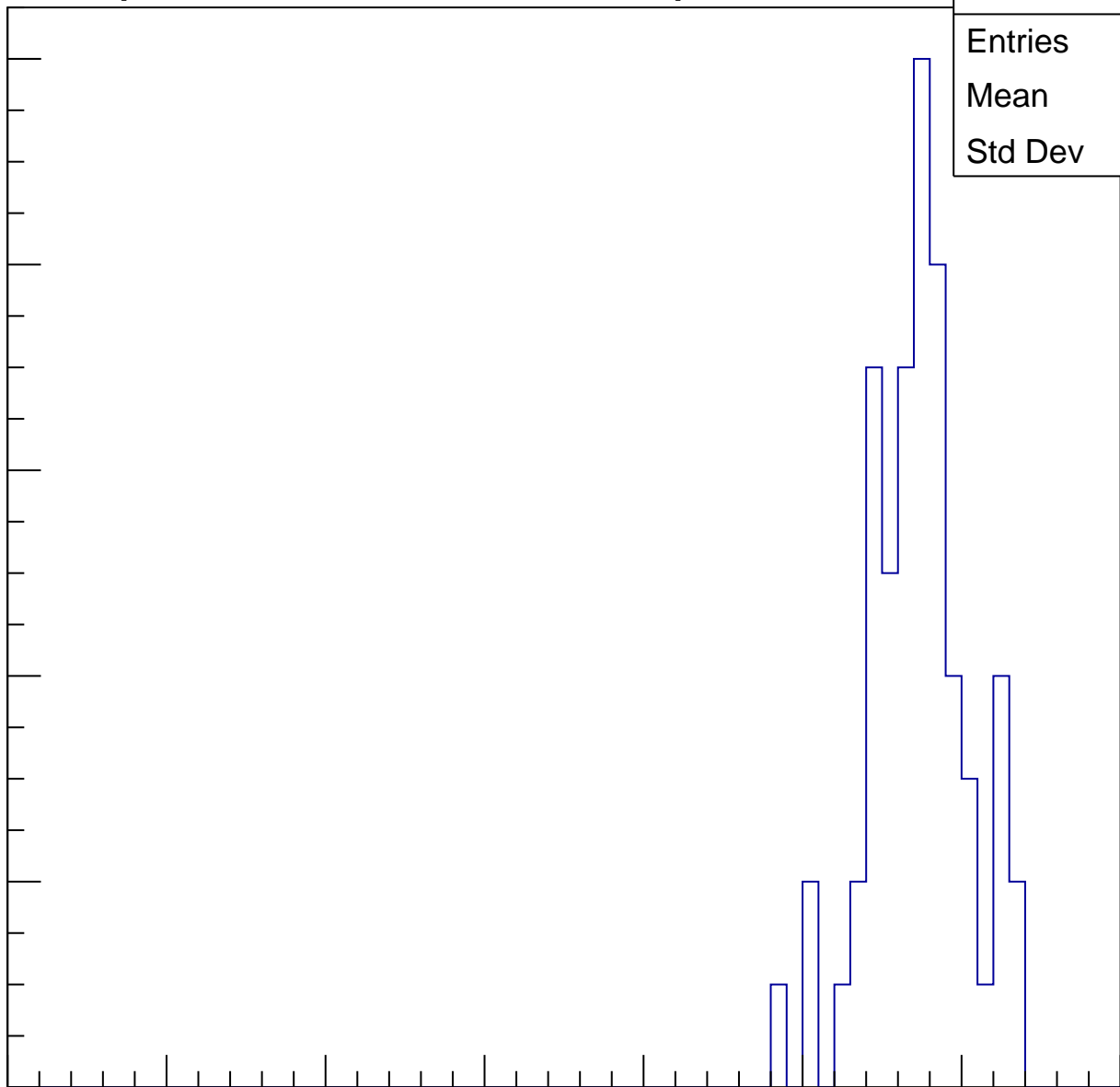
40

50

60

70

ampl

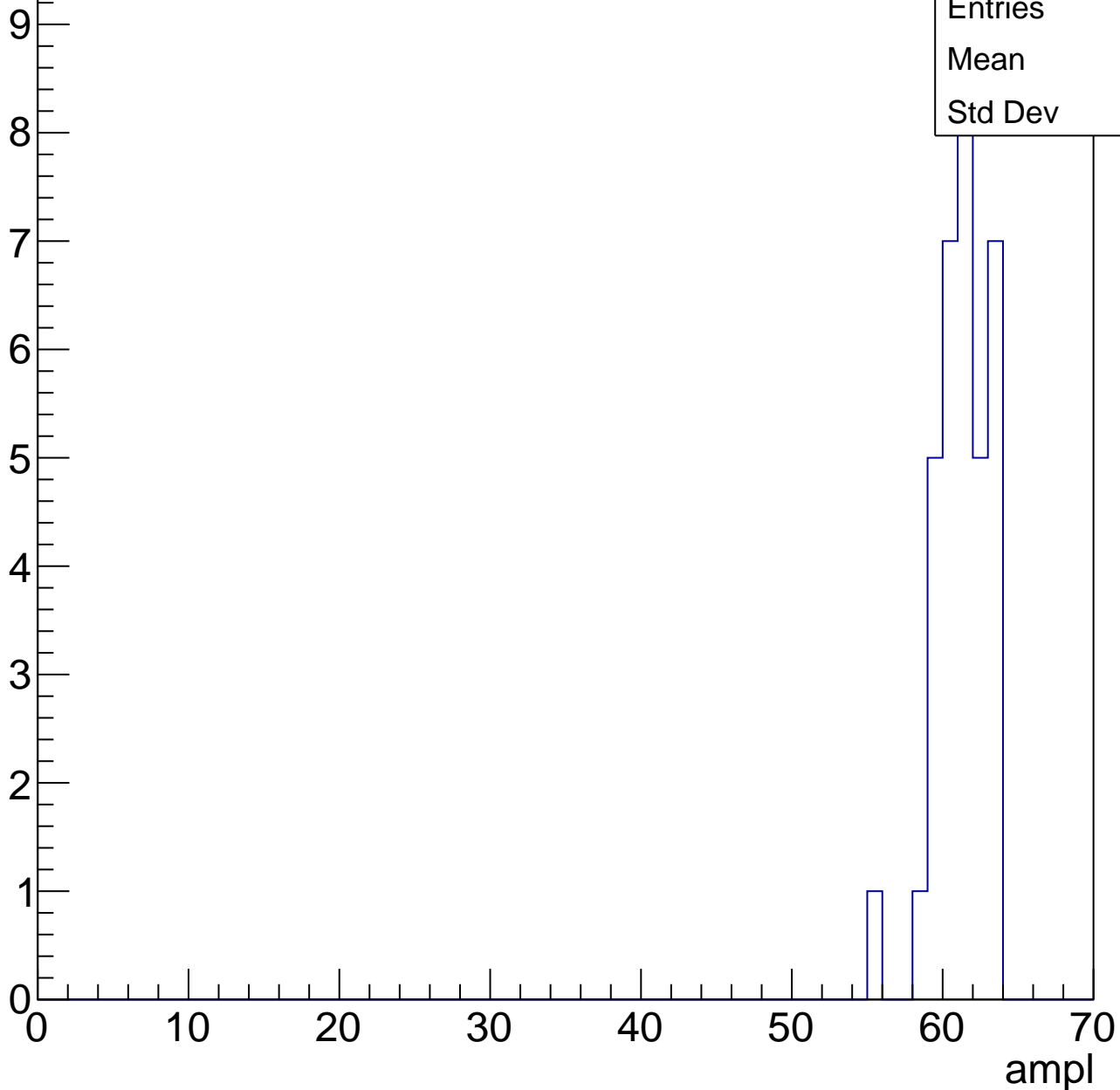


B0L001S, U19-ch69, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

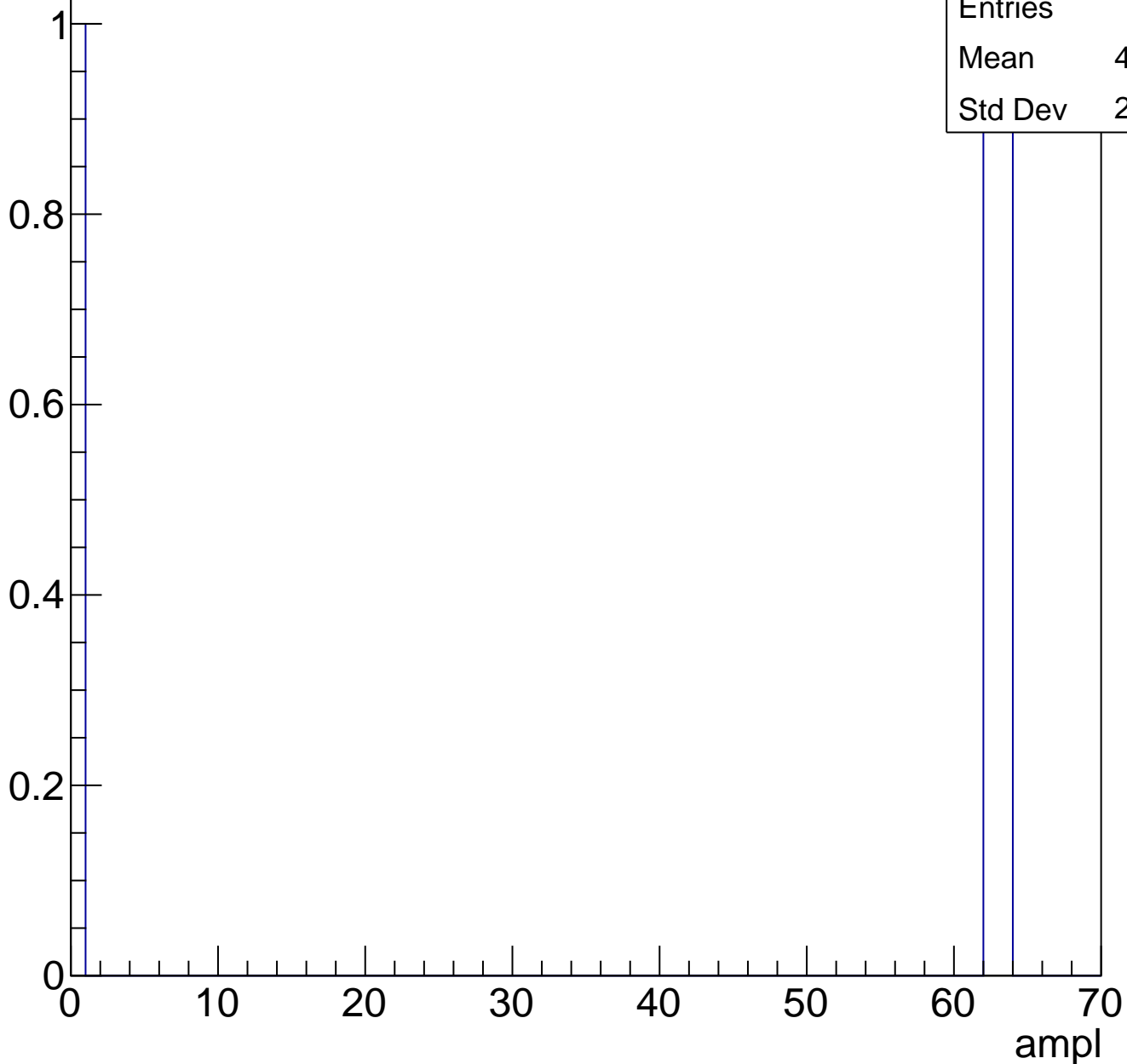
Entries	35
Mean	60.8
Std Dev	1.72



B0L001S, U19-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch70, adc0

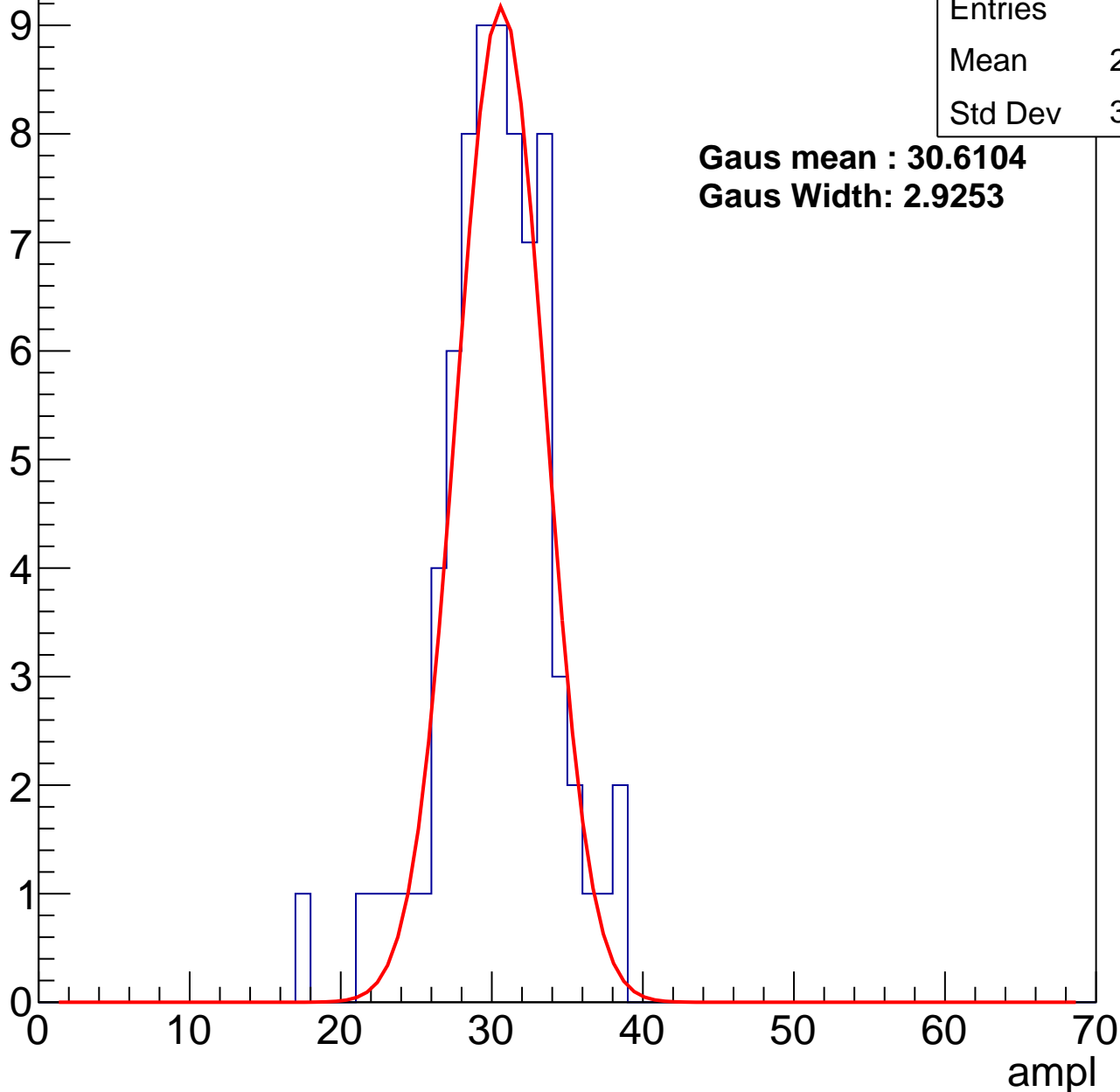
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	29.86
Std Dev	3.677

Gaus mean : 30.6104

Gaus Width: 2.9253



B0L001S, U19-ch70, adc1

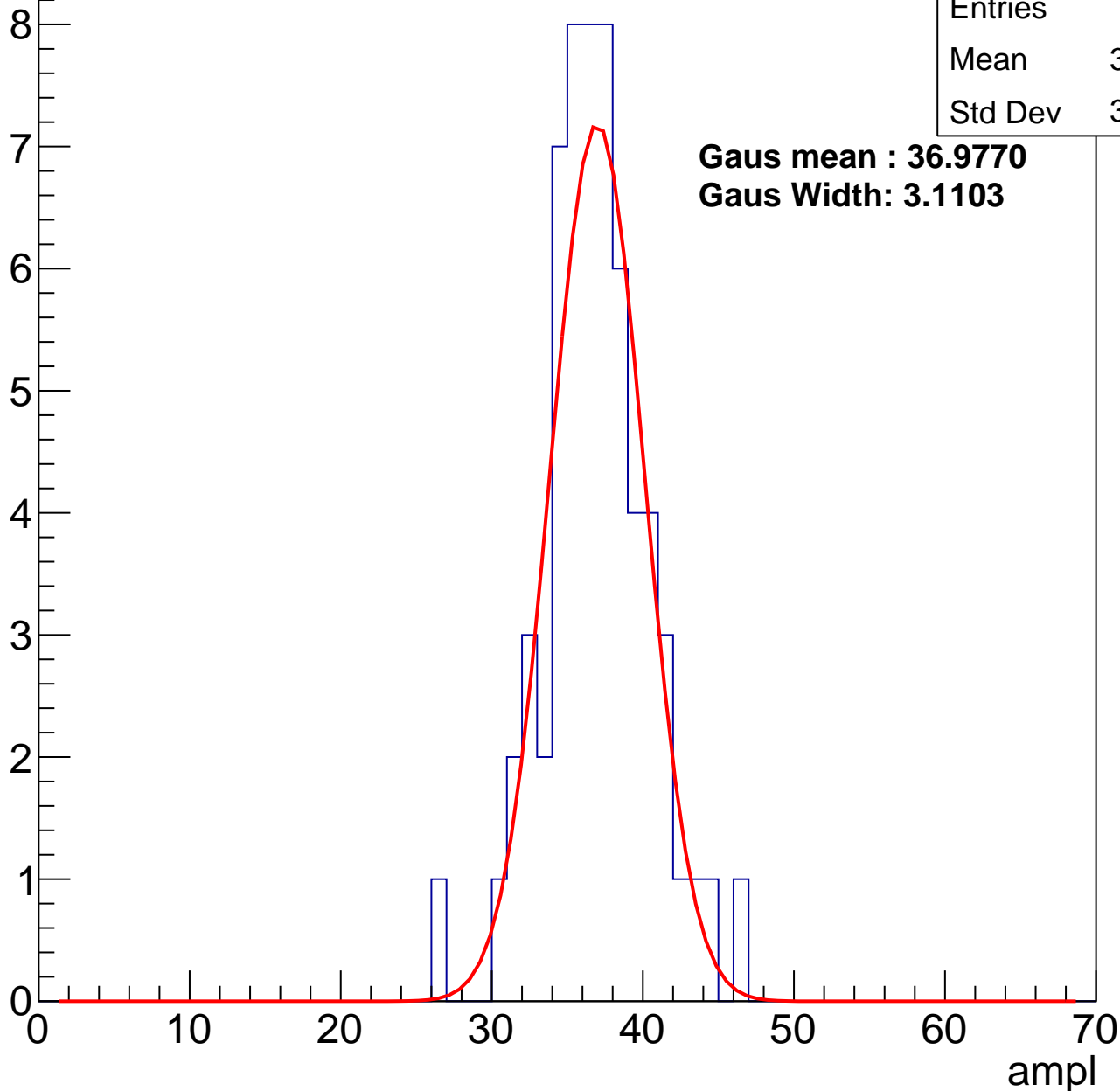
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	36.46
Std Dev	3.462

Gaus mean : 36.9770

Gaus Width: 3.1103



B0L001S, U19-ch70, adc2

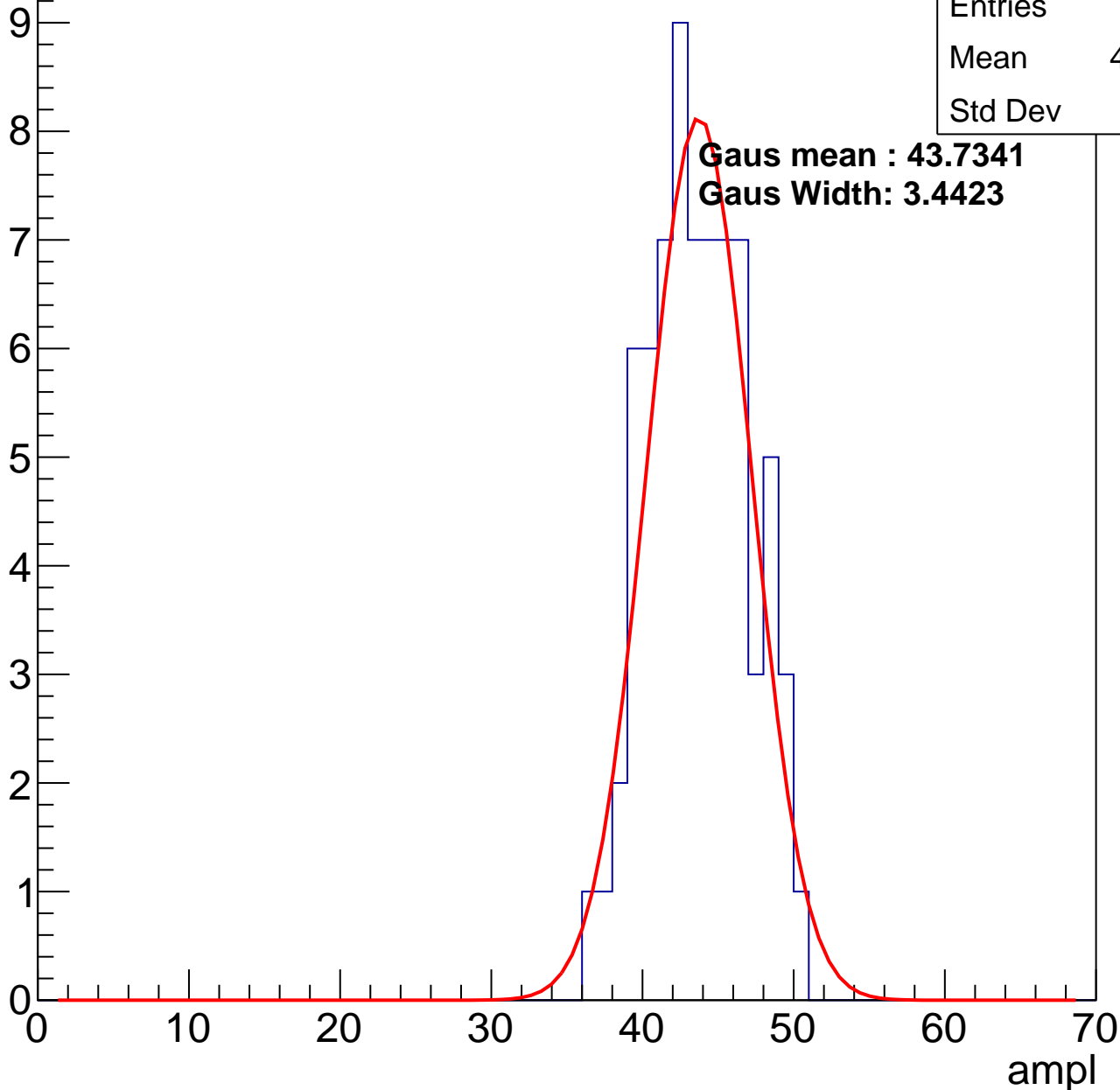
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.22
Std Dev	3.22

Gaus mean : 43.7341

Gaus Width: 3.4423

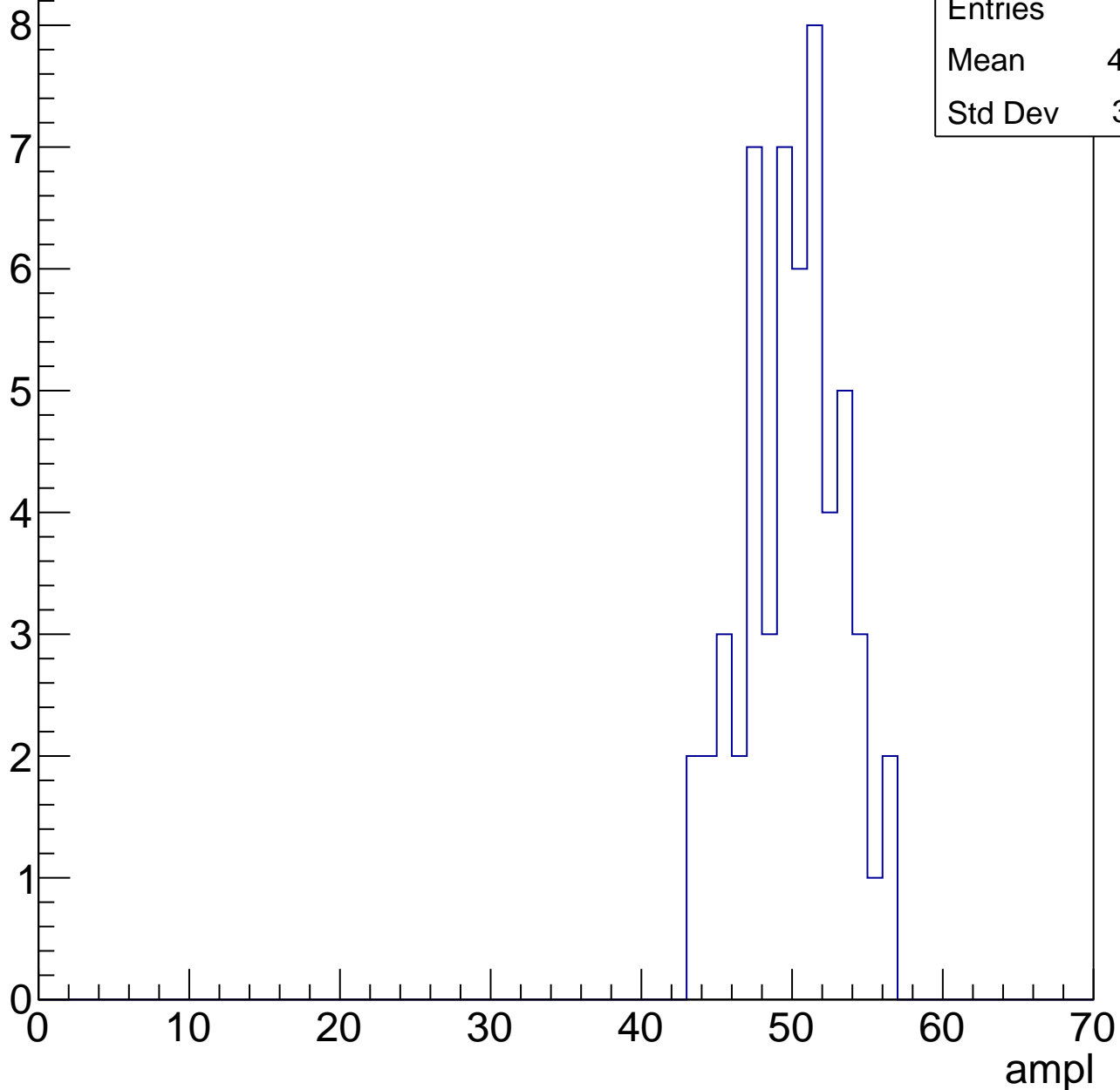


B0L001S, U19-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

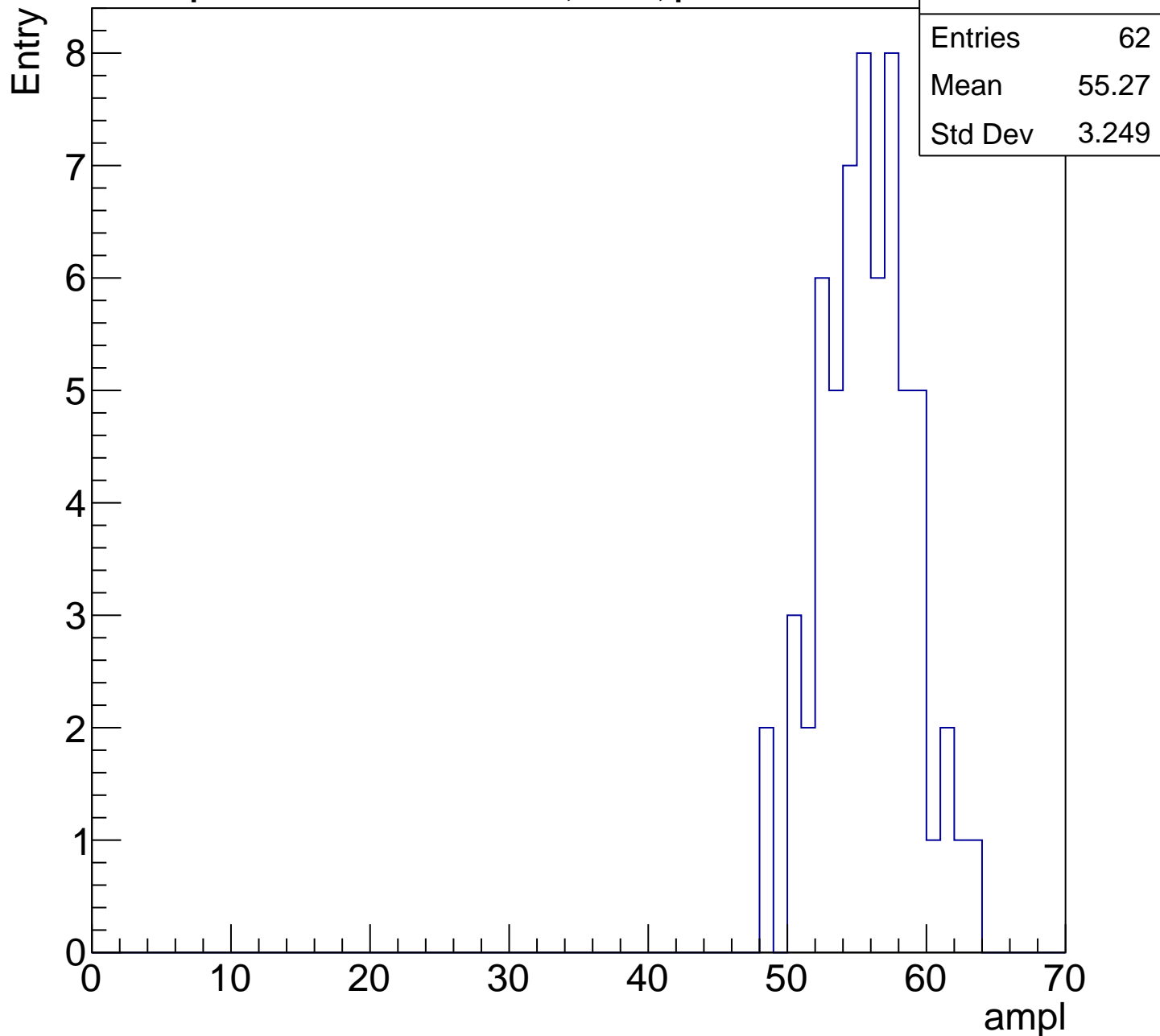
Entry

Entries	55
Mean	49.58
Std Dev	3.201



B0L001S, U19-ch70, adc4

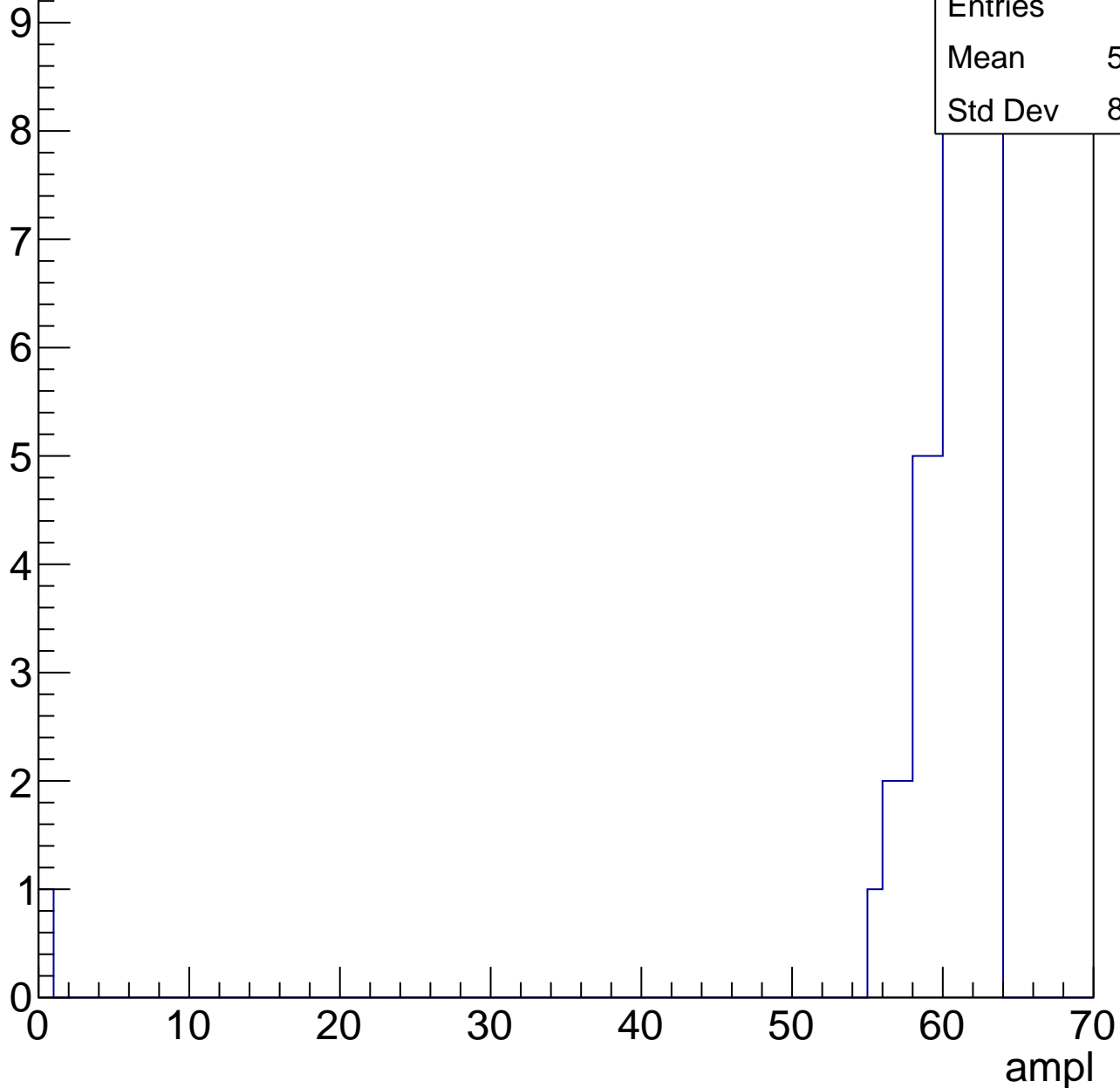
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U19-ch70, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	62
Std Dev	0

B0L001S, U19-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch71, adc0

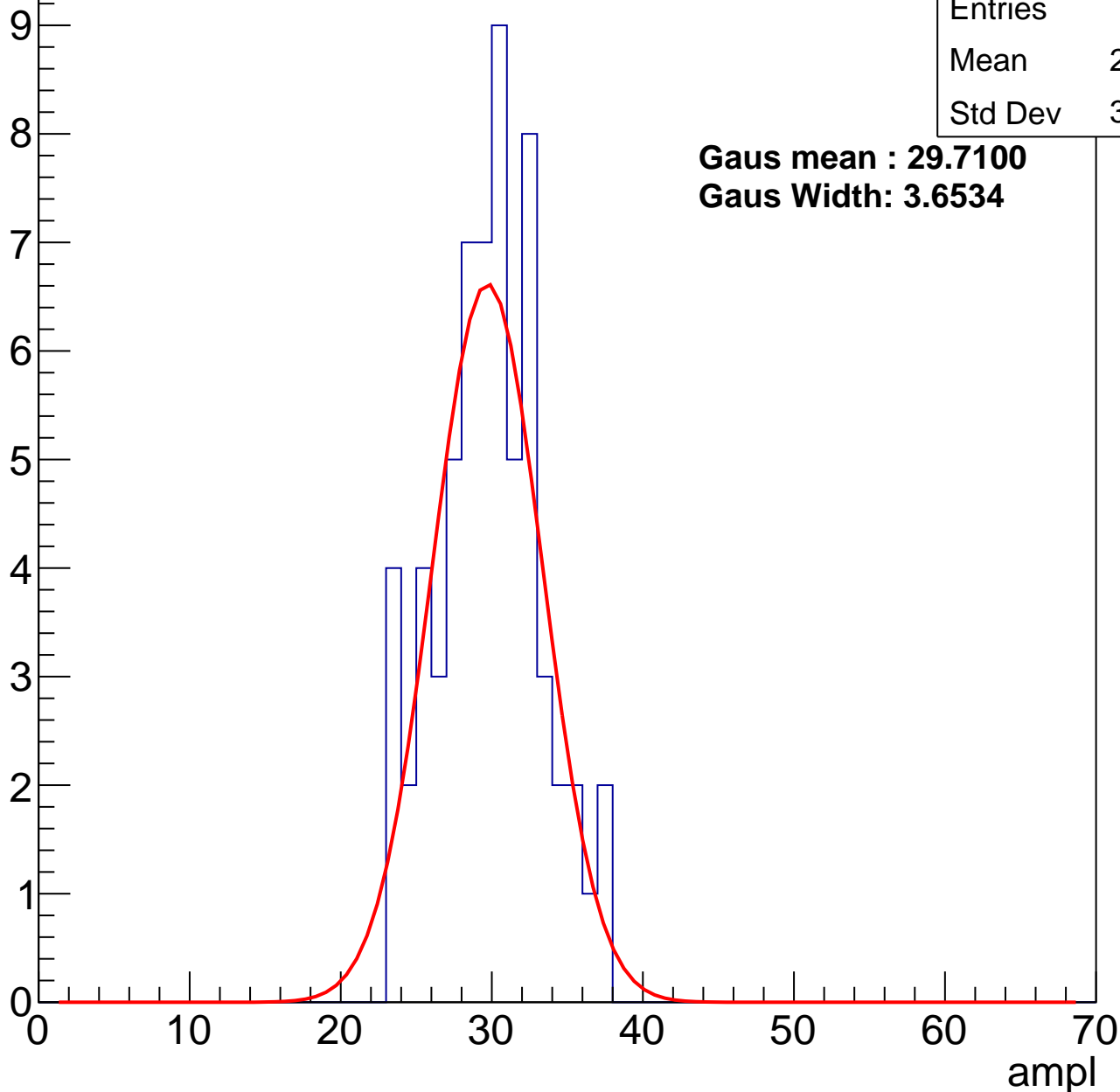
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	29.38
Std Dev	3.426

Gaus mean : 29.7100

Gaus Width: 3.6534



B0L001S, U19-ch71, adc1

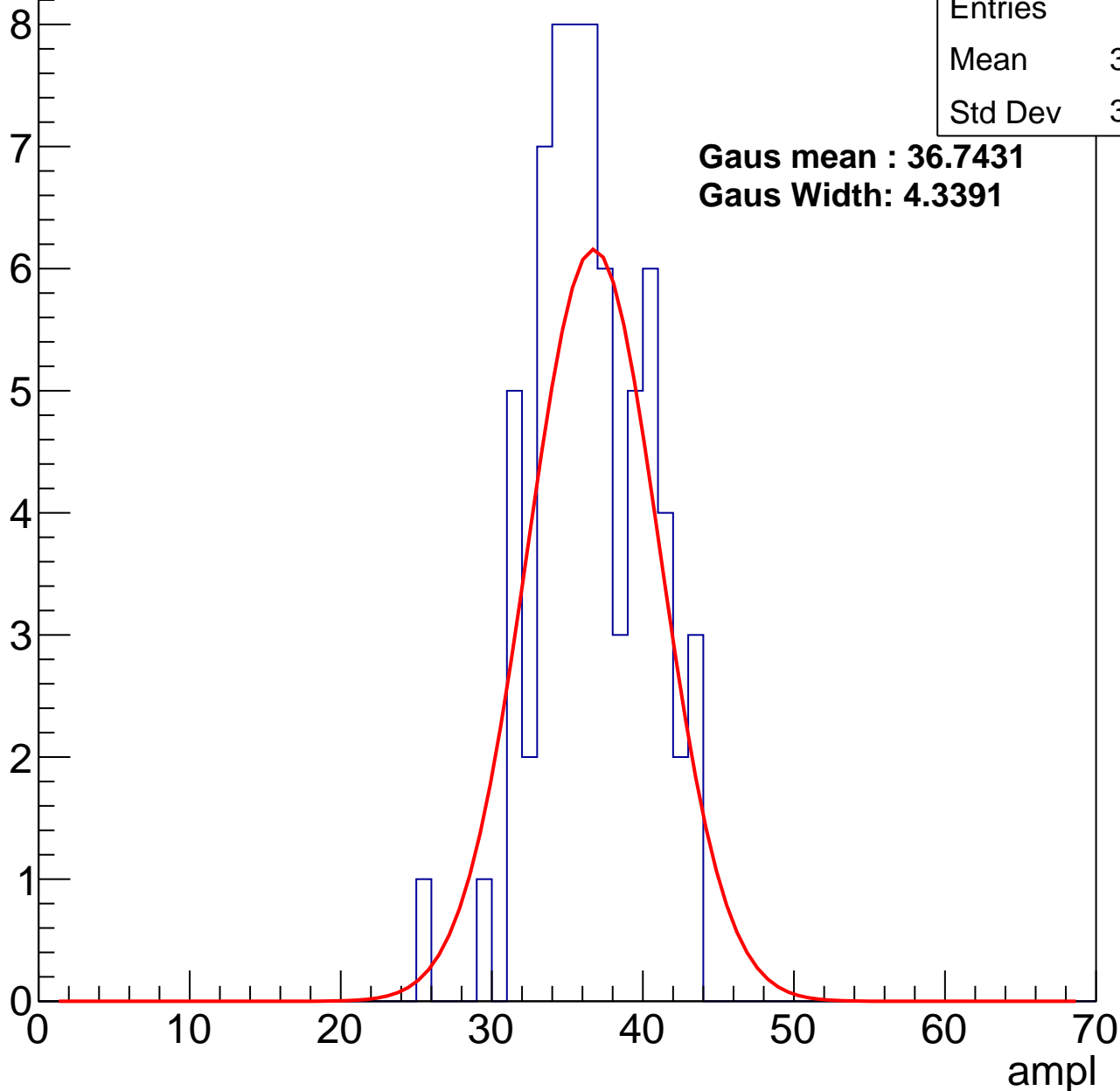
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	36.12
Std Dev	3.626

Gaus mean : 36.7431

Gaus Width: 4.3391



B0L001S, U19-ch71, adc2

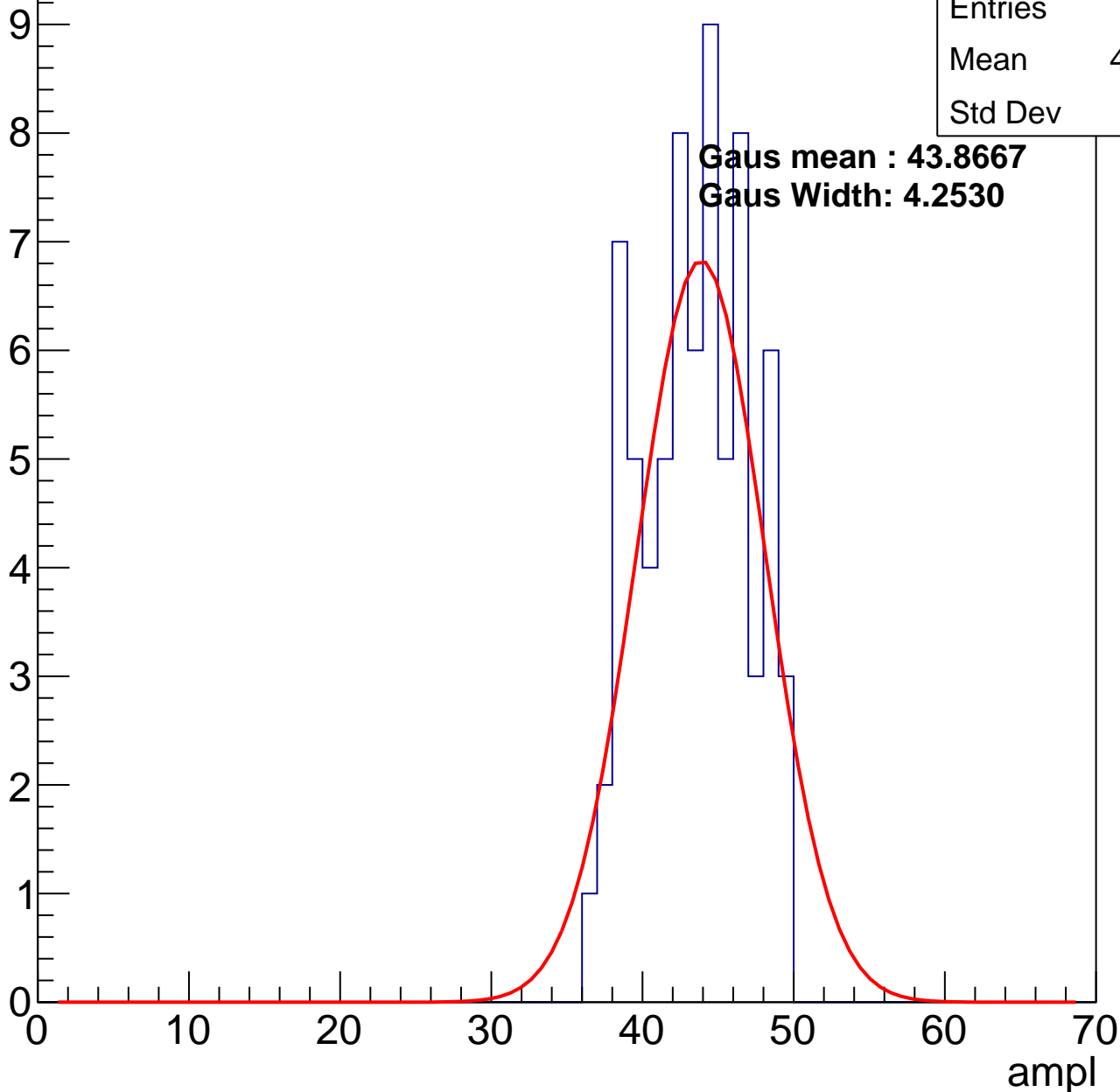
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	42.99
Std Dev	3.43

Gaus mean : 43.8667

Gaus Width: 4.2530



B0L001S, U19-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	50.41
Std Dev	3.622

Entry

10

8

6

4

2

0

0

10

20

30

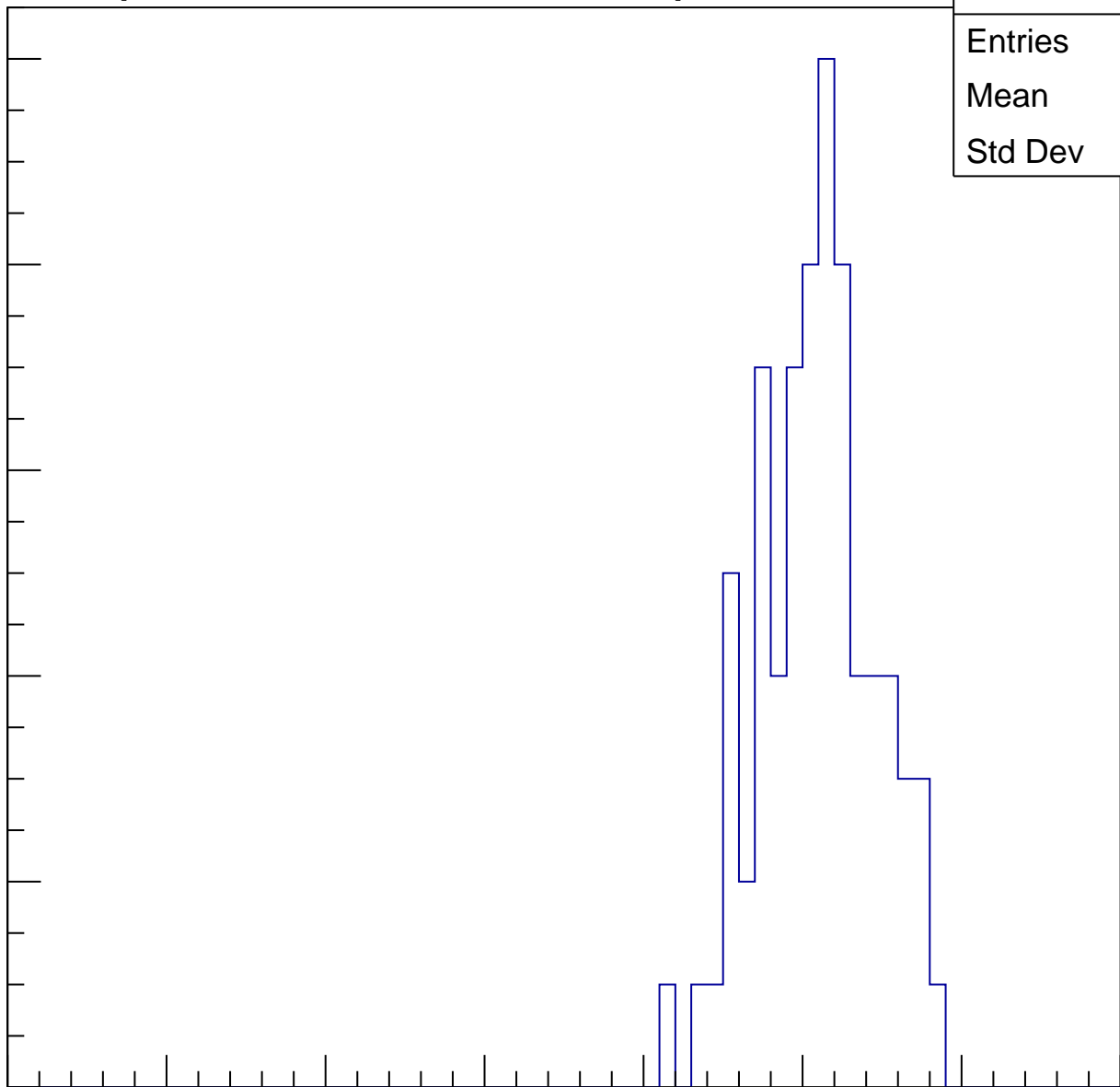
40

50

60

70

ampl

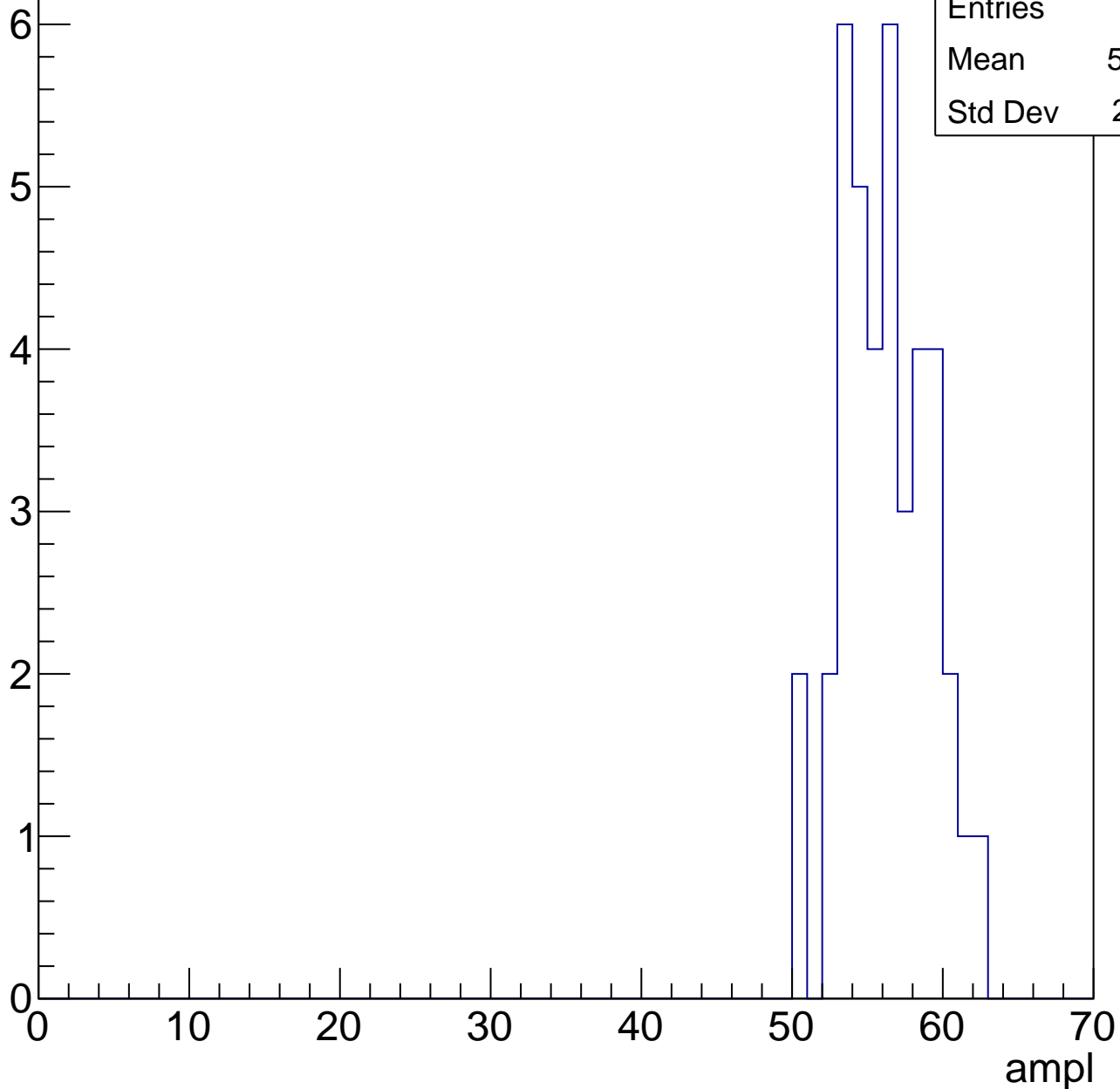


B0L001S, U19-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	55.75
Std Dev	2.861

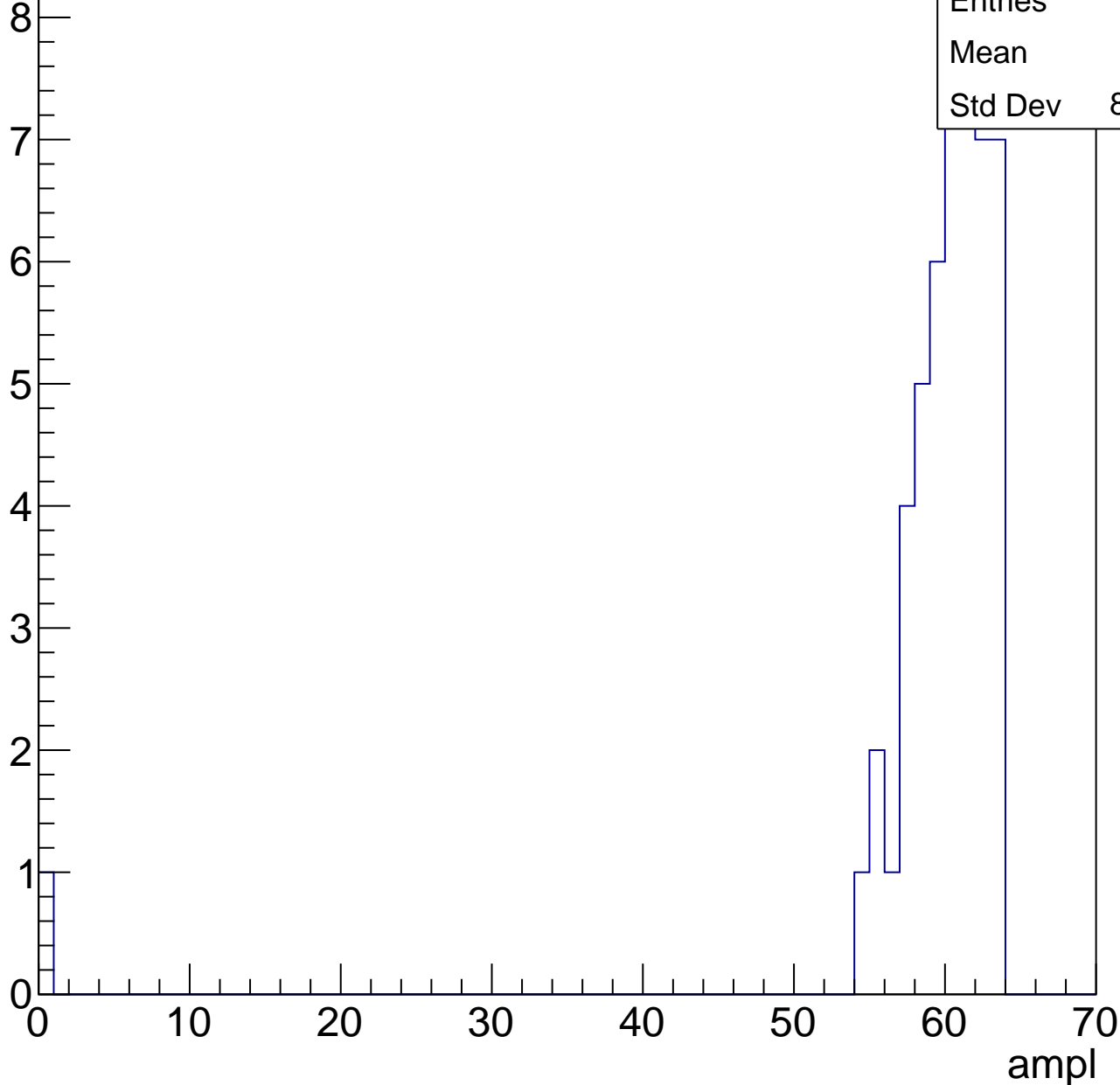


B0L001S, U19-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	58.7
Std Dev	8.693



B0L001S, U19-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	6
Mean	62.33
Std Dev	0.7454

B0L001S, U19-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch72, adc0

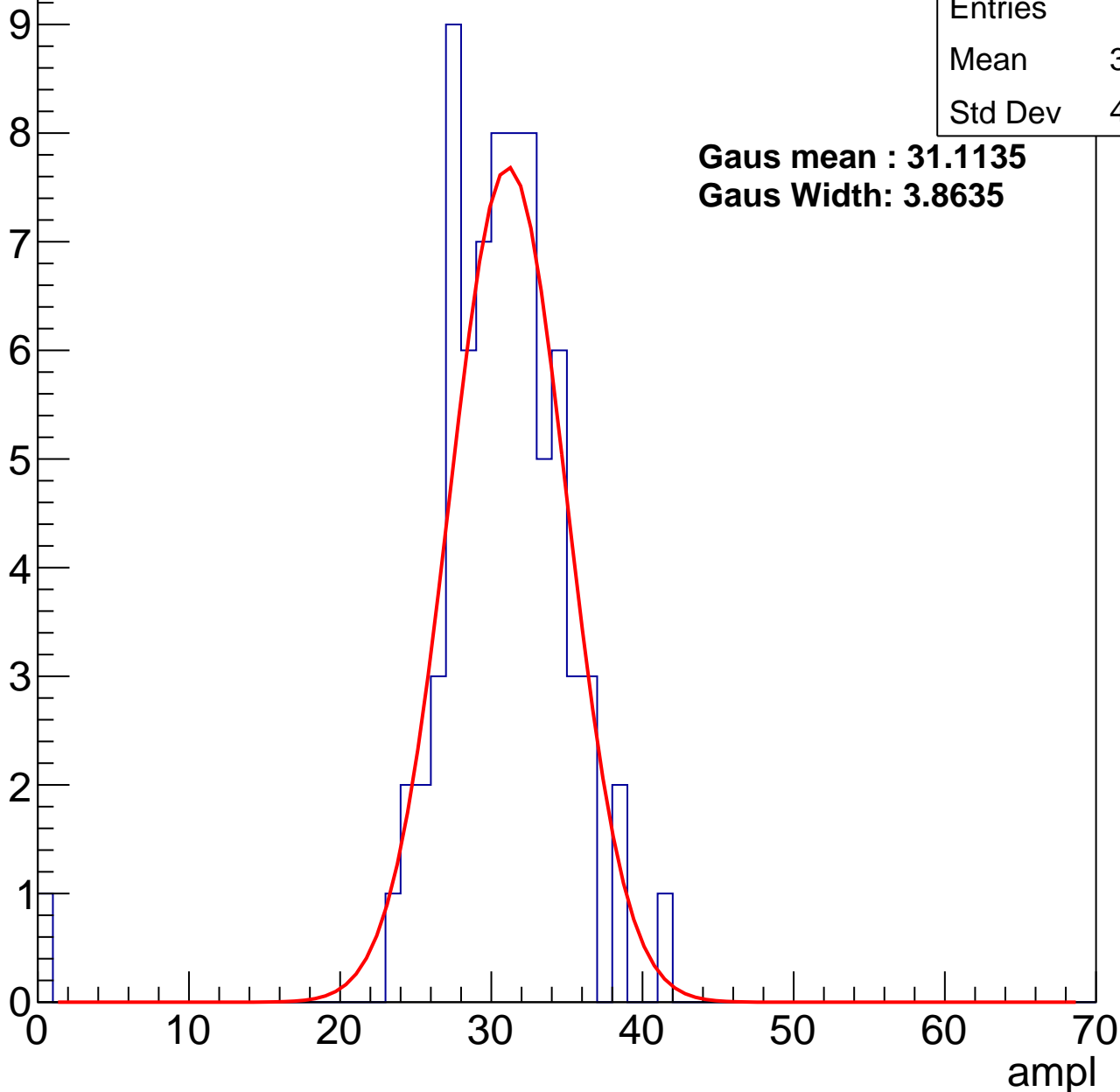
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	30.08
Std Dev	4.958

Gaus mean : 31.1135

Gaus Width: 3.8635



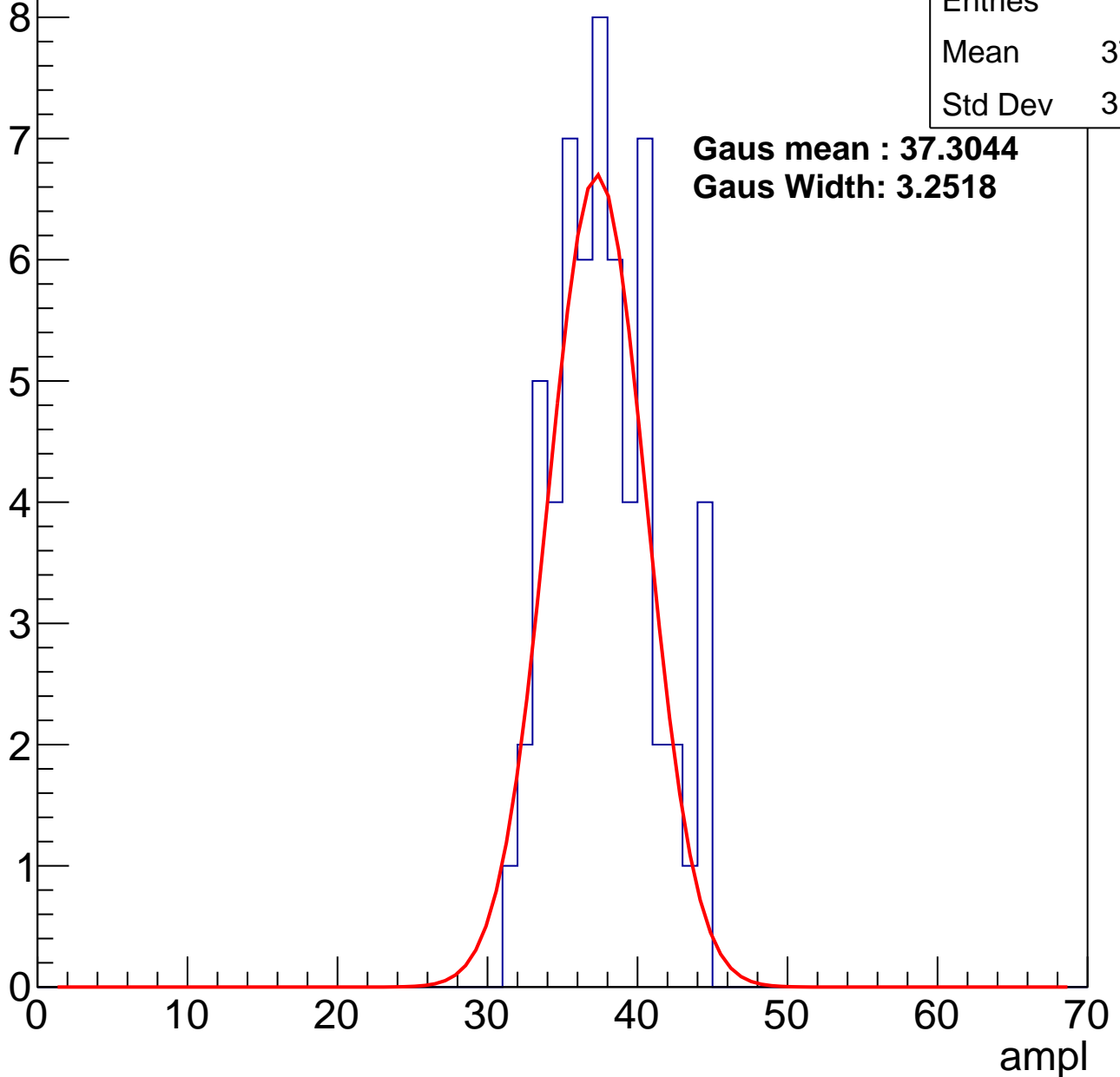
B0L001S, U19-ch72, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.32
Std Dev	3.265

Gaus mean : 37.3044
Gaus Width: 3.2518



B0L001S, U19-ch72, adc2

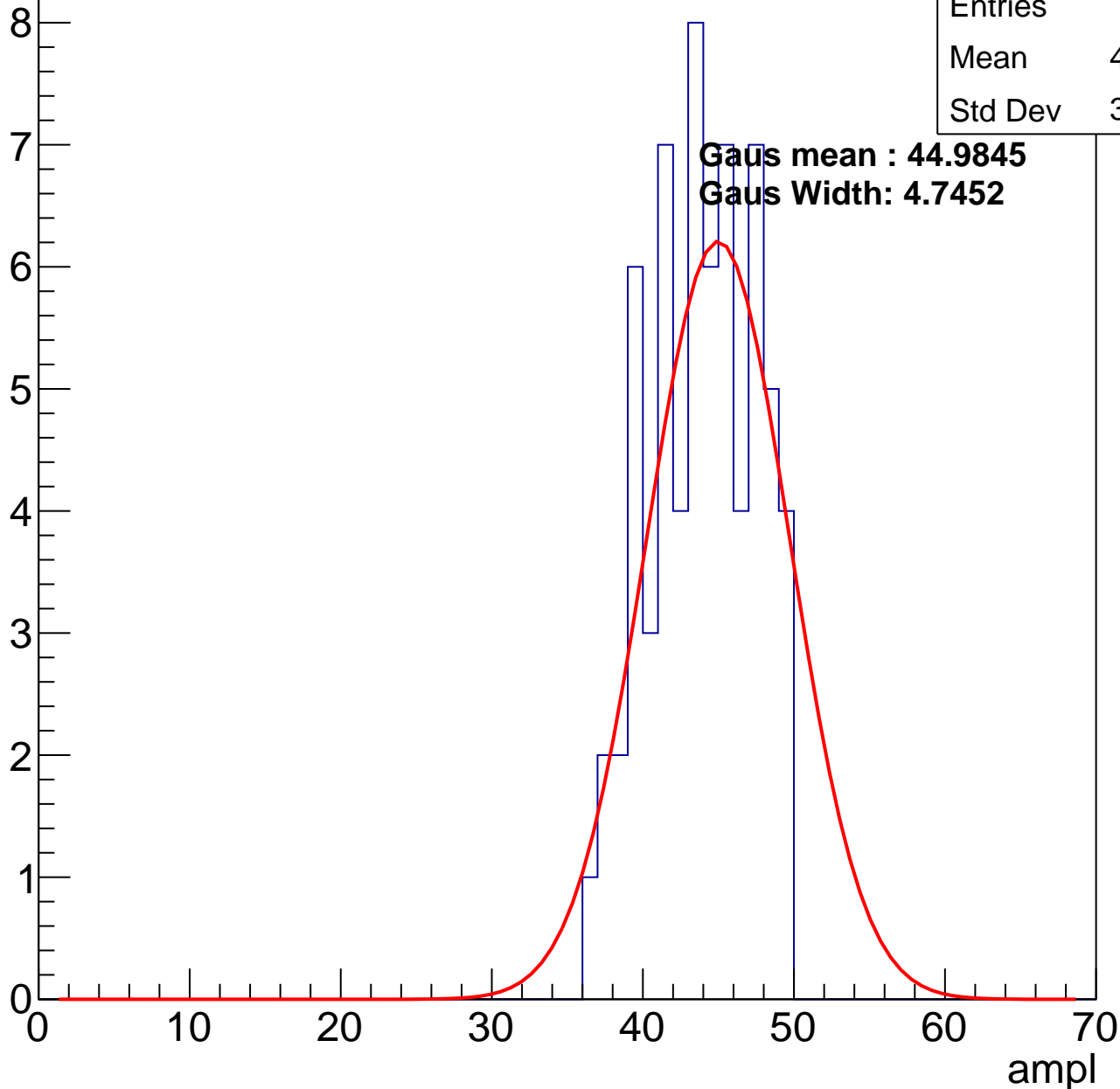
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.44
Std Dev	3.403

Gaus mean : 44.9845

Gaus Width: 4.7452

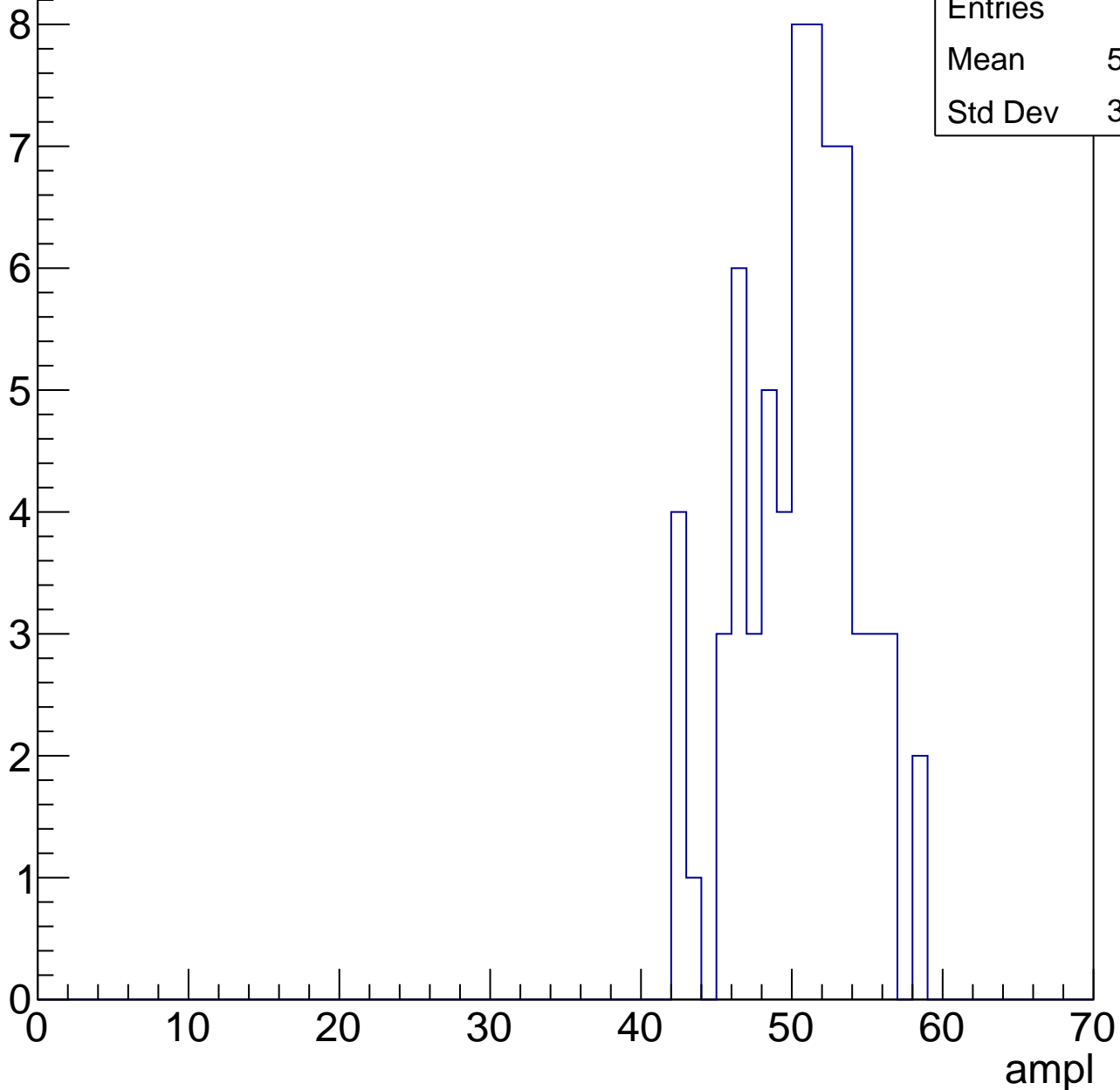


B0L001S, U19-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50.04
Std Dev	3.834

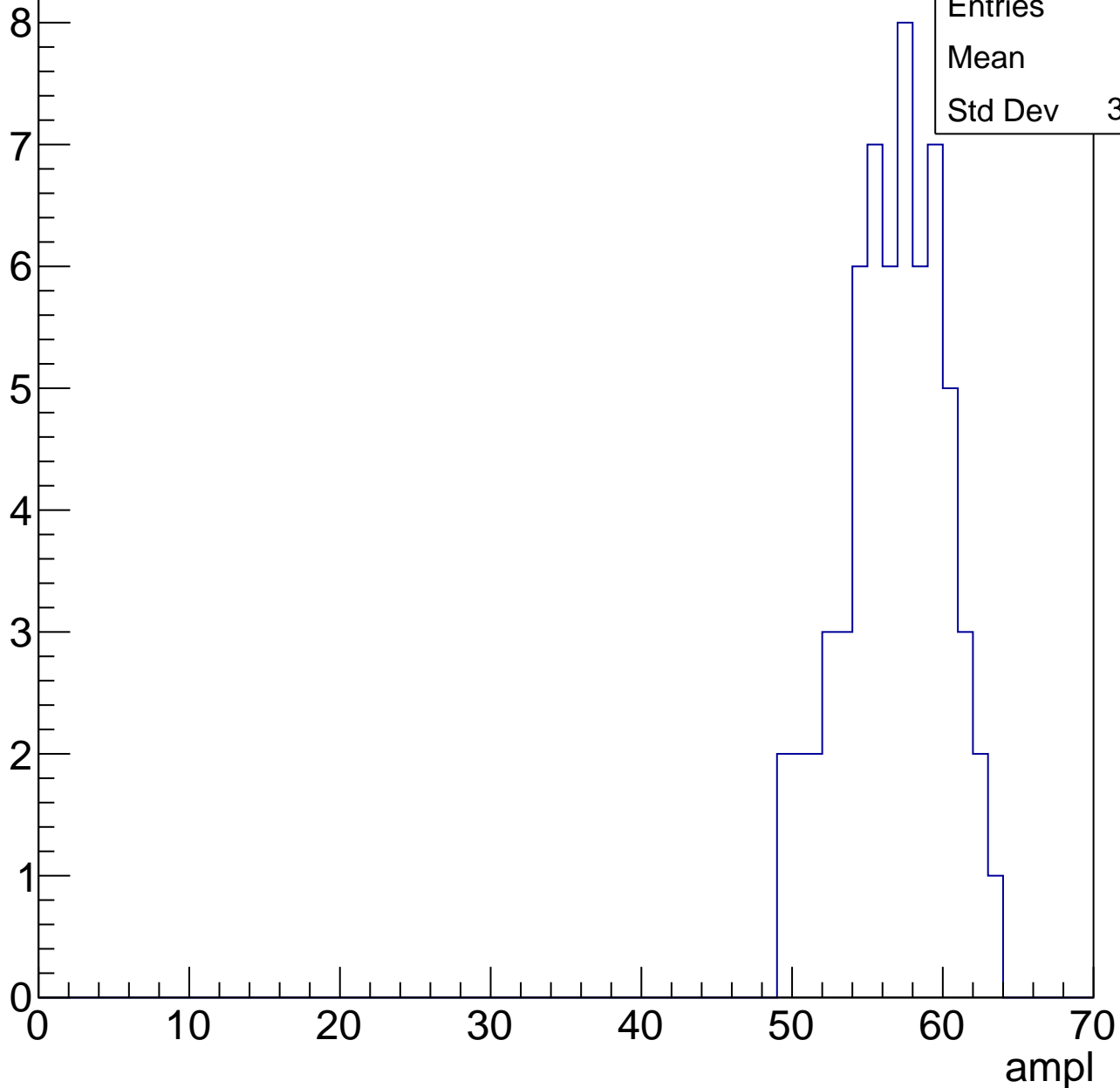


B0L001S, U19-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	56.3
Std Dev	3.312

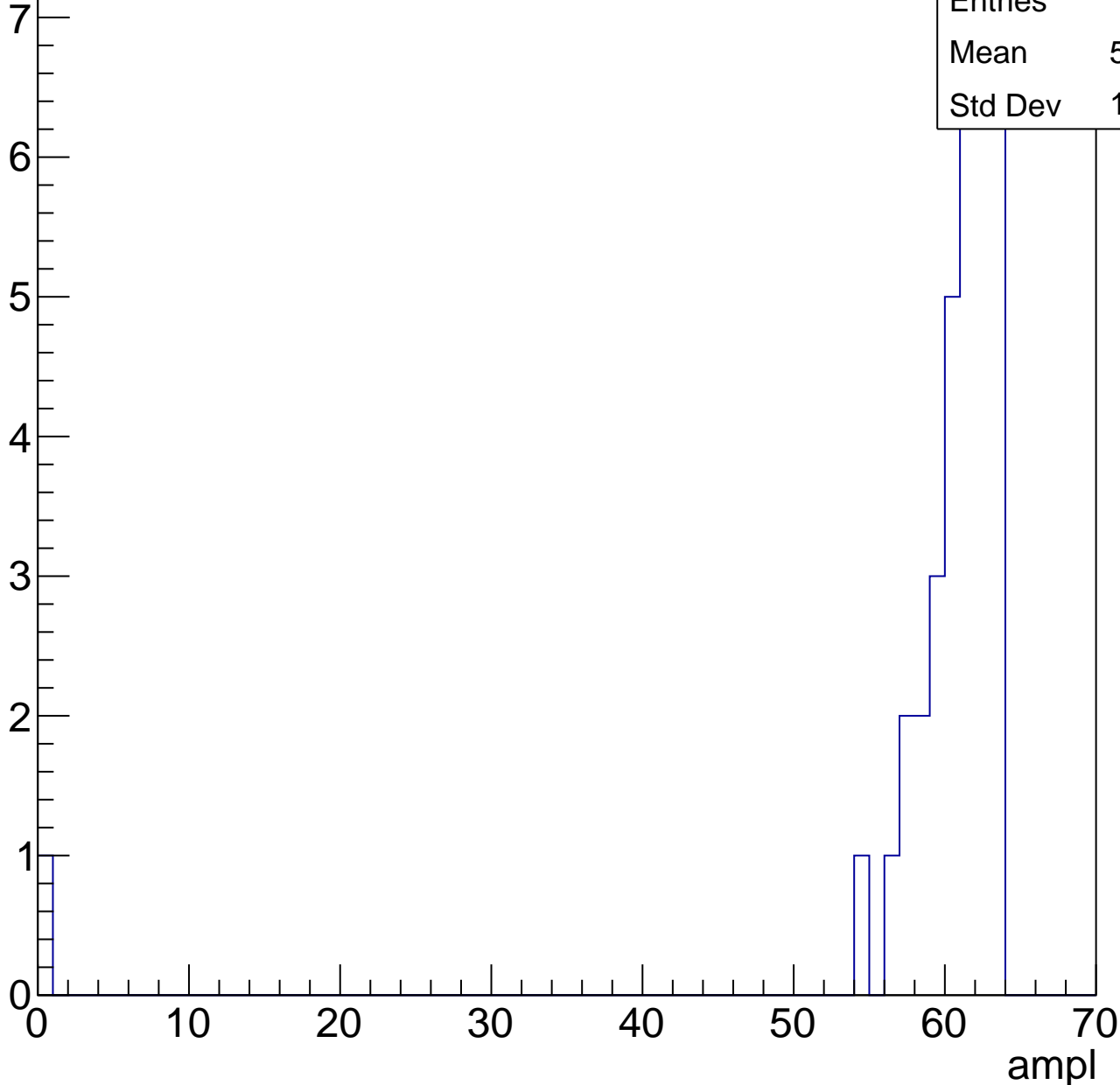


B0L001S, U19-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.86
Std Dev	10.18



B0L001S, U19-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U19-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch73, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	31.27
Std Dev	7.013

Gaus mean : 33.5881

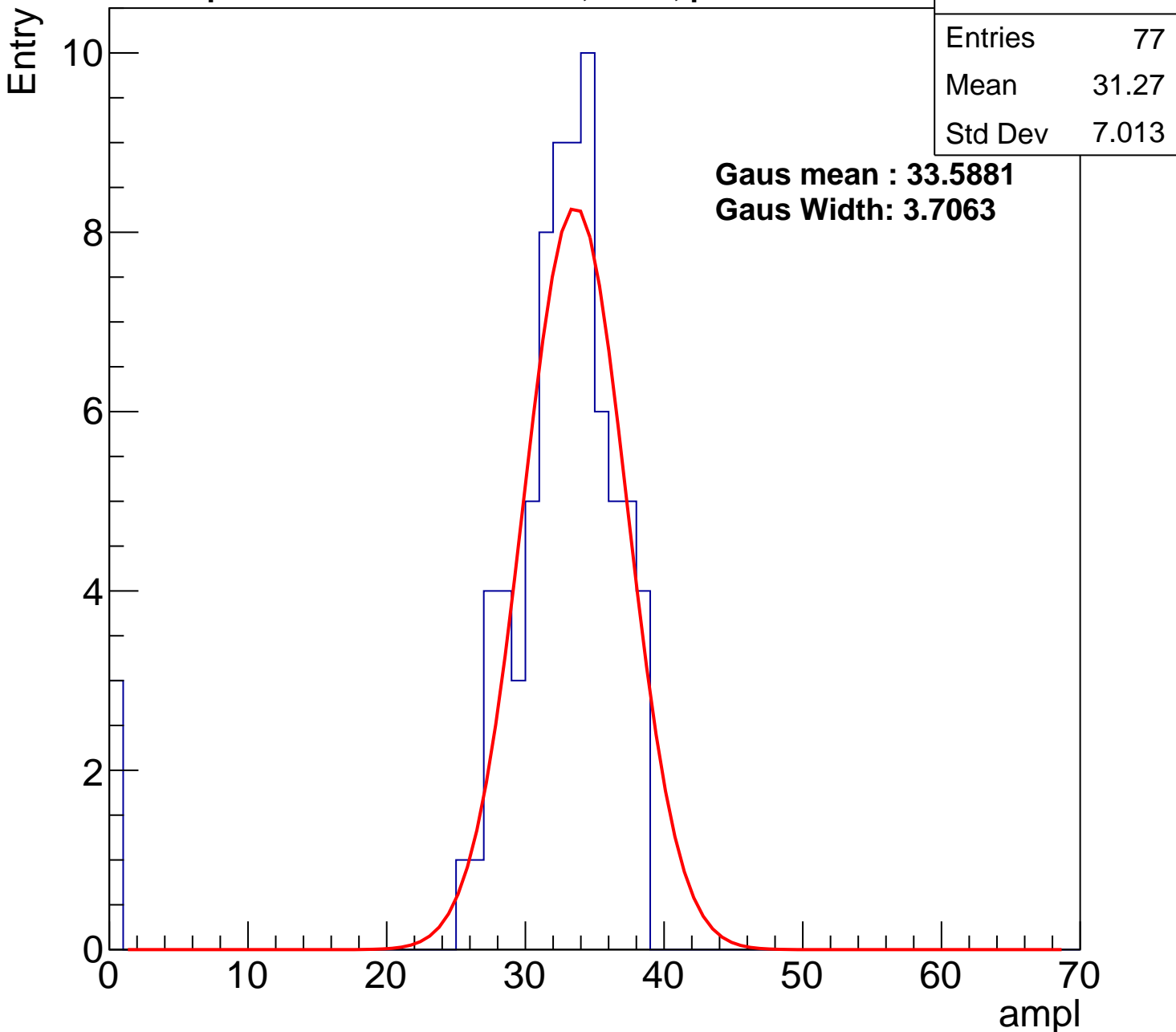
Gaus Width: 3.7063

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch73, adc1

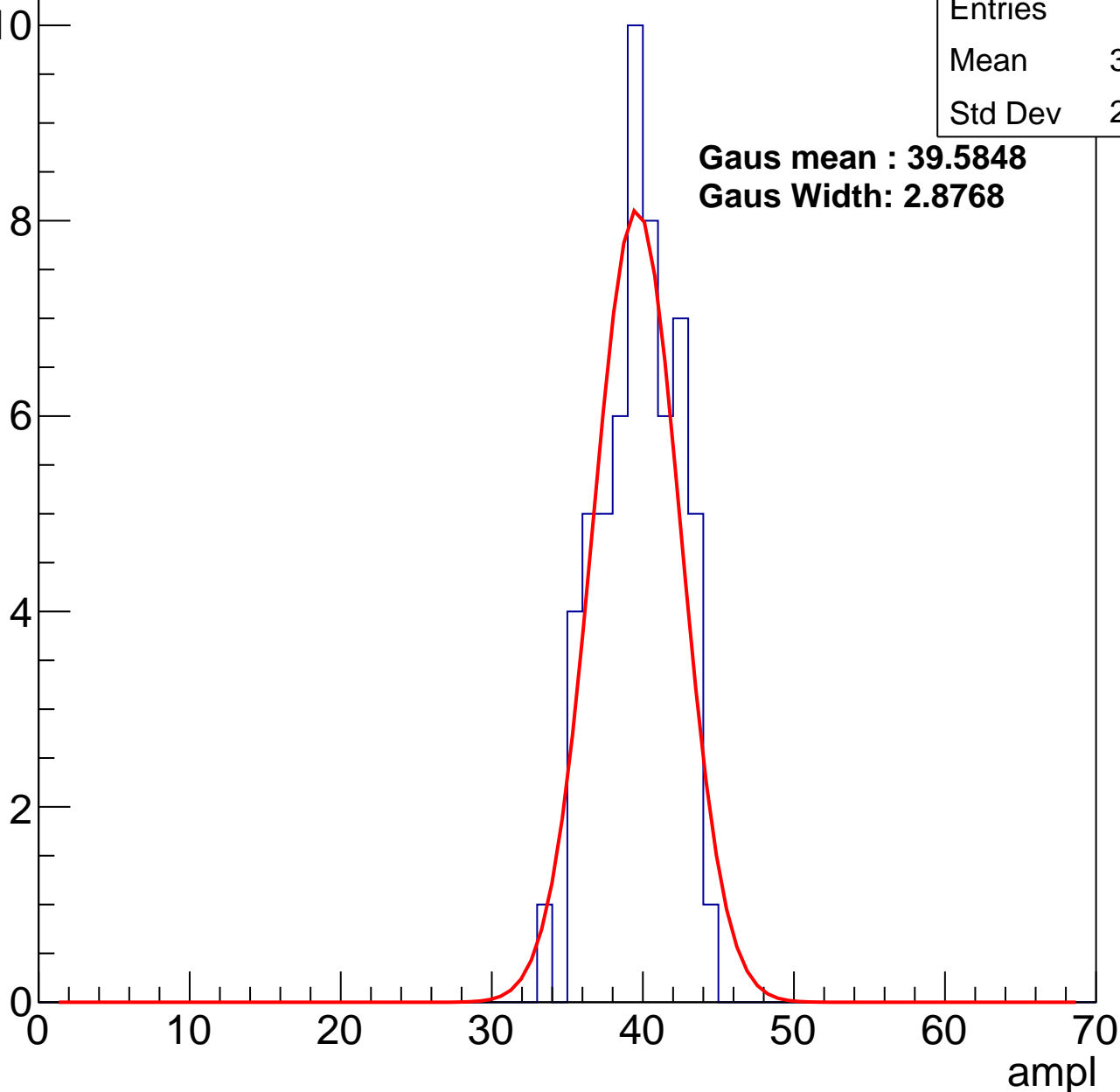
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	39.22
Std Dev	2.519

Gaus mean : 39.5848

Gaus Width: 2.8768



B0L001S, U19-ch73, adc2

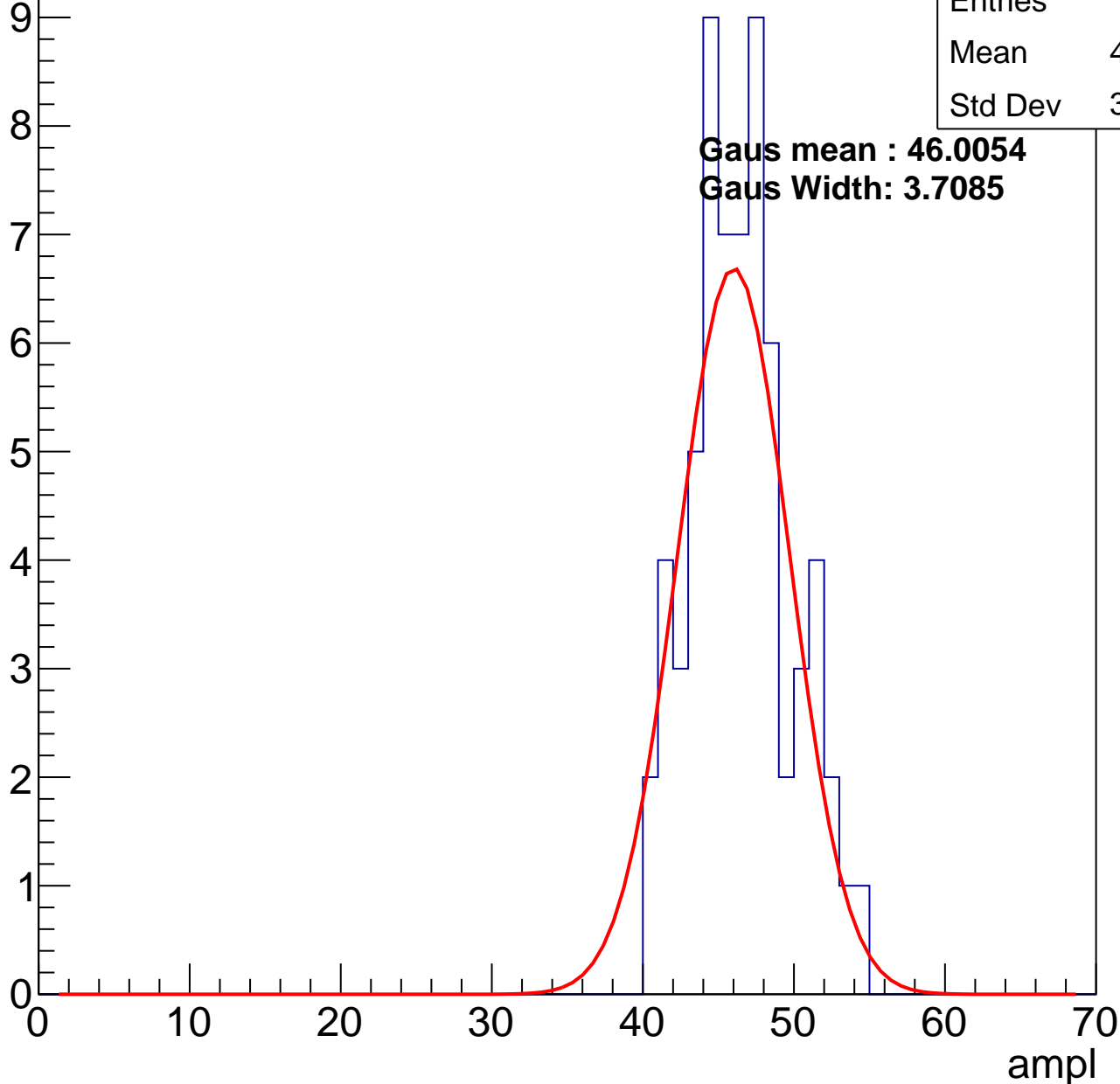
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	46.03
Std Dev	3.263

Gaus mean : 46.0054

Gaus Width: 3.7085

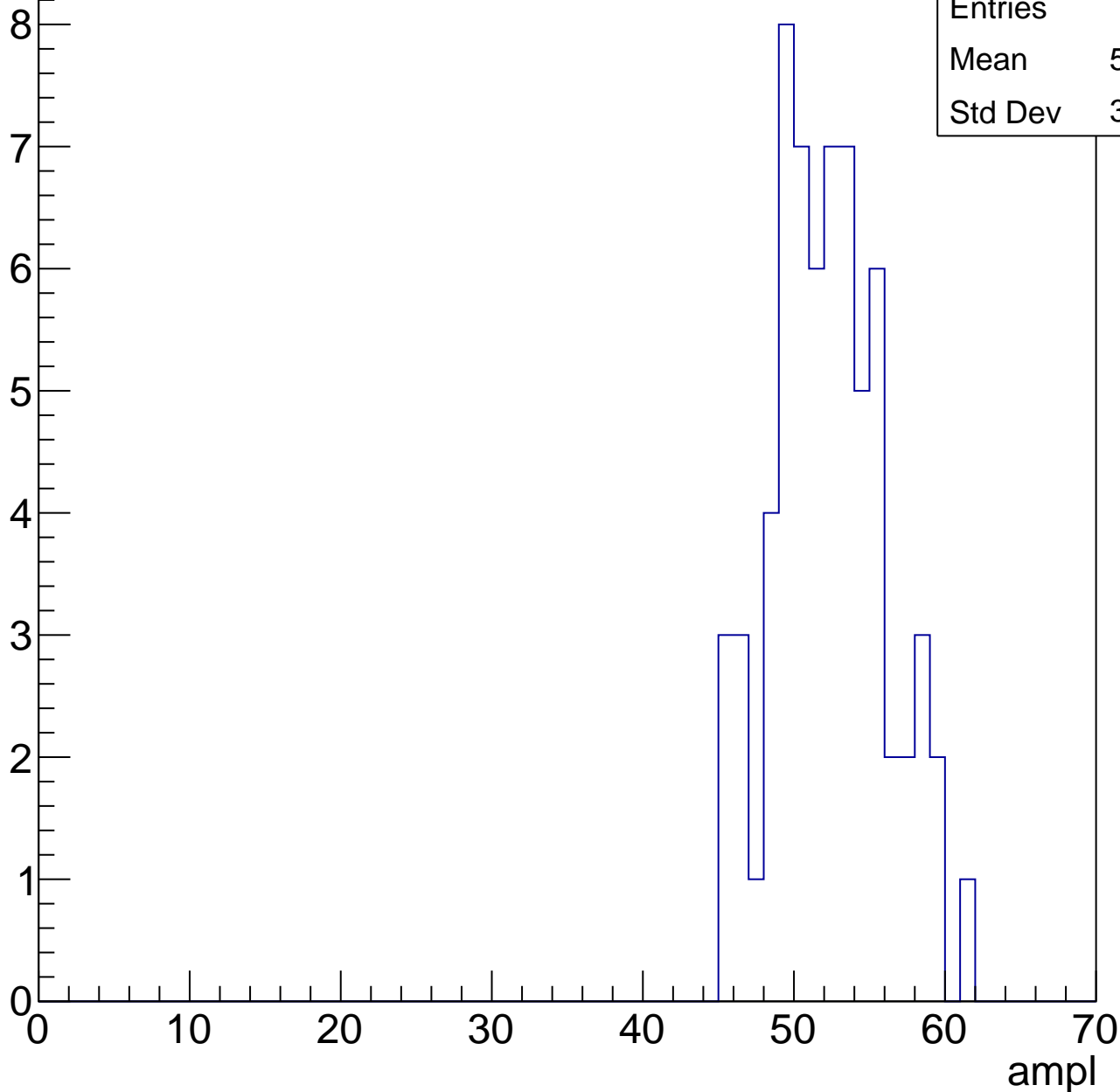


B0L001S, U19-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	51.85
Std Dev	3.662

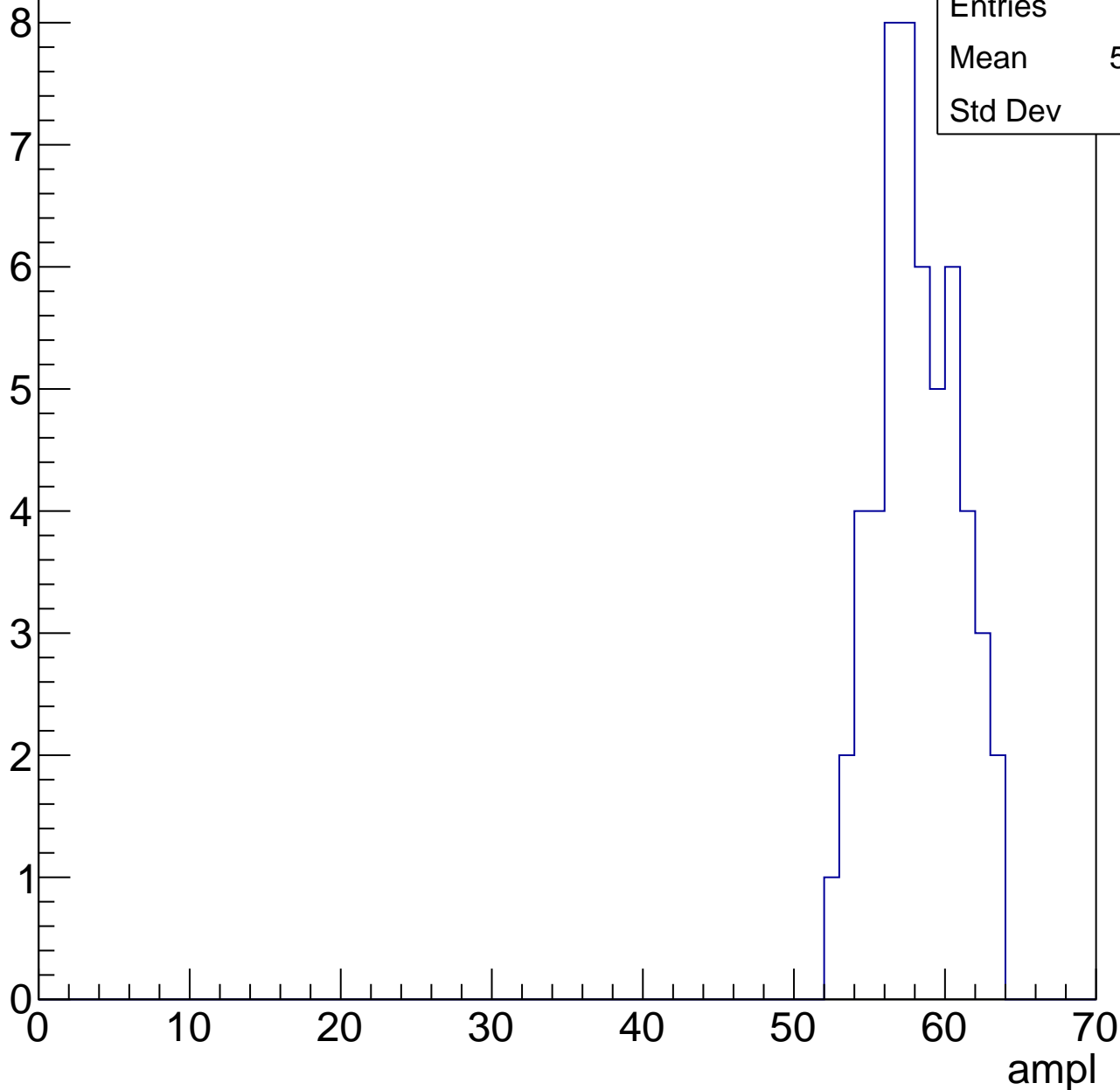


B0L001S, U19-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	57.68
Std Dev	2.69

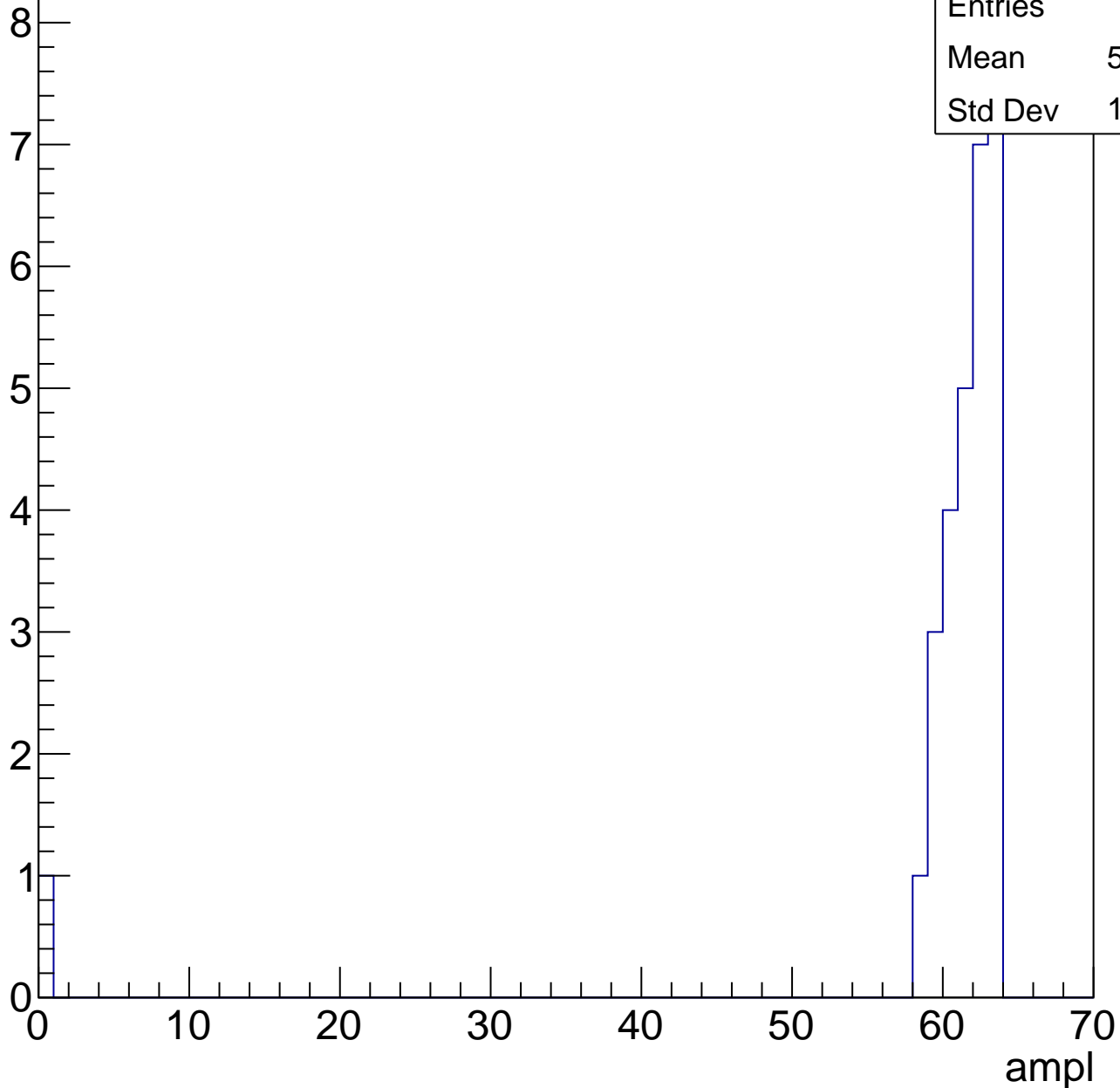


B0L001S, U19-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	59.24
Std Dev	11.29



B0L001S, U19-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch74, adc0

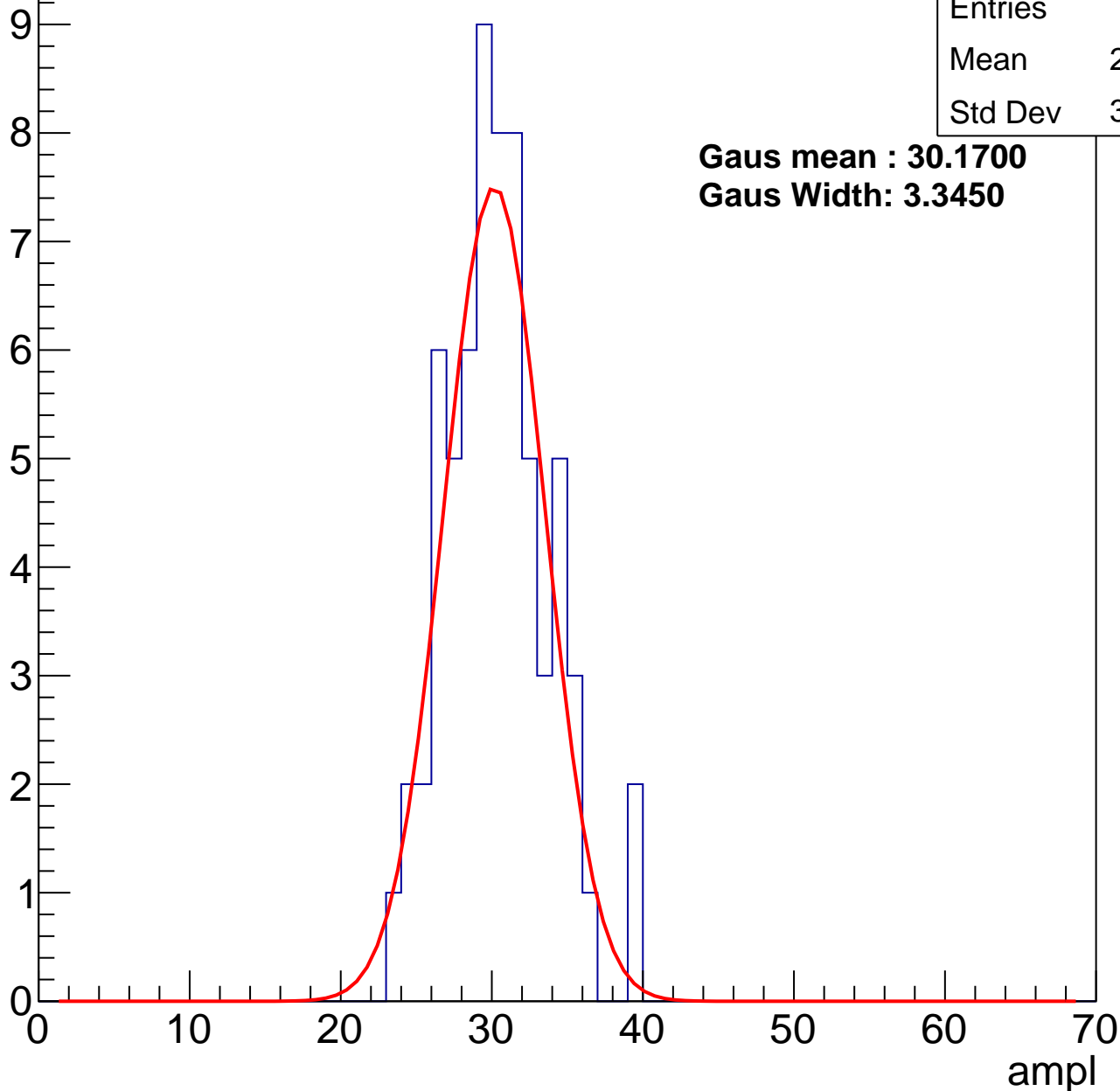
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	29.95
Std Dev	3.377

Gaus mean : 30.1700

Gaus Width: 3.3450



B0L001S, U19-ch74, adc1

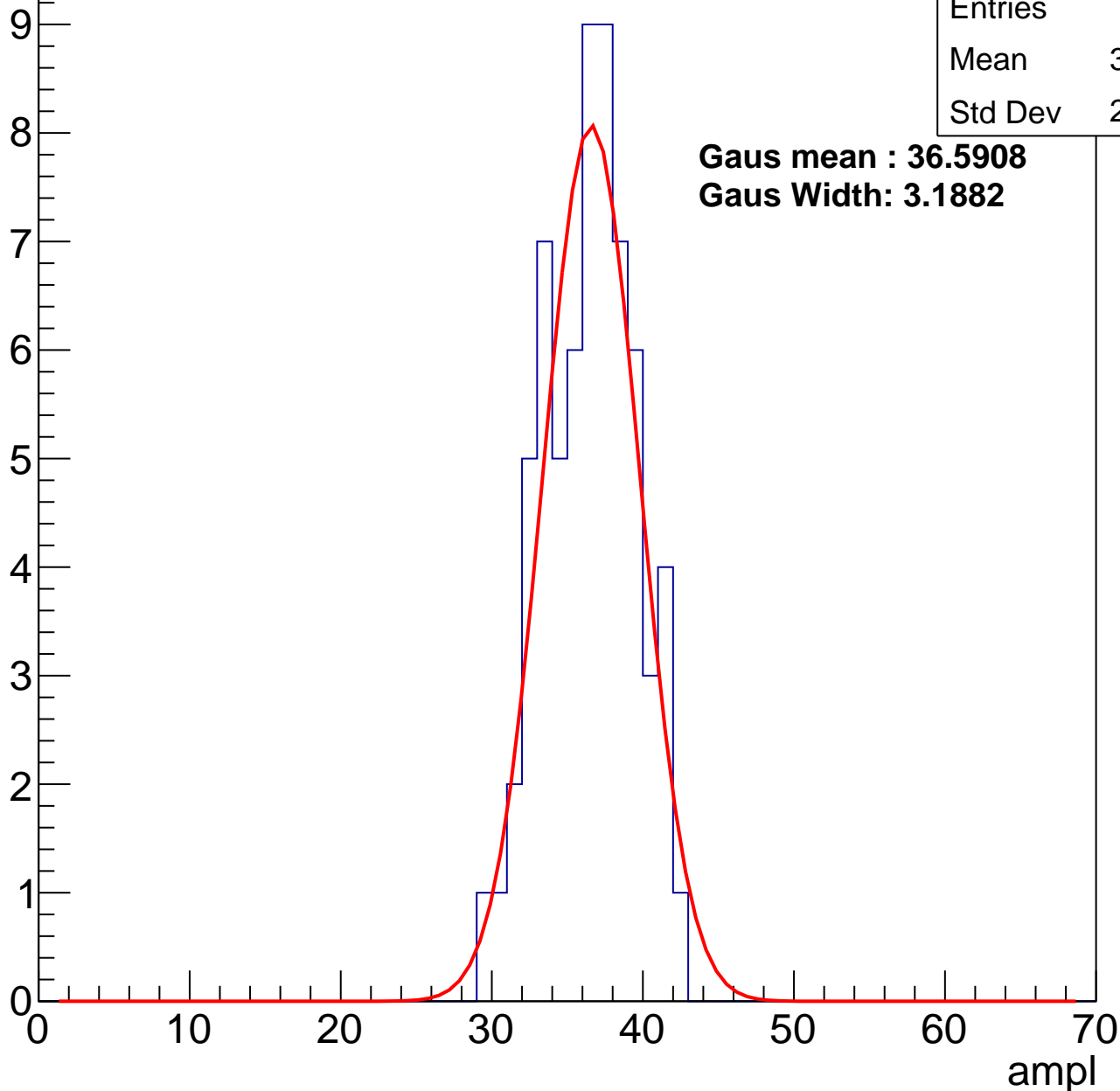
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	35.98
Std Dev	2.962

Gaus mean : 36.5908

Gaus Width: 3.1882



B0L001S, U19-ch74, adc2

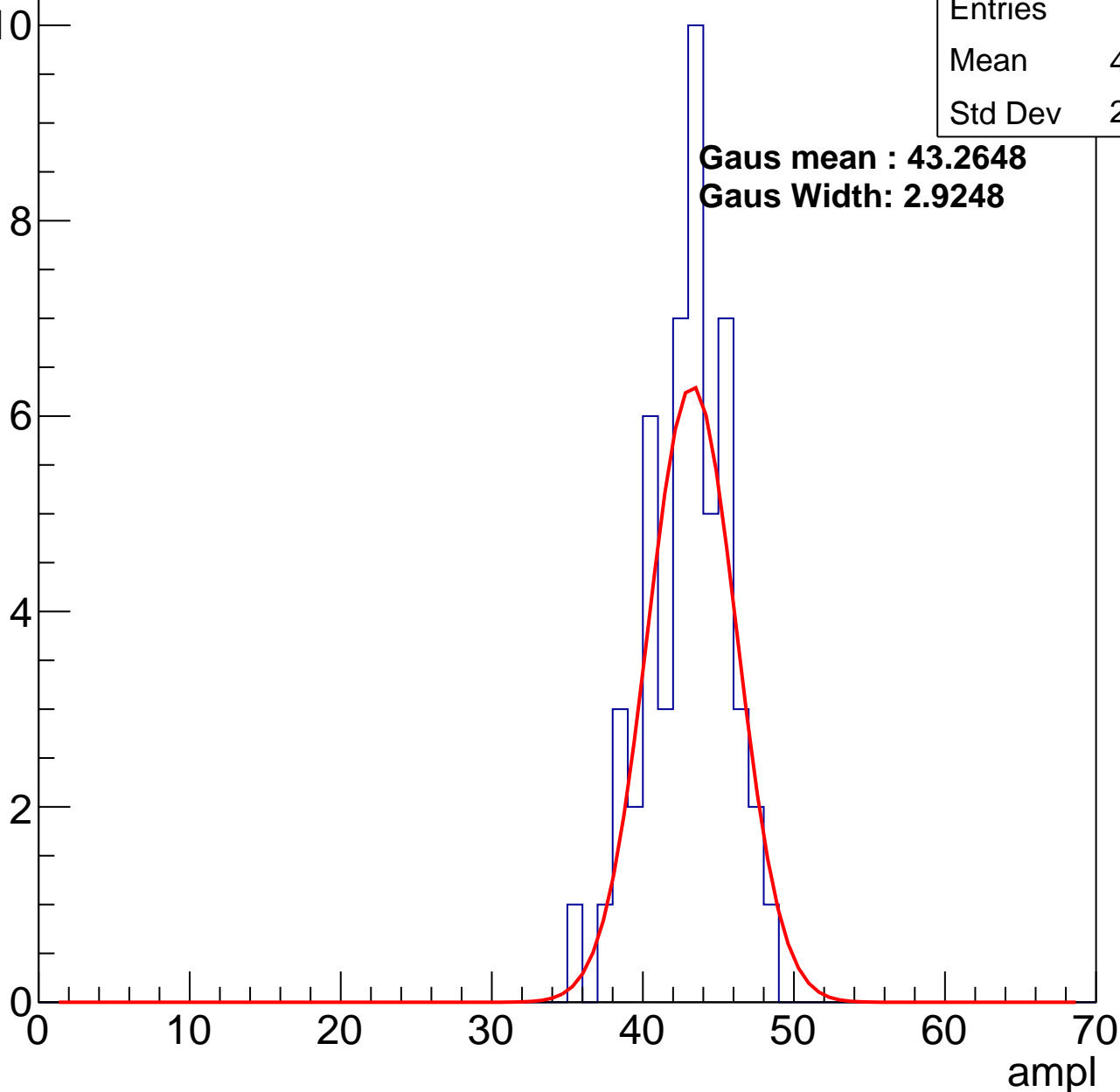
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	42.47
Std Dev	2.739

Gaus mean : 43.2648

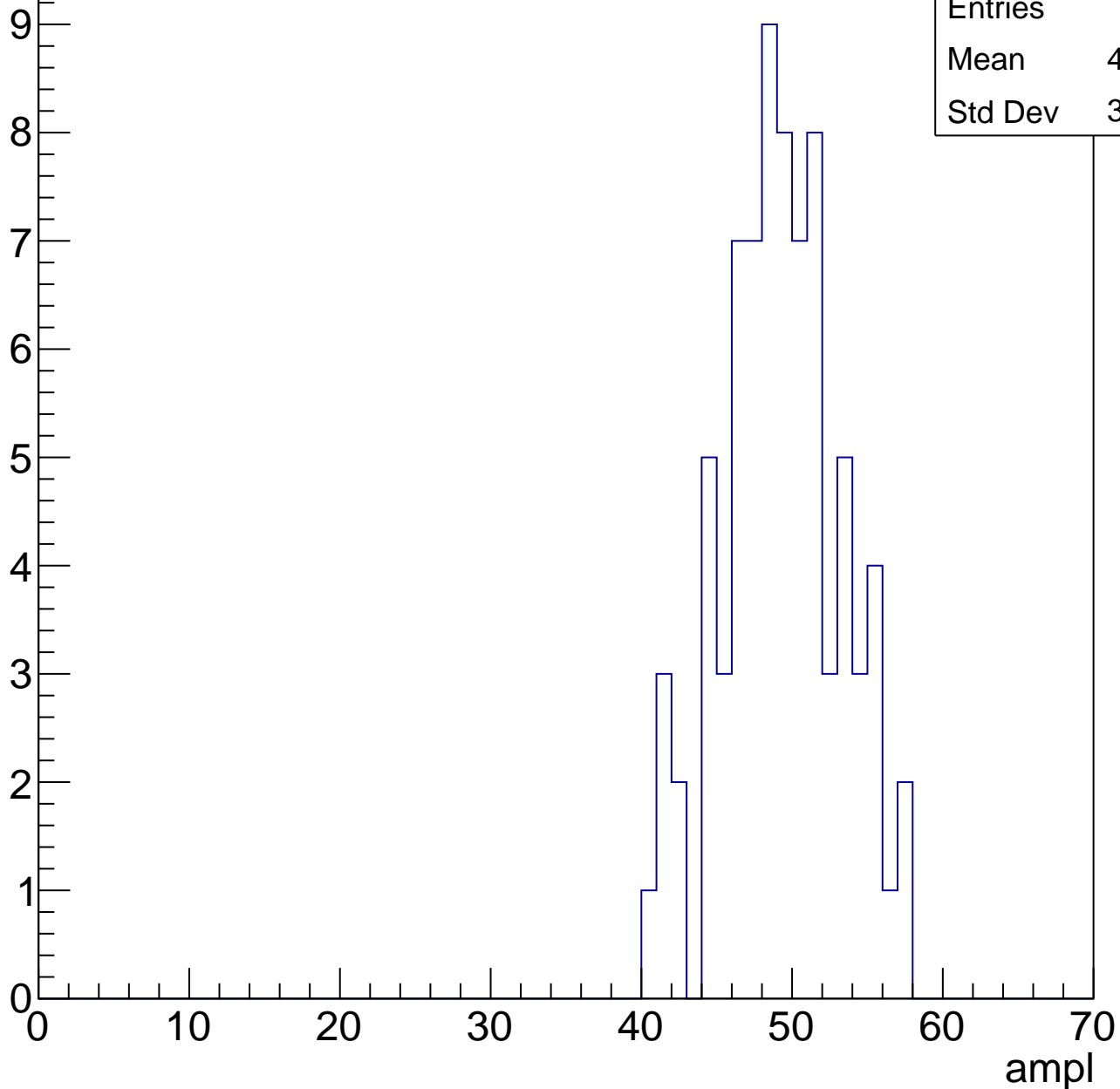
Gaus Width: 2.9248



B0L001S, U19-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



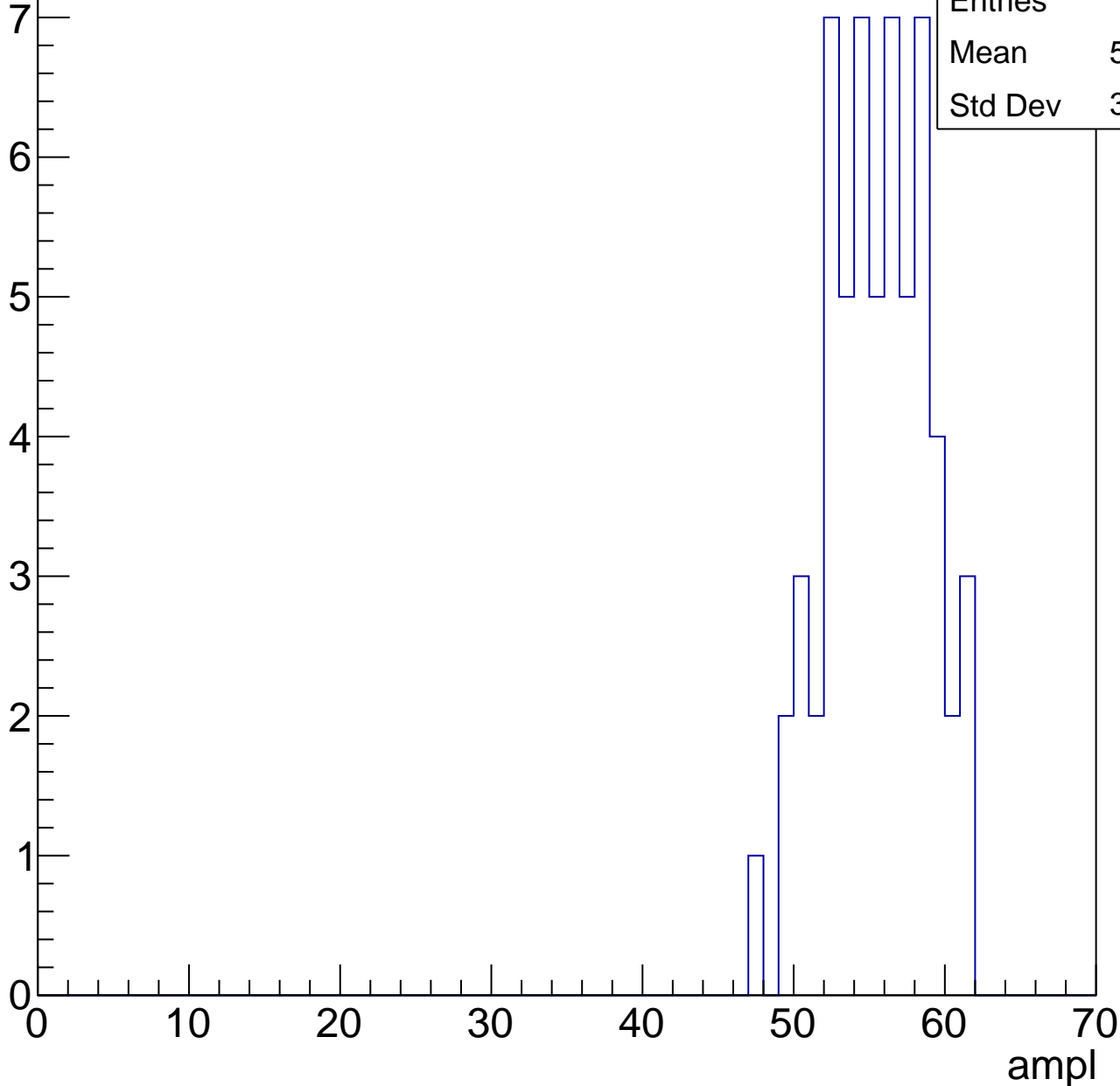
Entries	78
Mean	48.82
Std Dev	3.895

B0L001S, U19-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	55.02
Std Dev	3.279

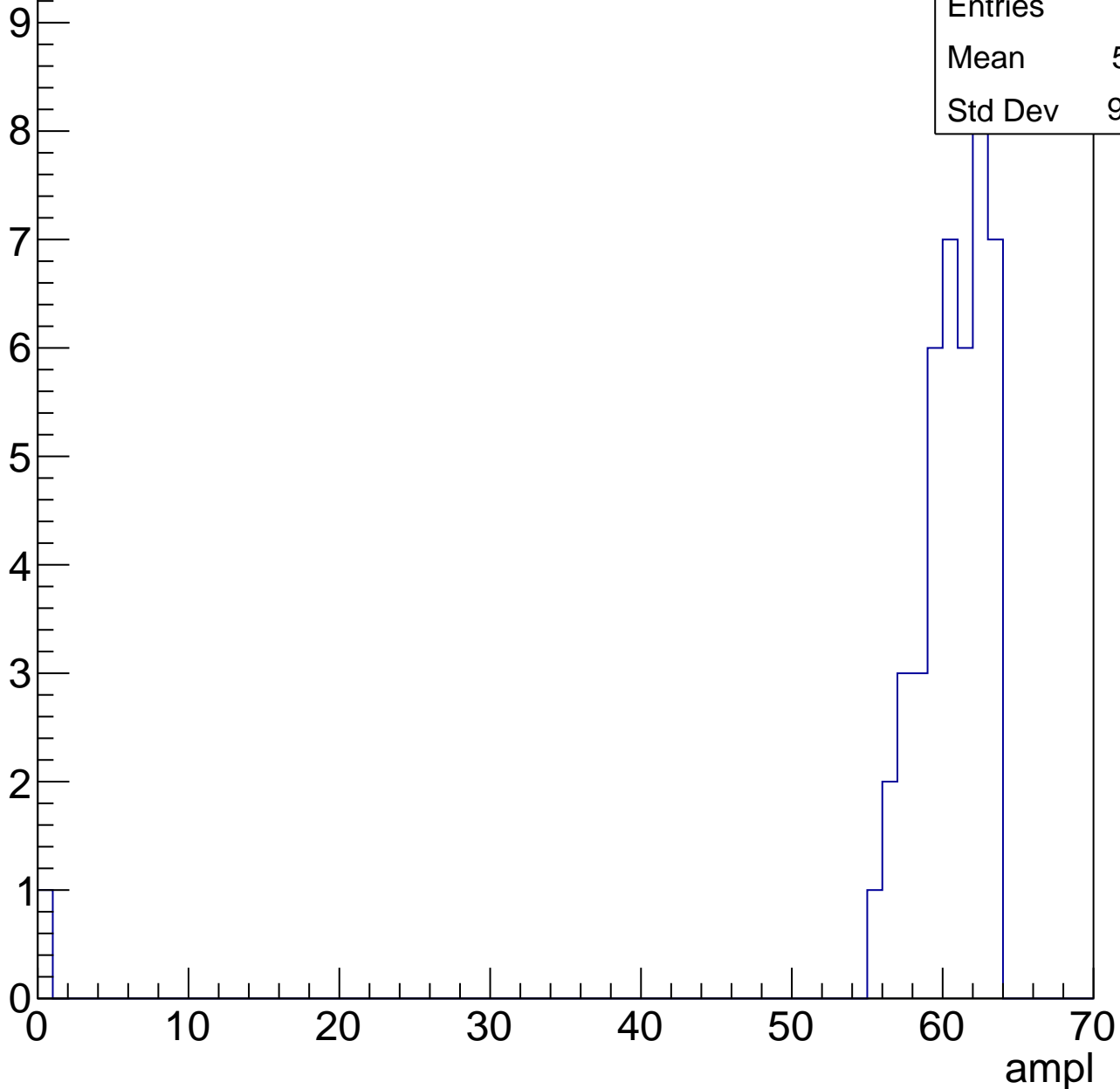


B0L001S, U19-ch74, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.91
Std Dev	9.133



B0L001S, U19-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B0L001S, U19-ch75, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	30
Std Dev	4.838

Gaus mean : 30.5467

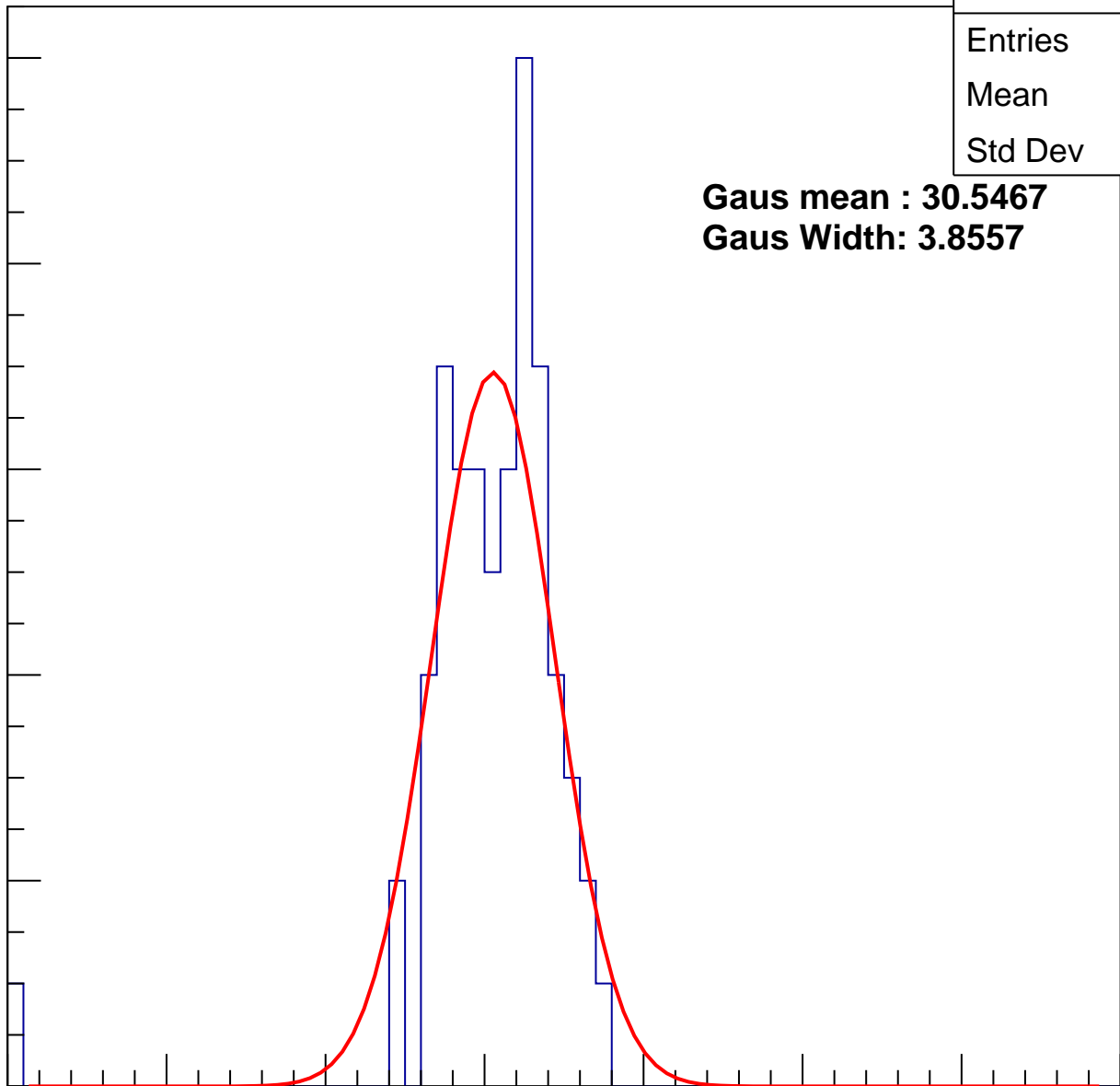
Gaus Width: 3.8557

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch75, adc1

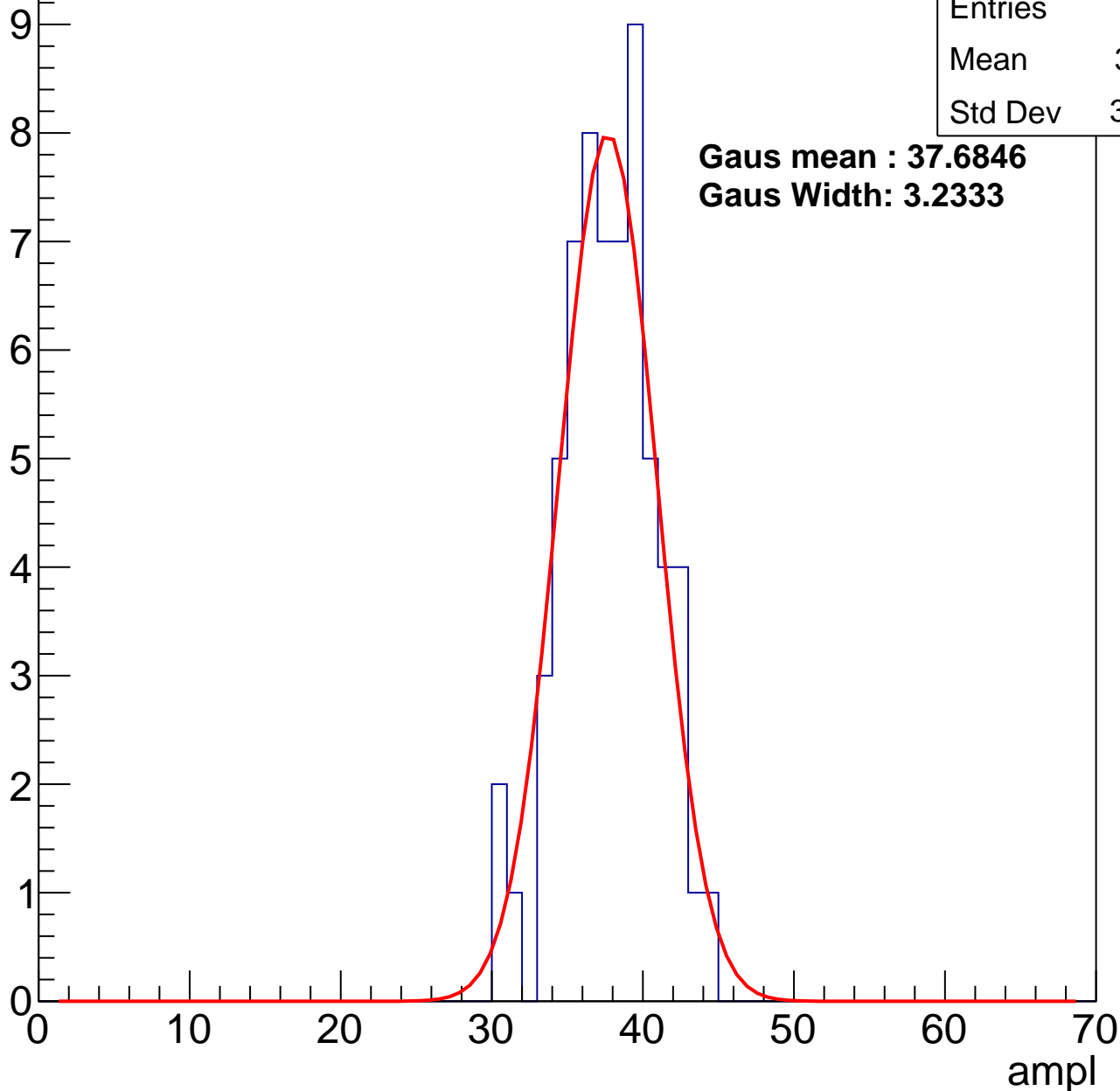
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.31
Std Dev	3.046

Gaus mean : 37.6846

Gaus Width: 3.2333



B0L001S, U19-ch75, adc2

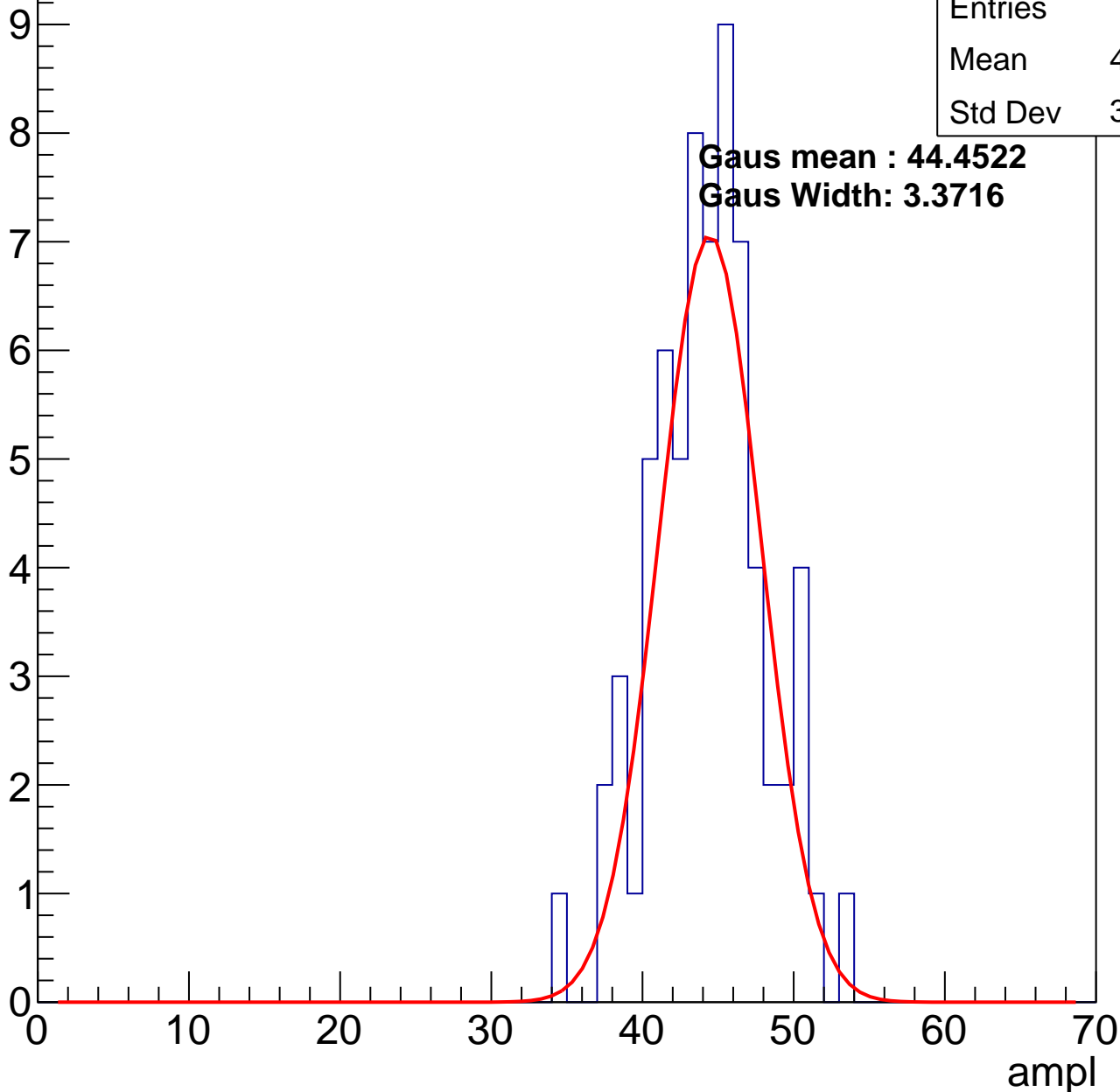
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.85
Std Dev	3.679

Gaus mean : 44.4522

Gaus Width: 3.3716

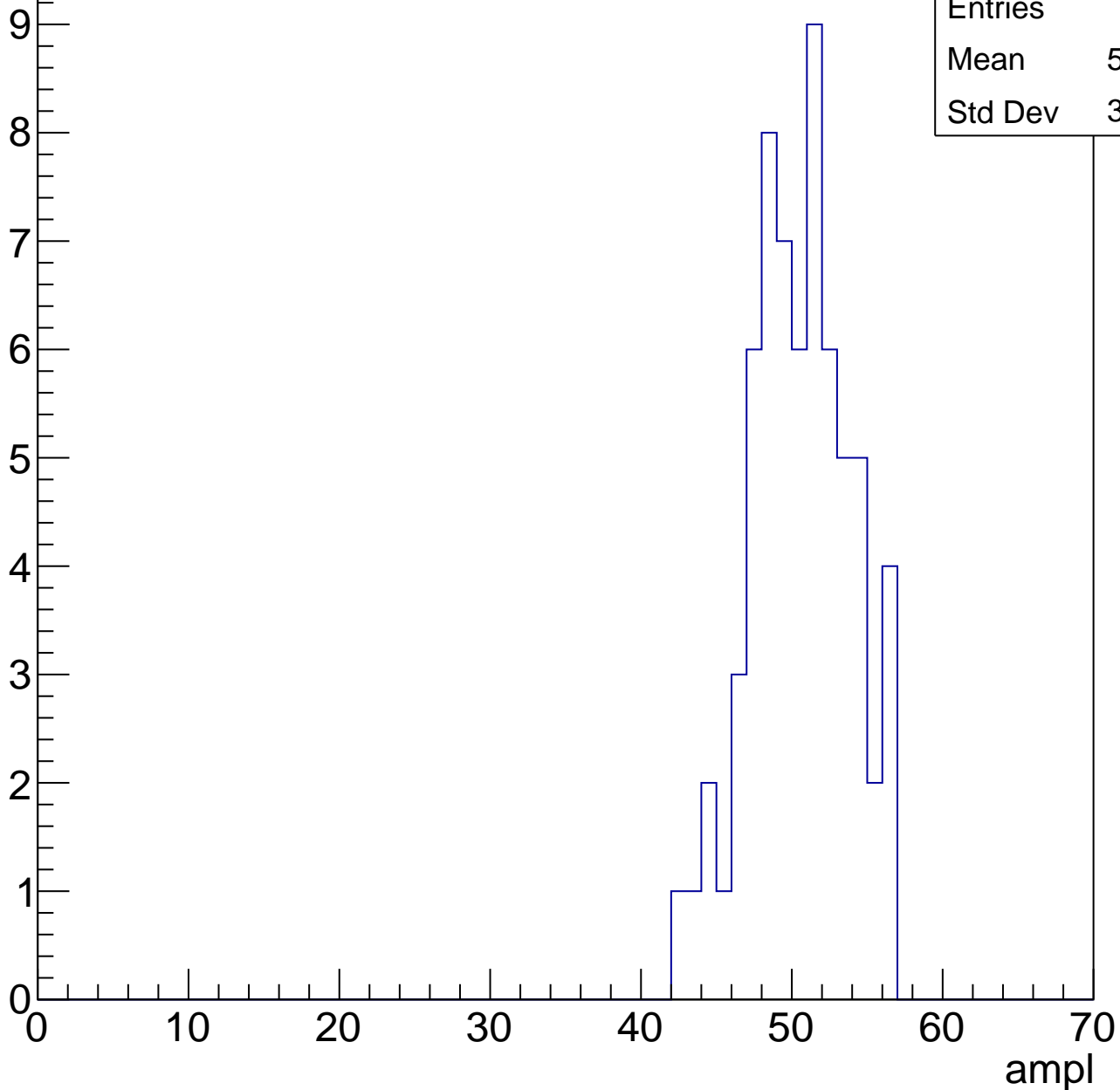


B0L001S, U19-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.08
Std Dev	3.263

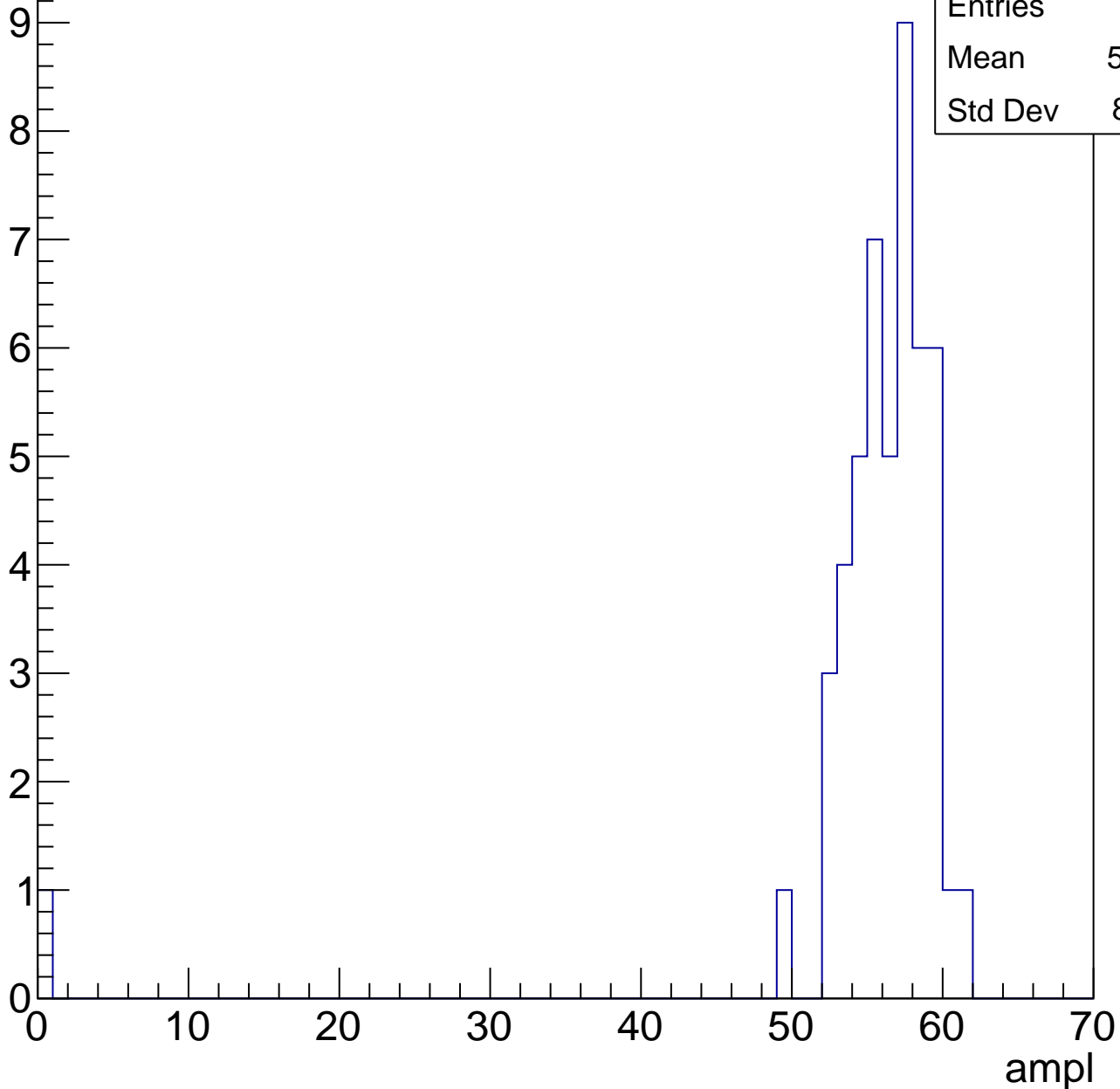


B0L001S, U19-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	54.86
Std Dev	8.281

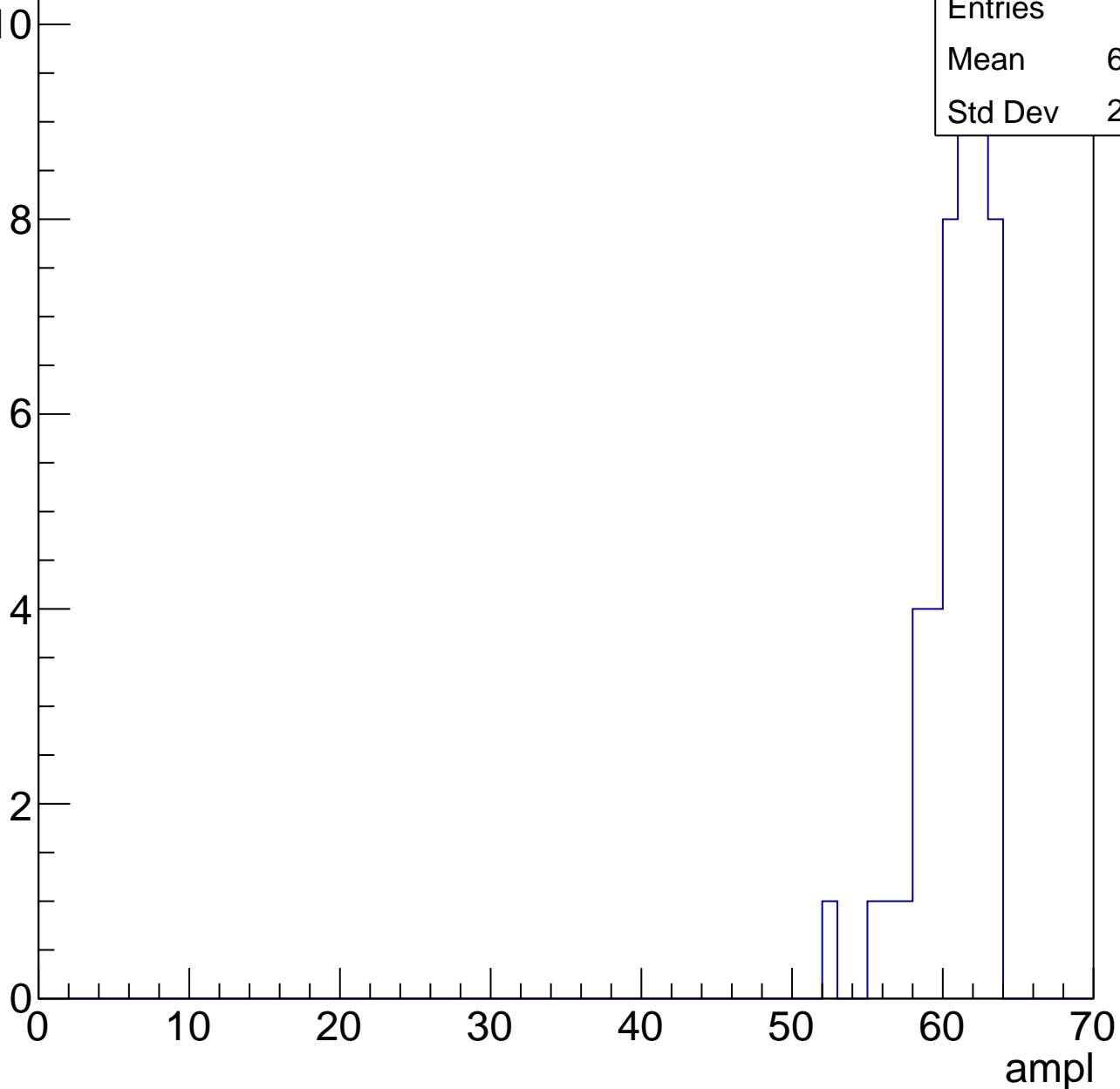


B0L001S, U19-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	60.45
Std Dev	2.286



B0L001S, U19-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch76, adc0

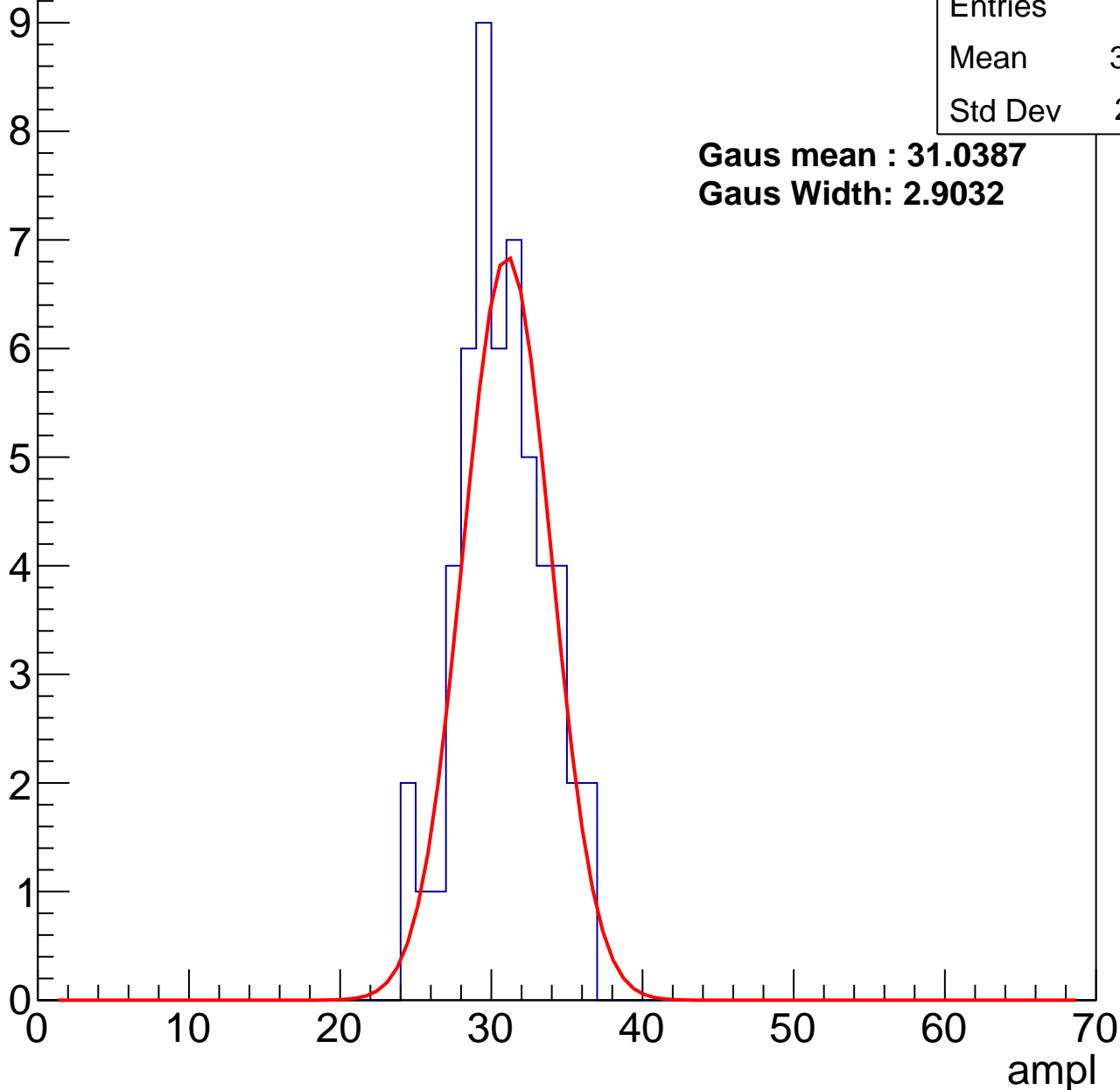
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	30.25
Std Dev	2.841

Gaus mean : 31.0387

Gaus Width: 2.9032



B0L001S, U19-ch76, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	36.86
Std Dev	3.433

Gaus mean : 37.3088

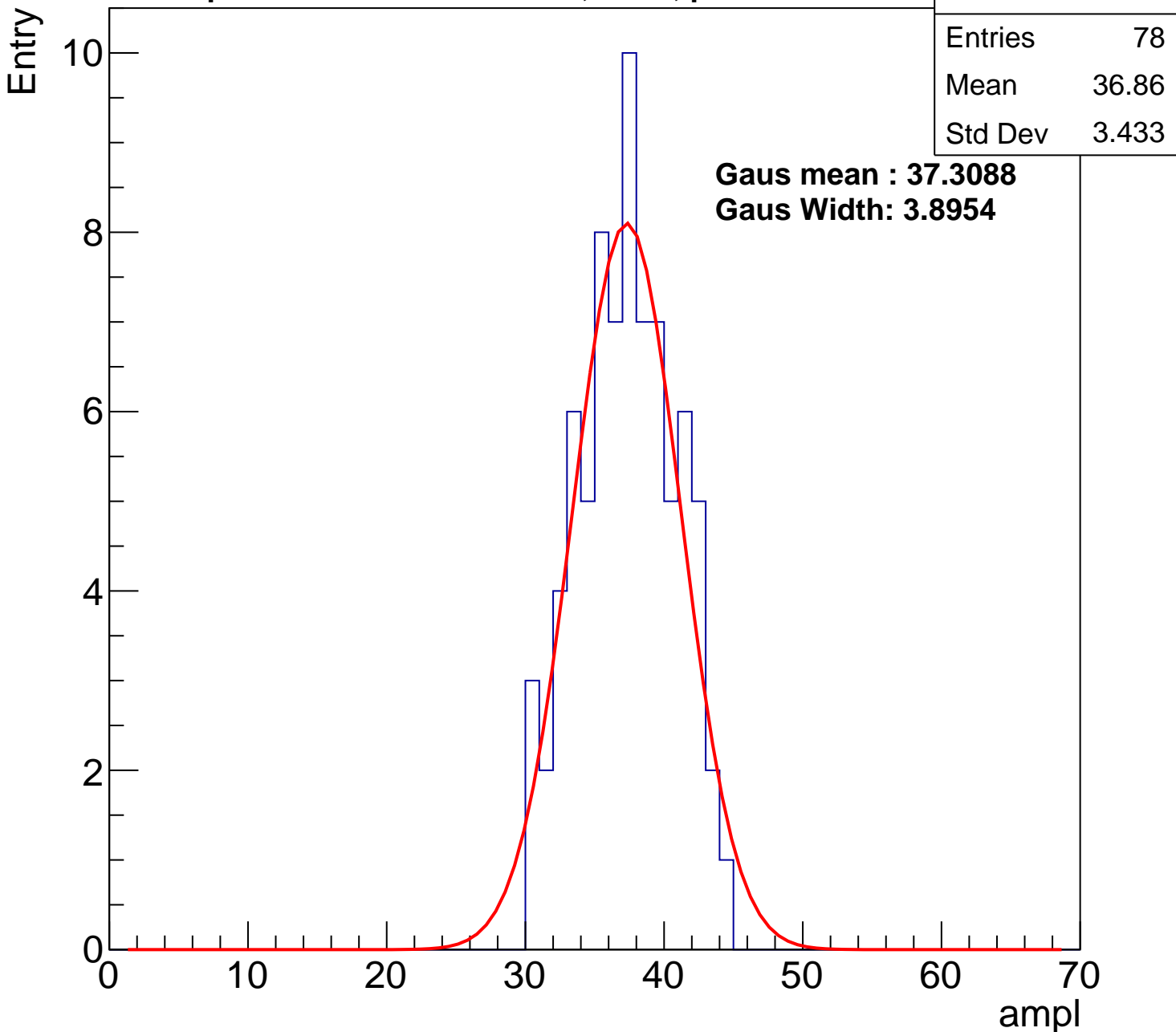
Gaus Width: 3.8954

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

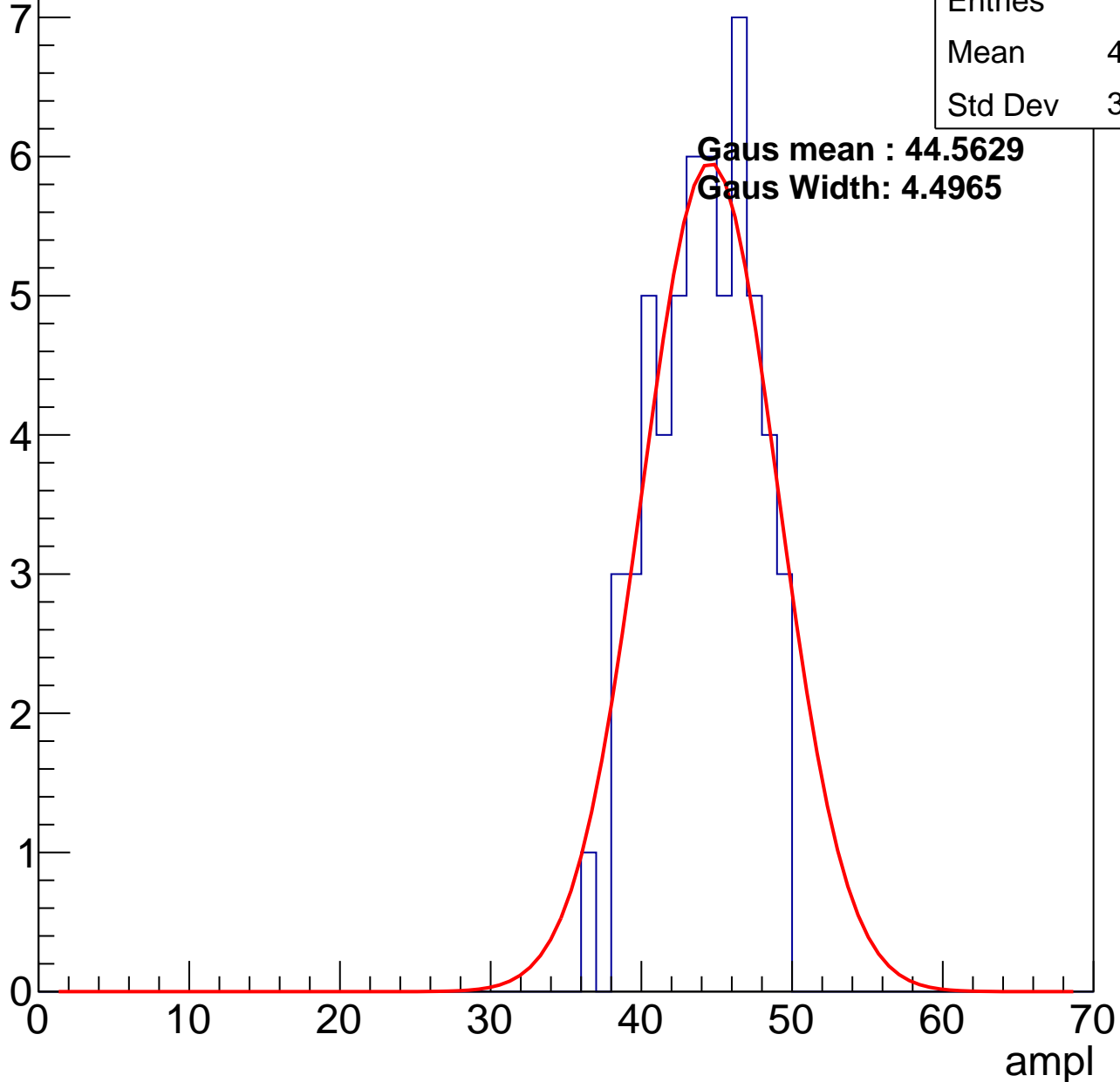


B0L001S, U19-ch76, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.58
Std Dev	3.233

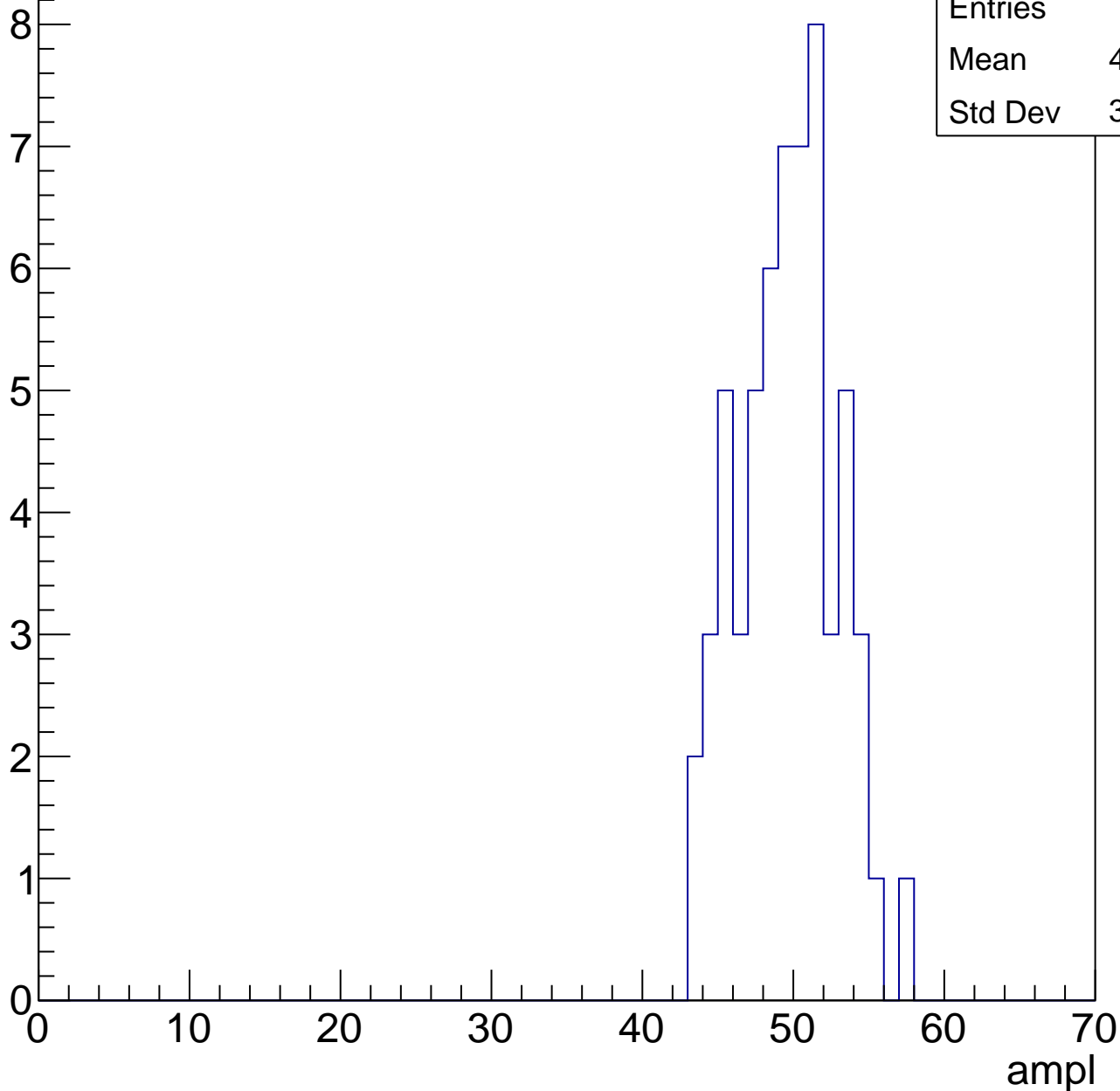


B0L001S, U19-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	49.15
Std Dev	3.188

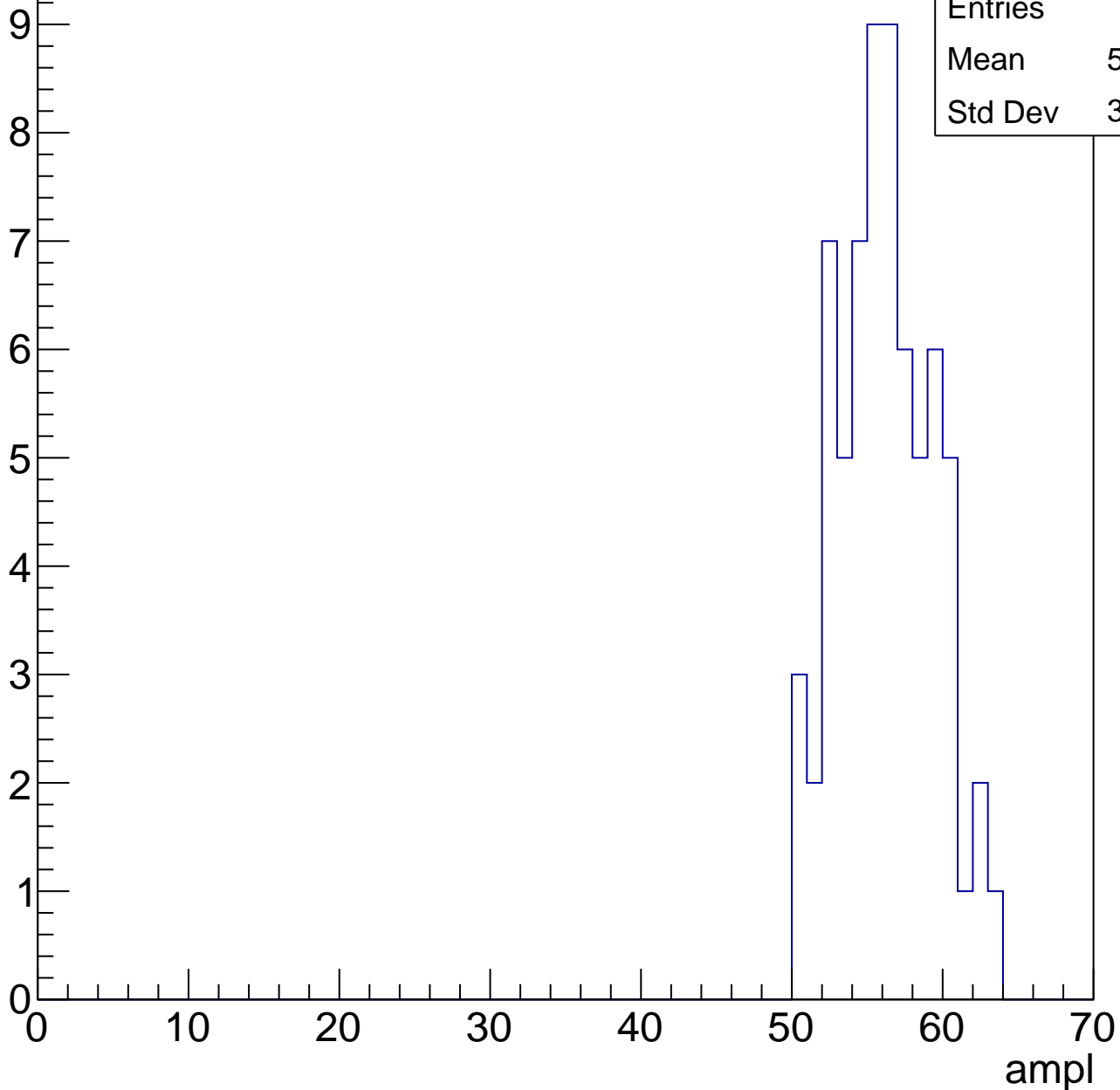


B0L001S, U19-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	55.76
Std Dev	3.102

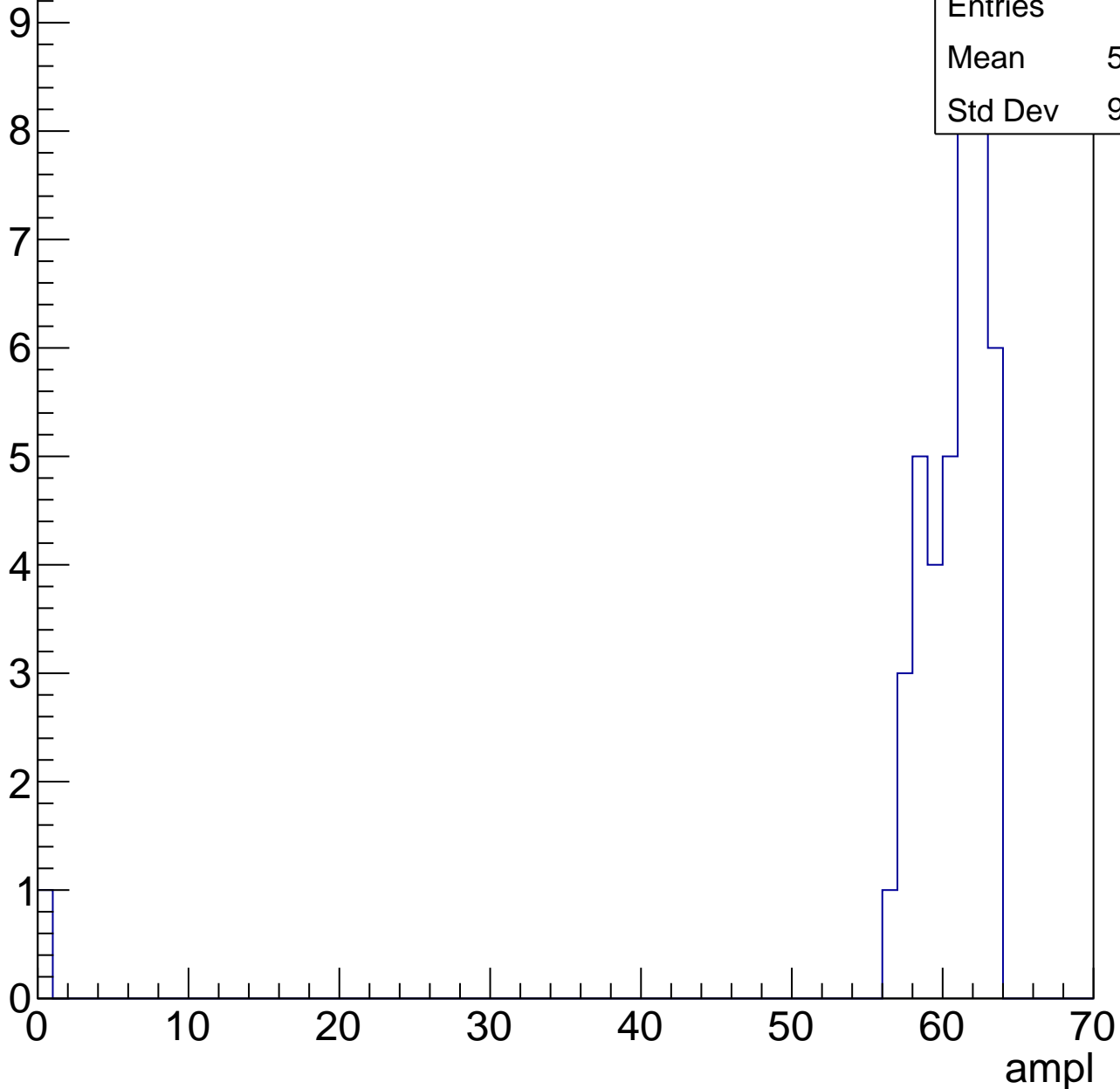


B0L001S, U19-ch76, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.95
Std Dev	9.406



B0L001S, U19-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch77, adc0

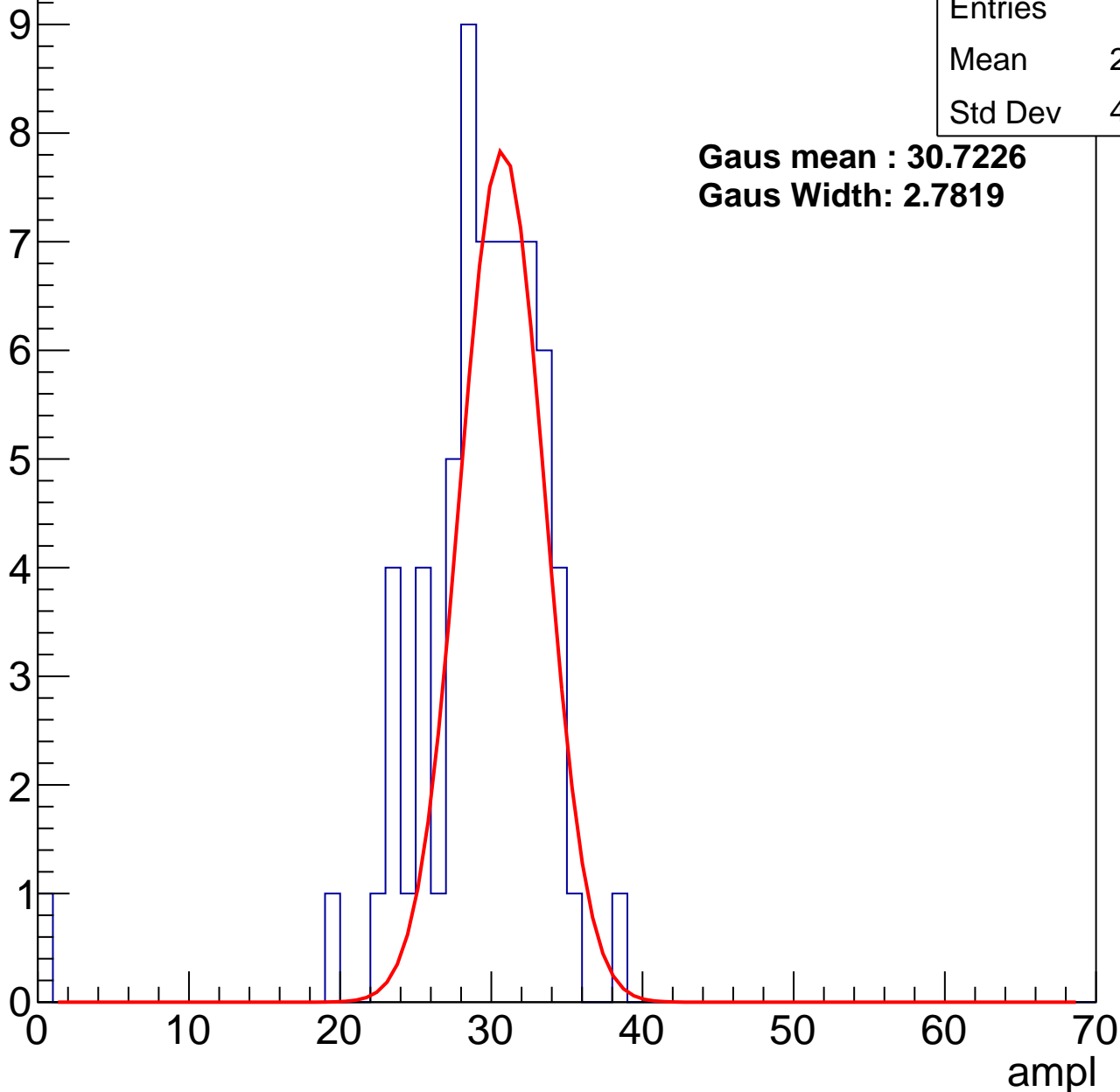
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	28.82
Std Dev	4.992

Gaus mean : 30.7226

Gaus Width: 2.7819



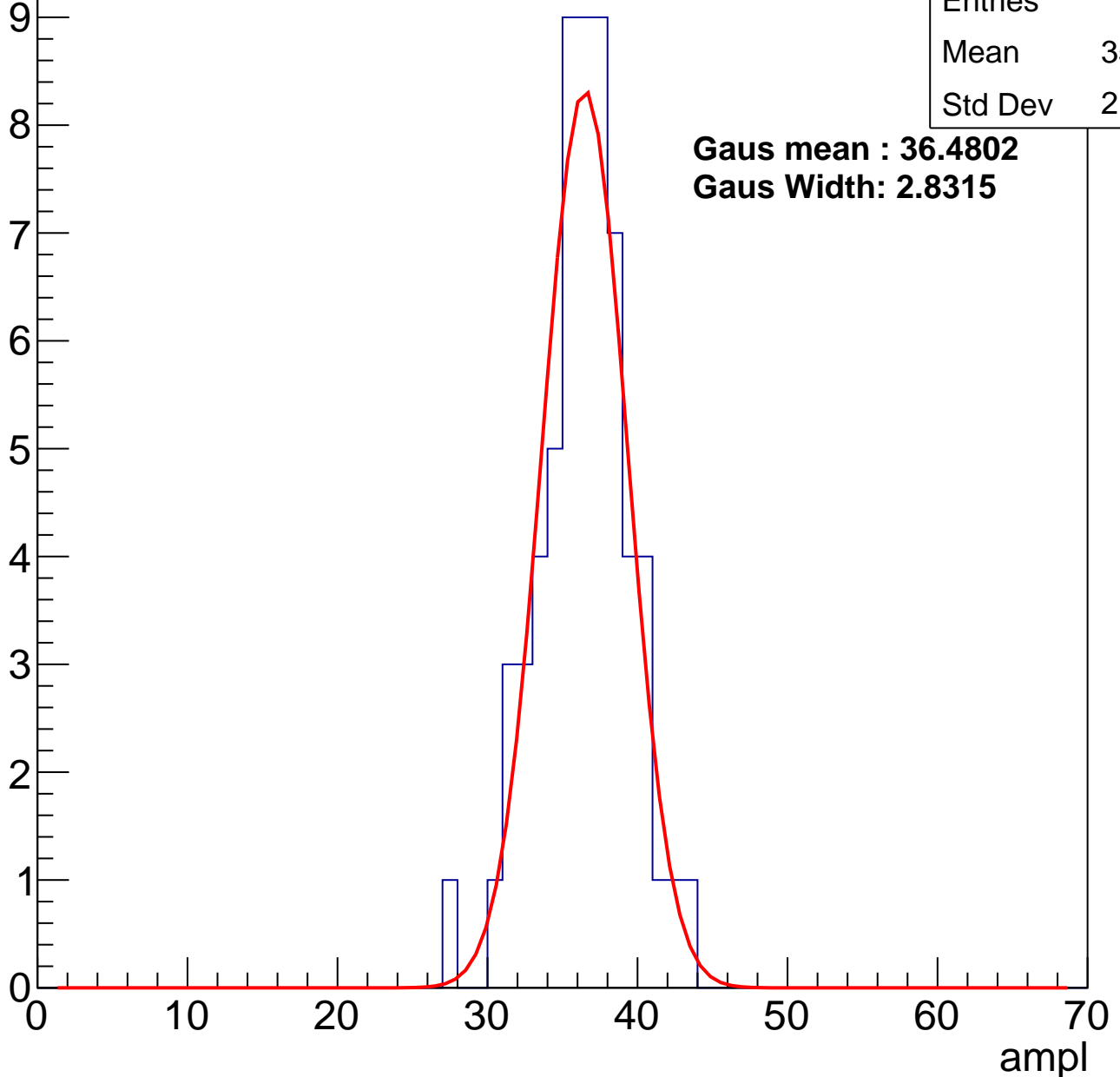
B0L001S, U19-ch77, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	35.94
Std Dev	2.983

Gaus mean : 36.4802
Gaus Width: 2.8315



B0L001S, U19-ch77, adc2

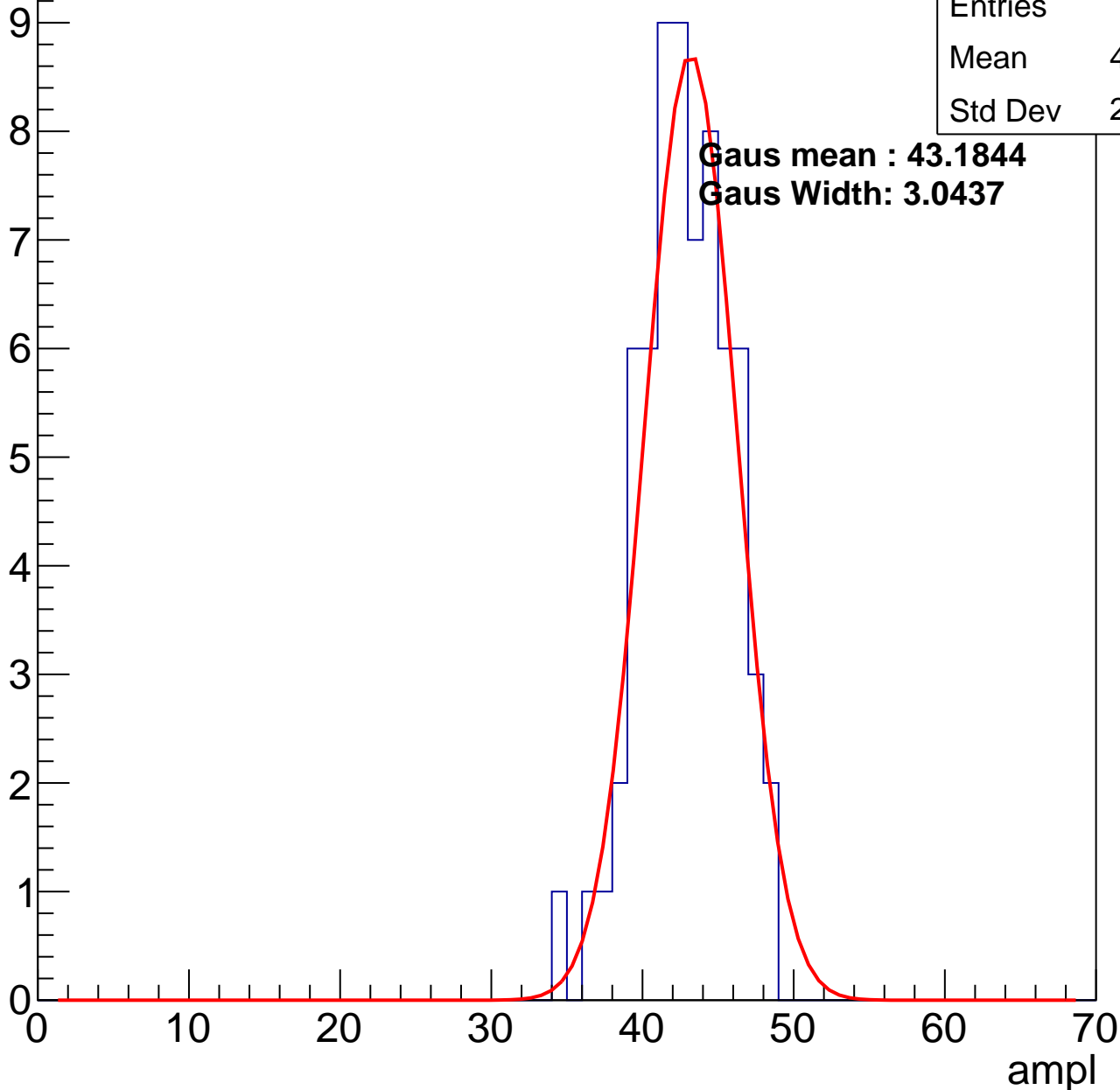
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	42.39
Std Dev	2.917

Gaus mean : 43.1844

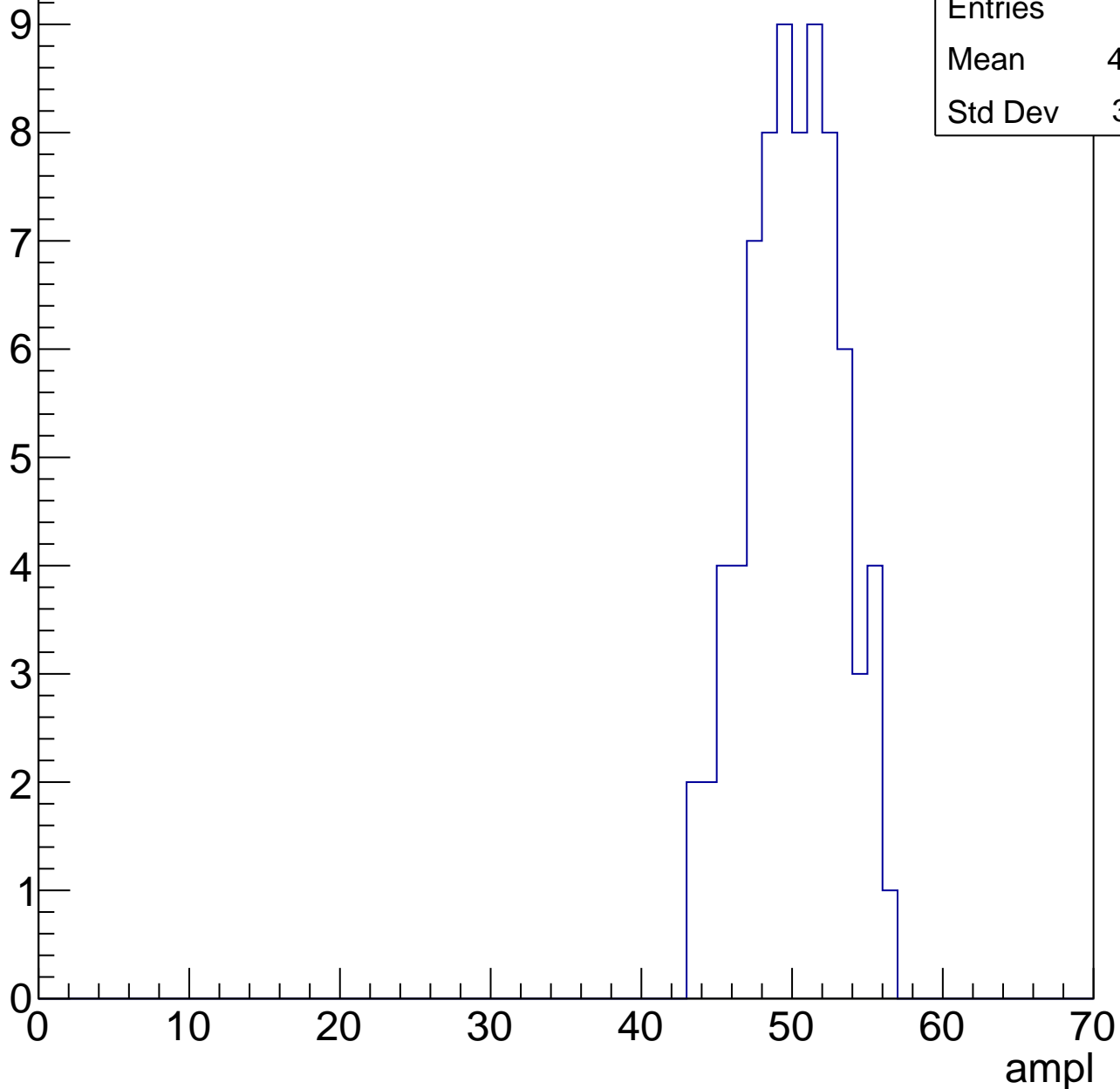
Gaus Width: 3.0437



B0L001S, U19-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

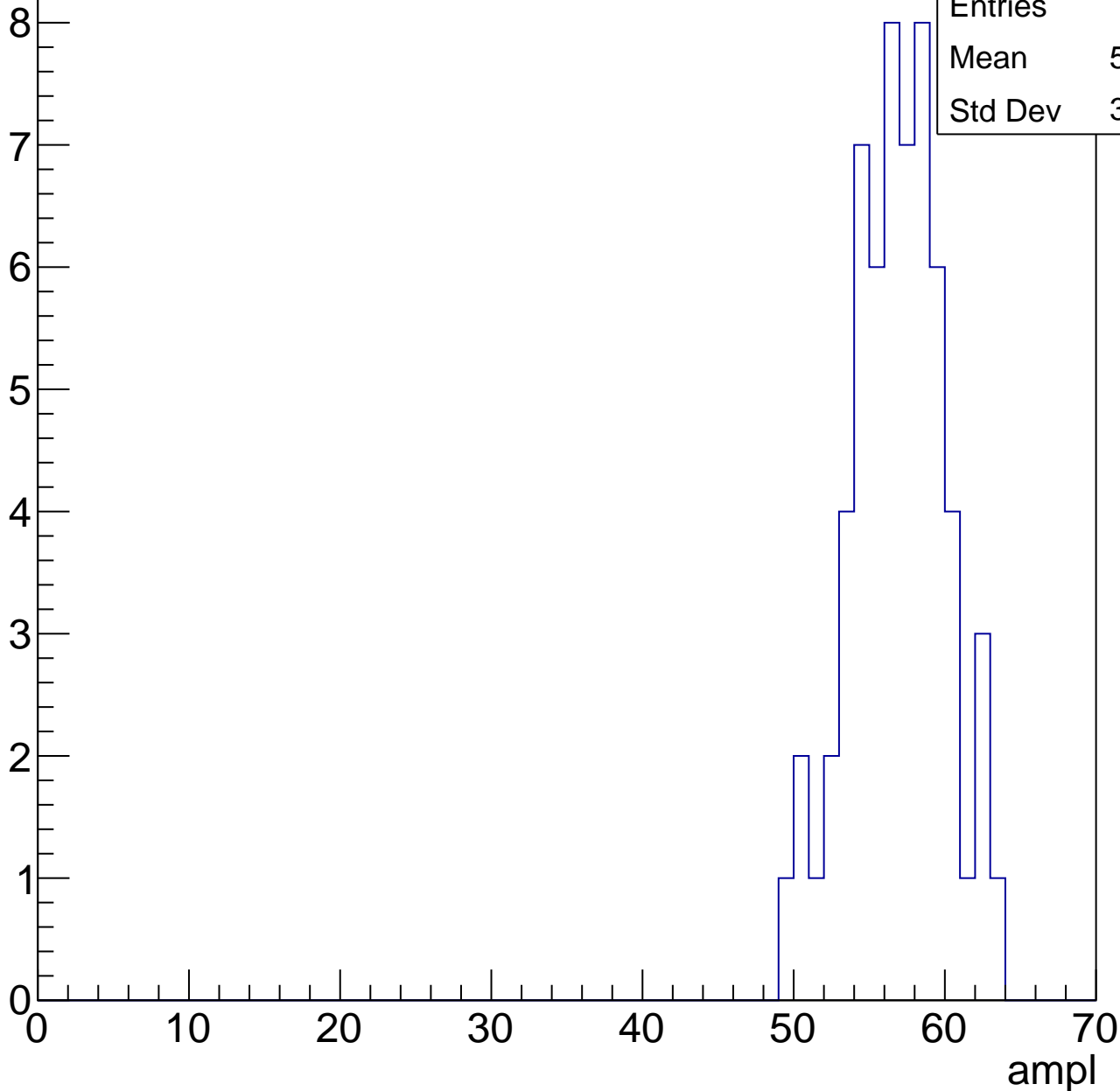


B0L001S, U19-ch77, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	56.38
Std Dev	3.079

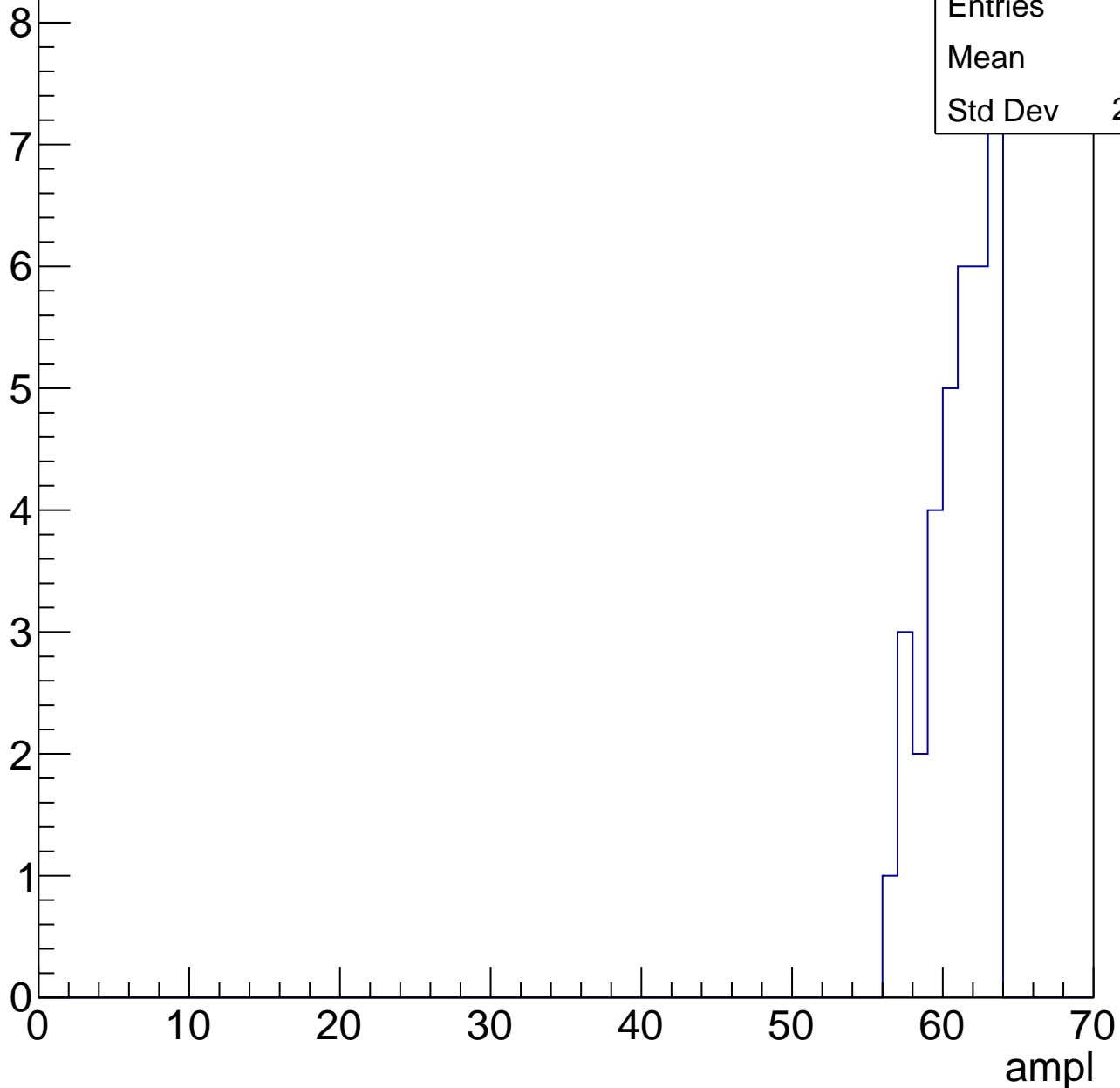


B0L001S, U19-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

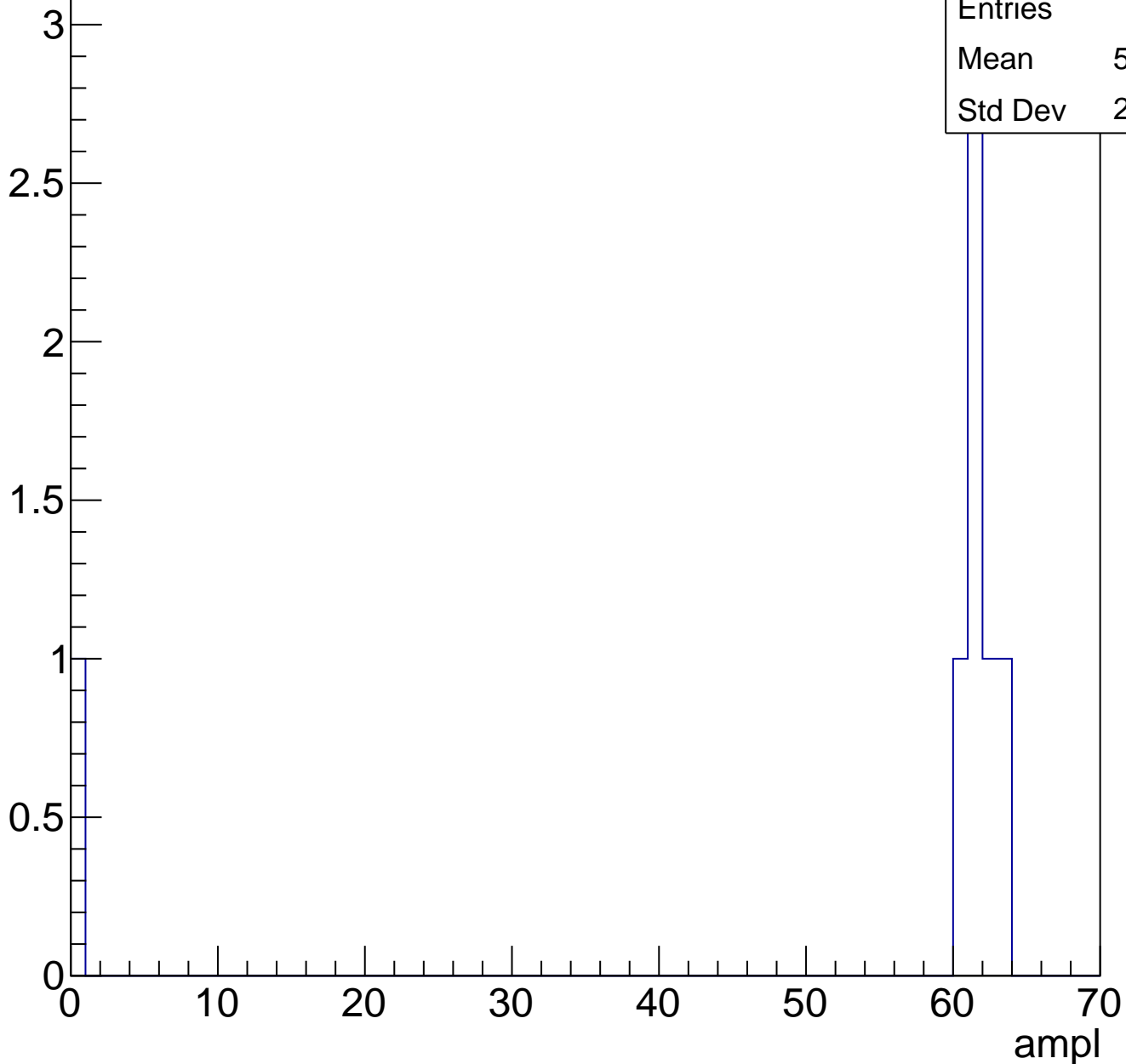
Entries	35
Mean	60.6
Std Dev	2.031



B0L001S, U19-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

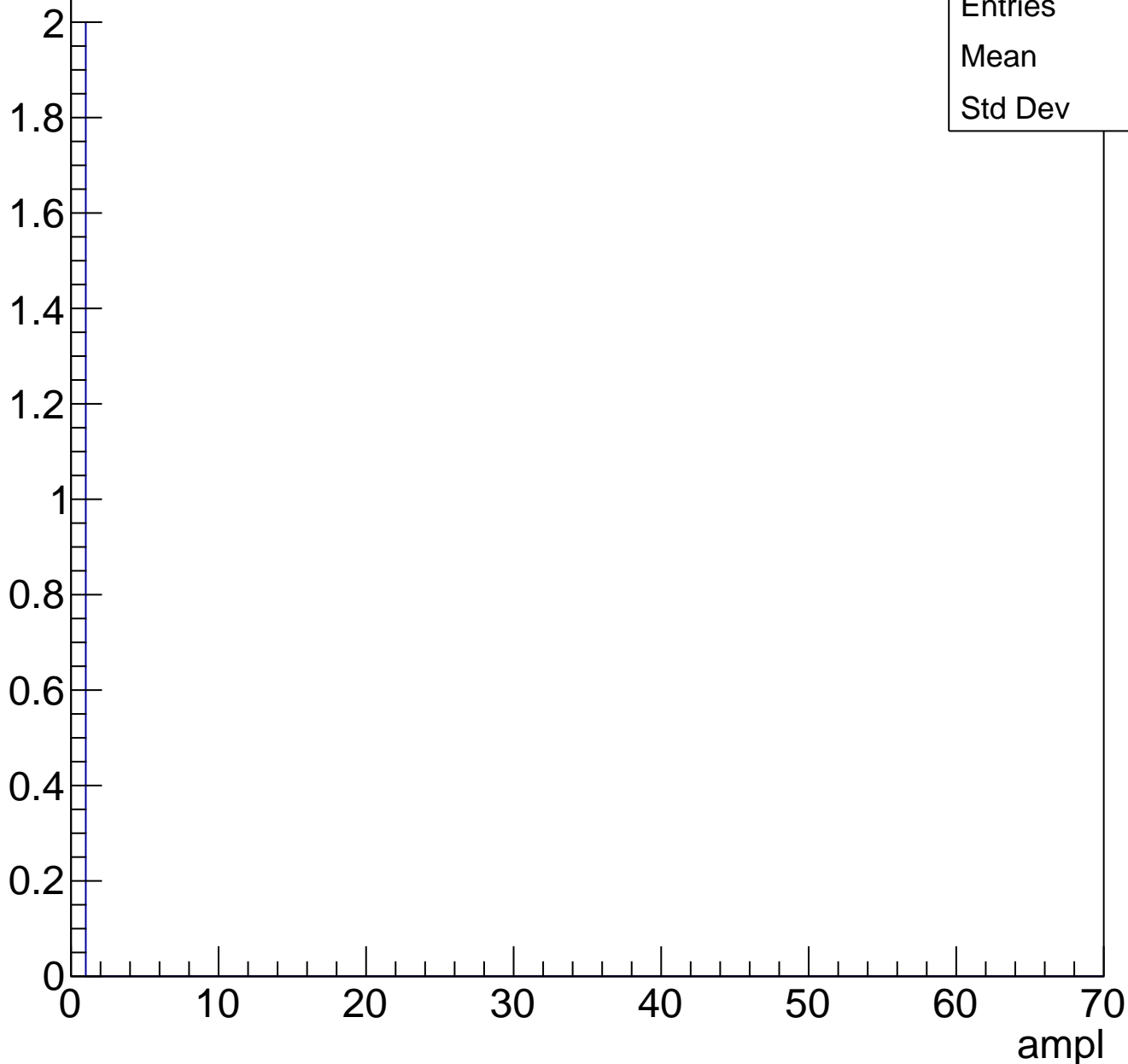
Entry



B0L001S, U19-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch78, adc0

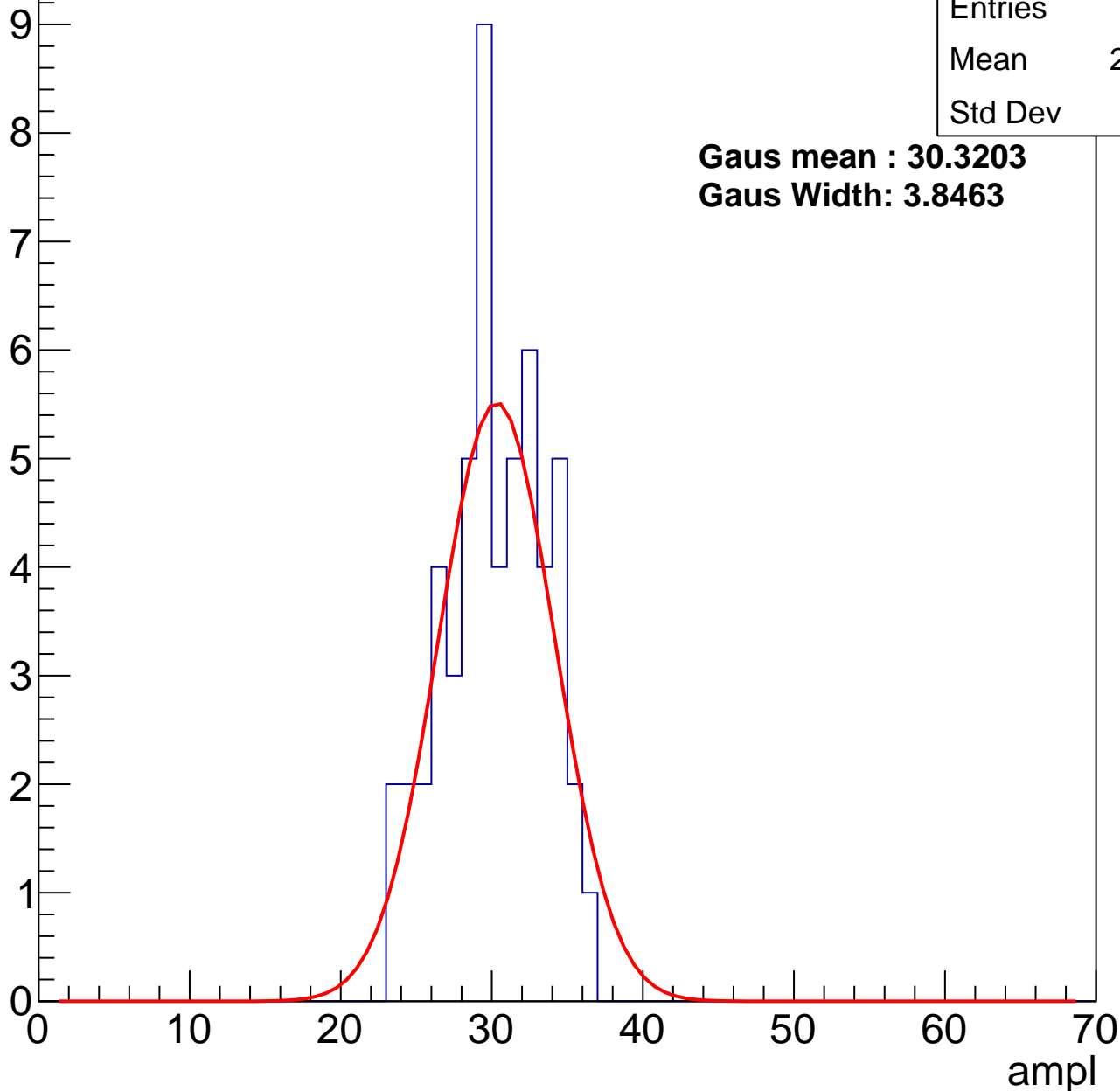
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	29.72
Std Dev	3.24

Gaus mean : 30.3203

Gaus Width: 3.8463



B0L001S, U19-ch78, adc1

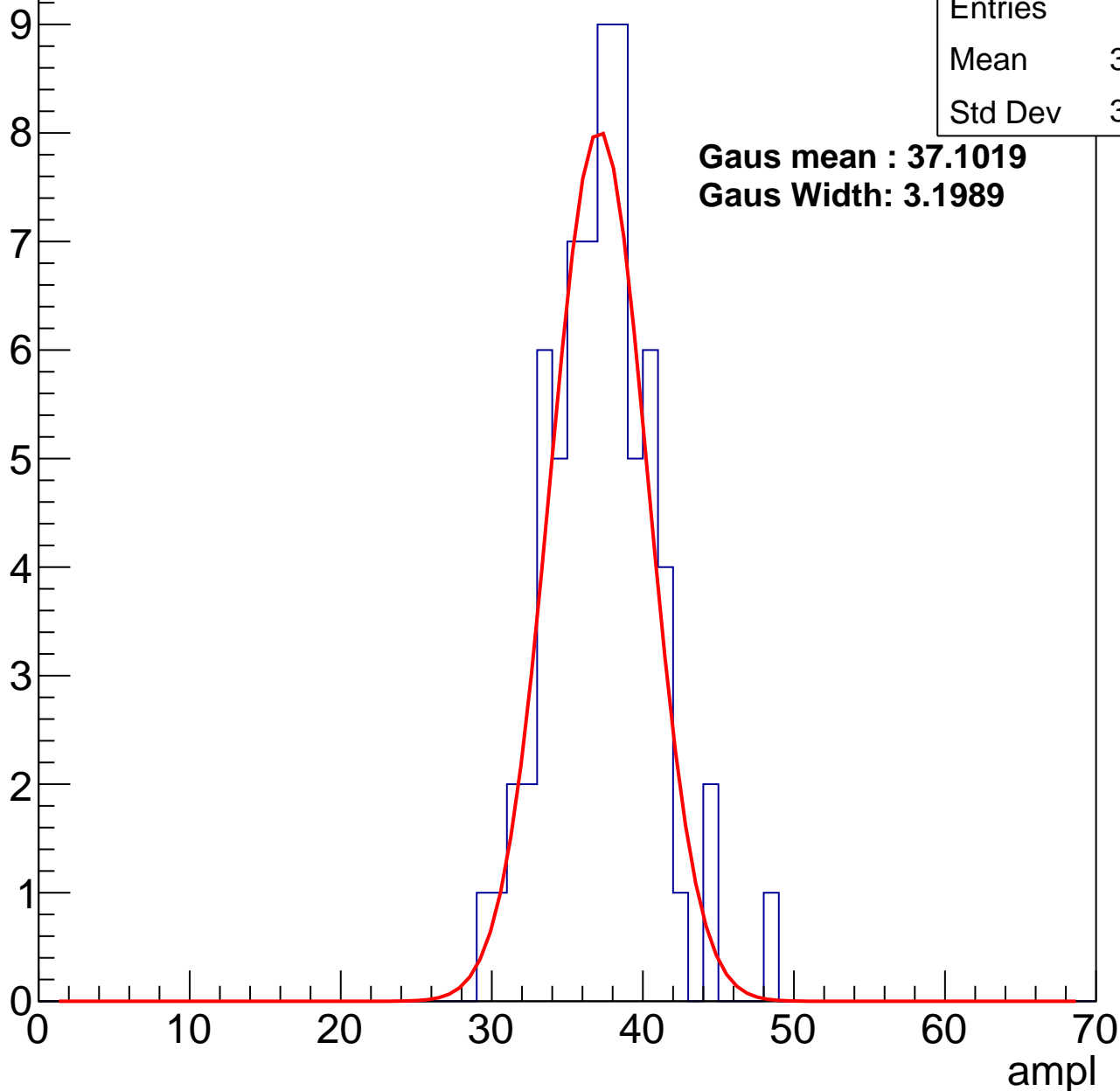
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	36.79
Std Dev	3.428

Gaus mean : 37.1019

Gaus Width: 3.1989



B0L001S, U19-ch78, adc2

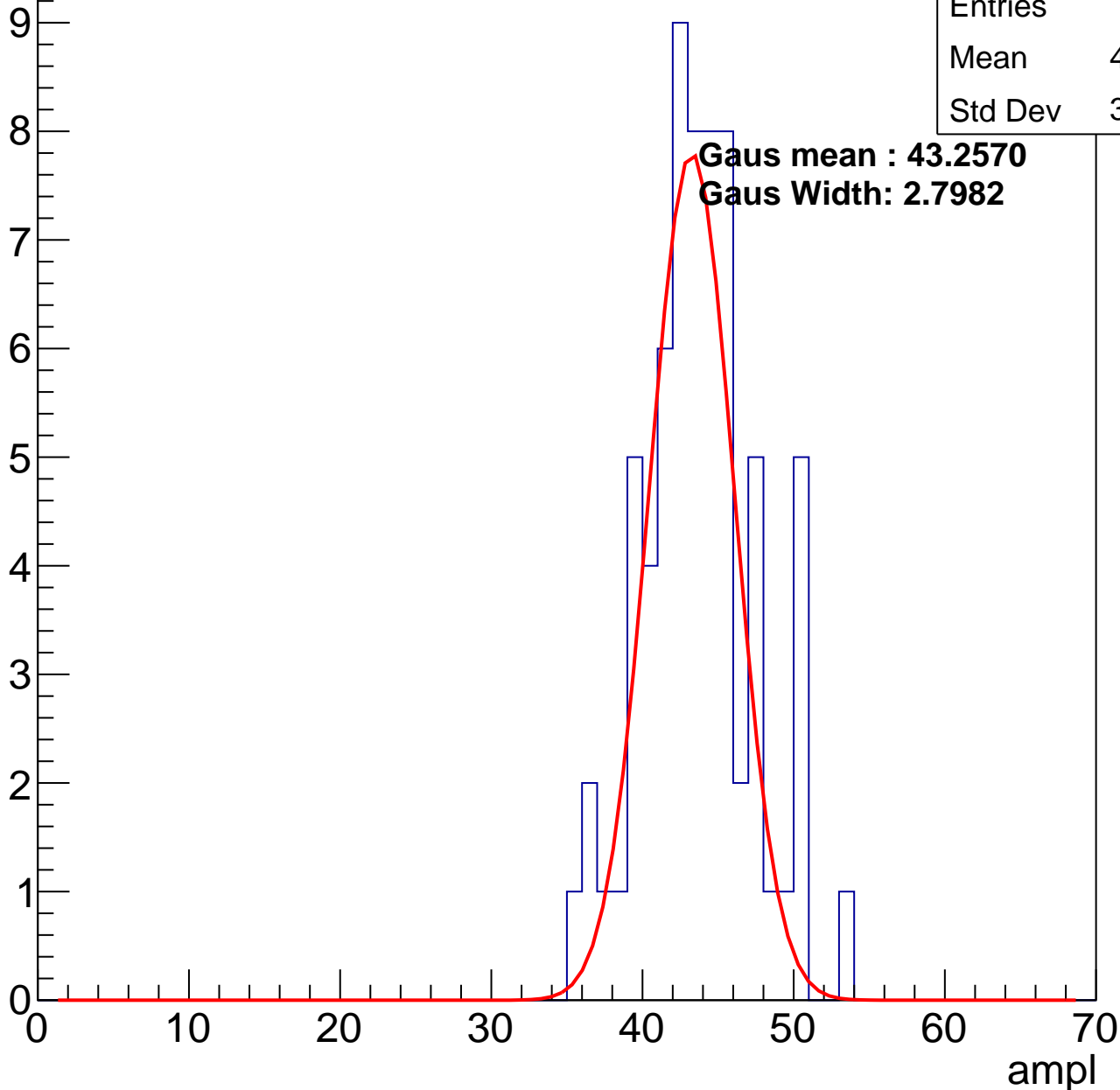
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.29
Std Dev	3.658

Gaus mean : 43.2570

Gaus Width: 2.7982

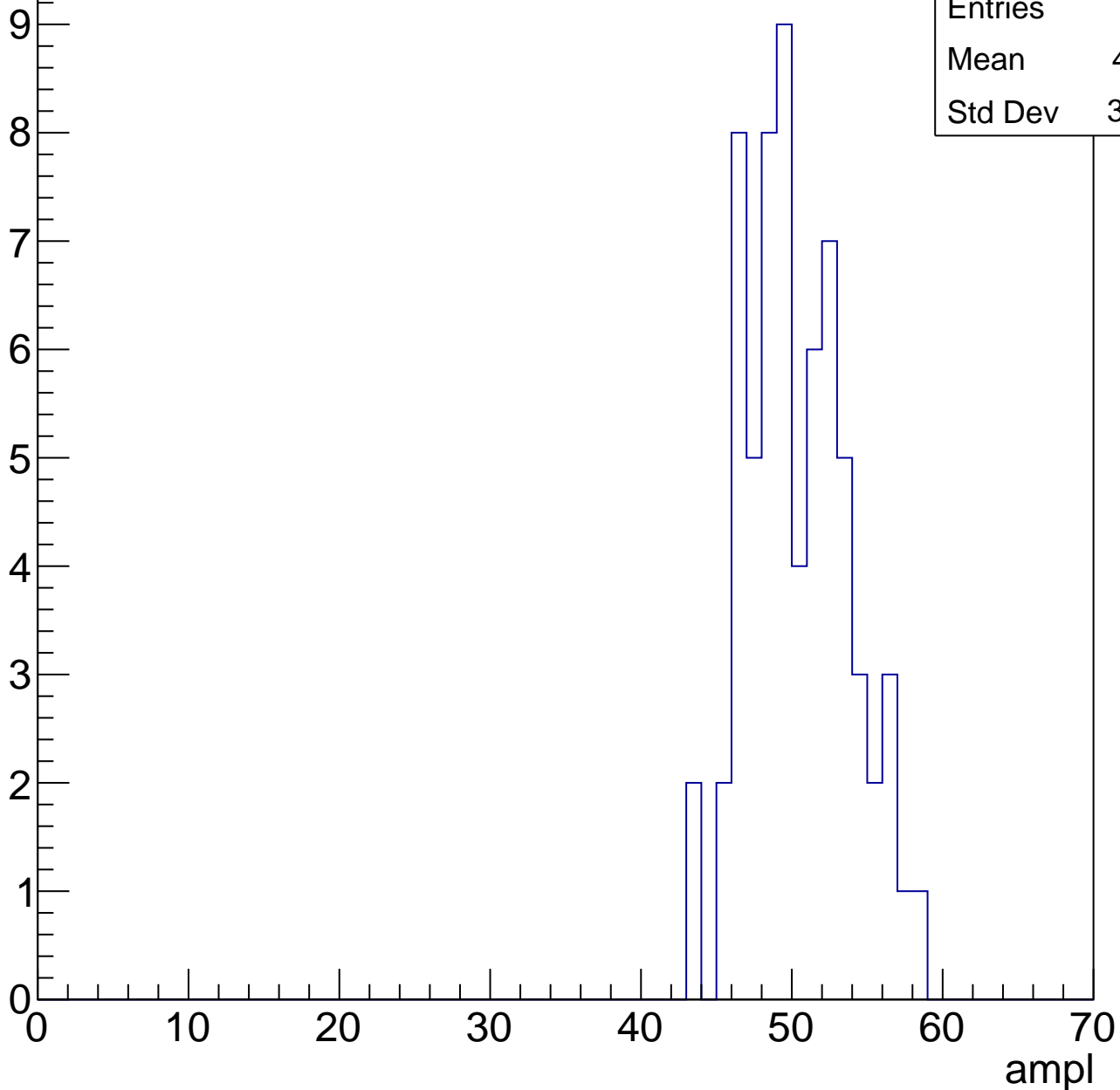


B0L001S, U19-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49.91
Std Dev	3.392

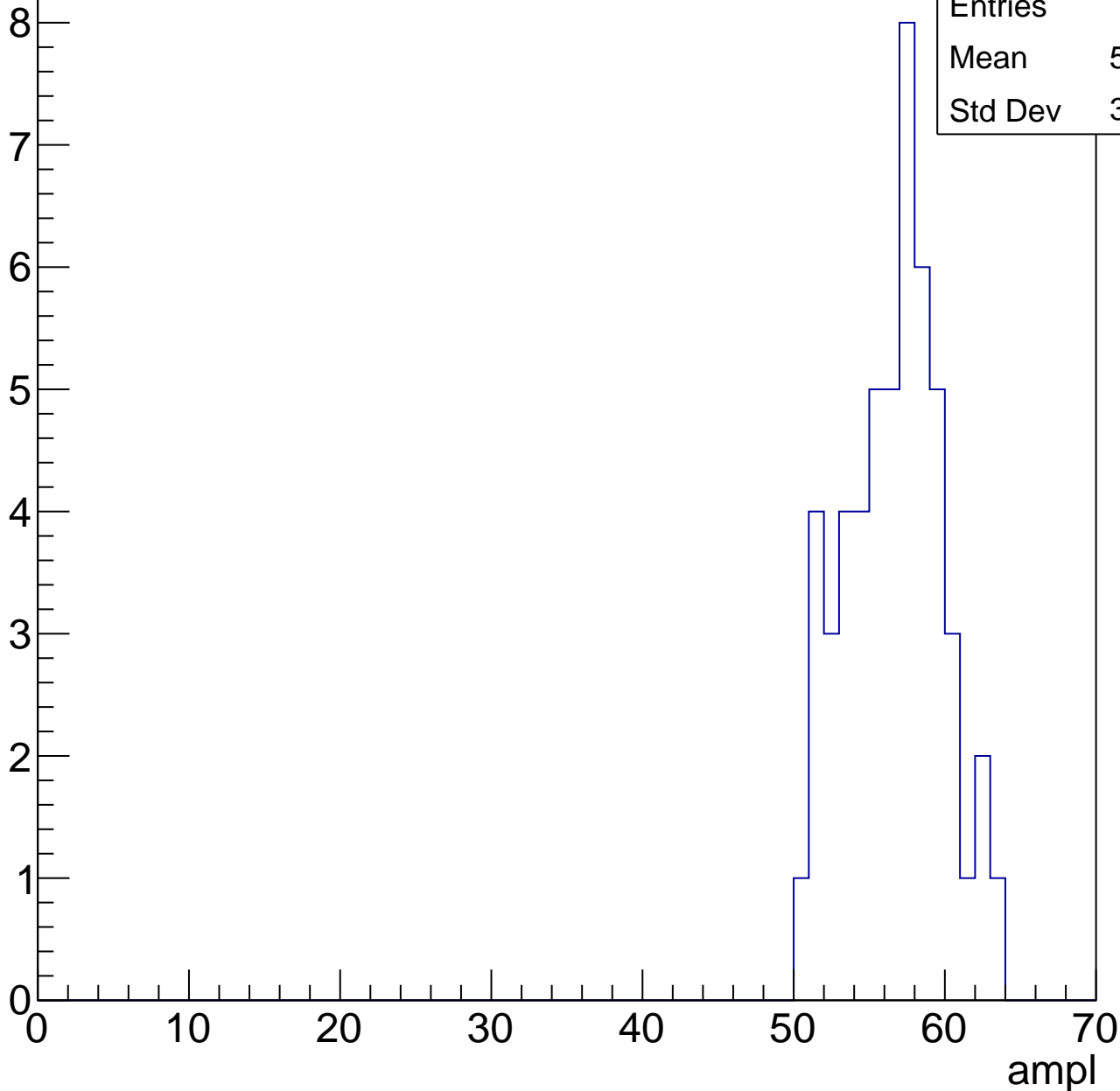


B0L001S, U19-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.15
Std Dev	3.134

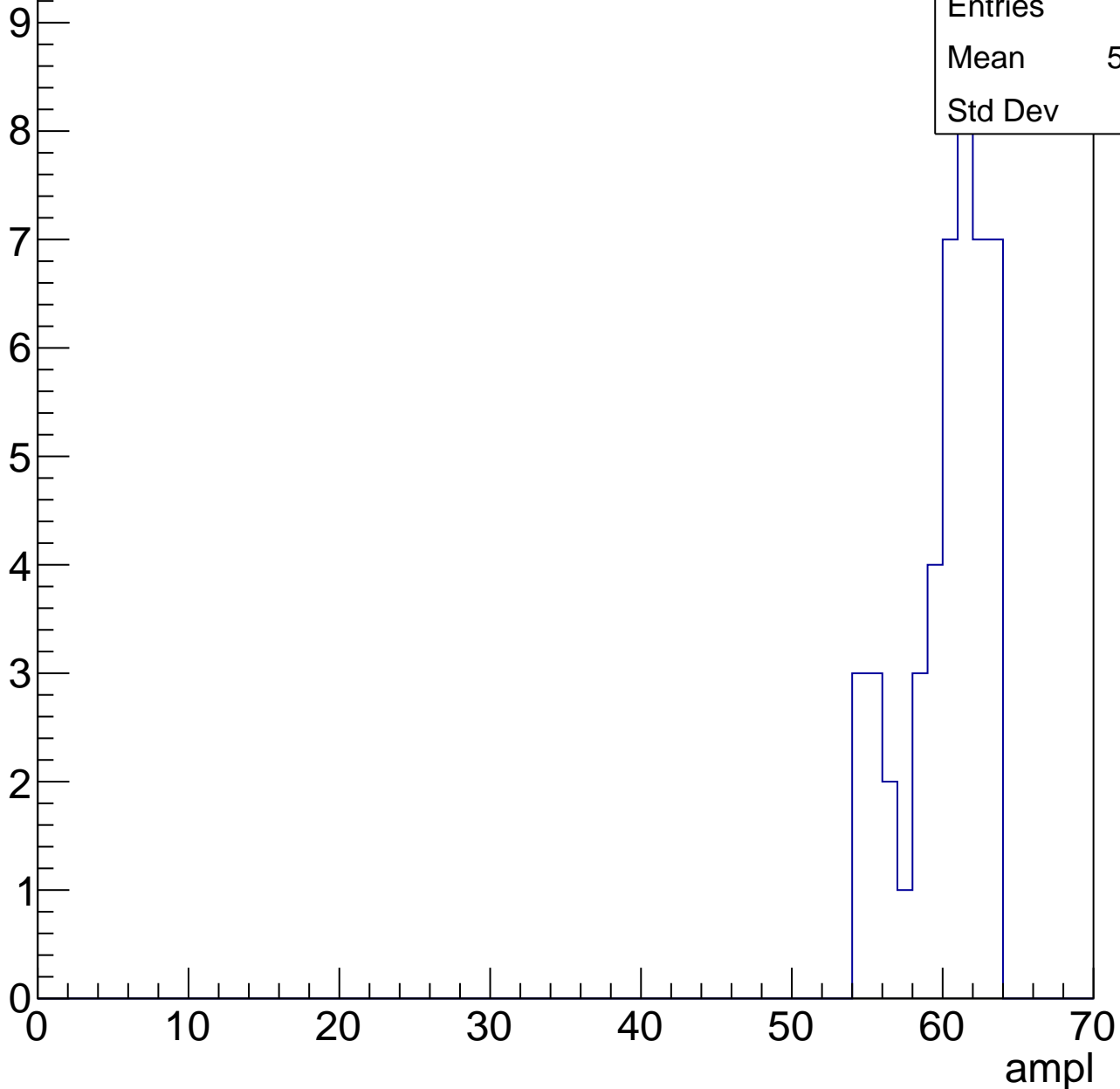


B0L001S, U19-ch78, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

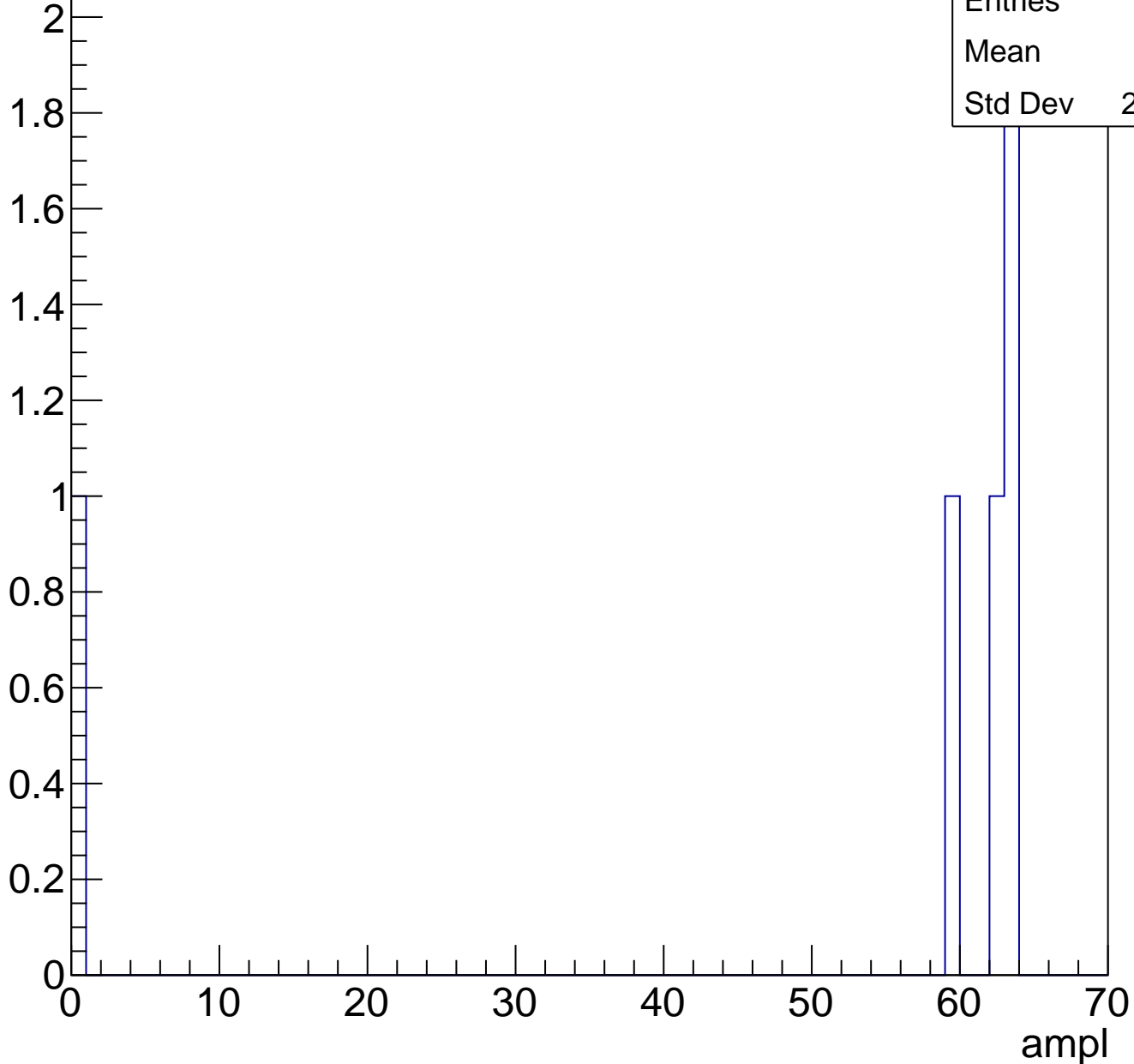
Entries	46
Mean	59.78
Std Dev	2.71



B0L001S, U19-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch79, adc0

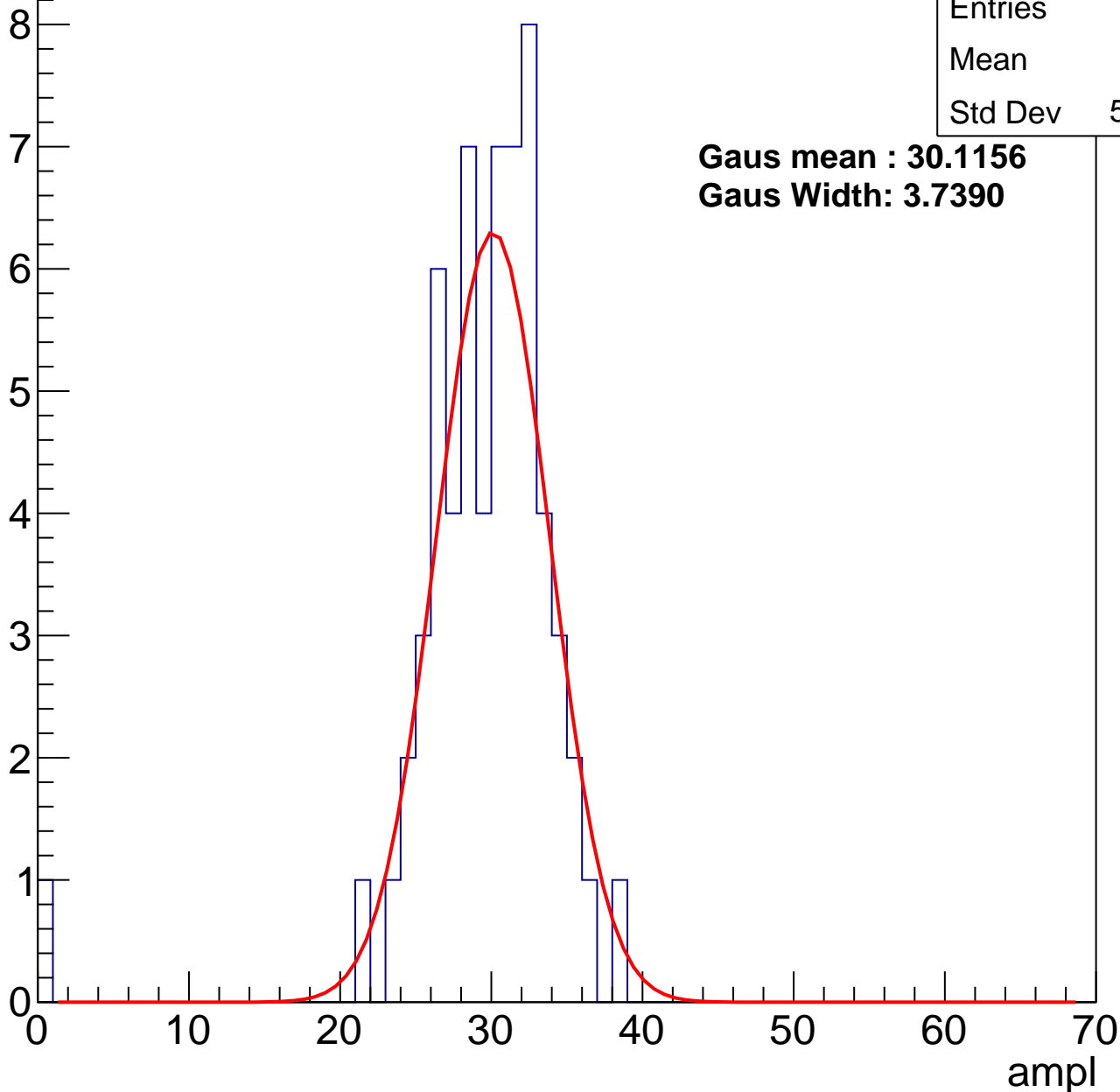
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	29.1
Std Dev	5.022

Gaus mean : 30.1156

Gaus Width: 3.7390



B0L001S, U19-ch79, adc1

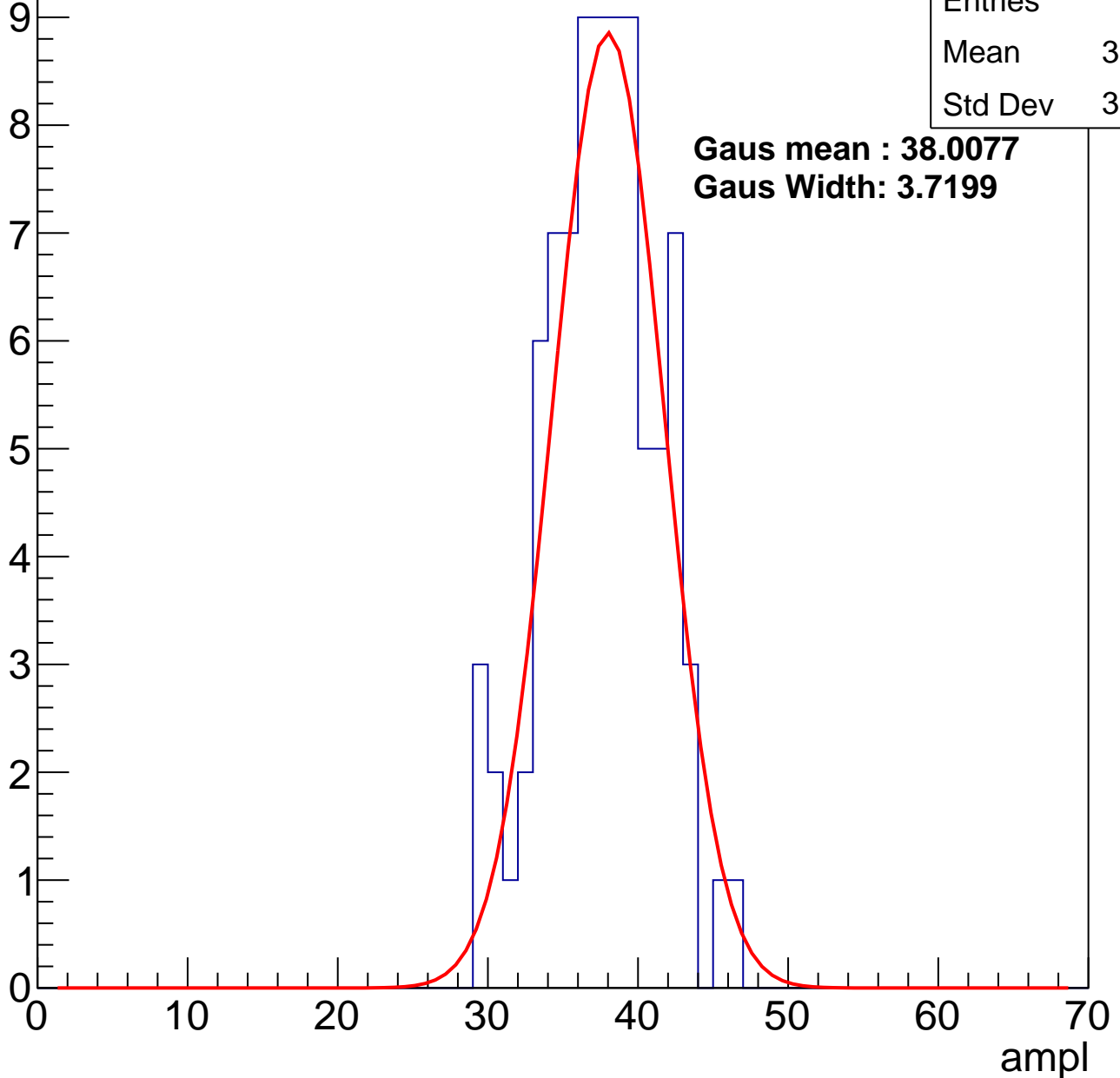
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	37.12
Std Dev	3.687

Gaus mean : 38.0077

Gaus Width: 3.7199



B0L001S, U19-ch79, adc2

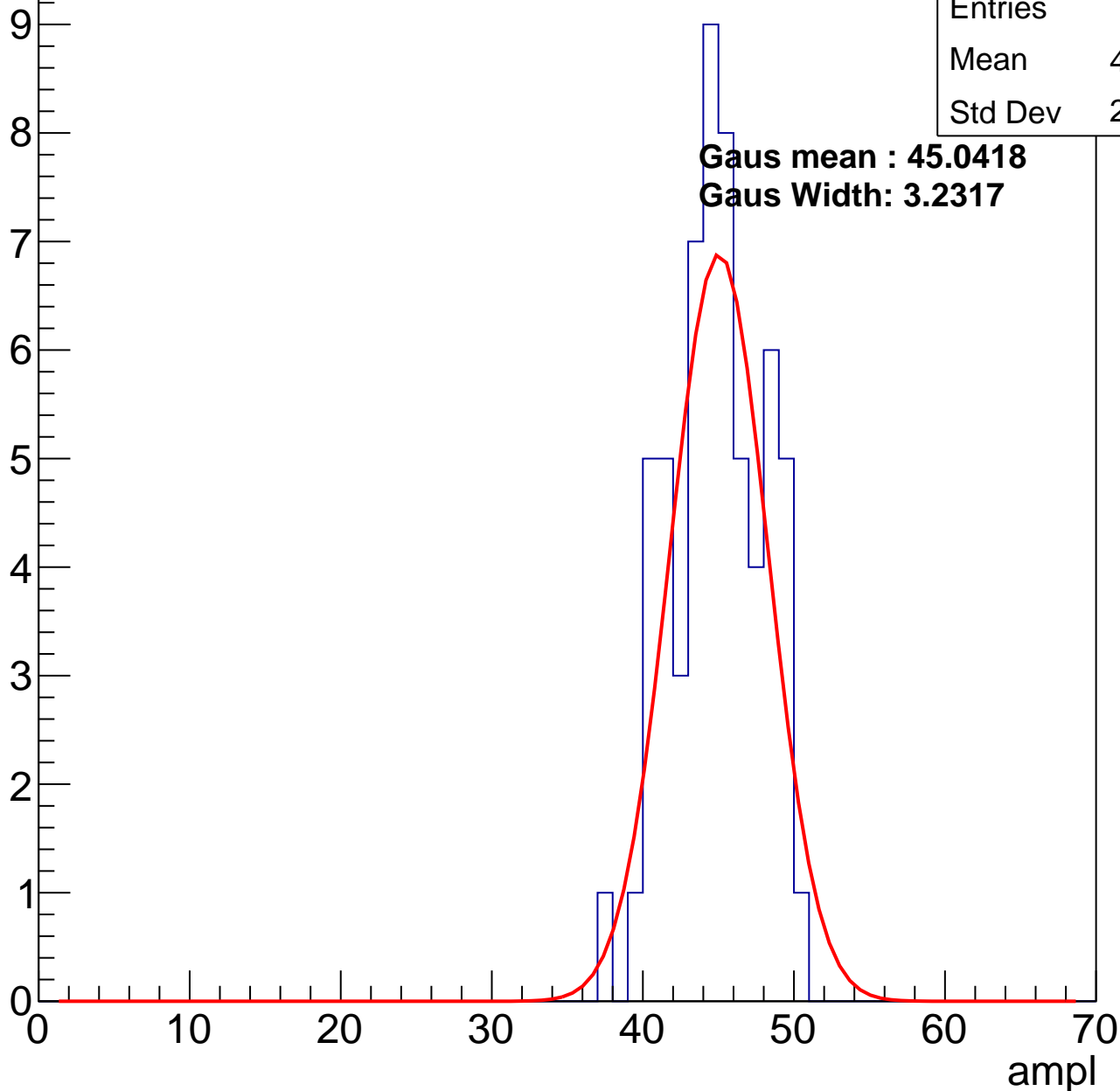
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.42
Std Dev	2.968

Gaus mean : 45.0418

Gaus Width: 3.2317

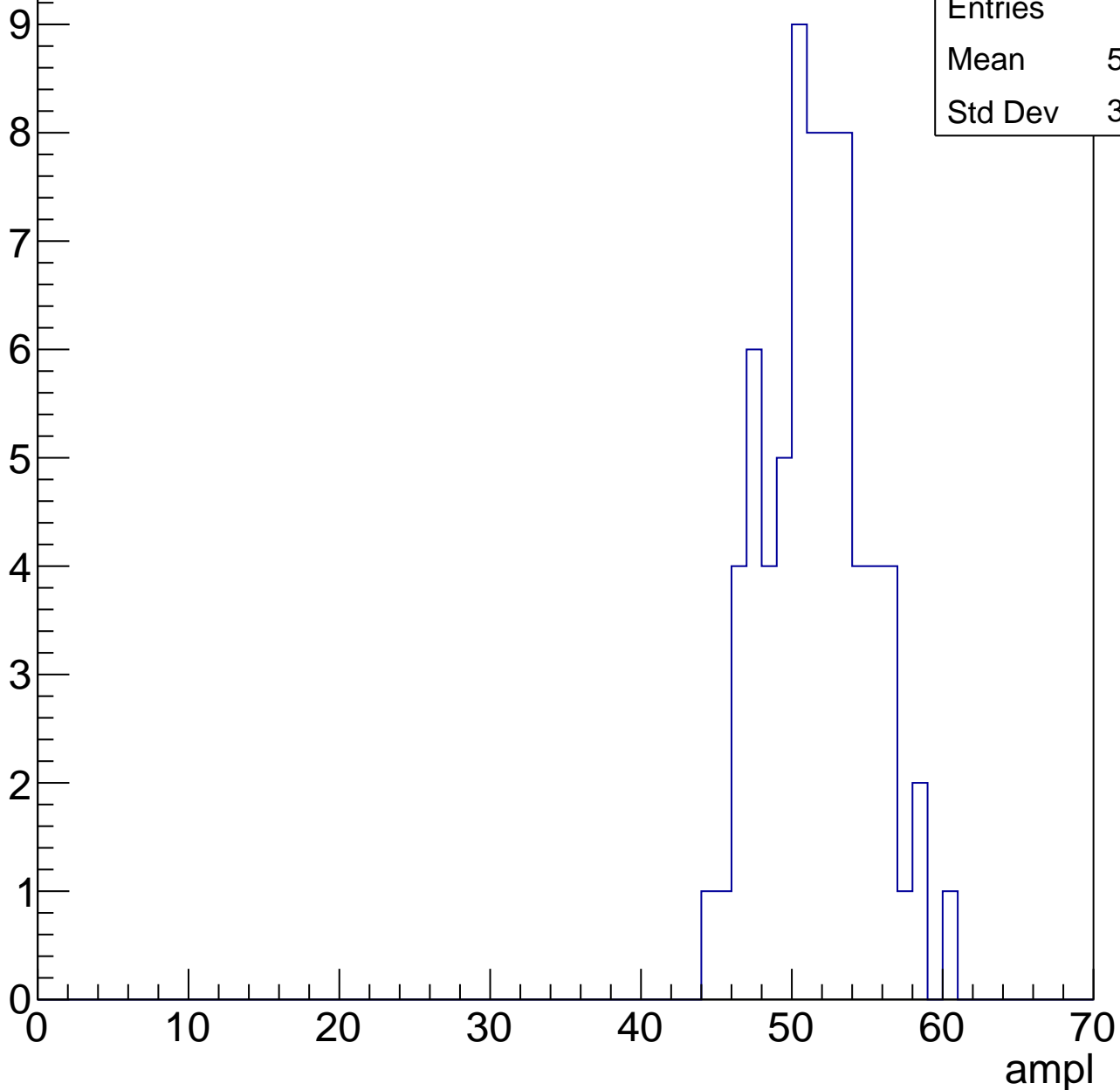


B0L001S, U19-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

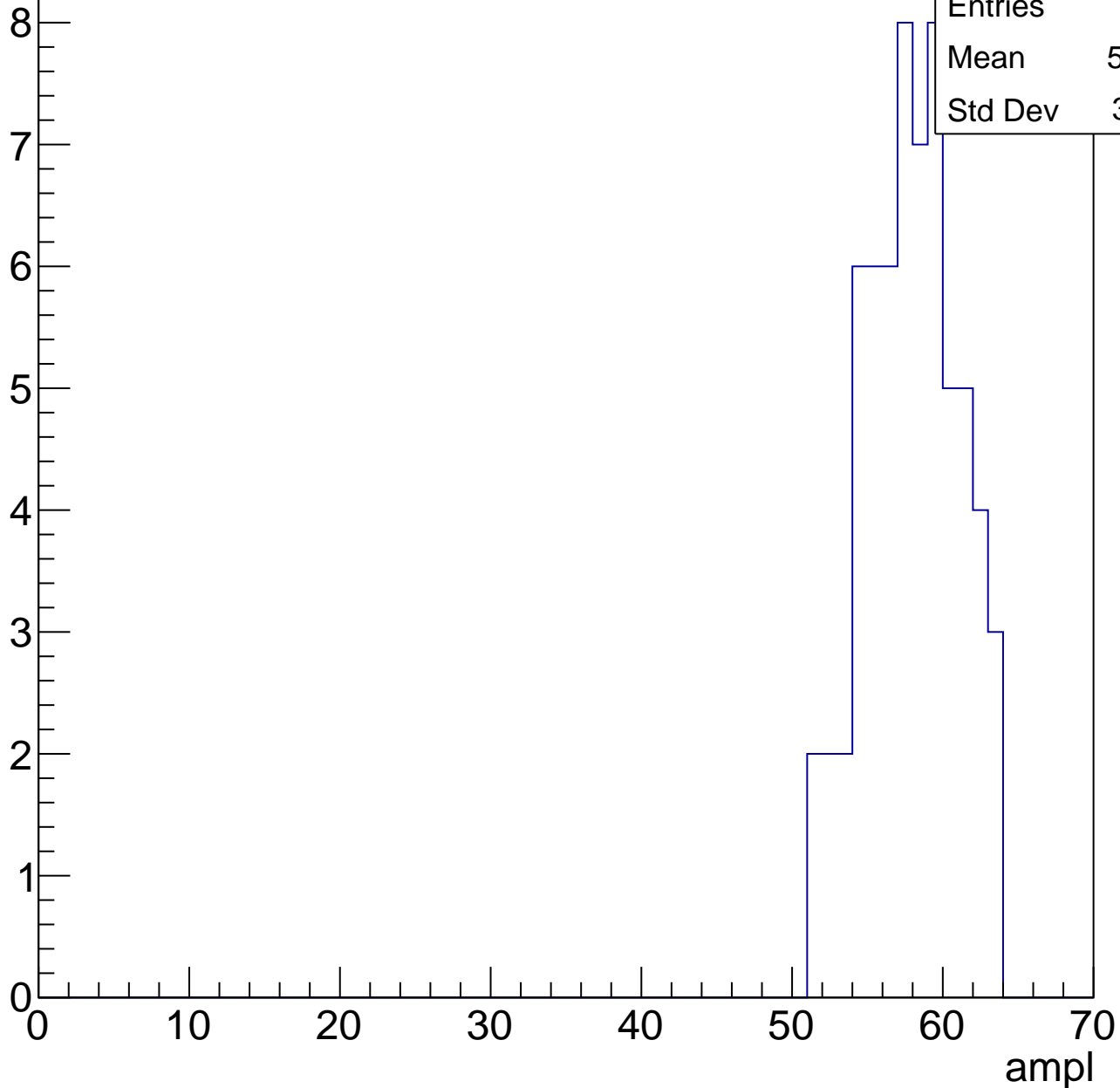
Entries	70
Mean	51.19
Std Dev	3.378



B0L001S, U19-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



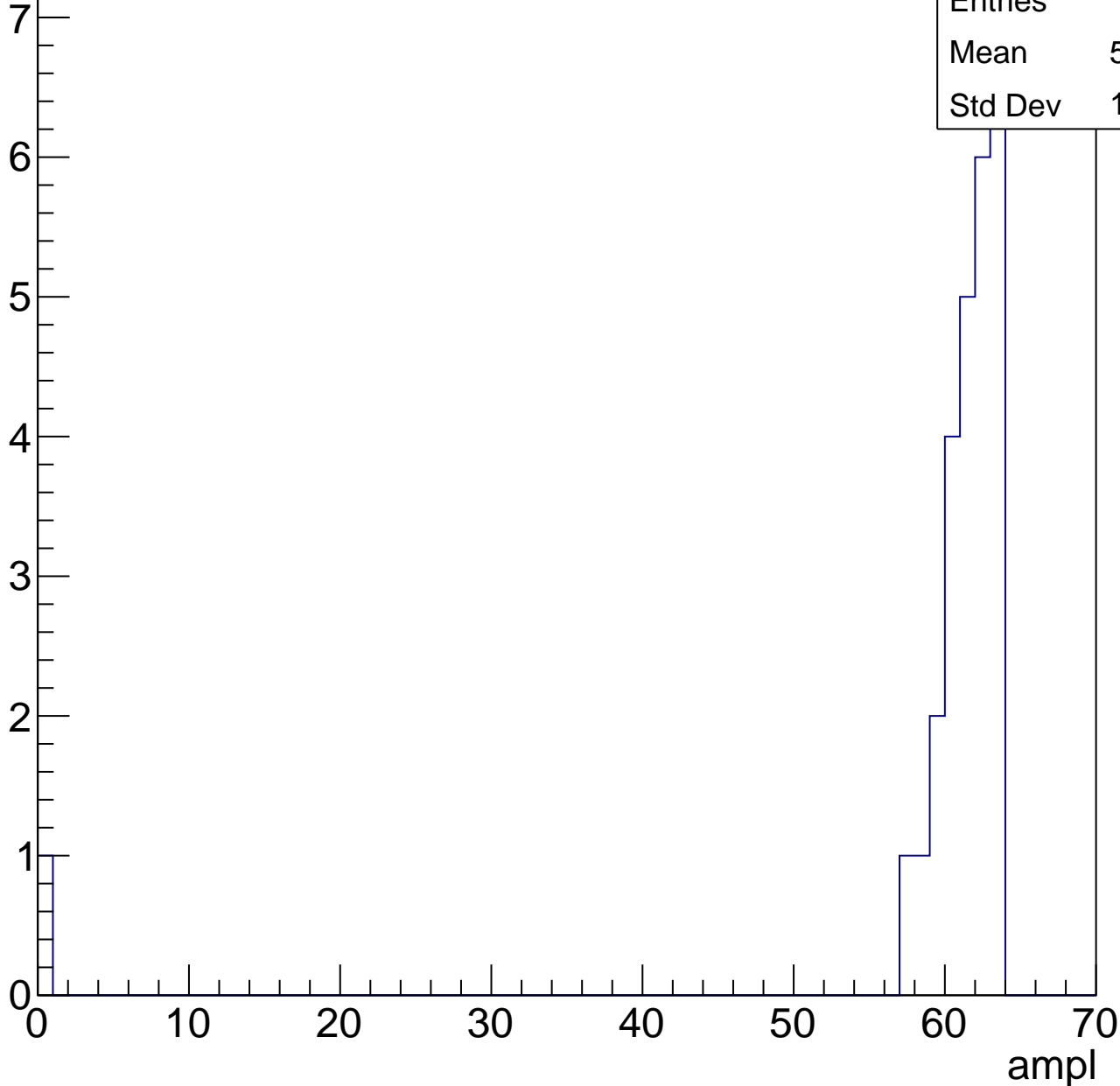
Entries	64
Mean	57.47
Std Dev	3.051

B0L001S, U19-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.93
Std Dev	11.67



B0L001S, U19-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch80, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	30.5
Std Dev	3.303

Gaus mean : 31.0880

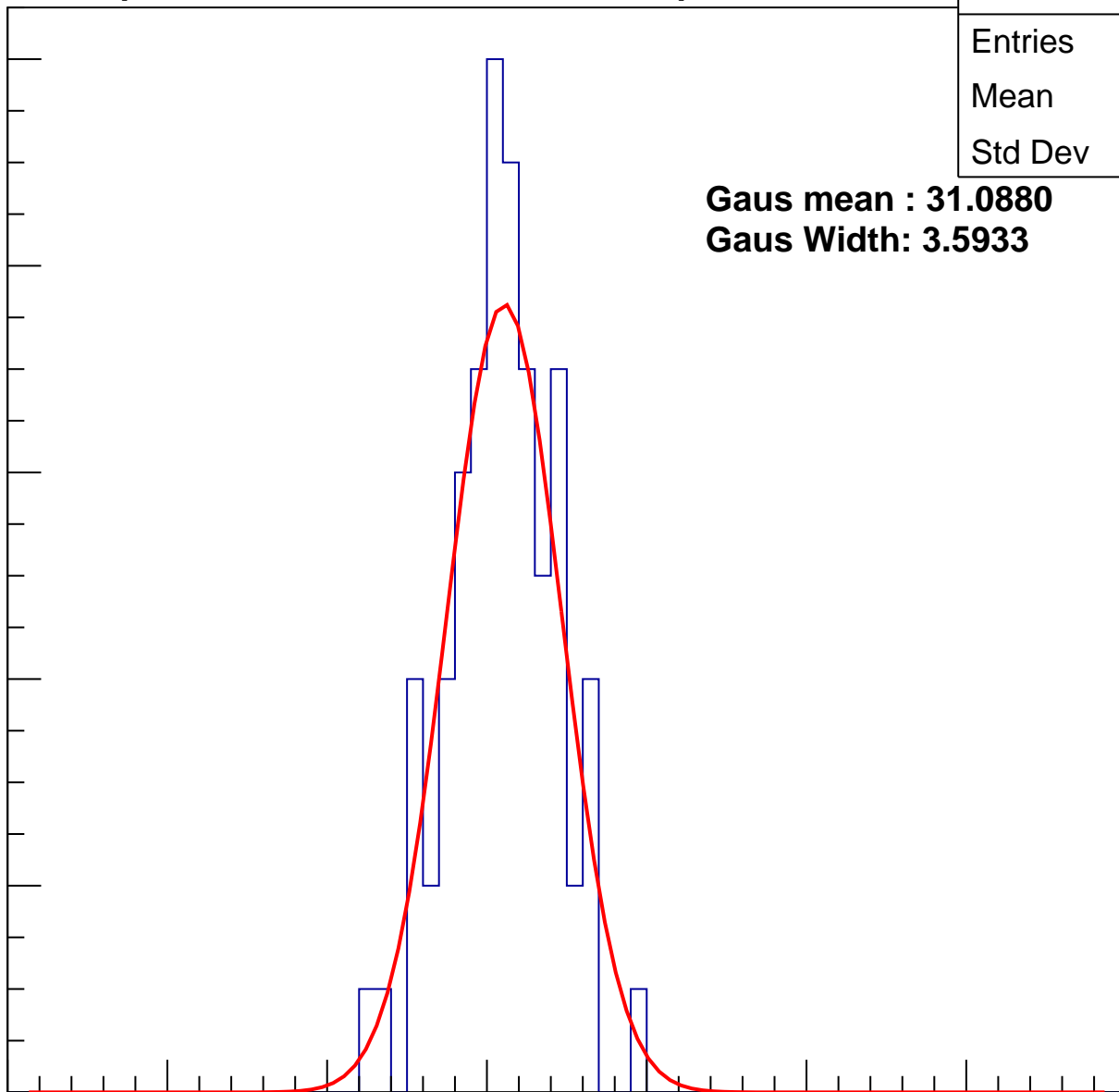
Gaus Width: 3.5933

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch80, adc1

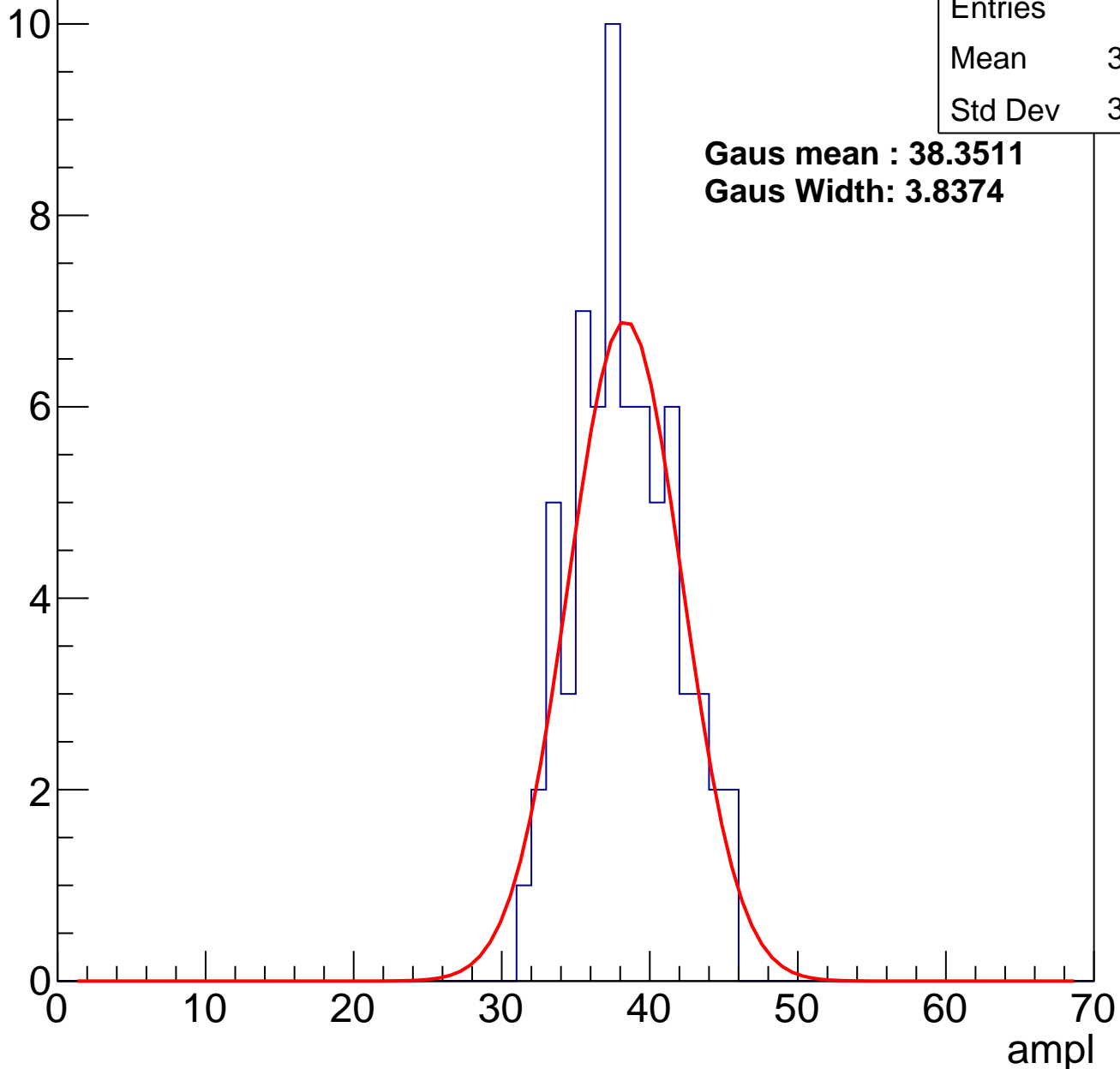
calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	37.82
Std Dev	3.372

Gaus mean : 38.3511

Gaus Width: 3.8374

Entry



B0L001S, U19-ch80, adc2

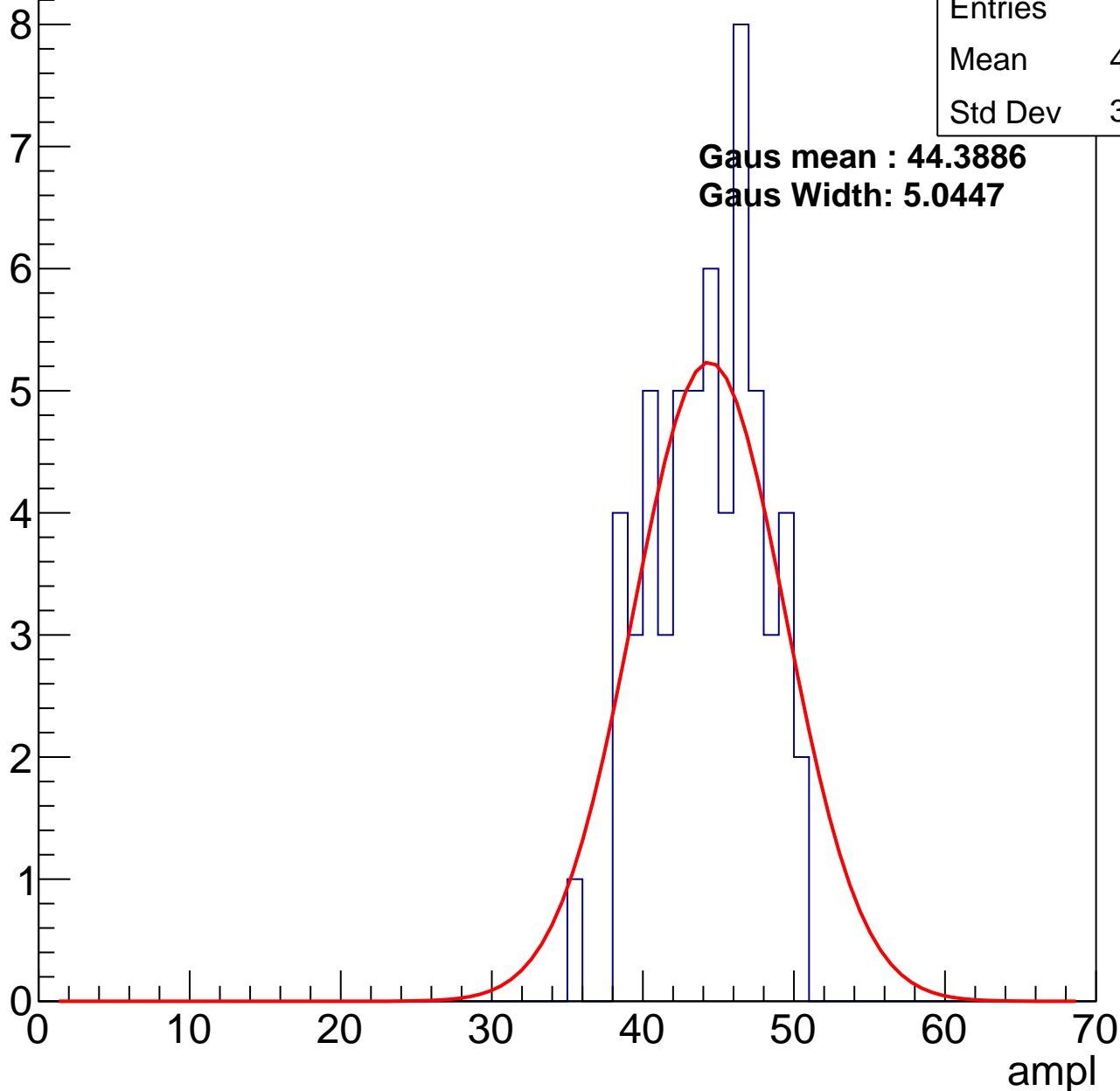
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.78
Std Dev	3.548

Gaus mean : 44.3886

Gaus Width: 5.0447

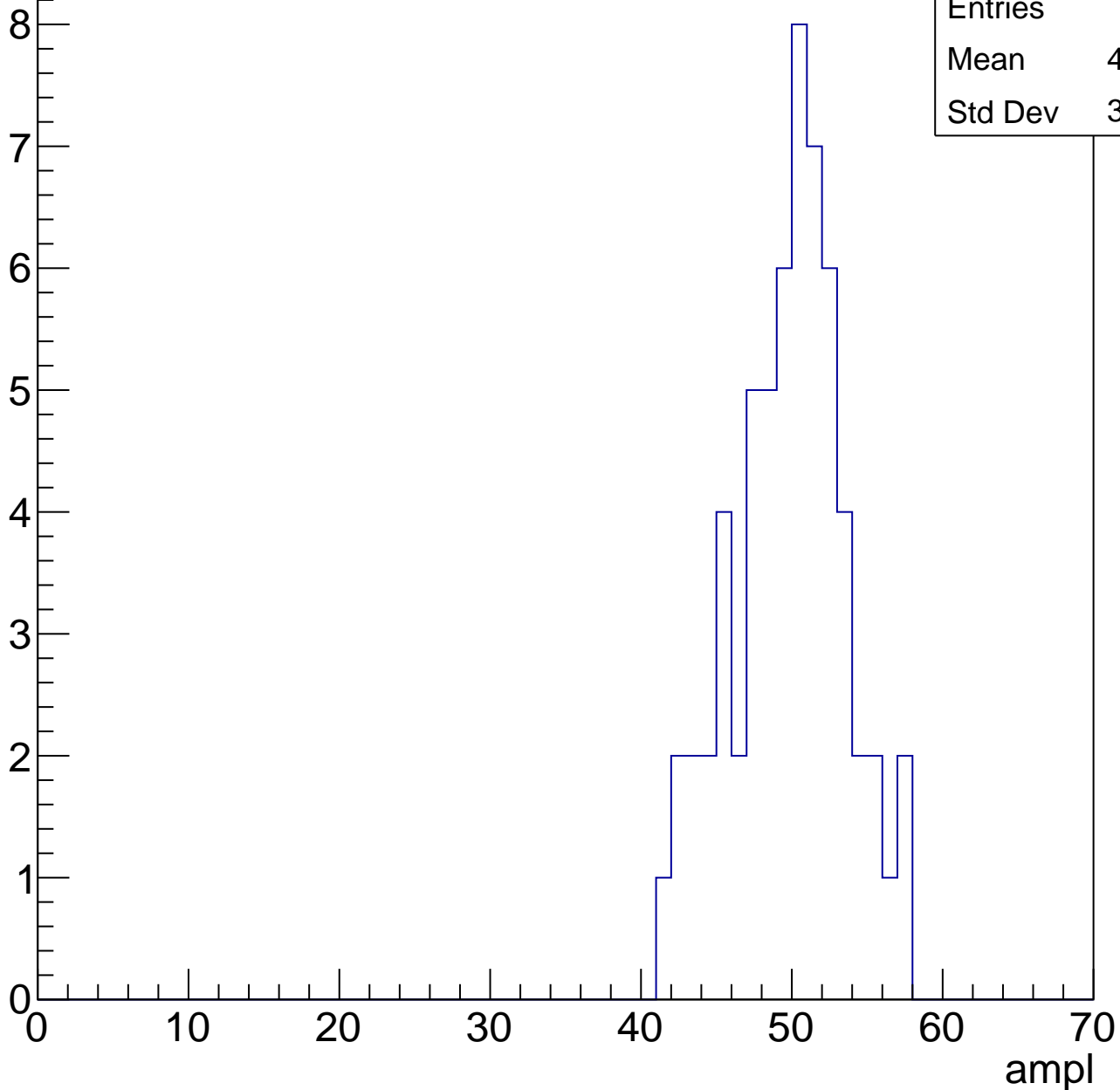


B0L001S, U19-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

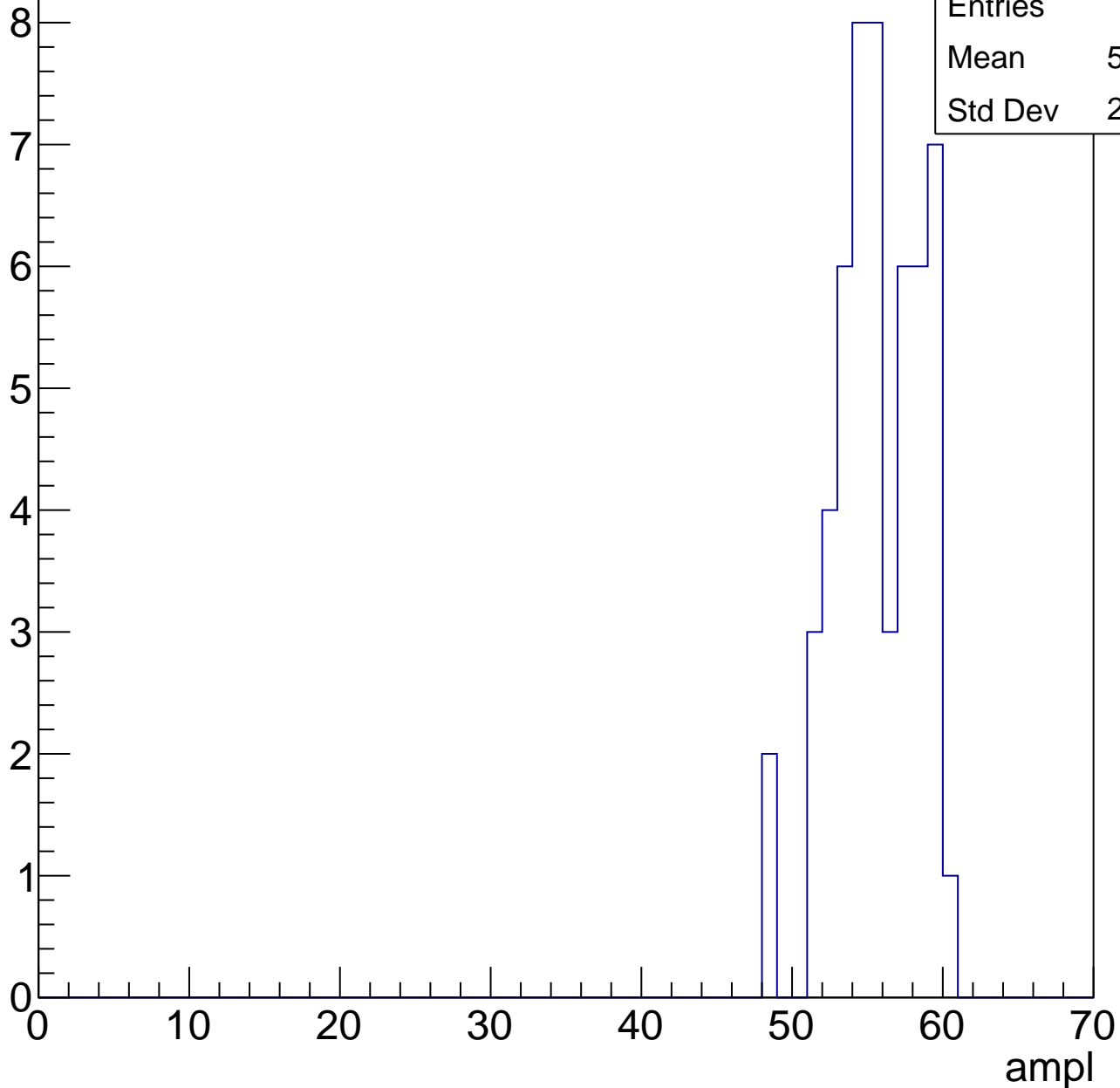
Entries	61
Mean	49.33
Std Dev	3.705



B0L001S, U19-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



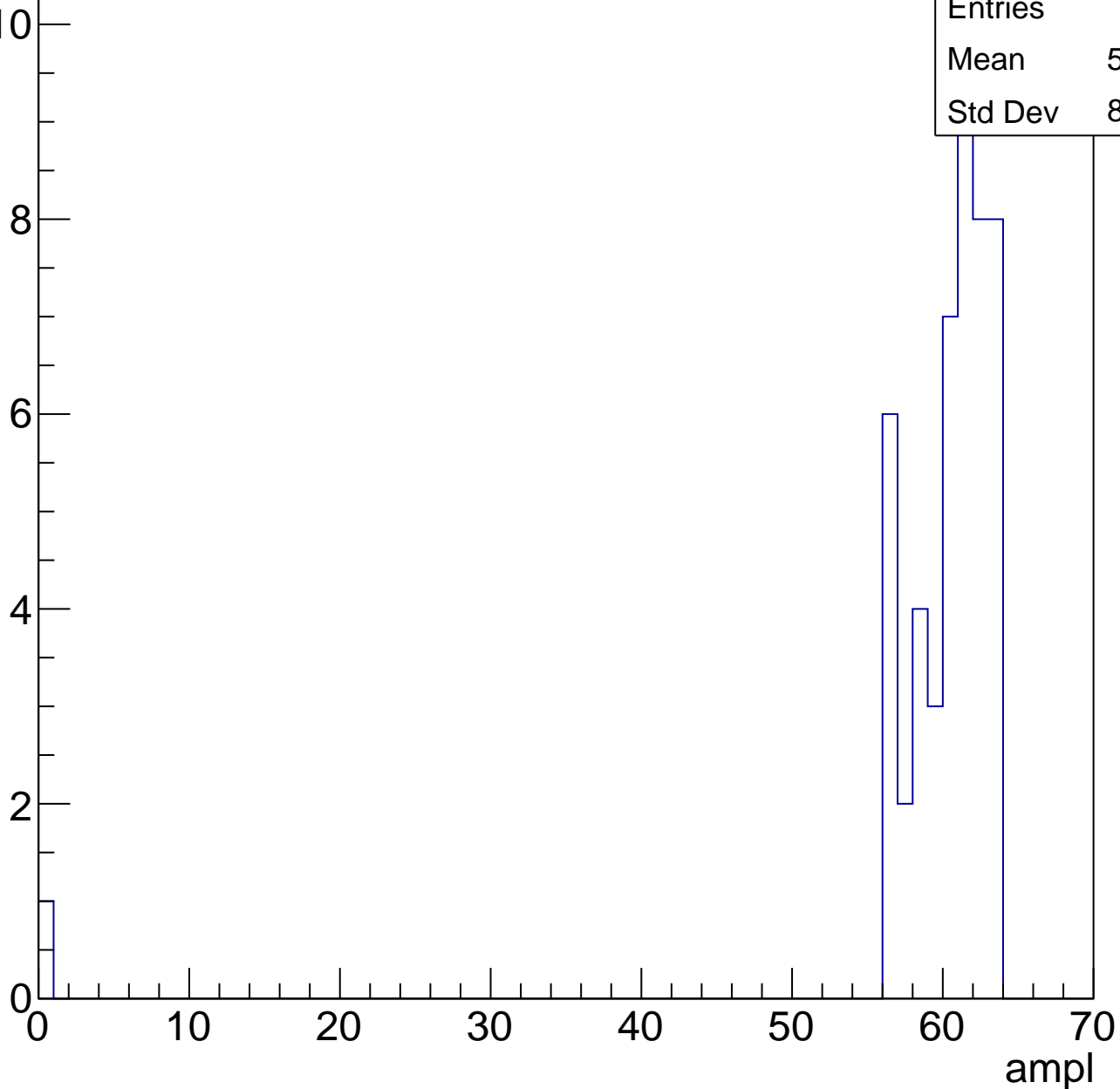
Entries	54
Mean	55.15
Std Dev	2.825

B0L001S, U19-ch80, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.96
Std Dev	8.799



B0L001S, U19-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.247

0 10 20 30 40 50 60 70

ampl

B0L001S, U19-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch81, adc0

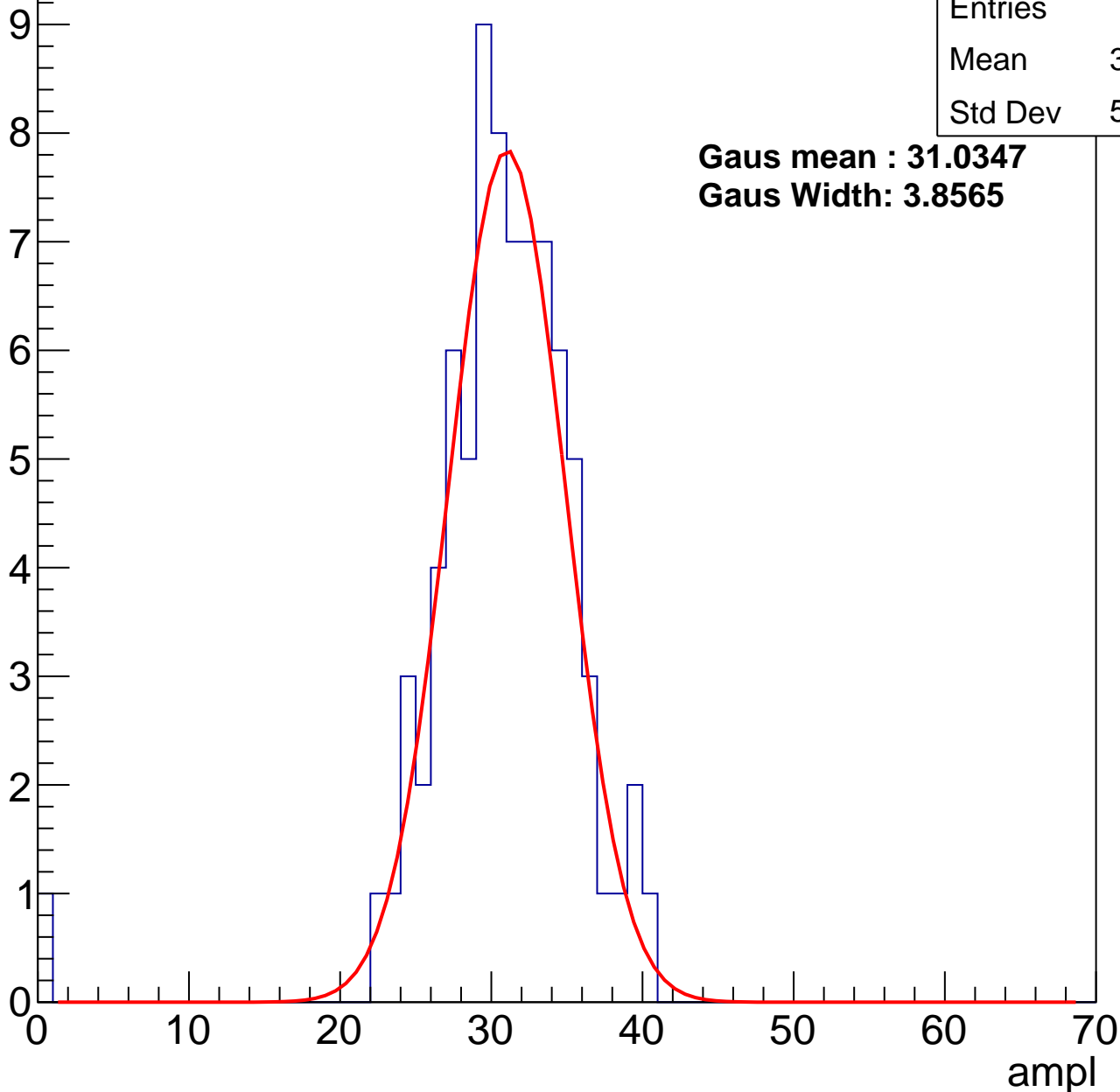
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	30.32
Std Dev	5.138

Gaus mean : 31.0347

Gaus Width: 3.8565



B0L001S, U19-ch81, adc1

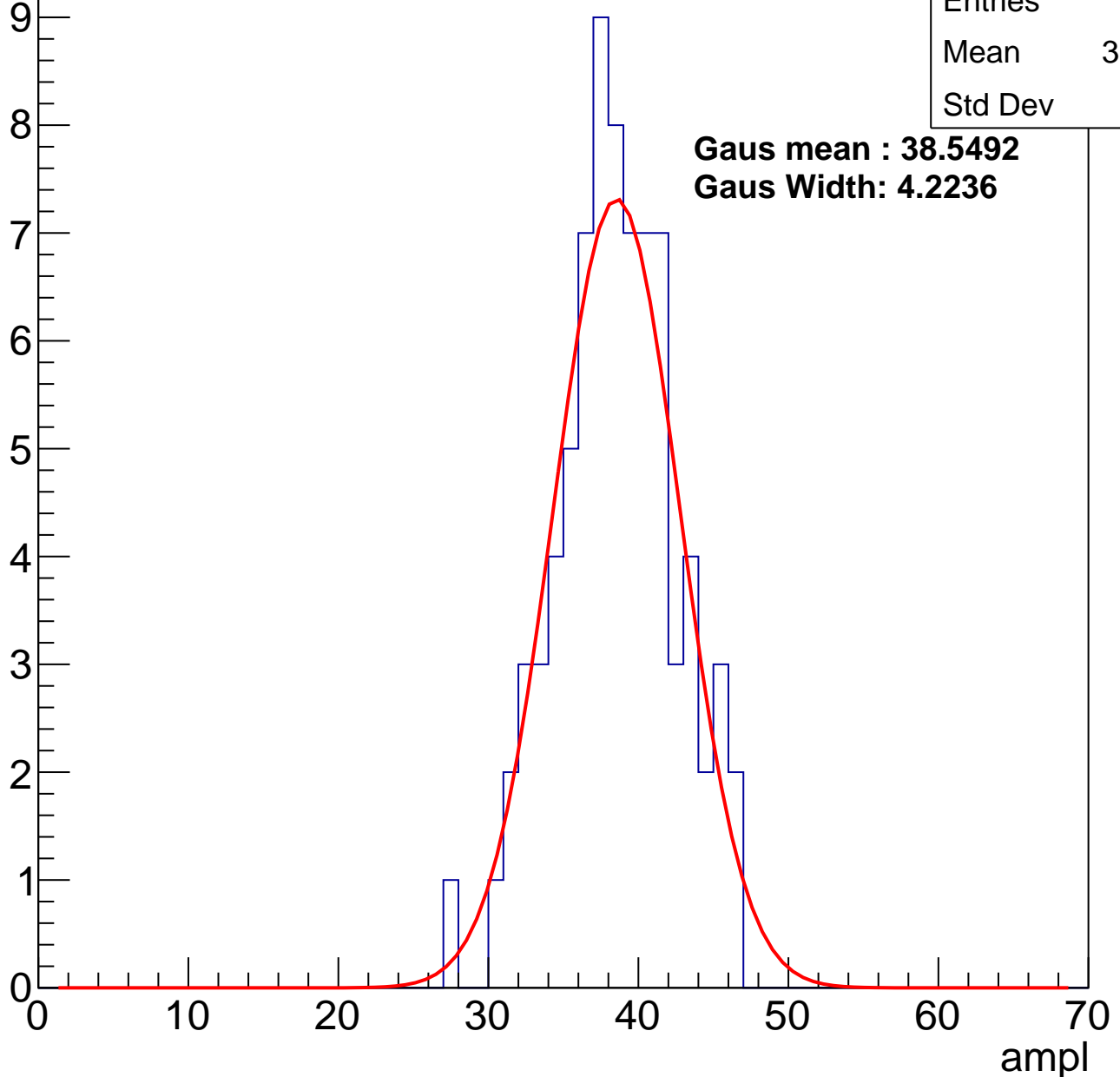
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	38.04
Std Dev	3.93

Gaus mean : 38.5492

Gaus Width: 4.2236



B0L001S, U19-ch81, adc2

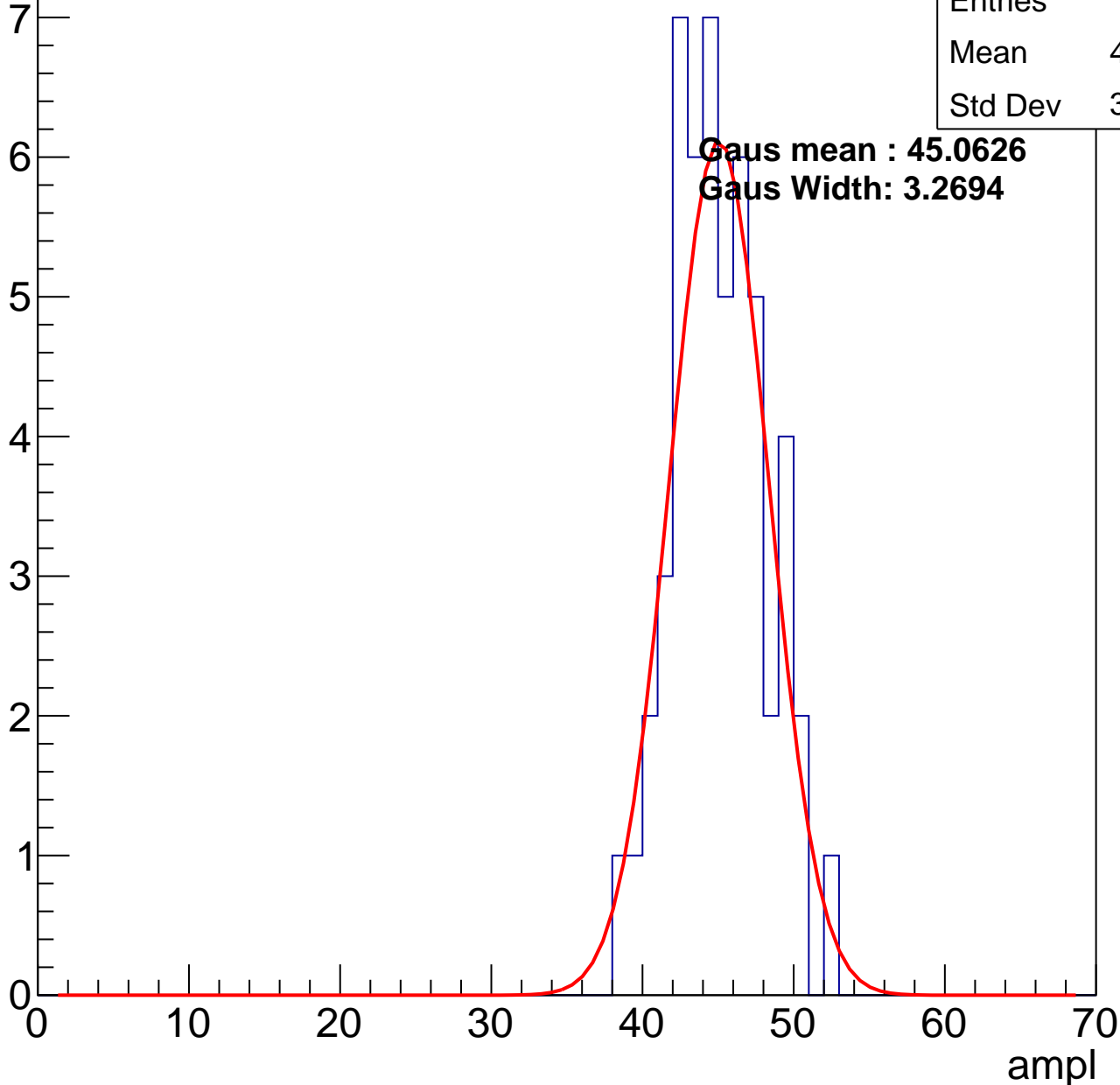
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44.62
Std Dev	3.027

Gaus mean : 45.0626

Gaus Width: 3.2694

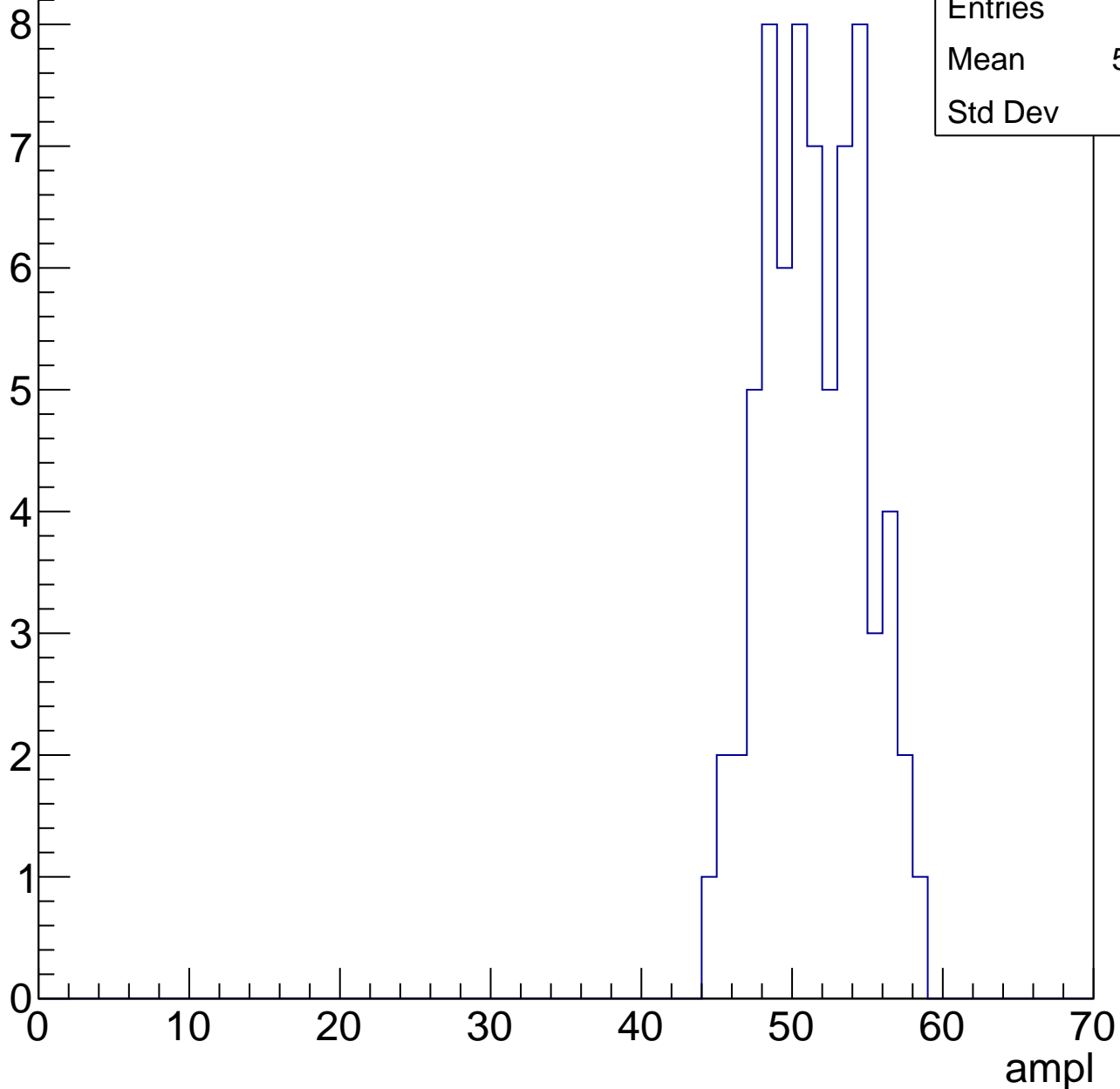


B0L001S, U19-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	51.01
Std Dev	3.25

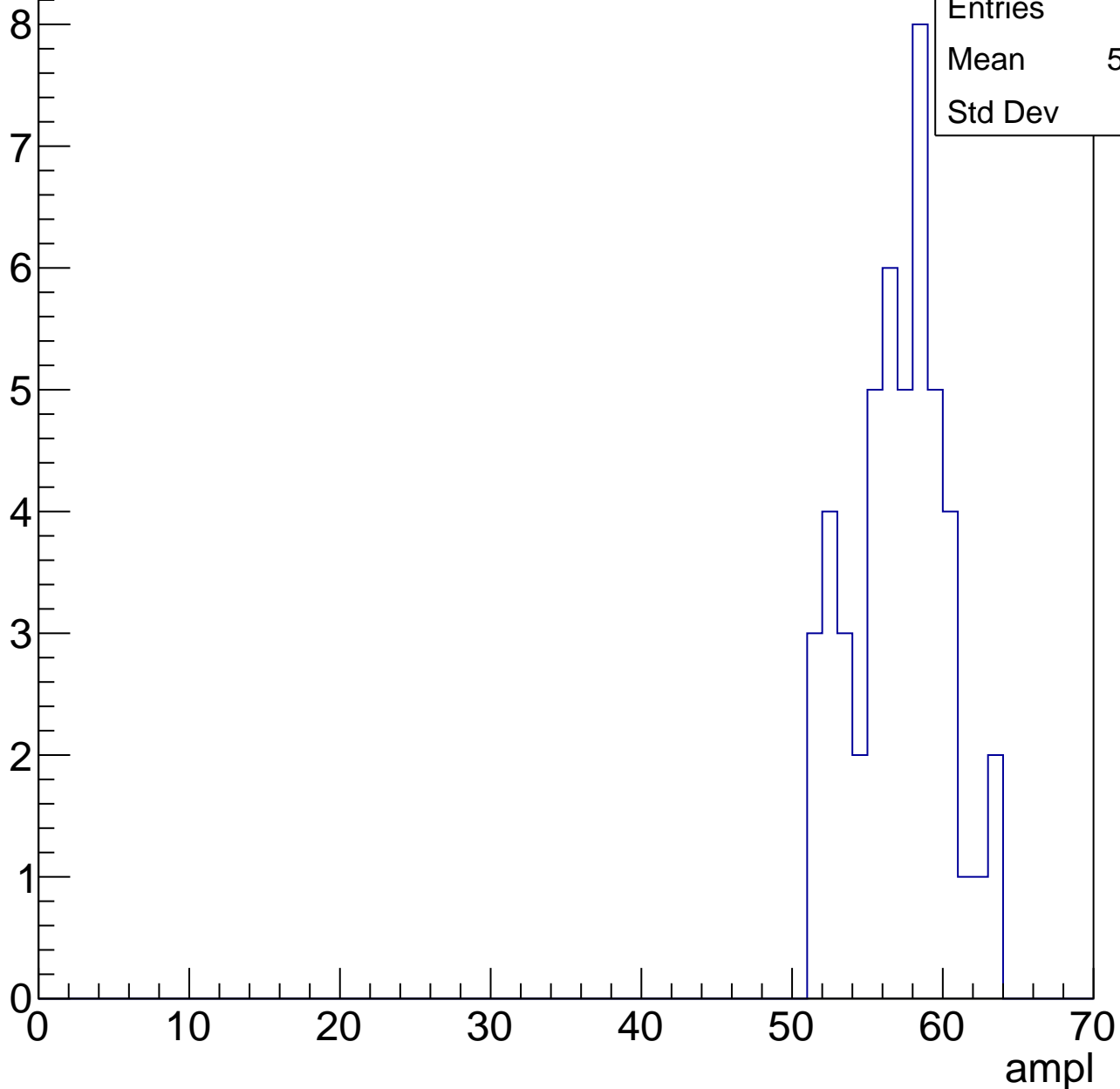


B0L001S, U19-ch81, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	56.57
Std Dev	3.09

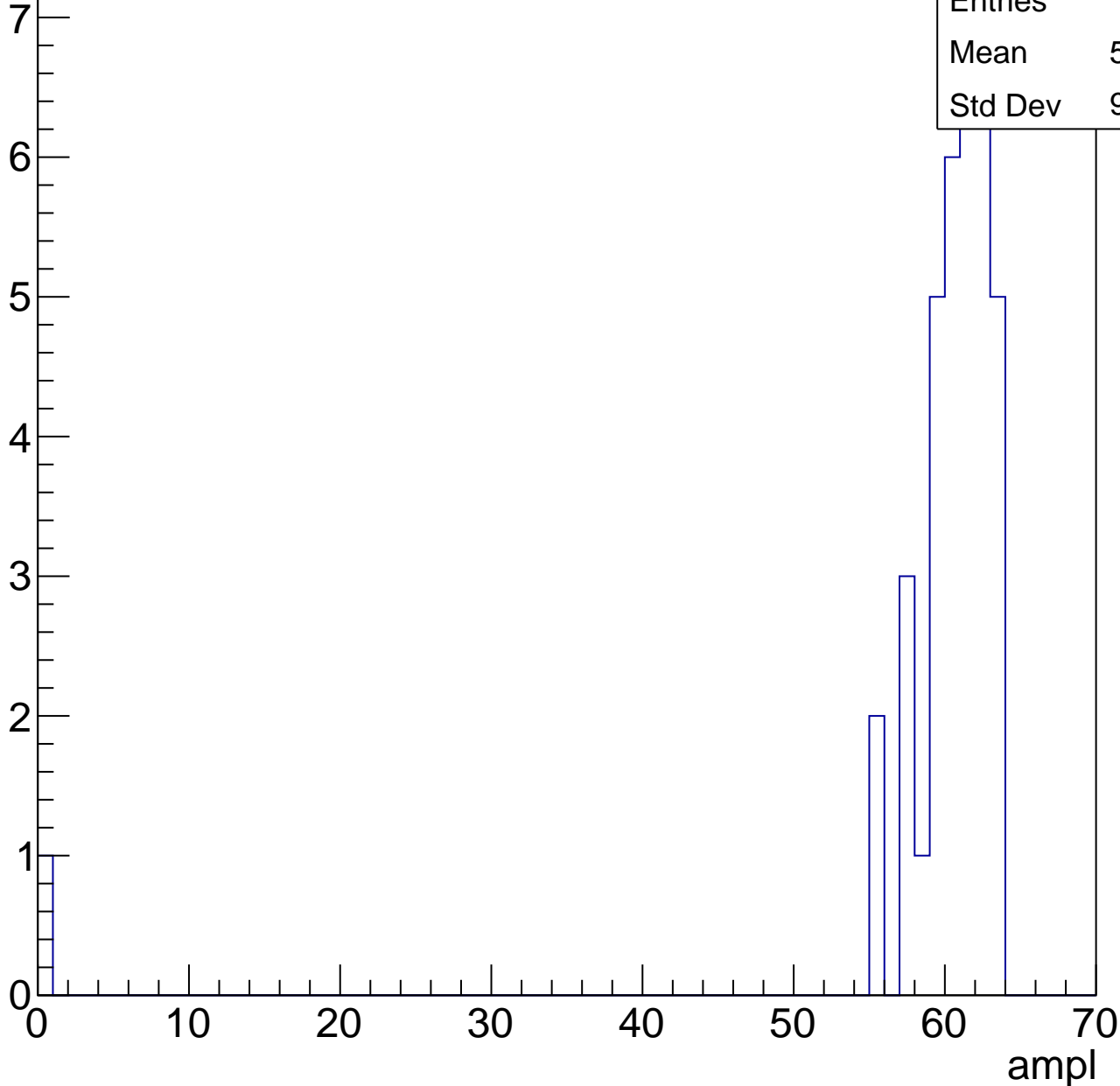


B0L001S, U19-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	58.65
Std Dev	9.998



B0L001S, U19-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch82, adc0

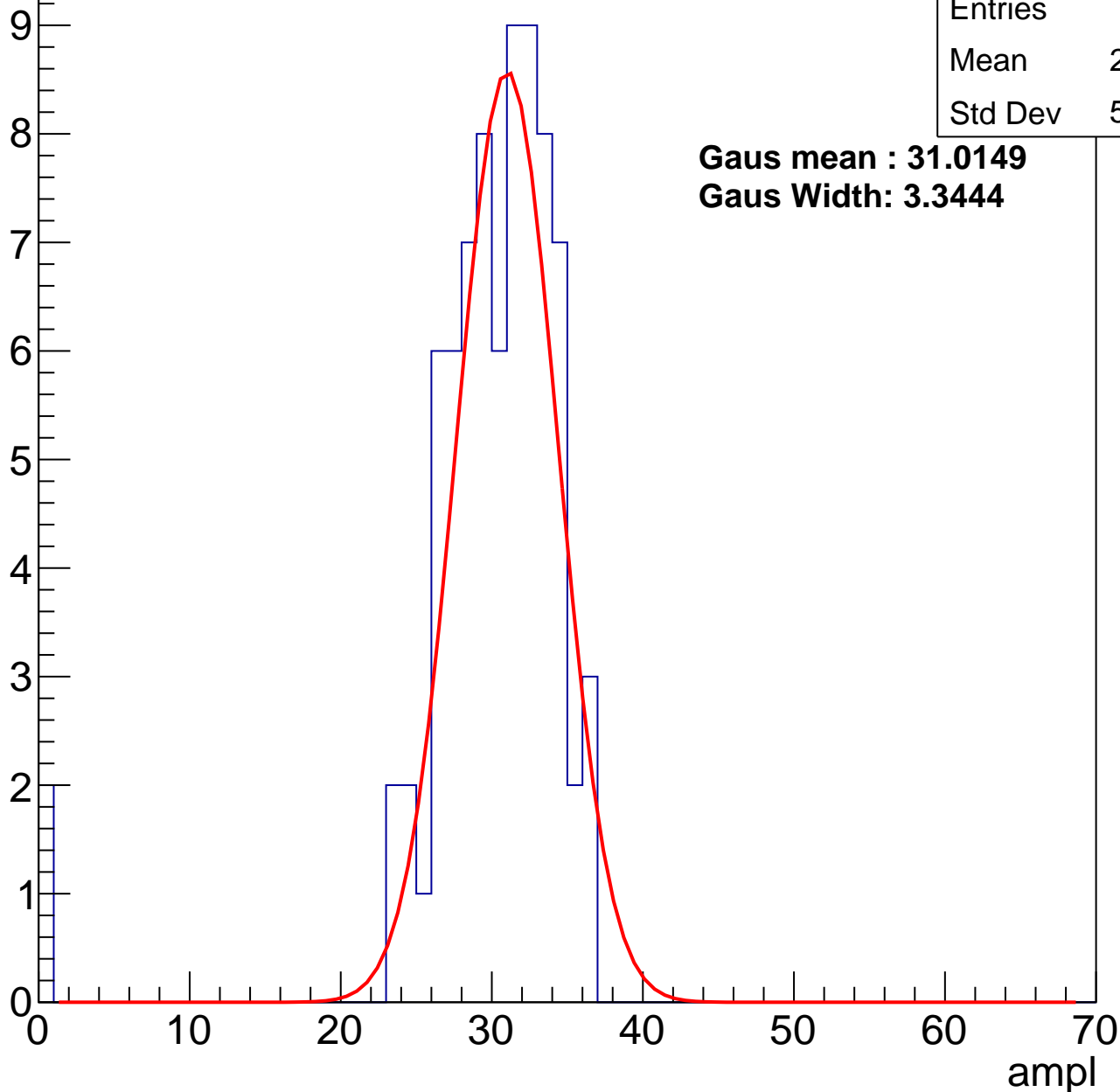
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	29.38
Std Dev	5.705

Gaus mean : 31.0149

Gaus Width: 3.3444



B0L001S, U19-ch82, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.11
Std Dev	3.111

Gaus mean : 37.9654

Gaus Width: 2.8855

10

8

6

4

2

0

0

10

20

30

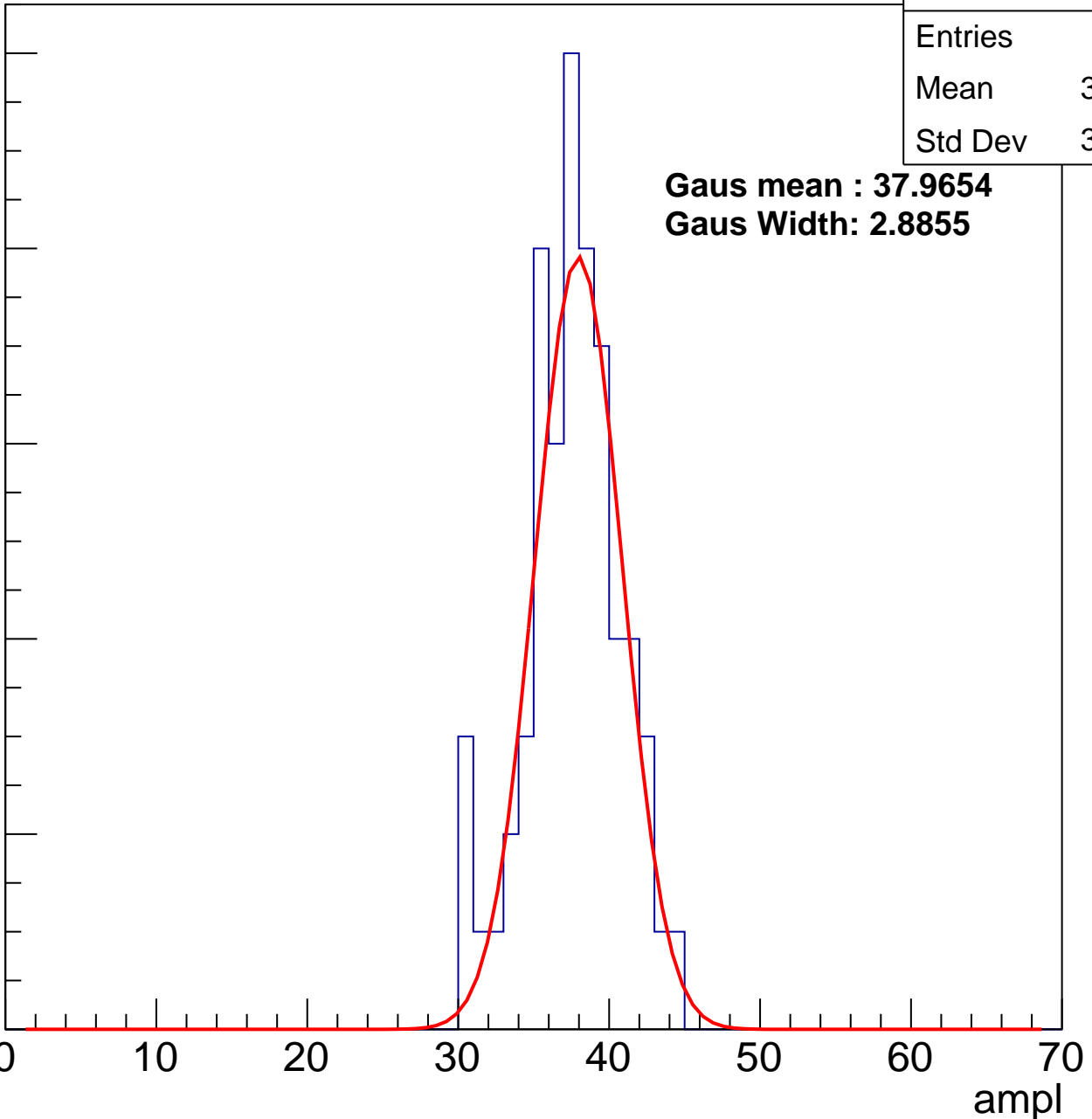
40

50

60

70

ampl



B0L001S, U19-ch82, adc2

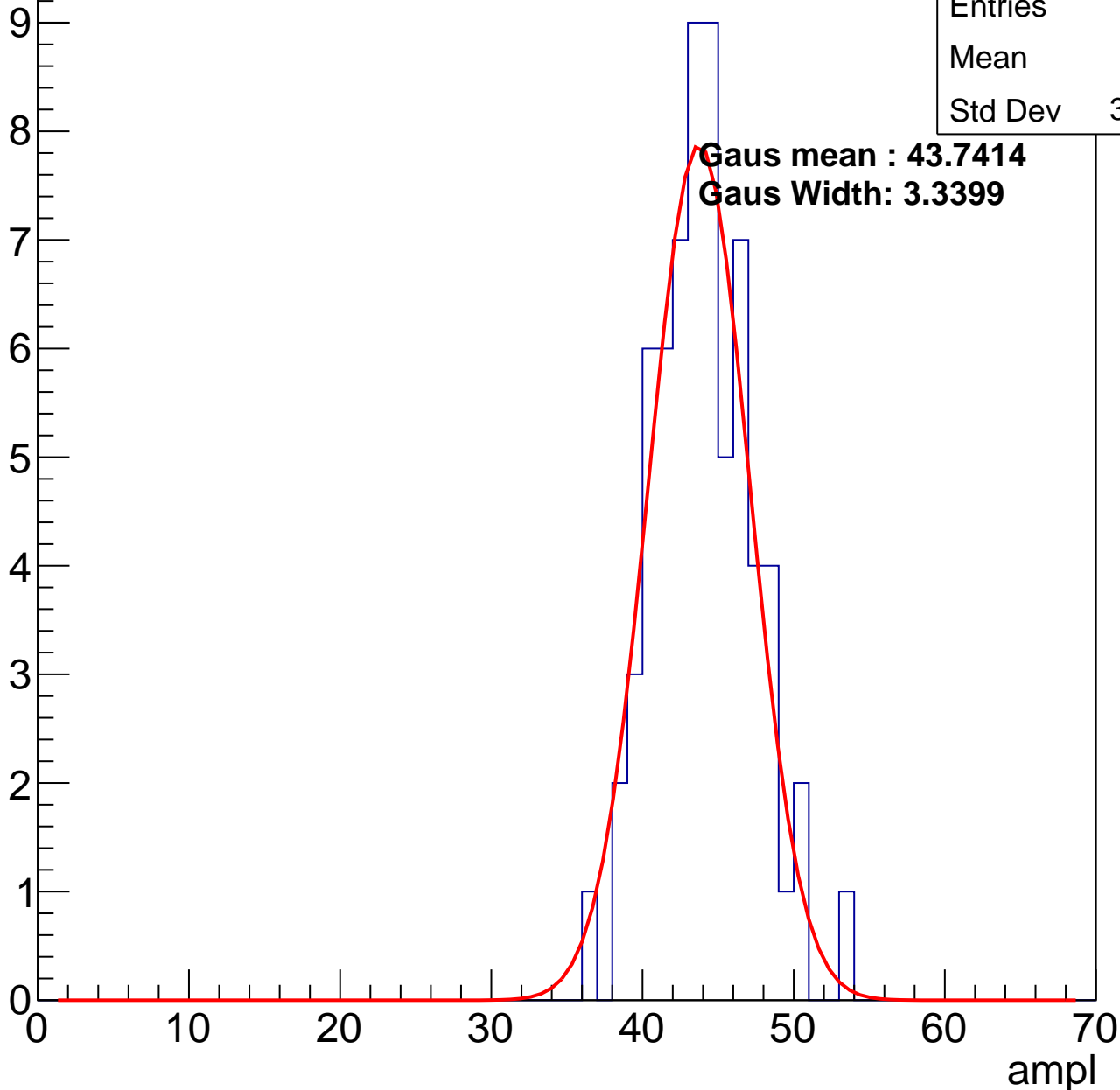
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.6
Std Dev	3.228

Gaus mean : 43.7414

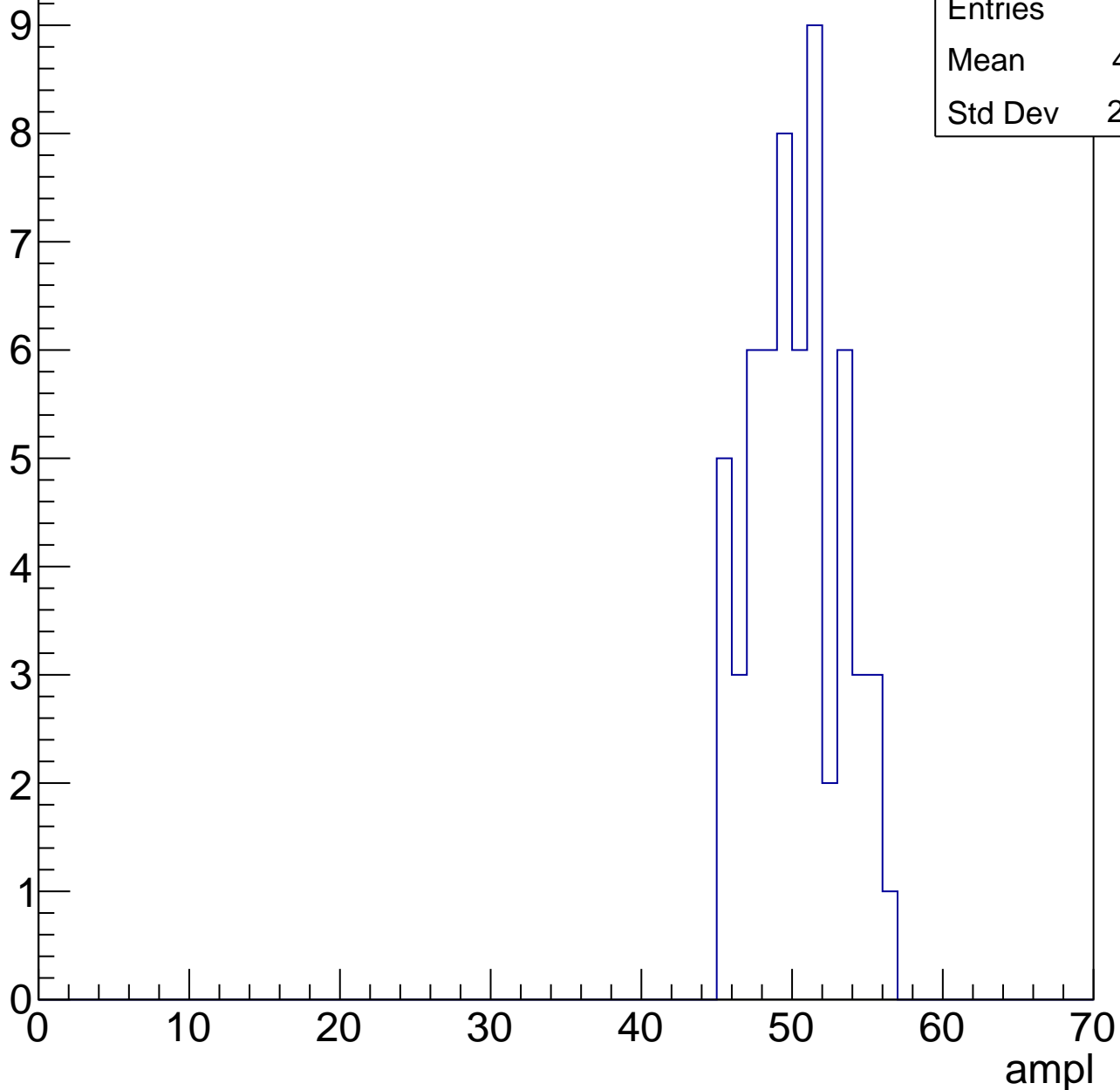
Gaus Width: 3.3399



B0L001S, U19-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



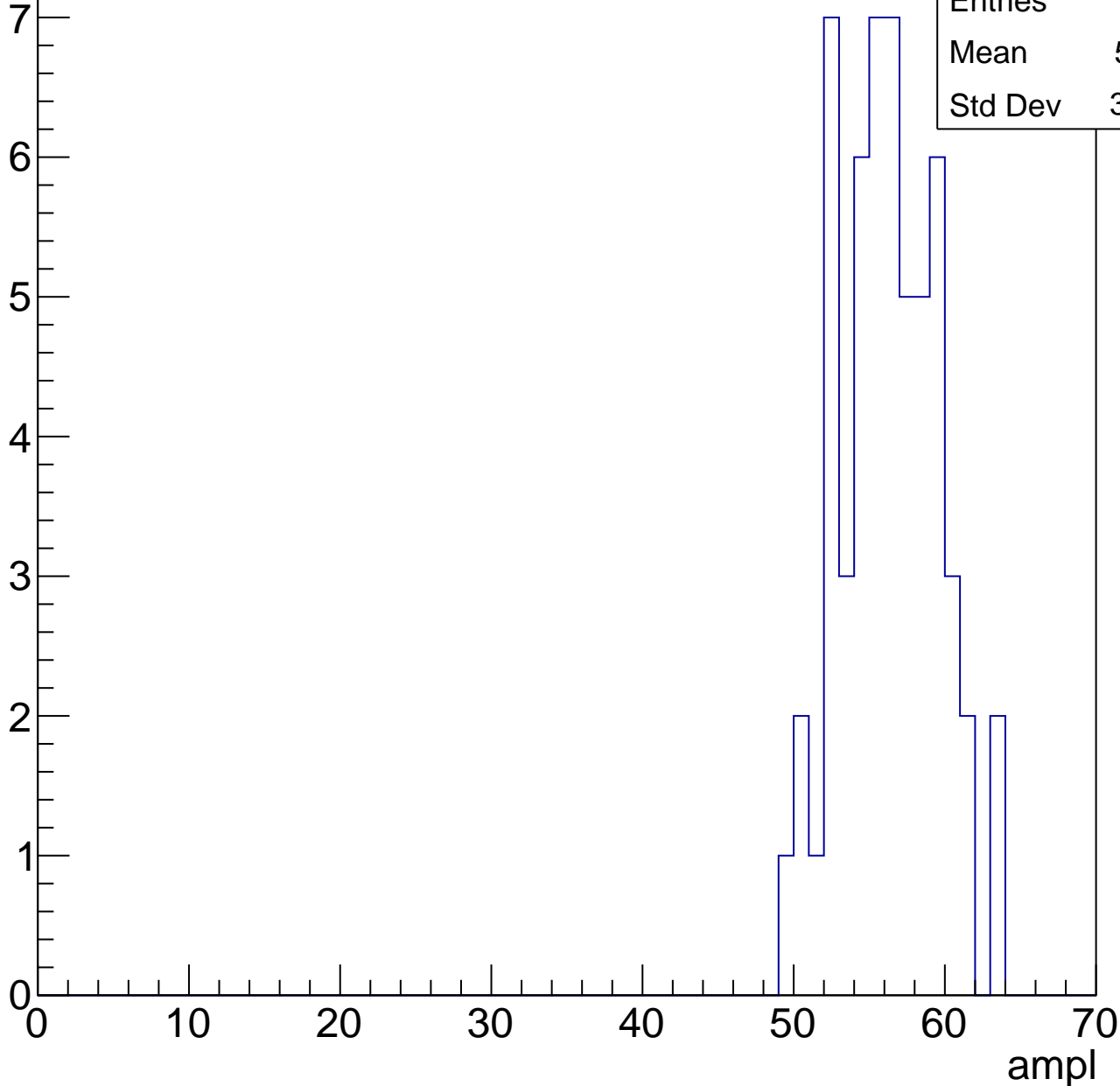
Entries	58
Mean	49.81
Std Dev	2.897

B0L001S, U19-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	55.81
Std Dev	3.214

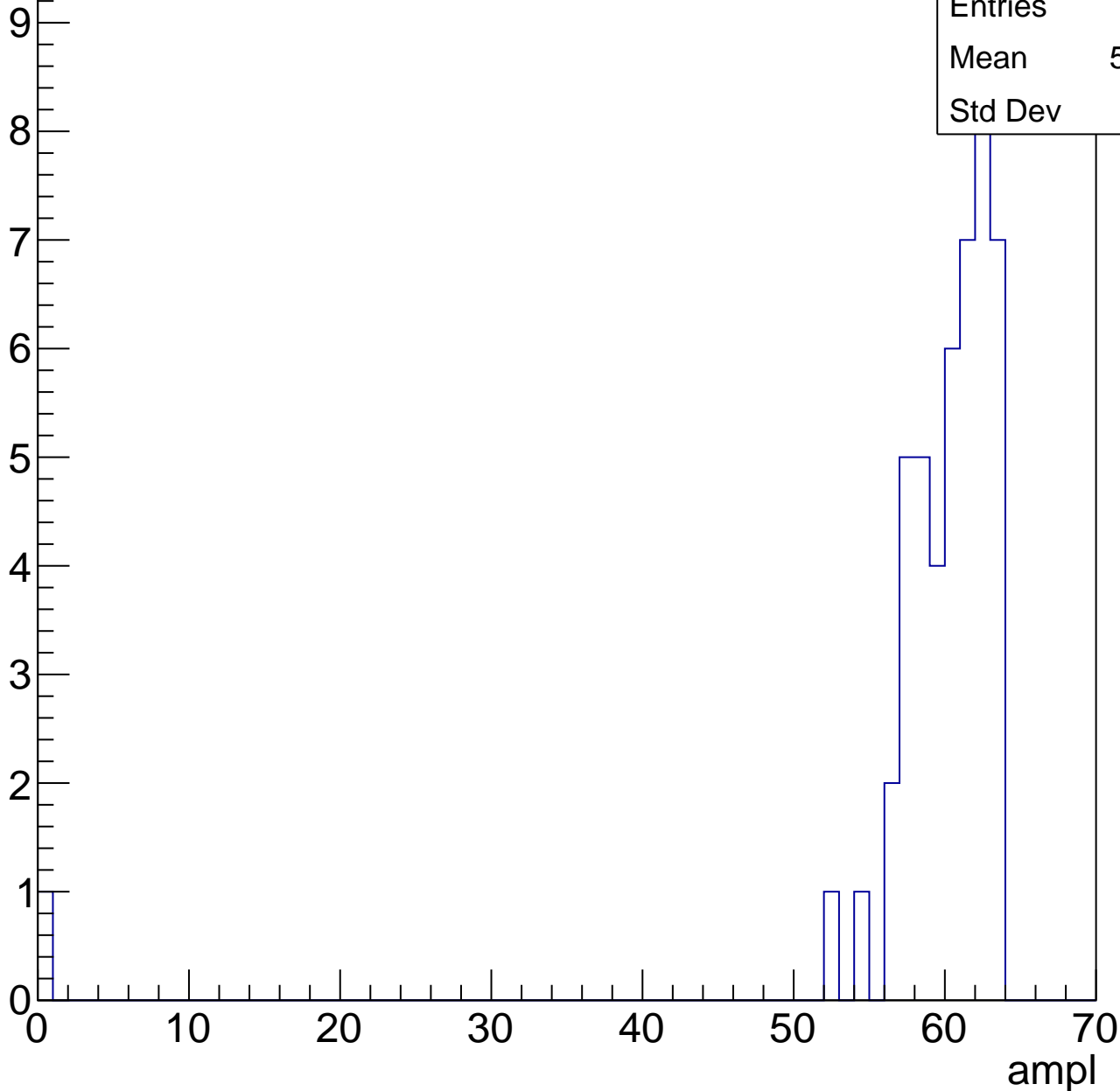


B0L001S, U19-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	58.65
Std Dev	8.92



B0L001S, U19-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch83, adc0

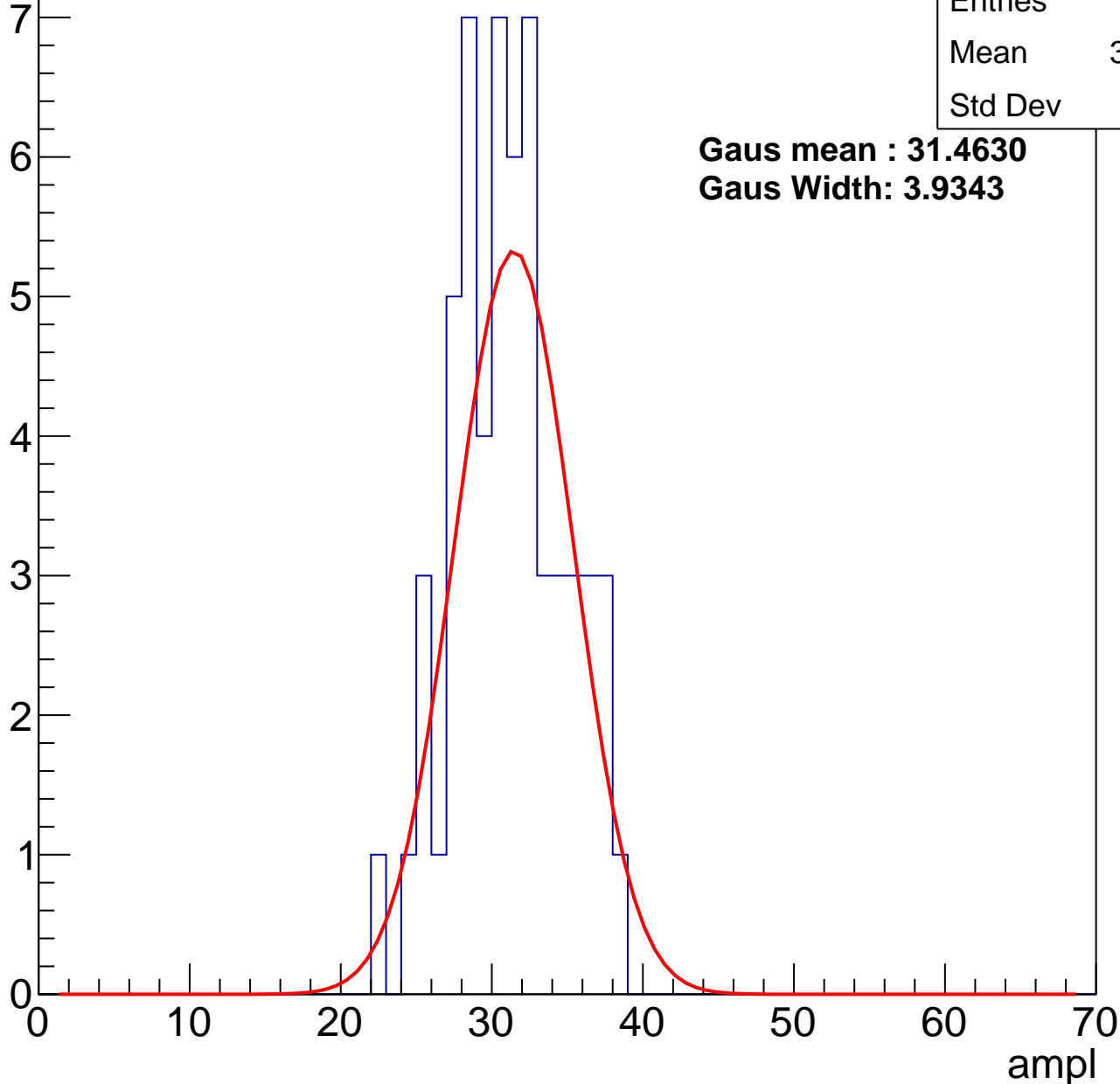
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	30.64
Std Dev	3.59

Gaus mean : 31.4630

Gaus Width: 3.9343



B0L001S, U19-ch83, adc1

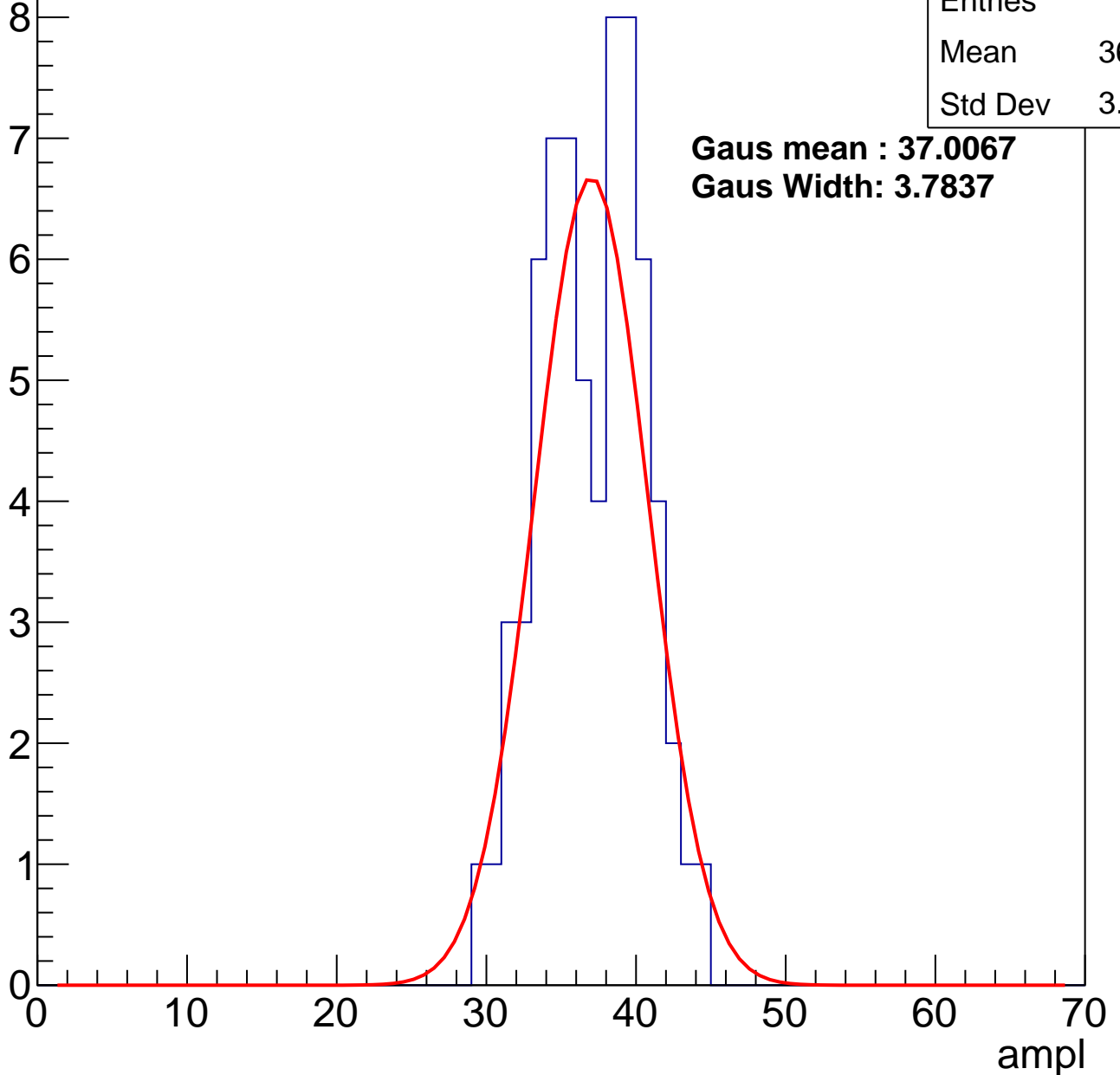
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.54
Std Dev	3.383

Gaus mean : 37.0067

Gaus Width: 3.7837



B0L001S, U19-ch83, adc2

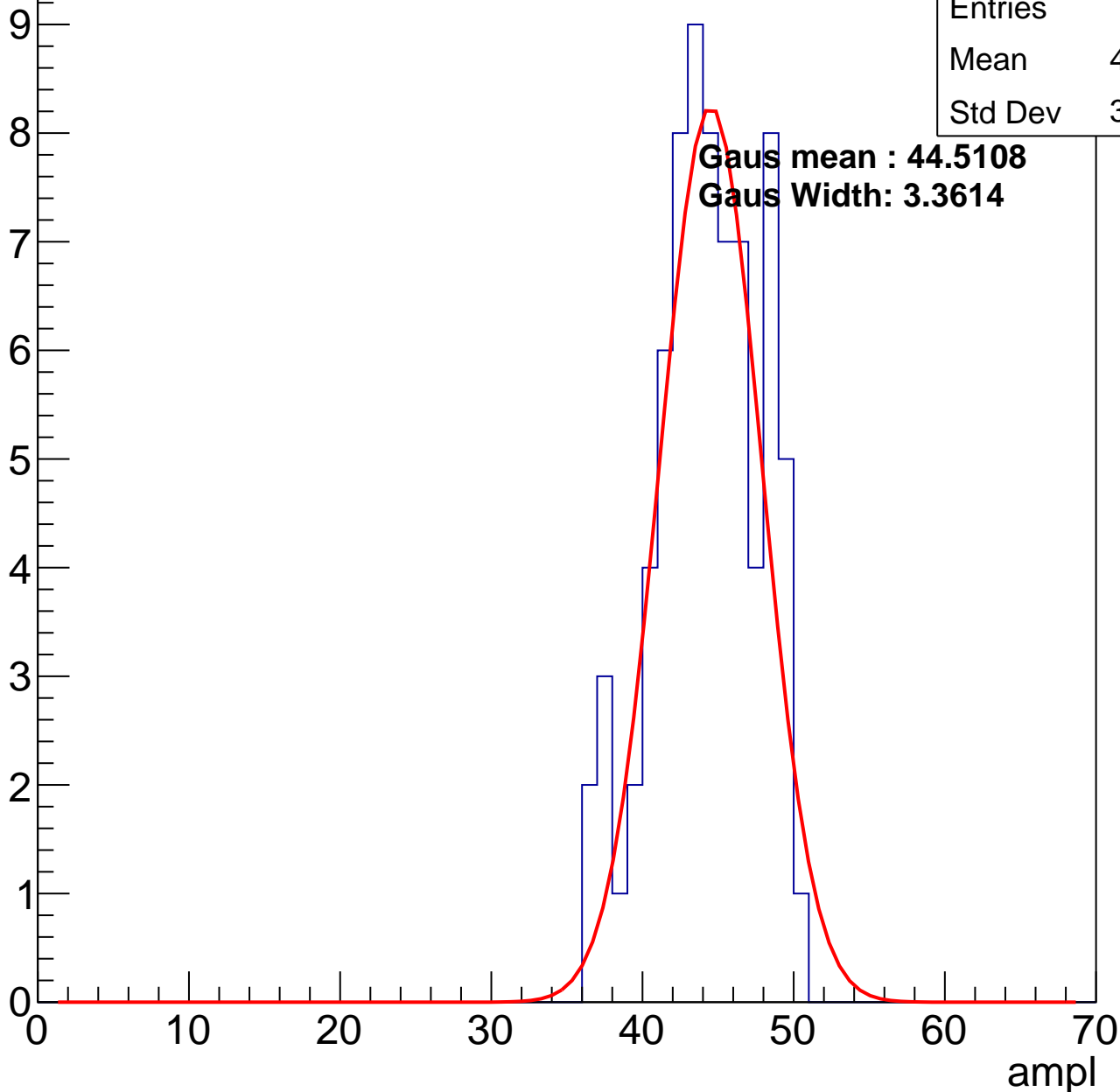
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.79
Std Dev	3.419

Gaus mean : 44.5108

Gaus Width: 3.3614

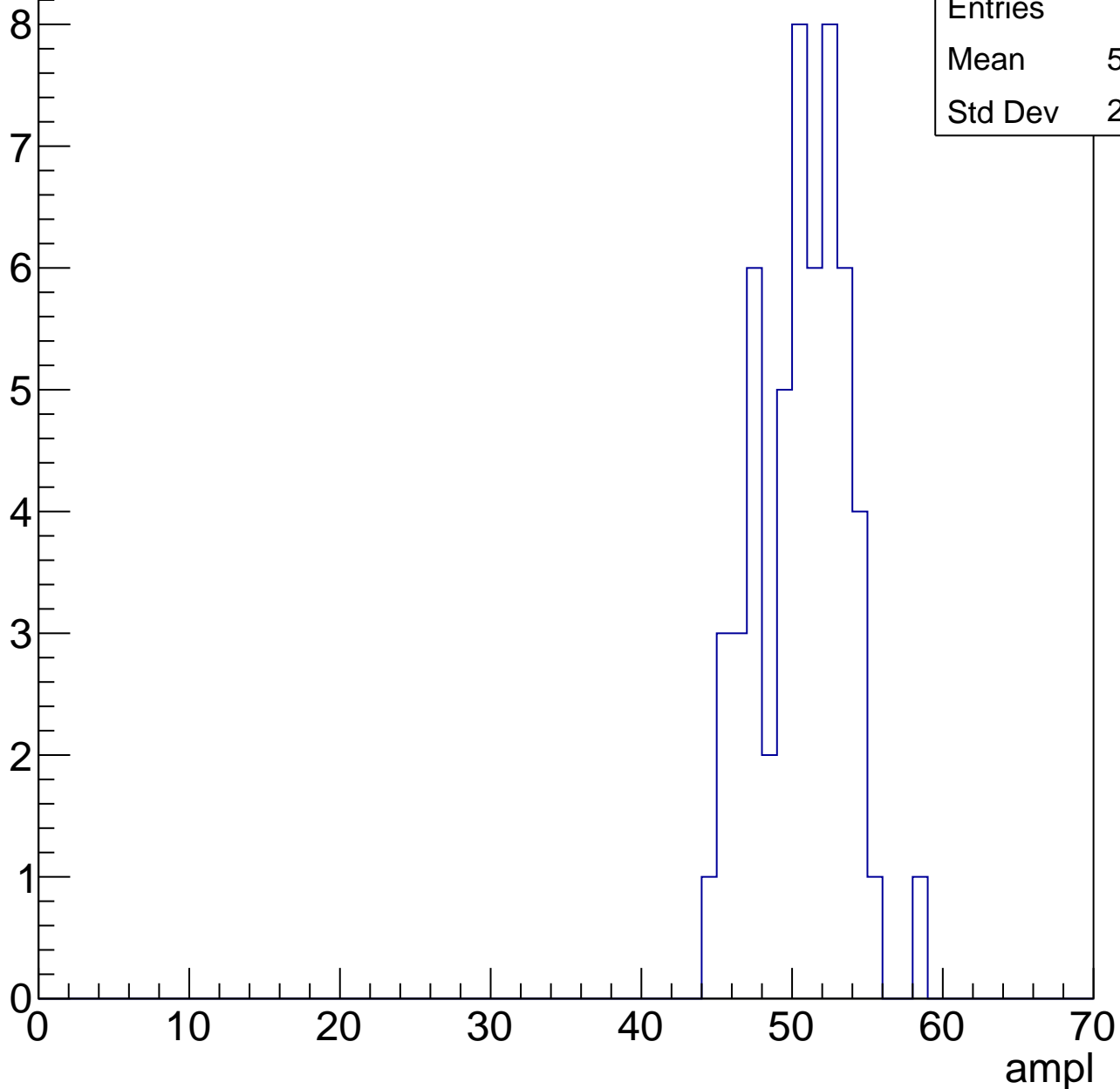


B0L001S, U19-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	50.17
Std Dev	2.949

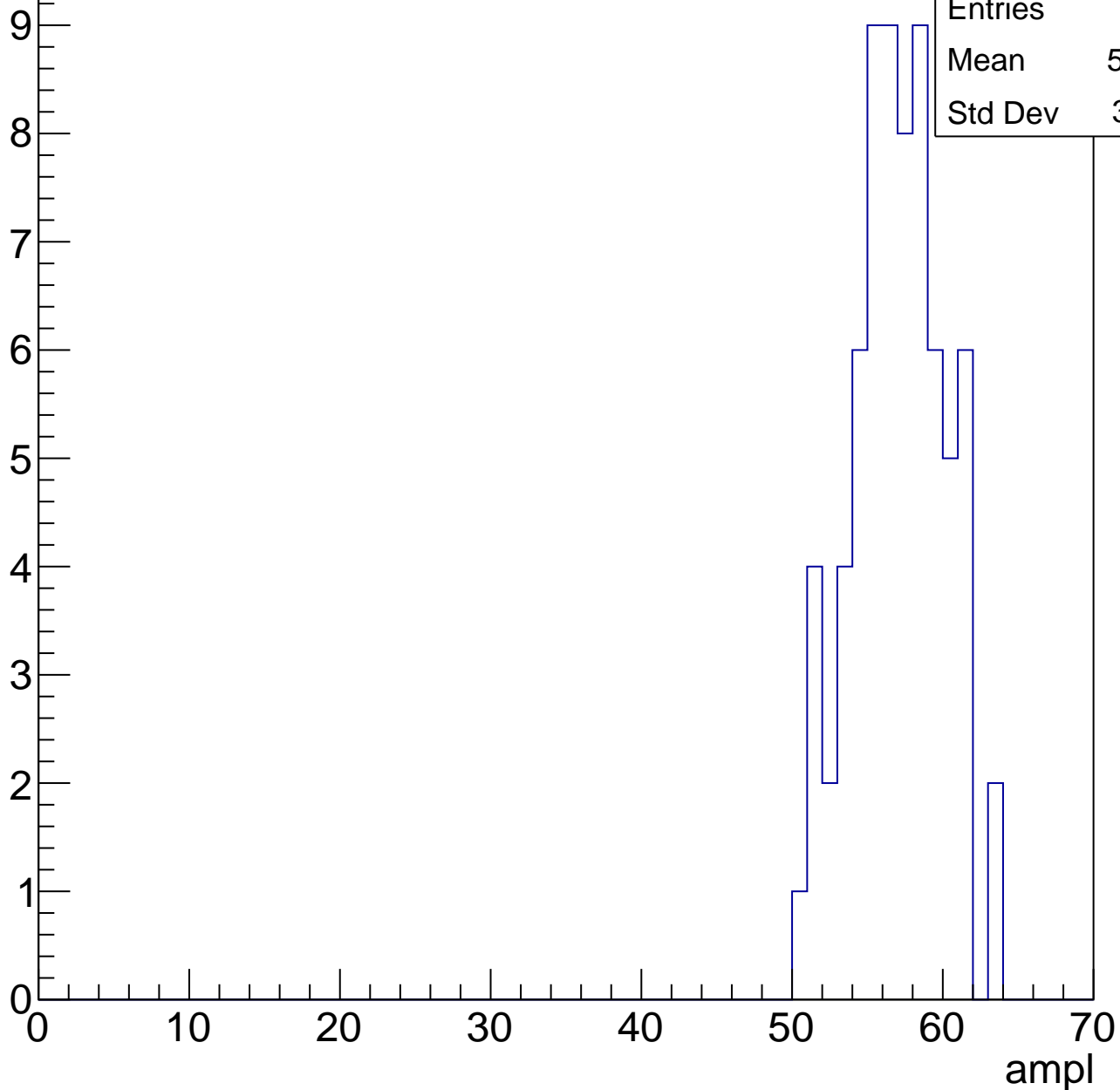


B0L001S, U19-ch83, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	56.58
Std Dev	3.001

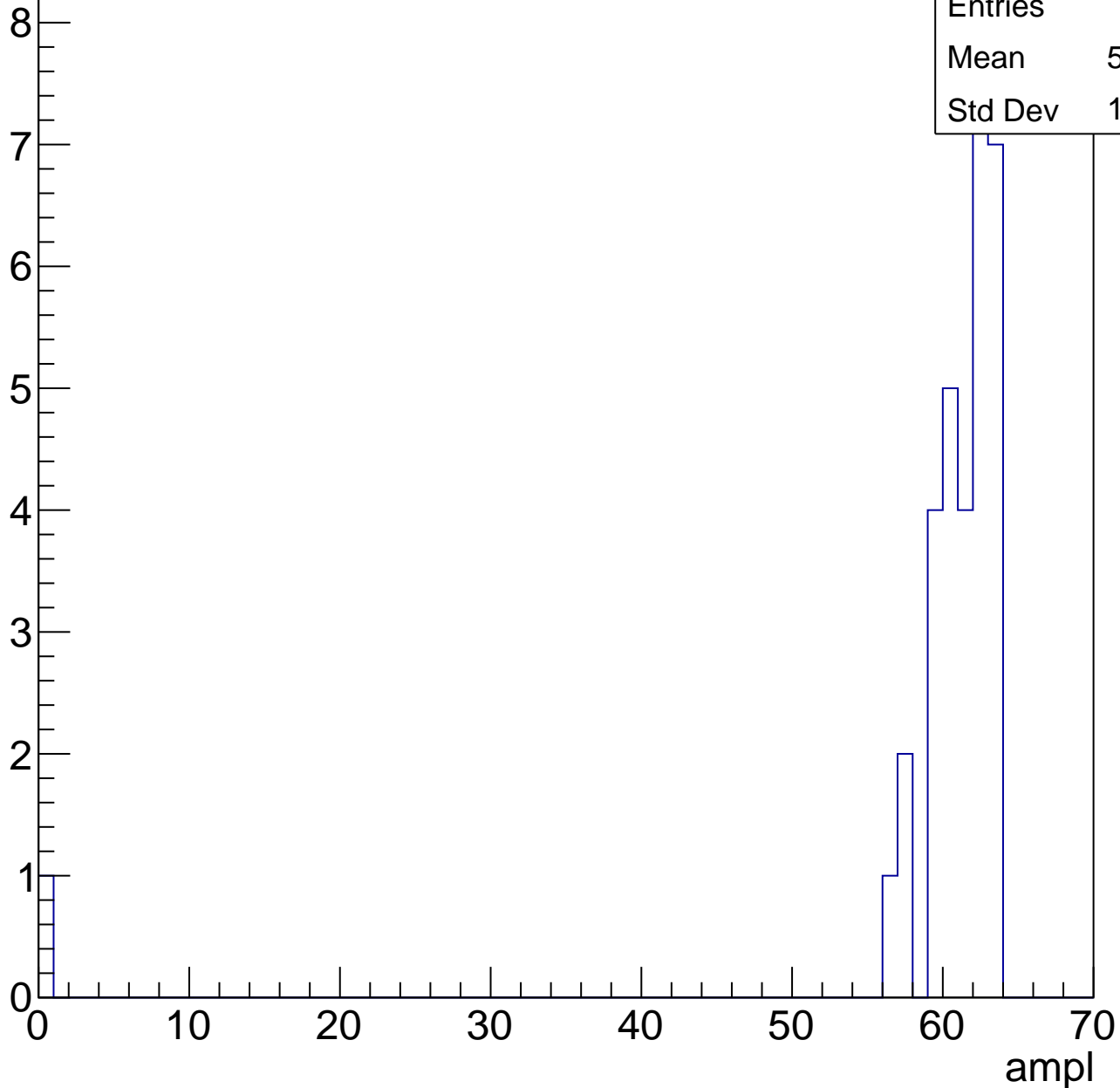


B0L001S, U19-ch83, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.97
Std Dev	10.76



B0L001S, U19-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B0L001S, U19-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch84, adc0

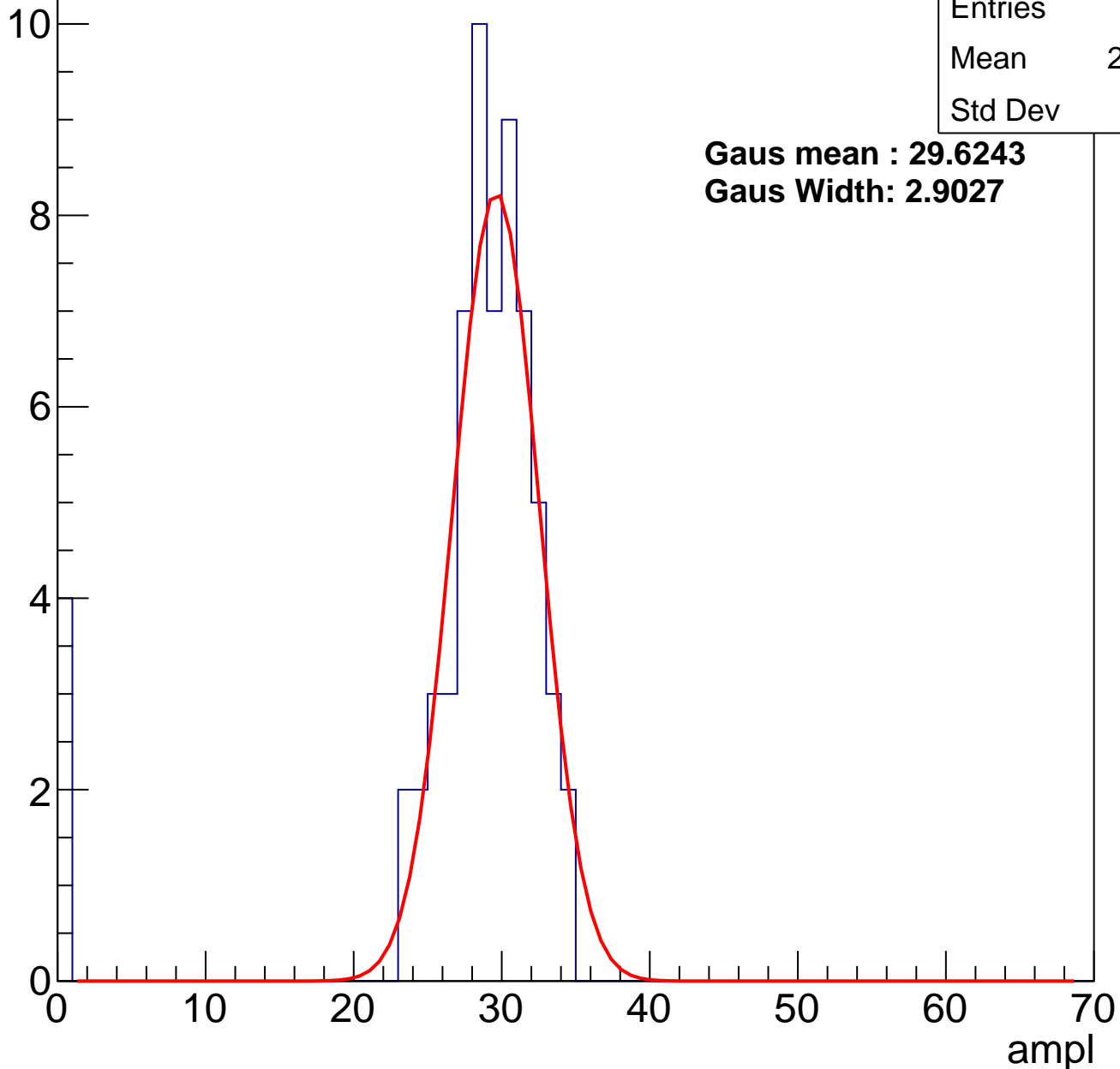
calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	27.08
Std Dev	7.44

Gaus mean : 29.6243

Gaus Width: 2.9027

Entry



B0L001S, U19-ch84, adc1

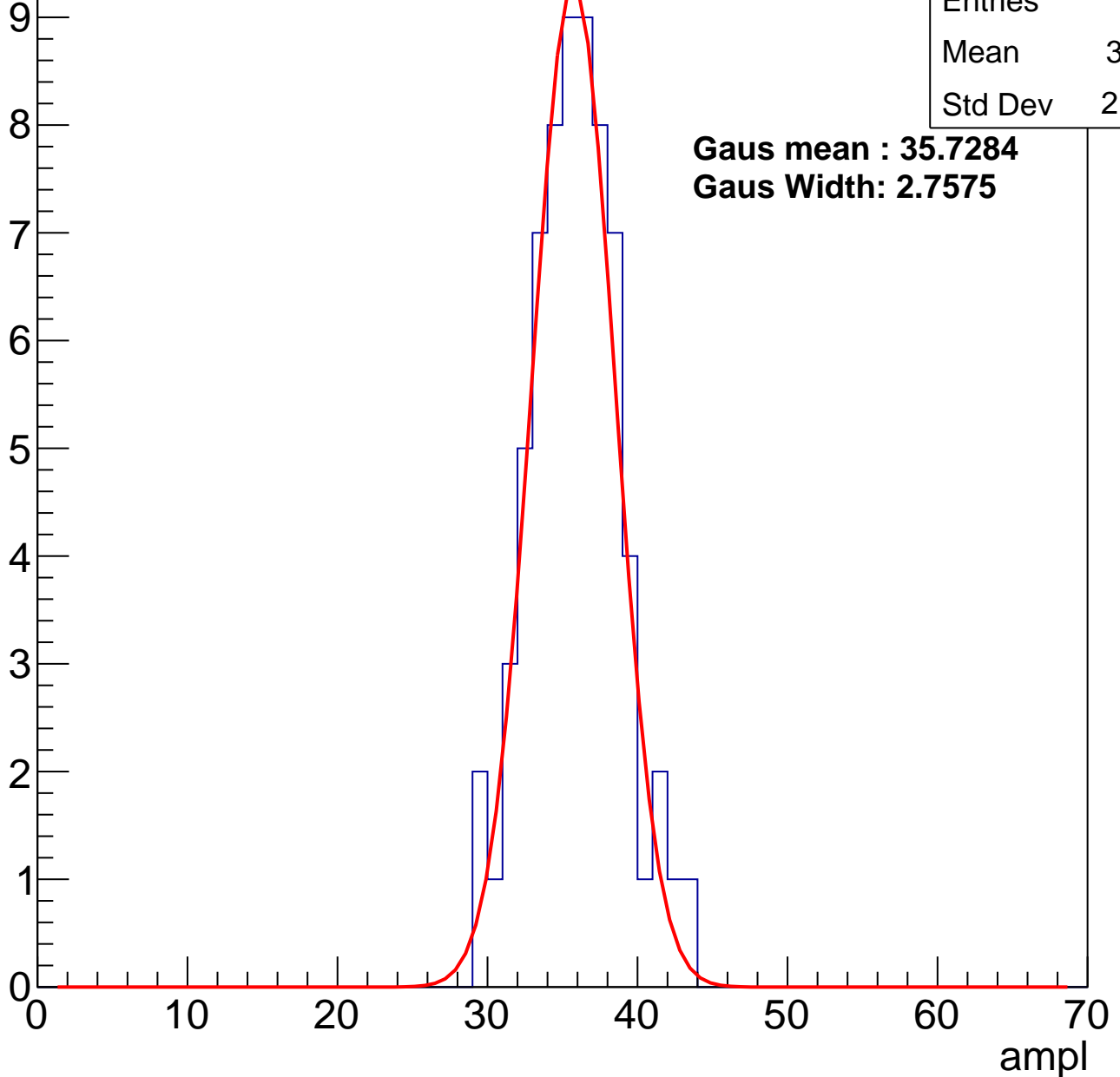
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	35.41
Std Dev	2.952

Gaus mean : 35.7284

Gaus Width: 2.7575



B0L001S, U19-ch84, adc2

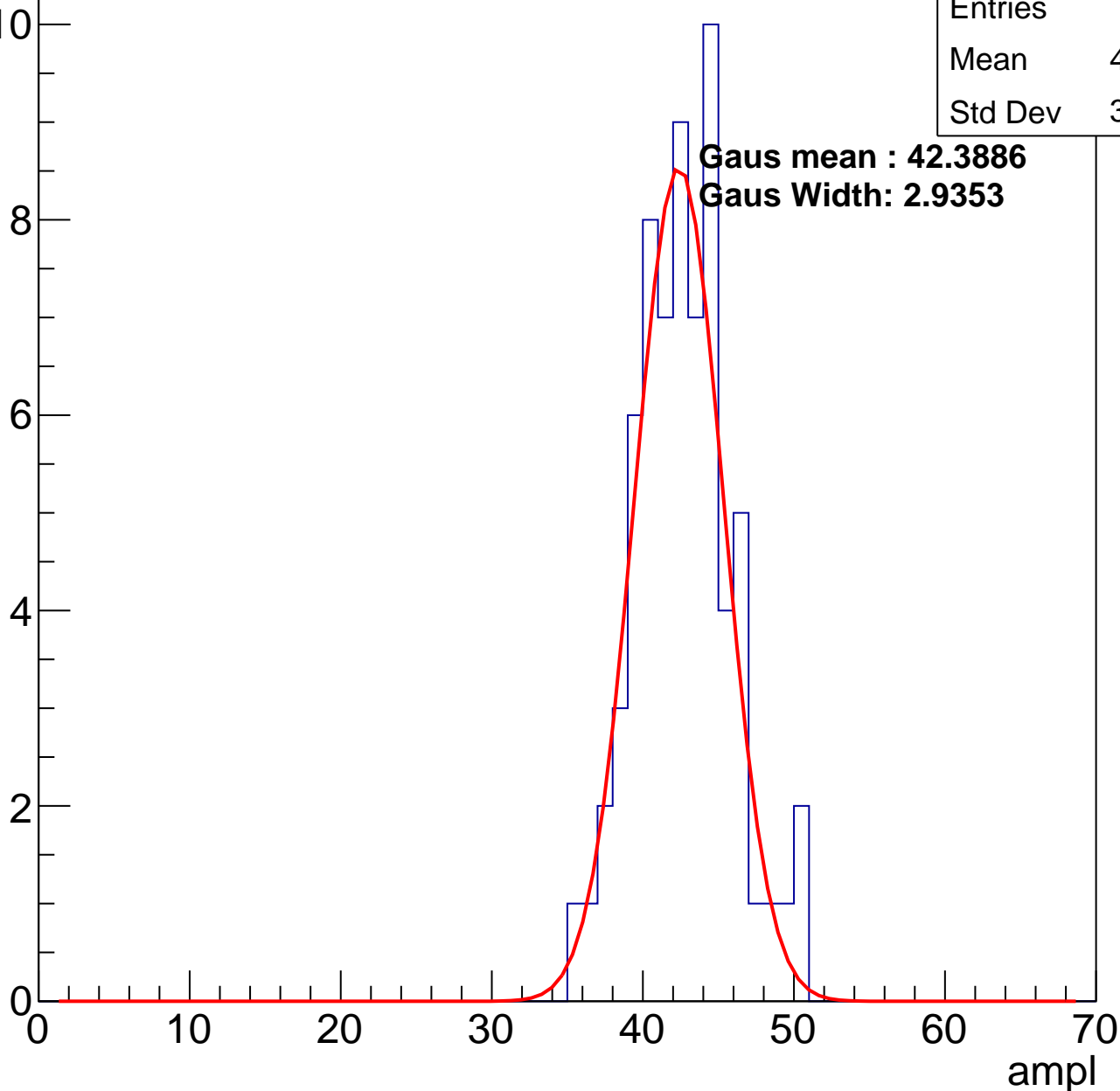
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	42.25
Std Dev	3.145

Gaus mean : 42.3886

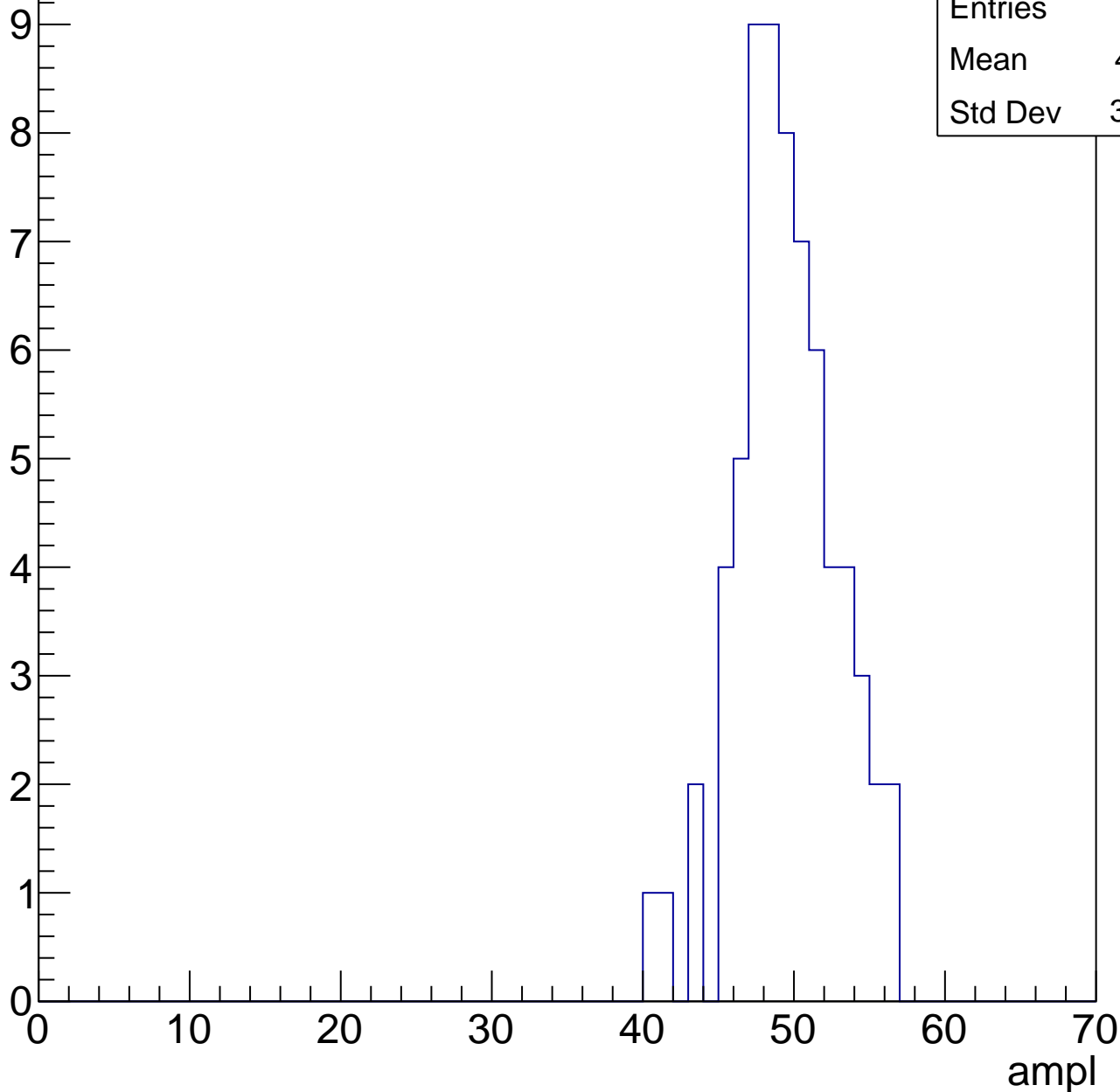
Gaus Width: 2.9353



B0L001S, U19-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



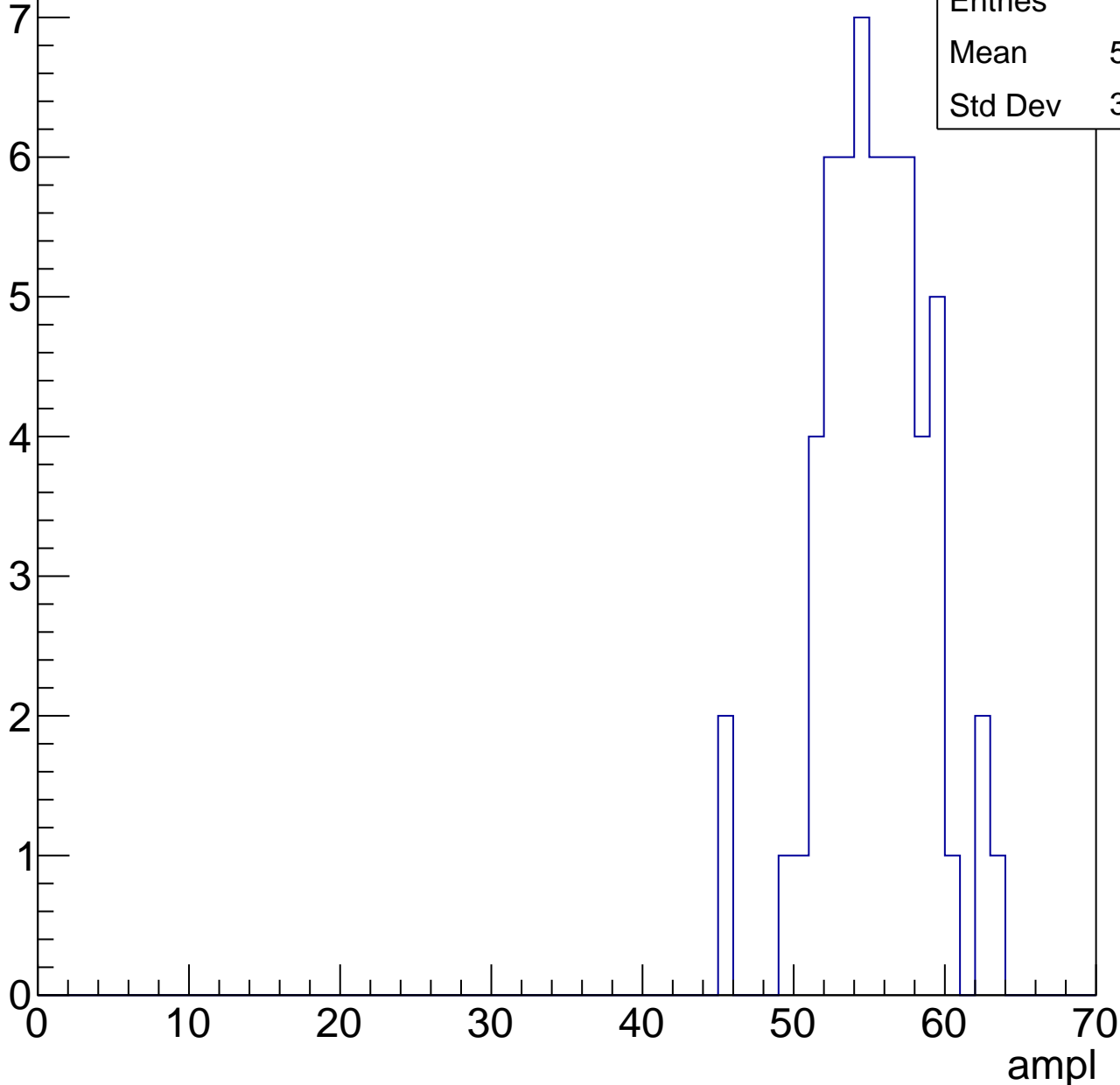
Entries	67
Mean	49.01
Std Dev	3.339

B0L001S, U19-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	54.88
Std Dev	3.577

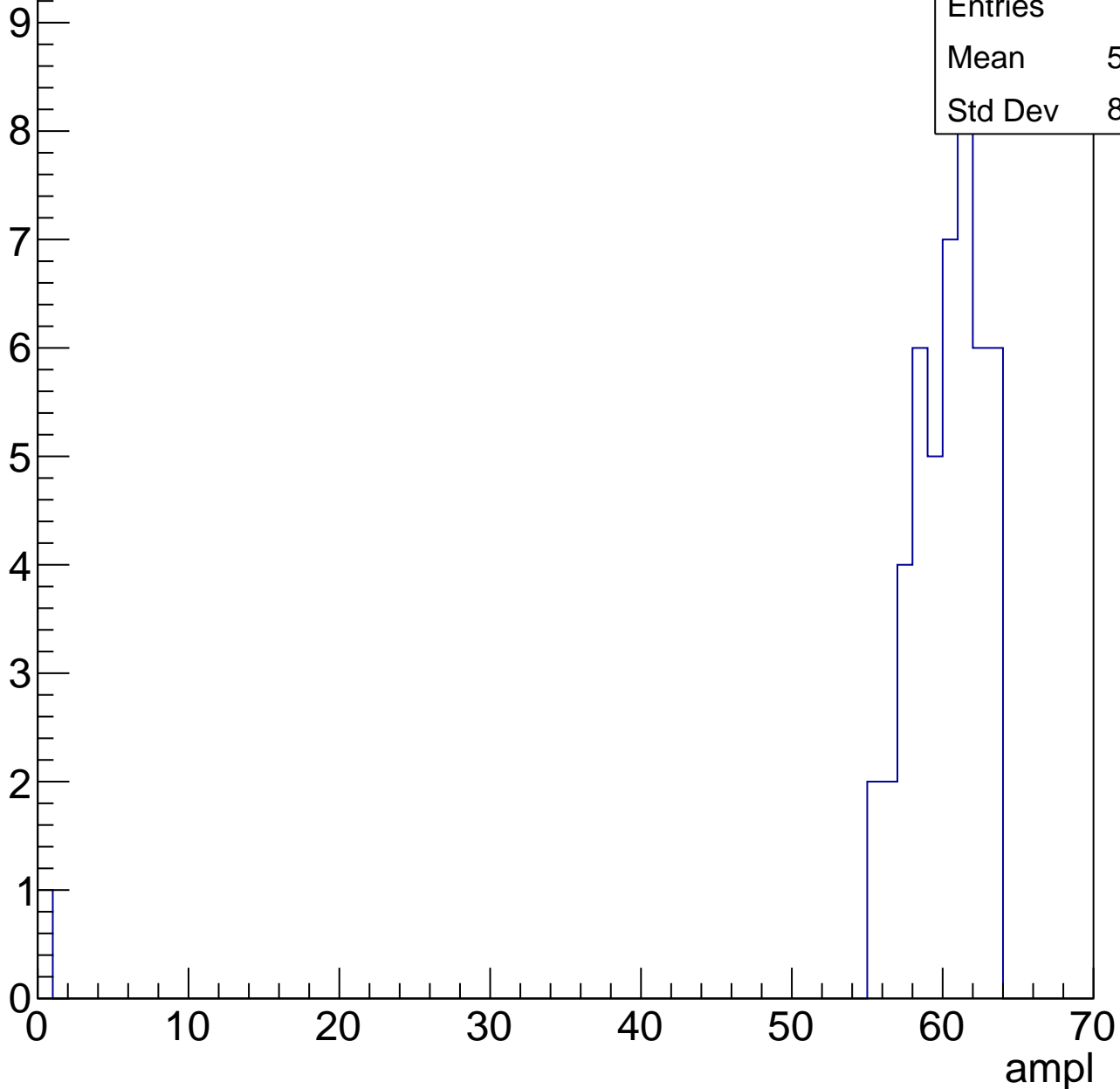


B0L001S, U19-ch84, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

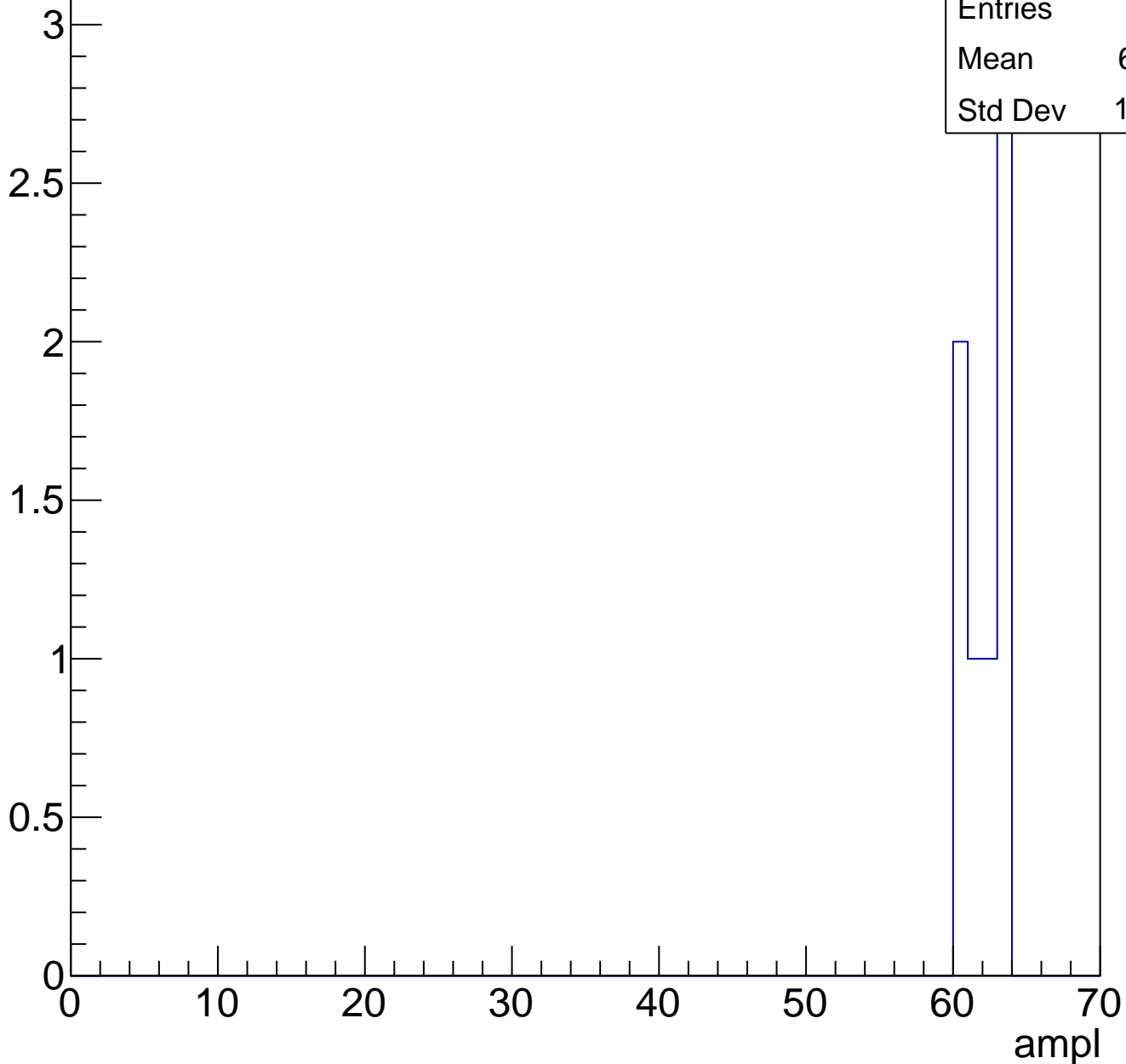
Entries	48
Mean	58.58
Std Dev	8.824



B0L001S, U19-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch84, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B0L001S, U19-ch85, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	29.34
Std Dev	4.656

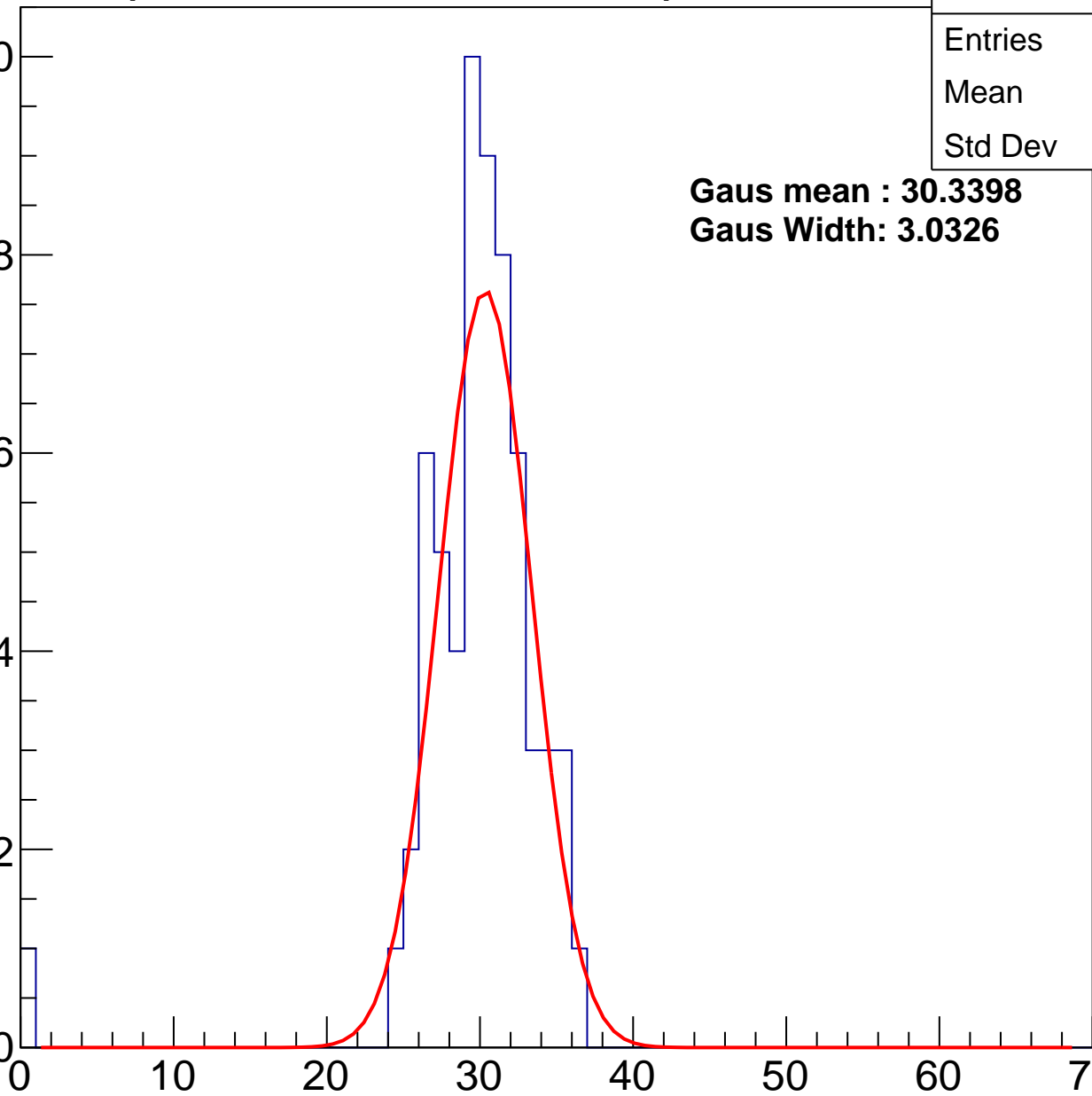
Gaus mean : 30.3398

Gaus Width: 3.0326

Entry

10
8
6
4
2
0

ampl



B0L001S, U19-ch85, adc1

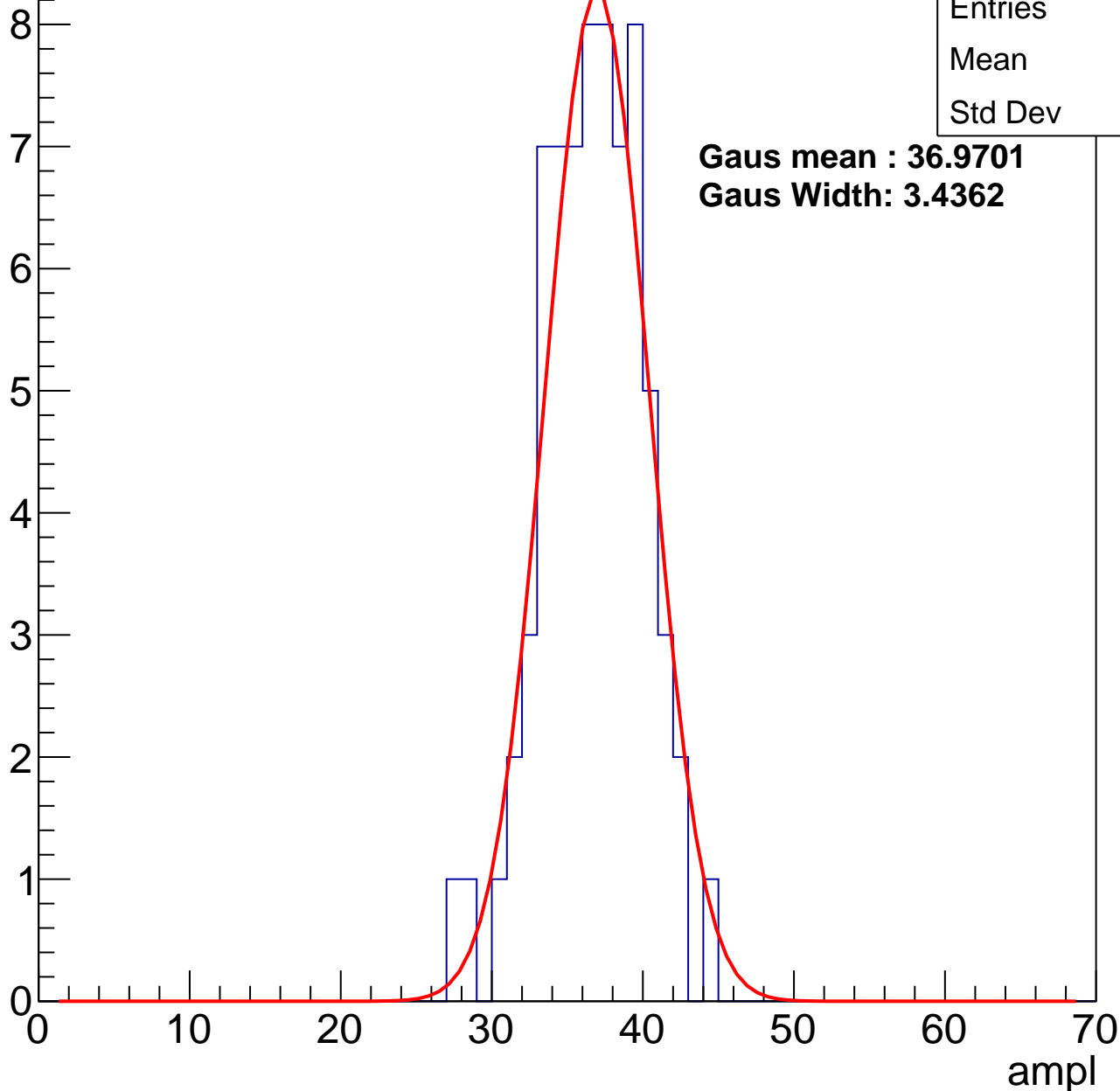
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	36.2
Std Dev	3.3

Gaus mean : 36.9701

Gaus Width: 3.4362



B0L001S, U19-ch85, adc2

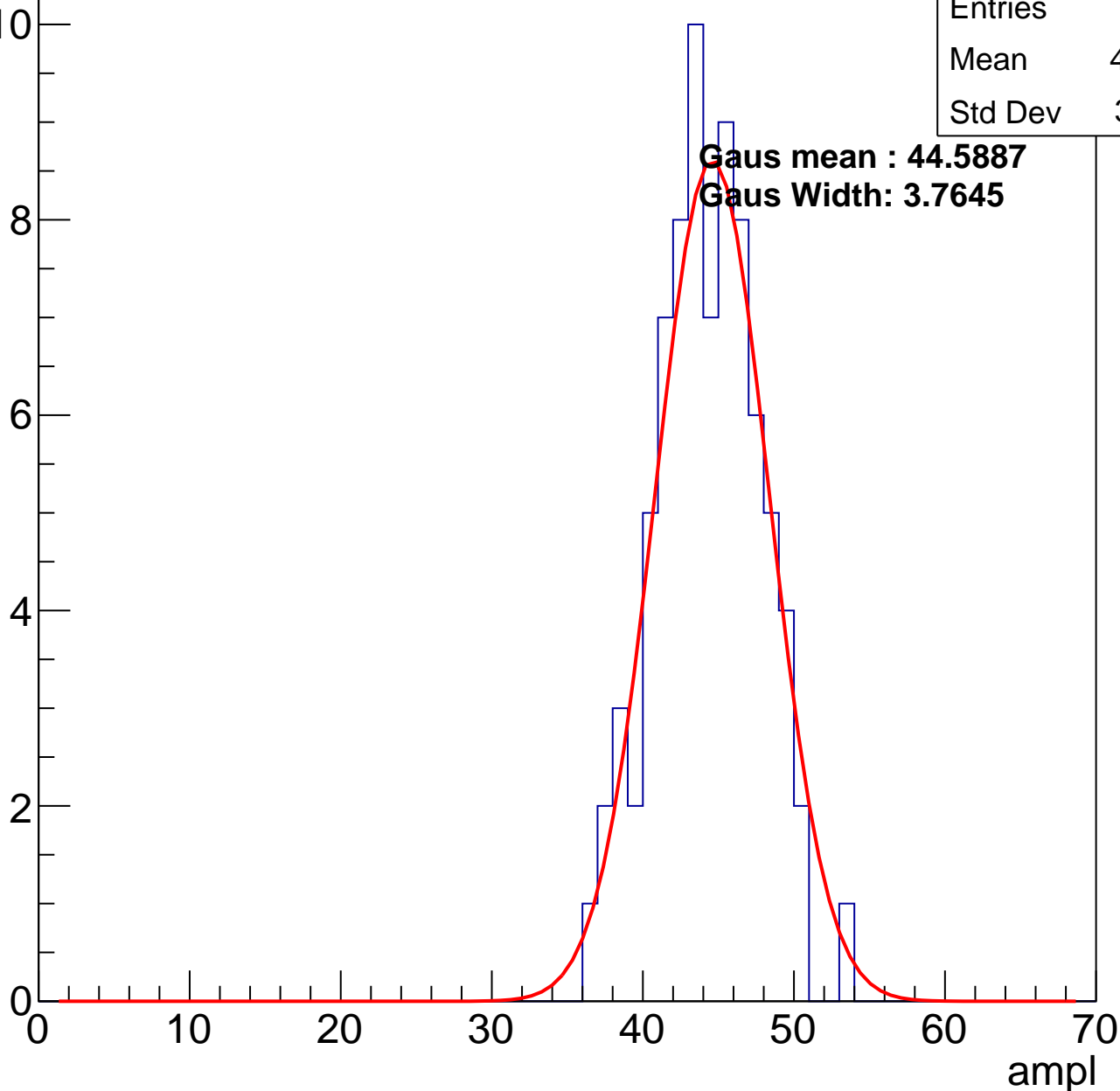
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	43.84
Std Dev	3.411

Gaus mean : 44.5887

Gaus Width: 3.7645

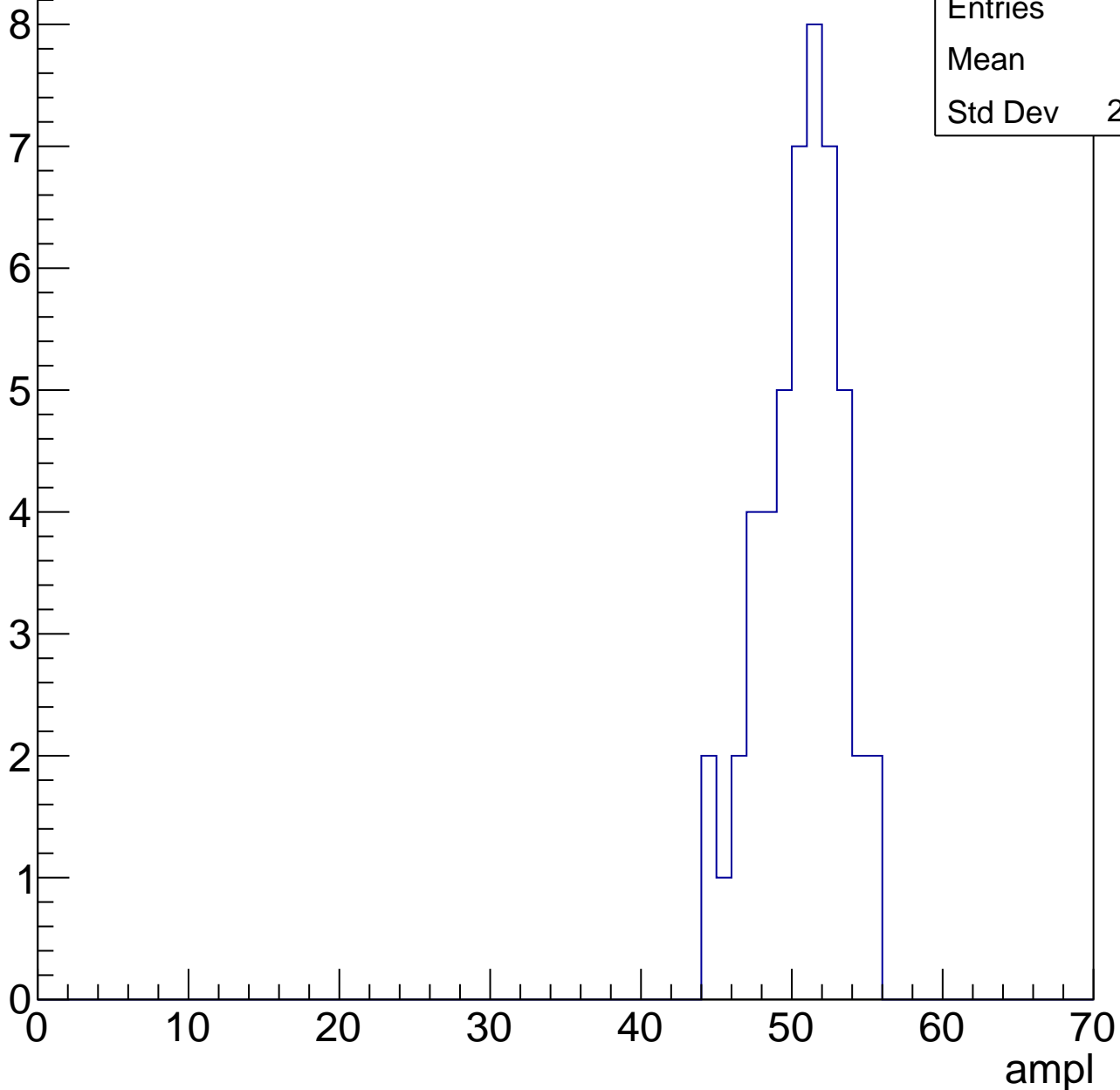


B0L001S, U19-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	50.1
Std Dev	2.667

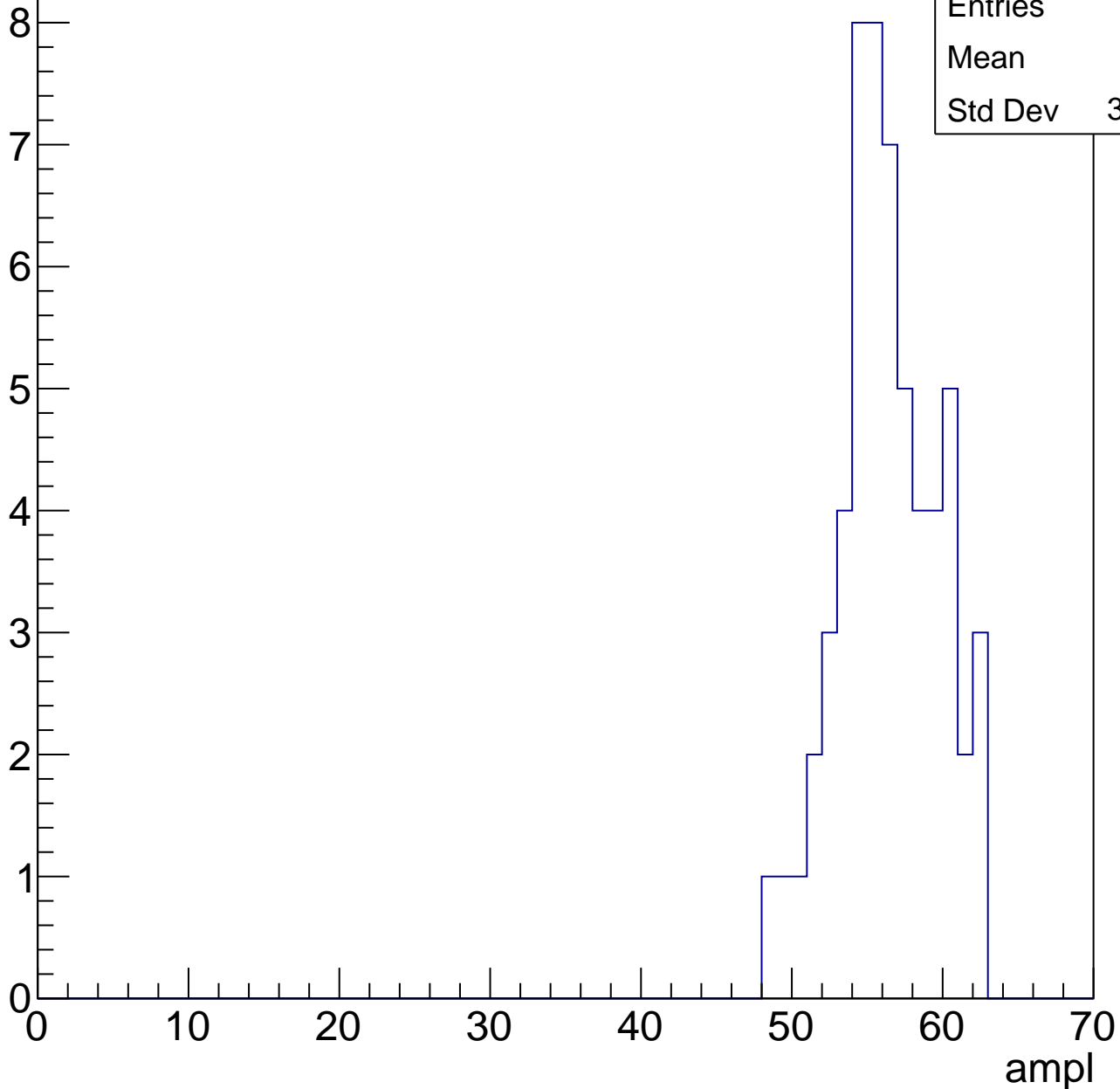


B0L001S, U19-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	55.9
Std Dev	3.263

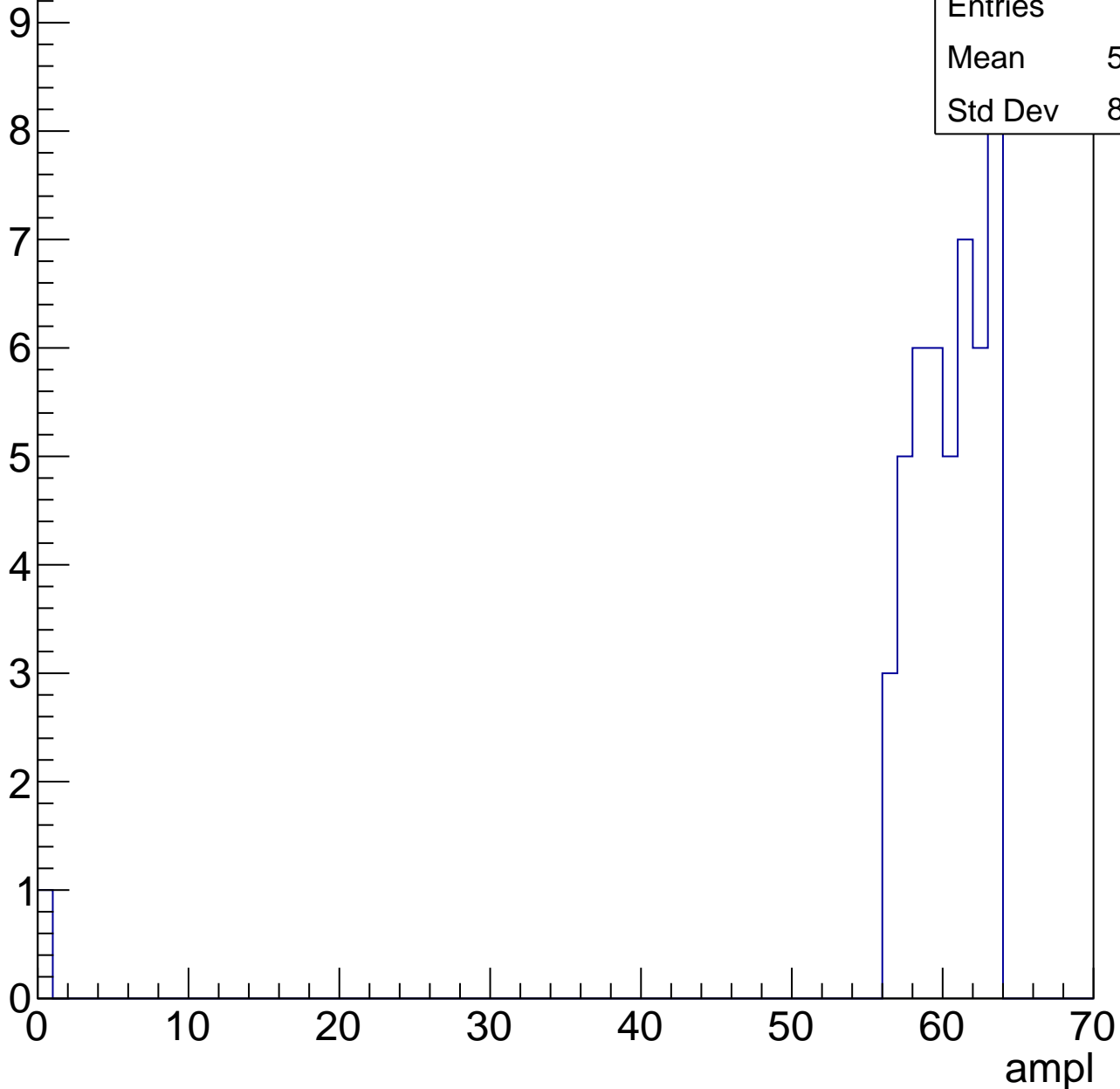


B0L001S, U19-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	58.77
Std Dev	8.854



B0L001S, U19-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch86, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	30.92
Std Dev	3.297

Gaus mean : 31.4543

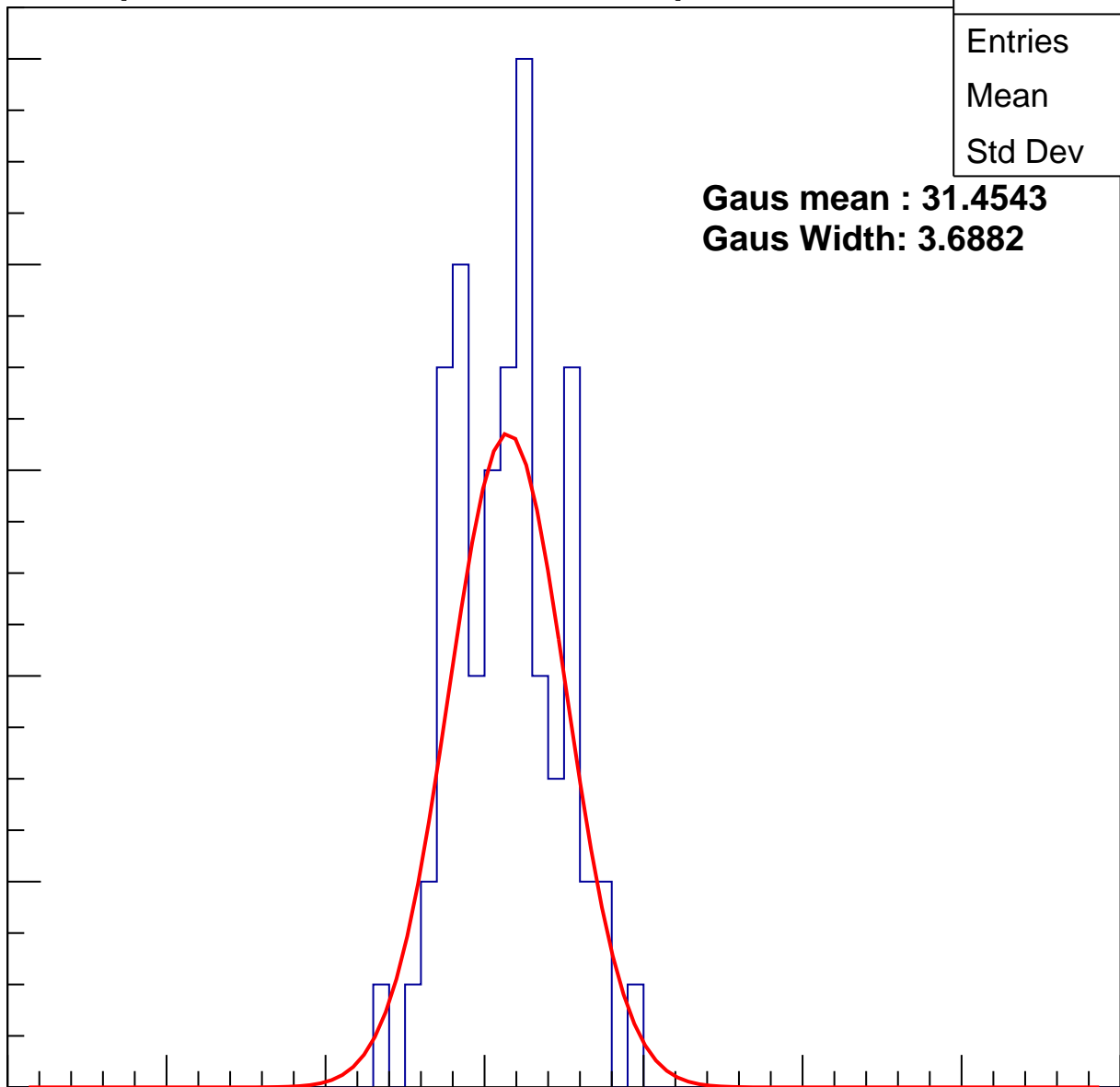
Gaus Width: 3.6882

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch86, adc1

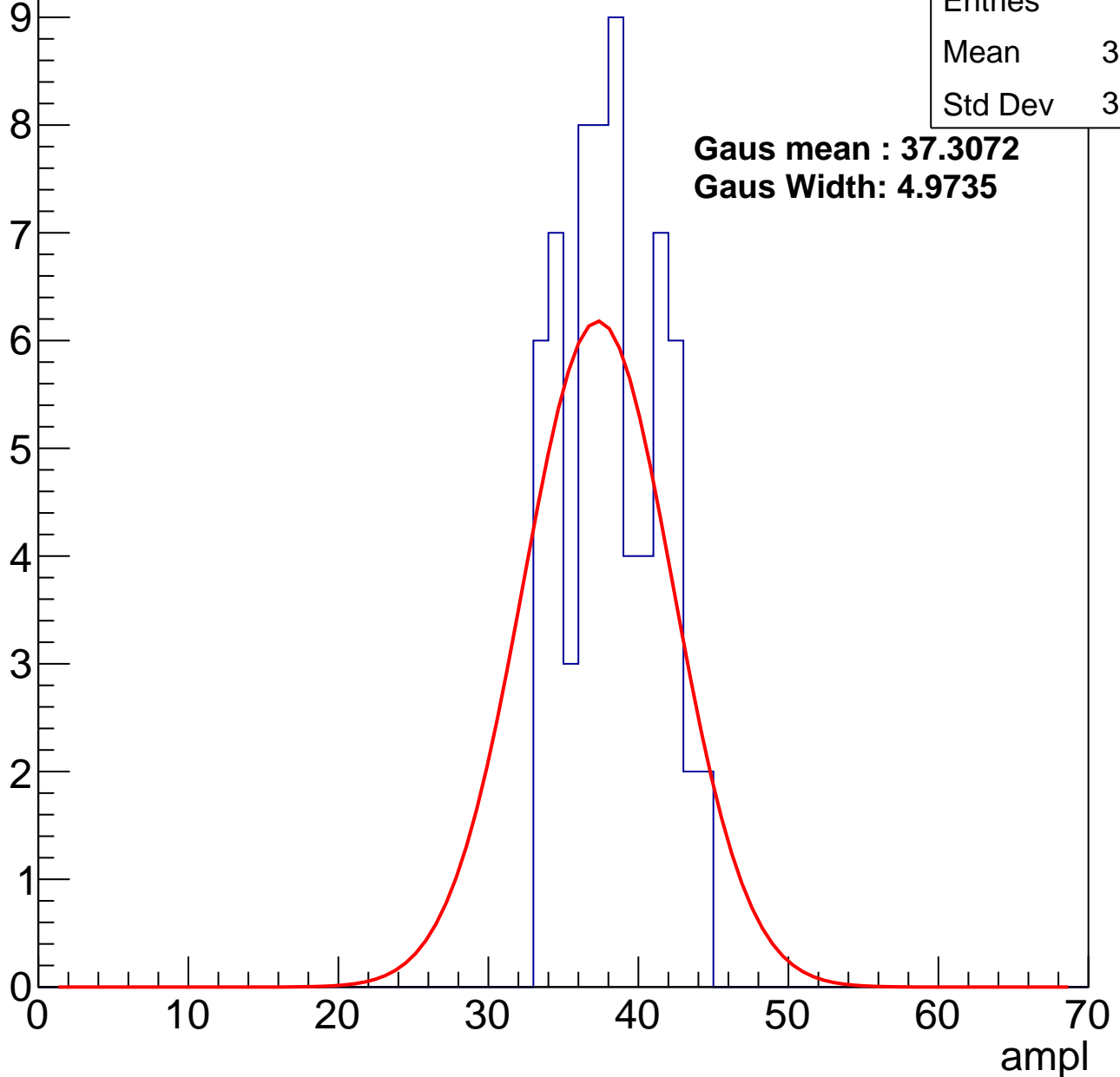
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.82
Std Dev	3.084

Gaus mean : 37.3072

Gaus Width: 4.9735

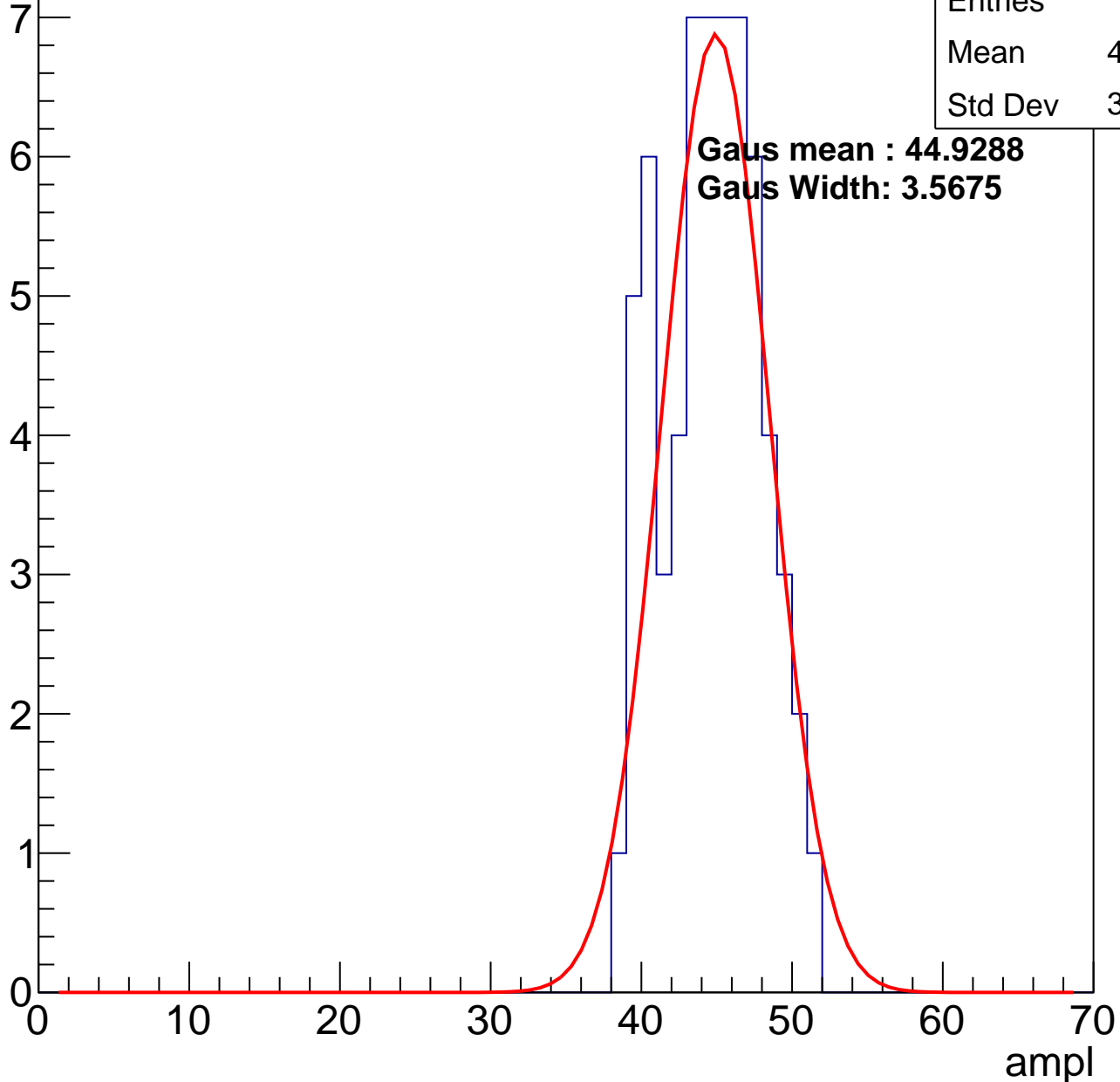


B0L001S, U19-ch86, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.16
Std Dev	3.223

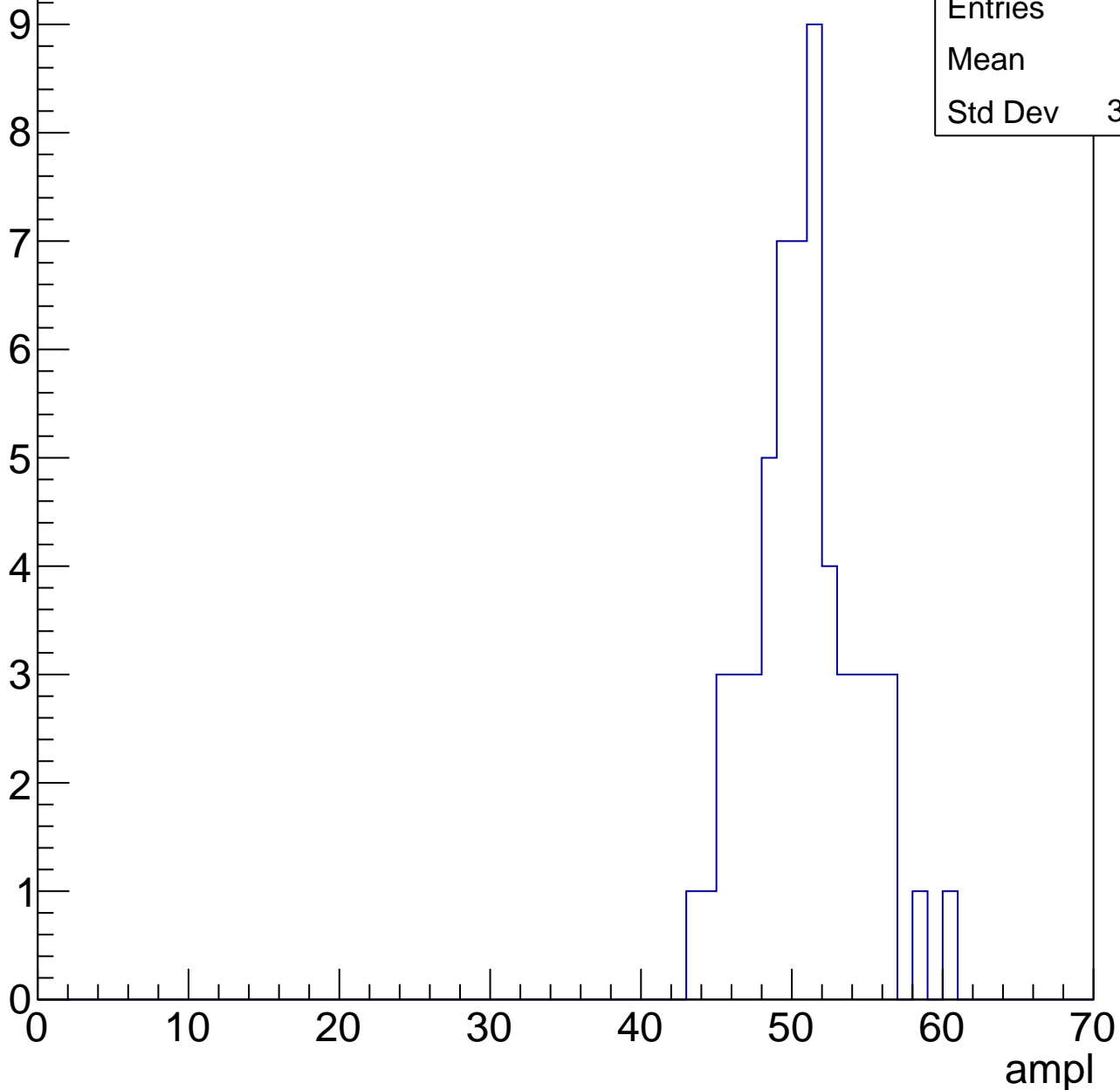


B0L001S, U19-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	50.4
Std Dev	3.504

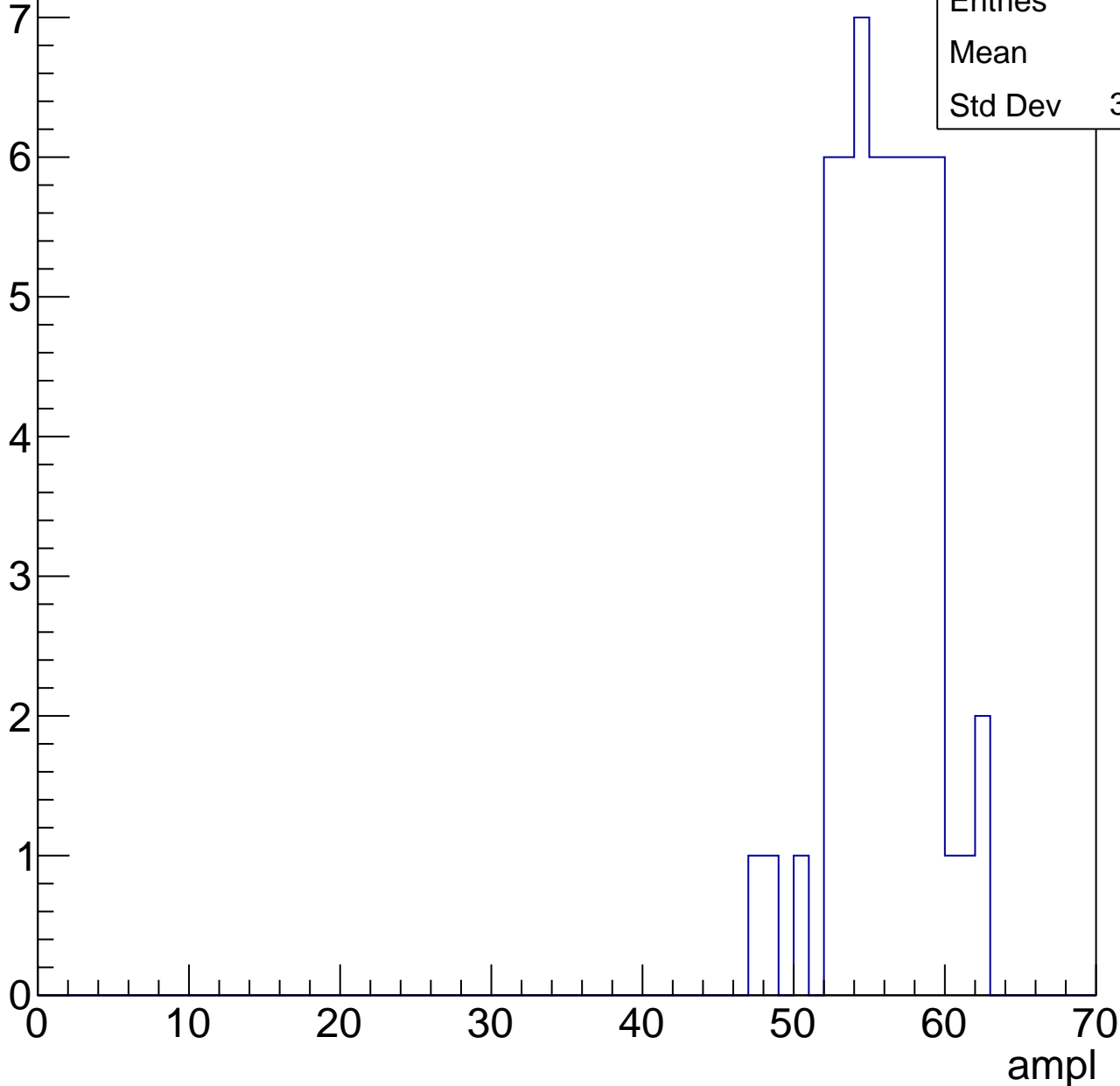


B0L001S, U19-ch86, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	55.5
Std Dev	3.128

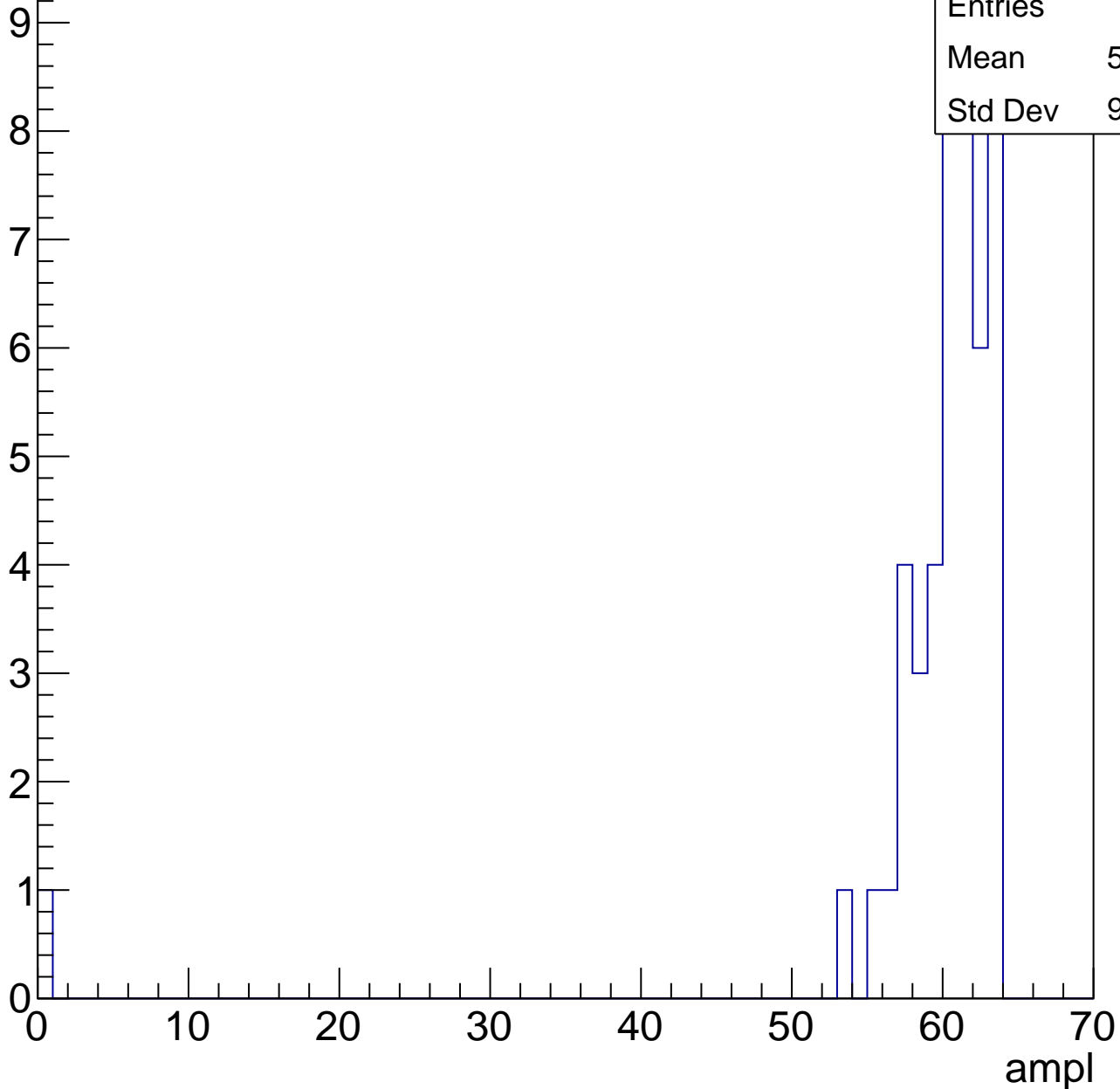


B0L001S, U19-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.85
Std Dev	9.072



B0L001S, U19-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	23
Std Dev	0

B0L001S, U19-ch87, adc0

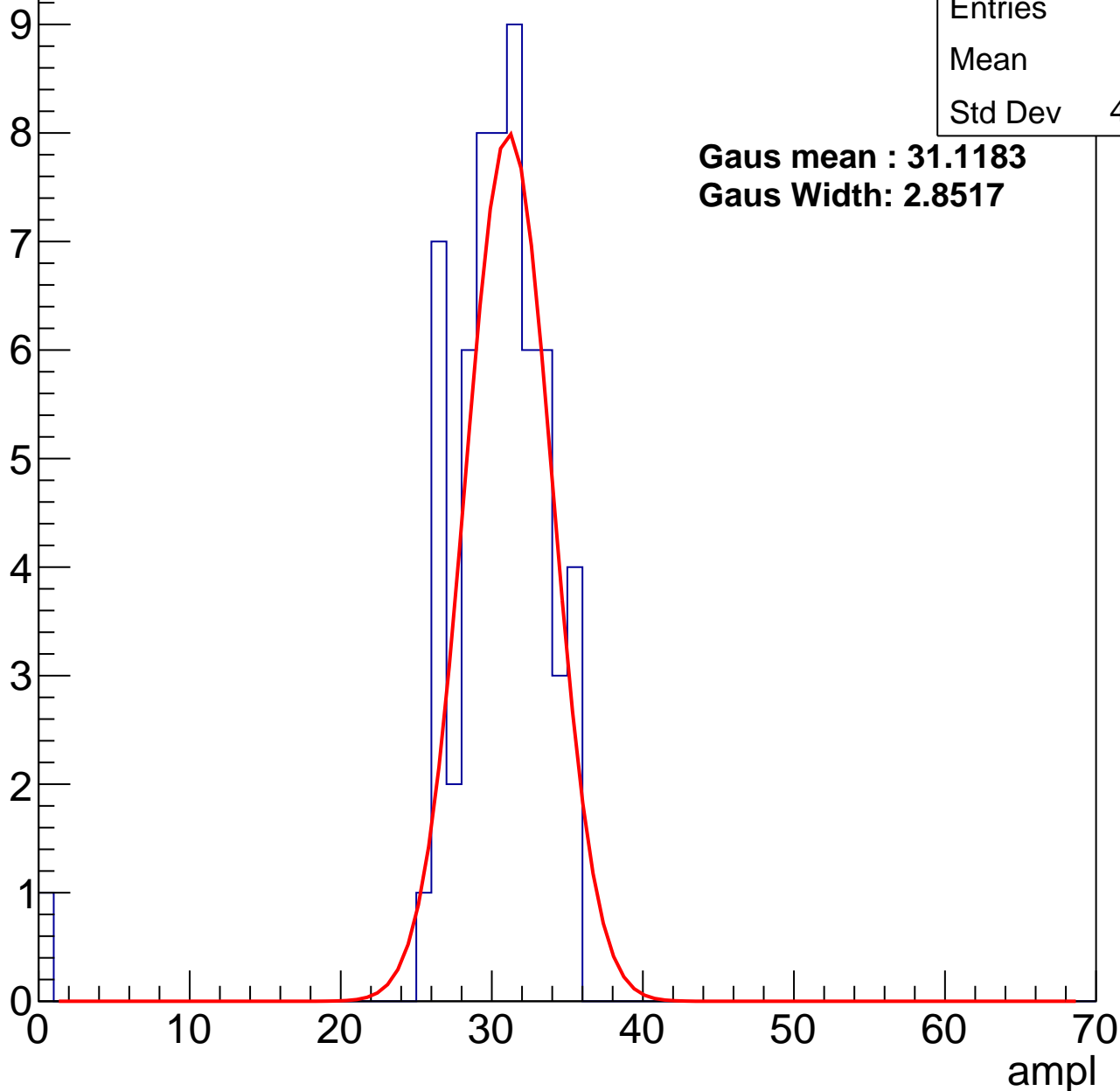
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	29.7
Std Dev	4.646

Gaus mean : 31.1183

Gaus Width: 2.8517



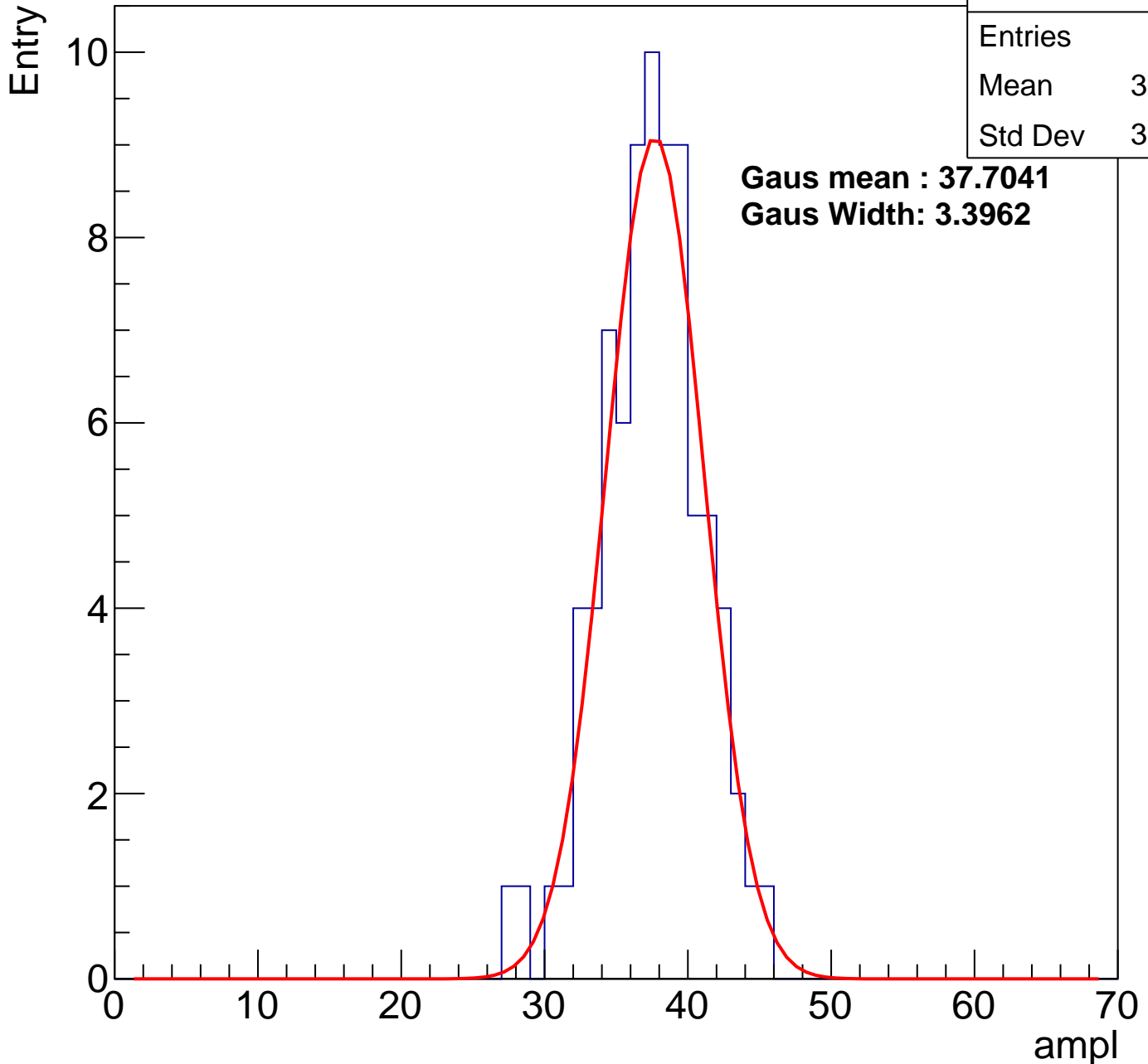
B0L001S, U19-ch87, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	36.99
Std Dev	3.502

Gaus mean : 37.7041

Gaus Width: 3.3962



B0L001S, U19-ch87, adc2

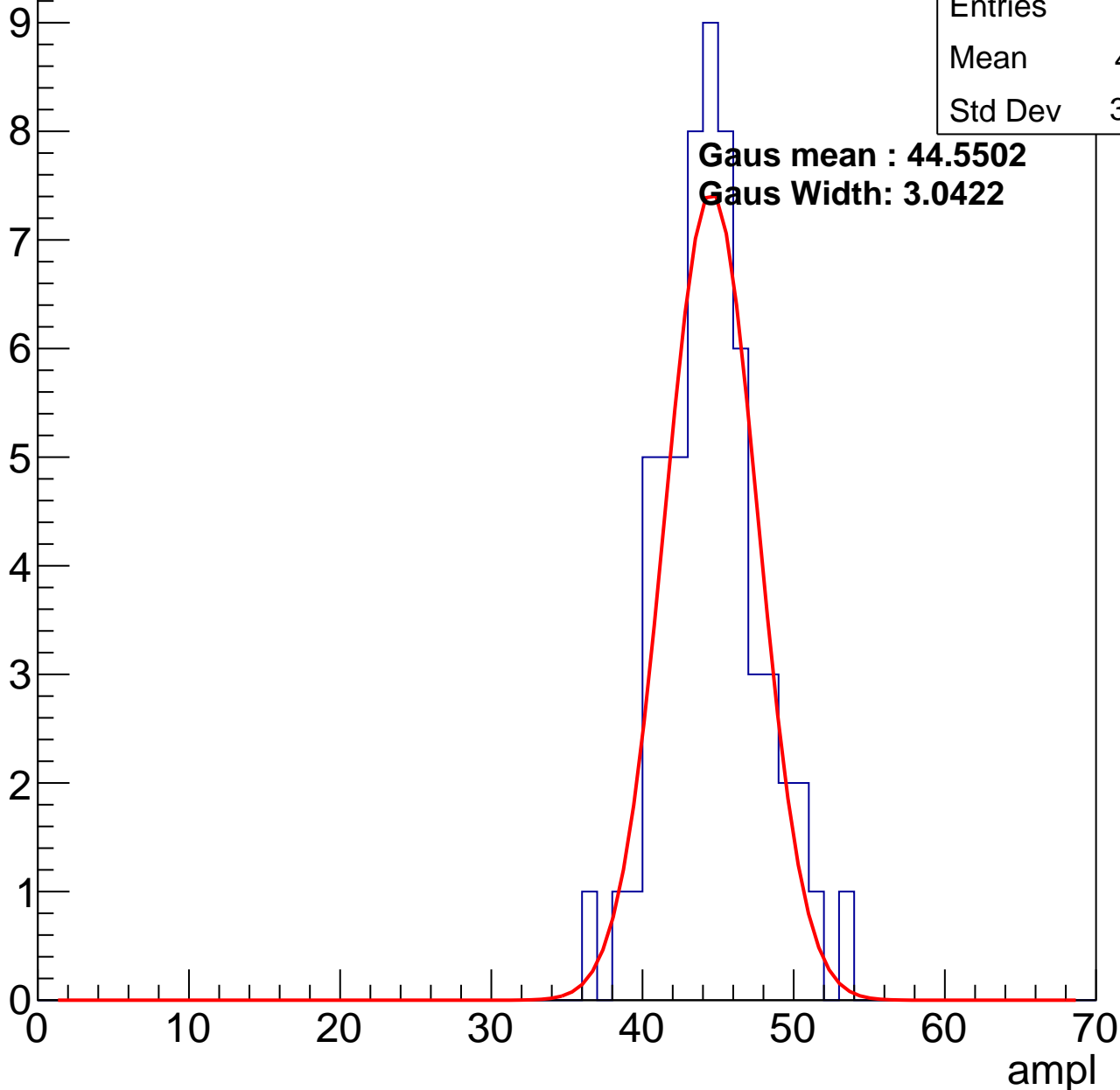
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.11
Std Dev	3.229

Gaus mean : 44.5502

Gaus Width: 3.0422

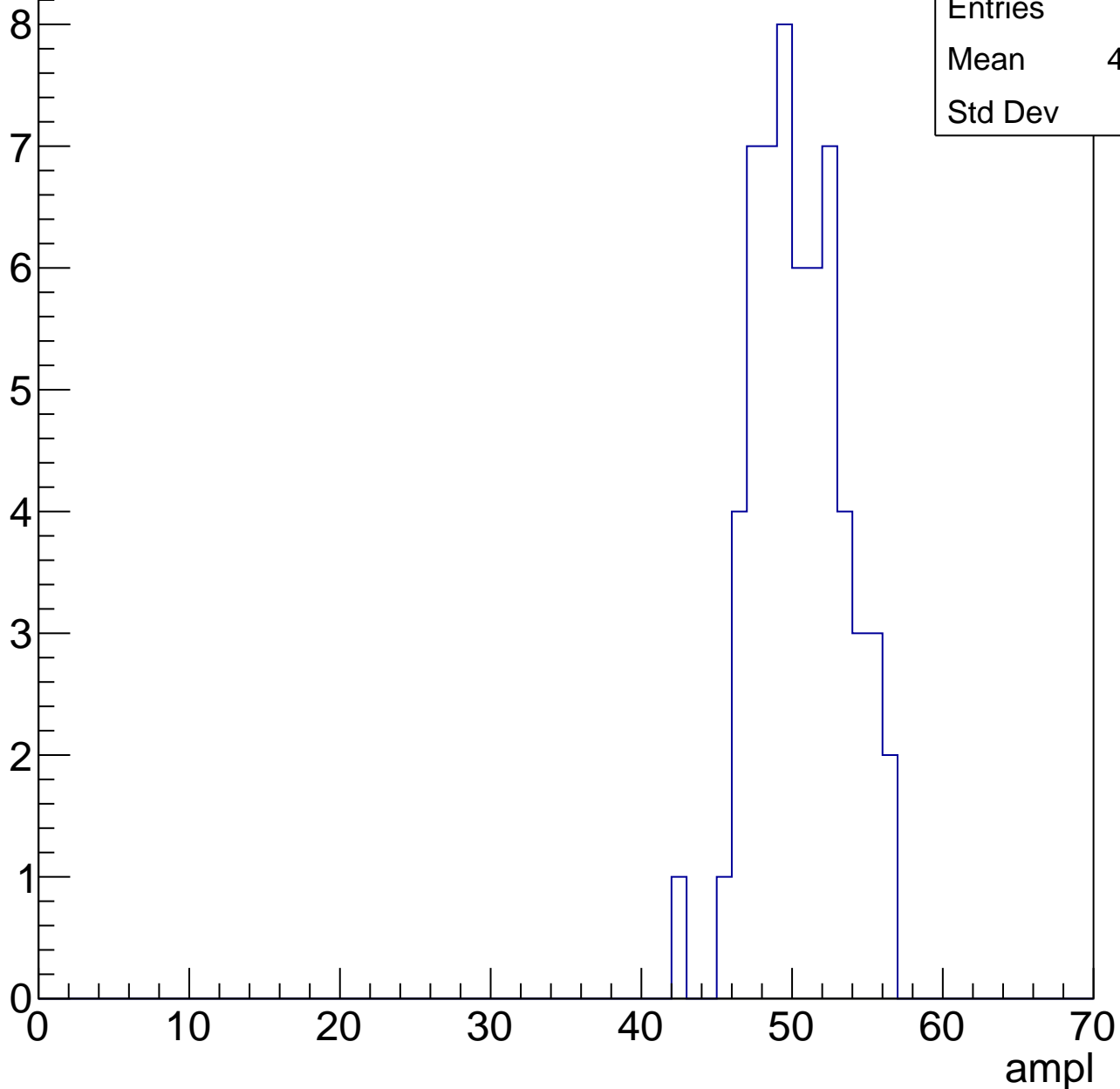


B0L001S, U19-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	49.98
Std Dev	2.96

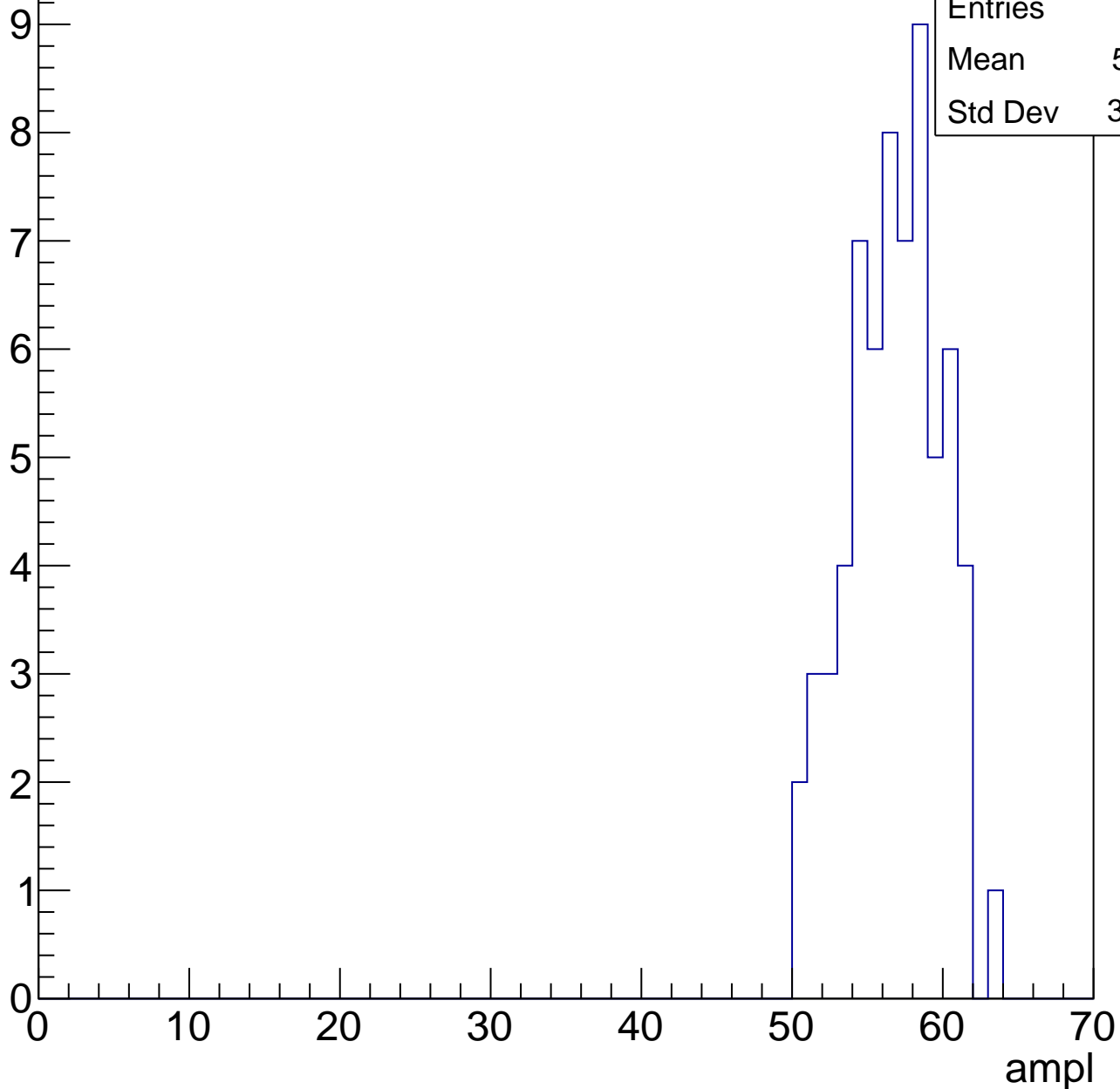


B0L001S, U19-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	56.31
Std Dev	3.018

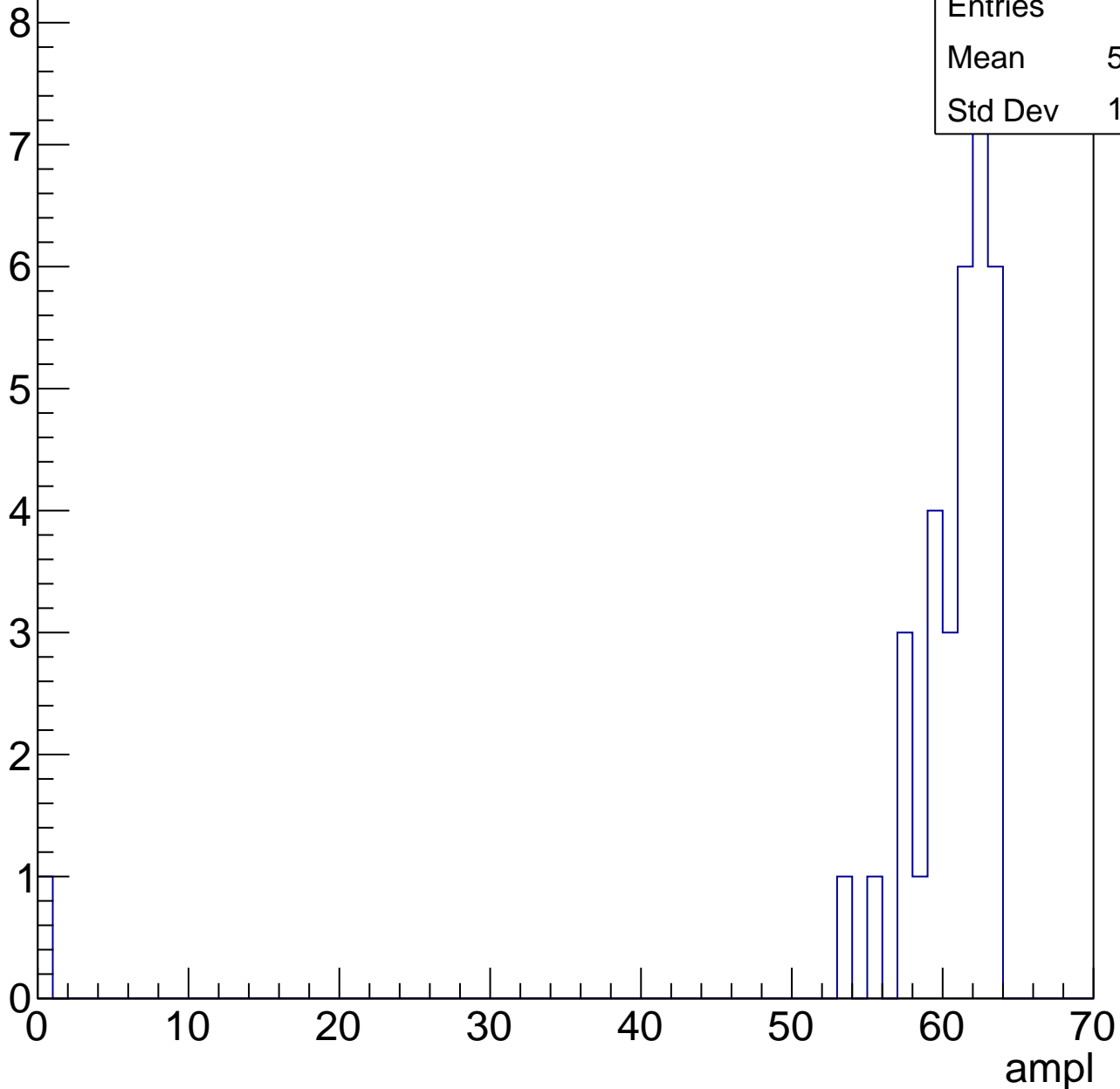


B0L001S, U19-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

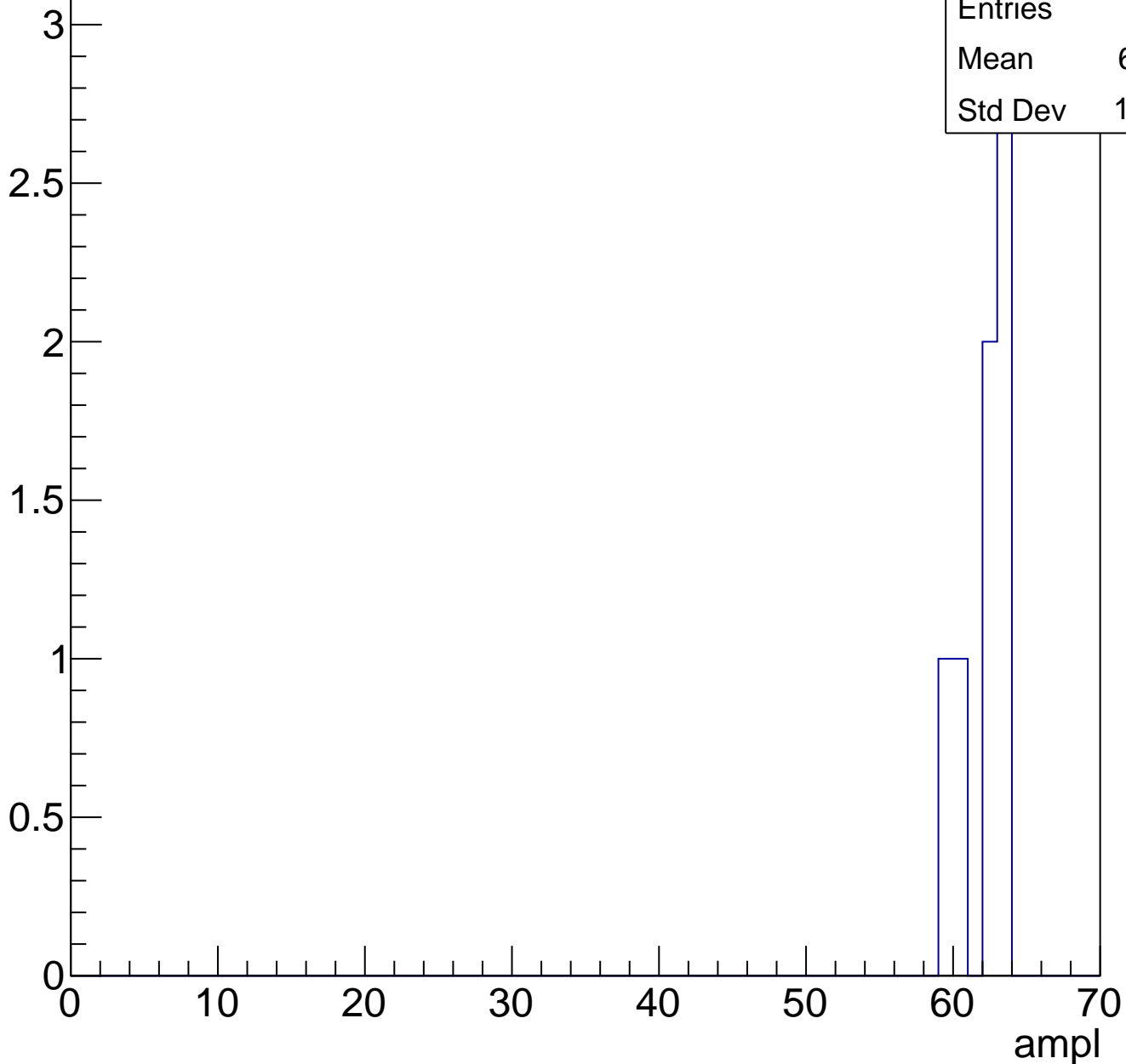
Entries	34
Mean	58.62
Std Dev	10.48



B0L001S, U19-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch88, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	30.62
Std Dev	4.845

Gaus mean : 31.5164

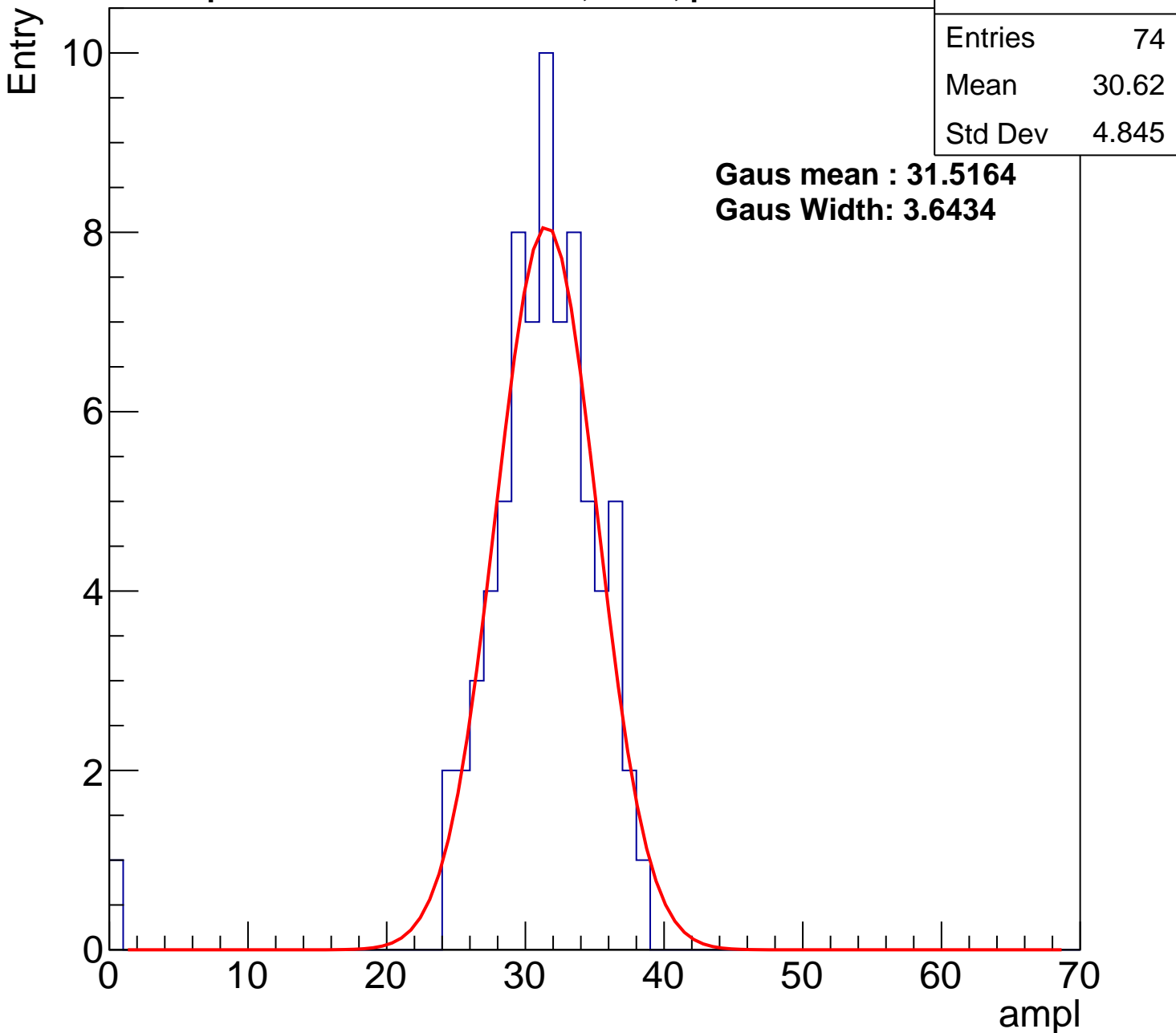
Gaus Width: 3.6434

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch88, adc1

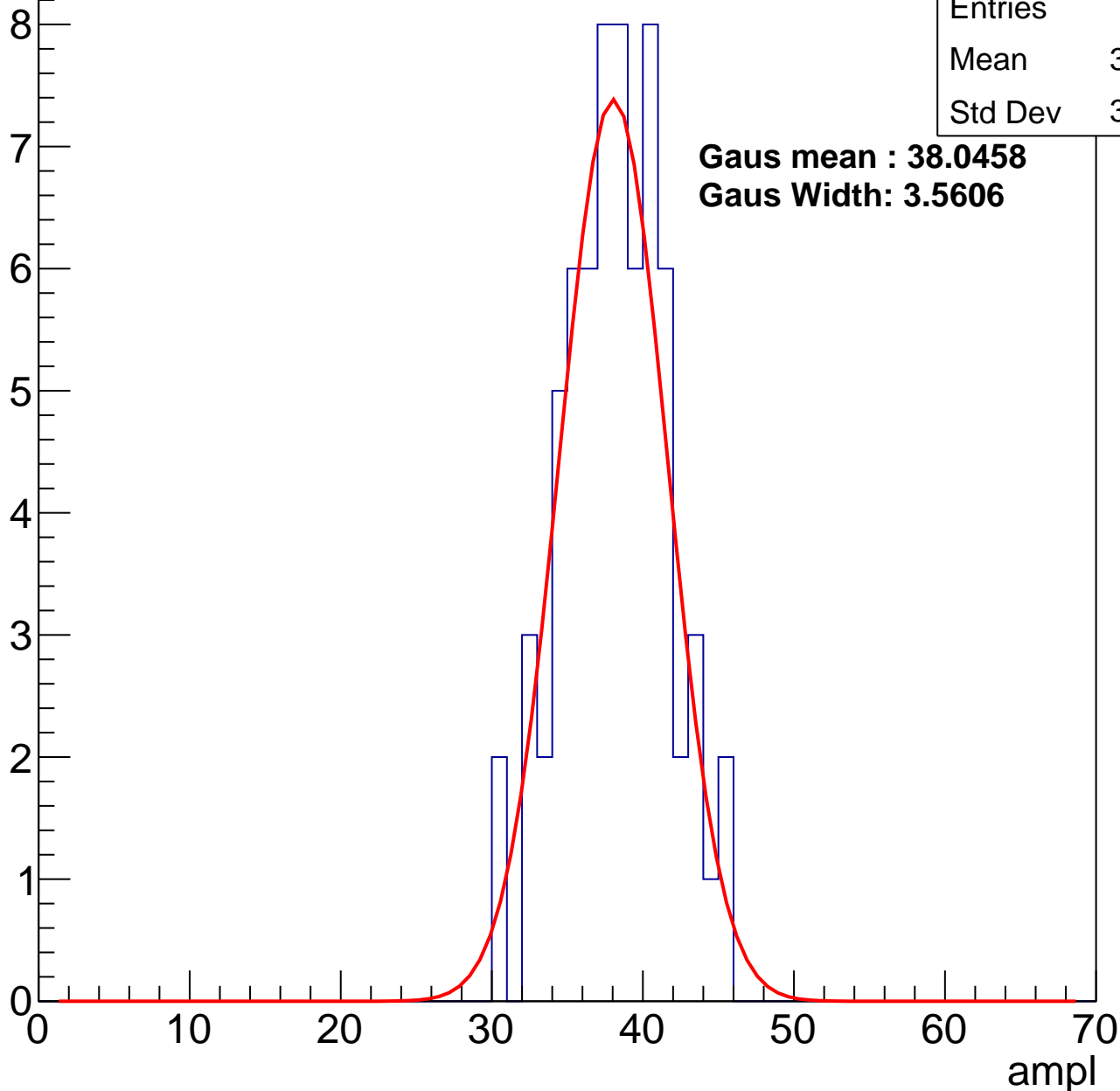
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.72
Std Dev	3.386

Gaus mean : 38.0458

Gaus Width: 3.5606



B0L001S, U19-ch88, adc2

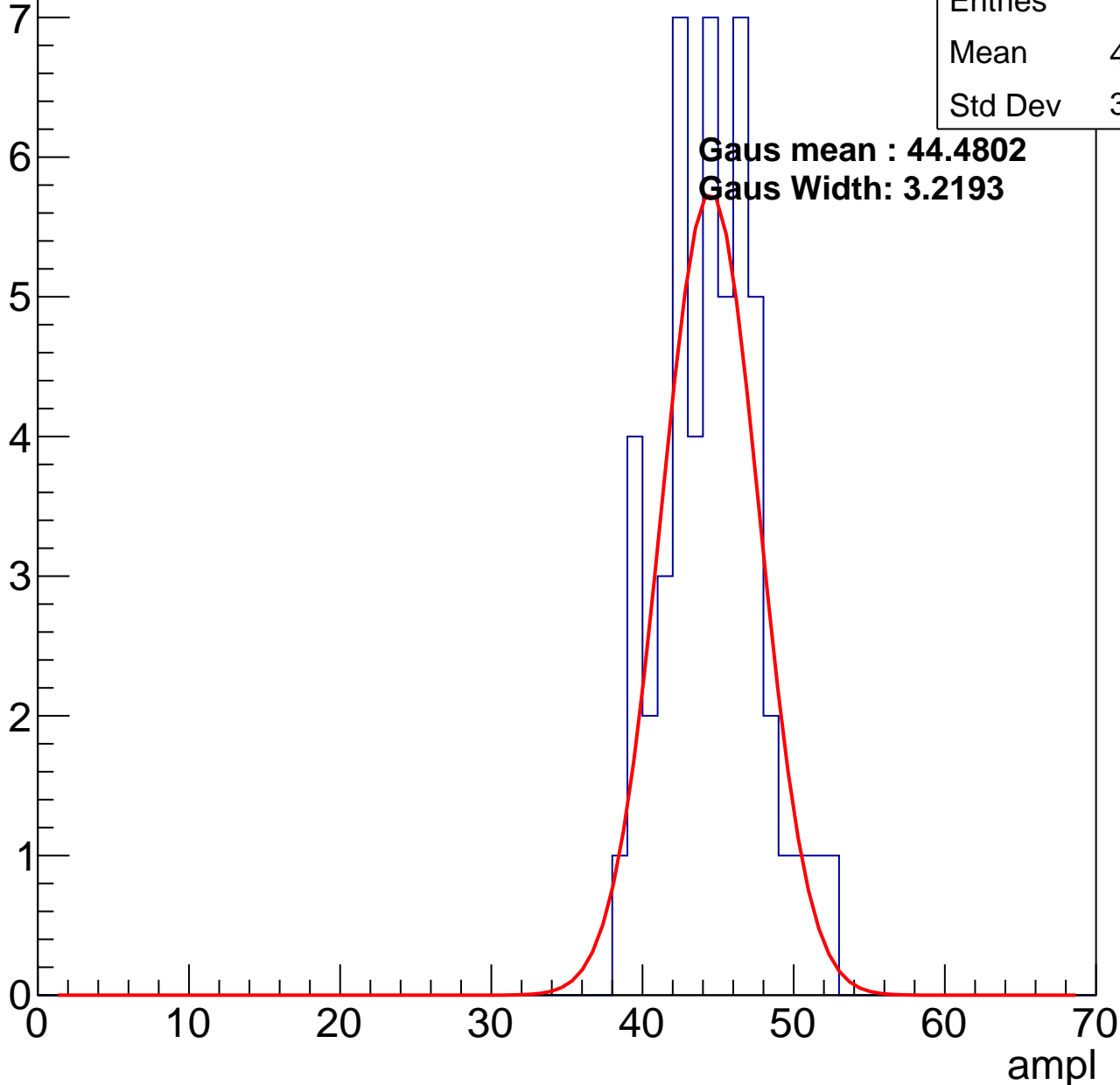
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	44.14
Std Dev	3.162

Gaus mean : 44.4802

Gaus Width: 3.2193

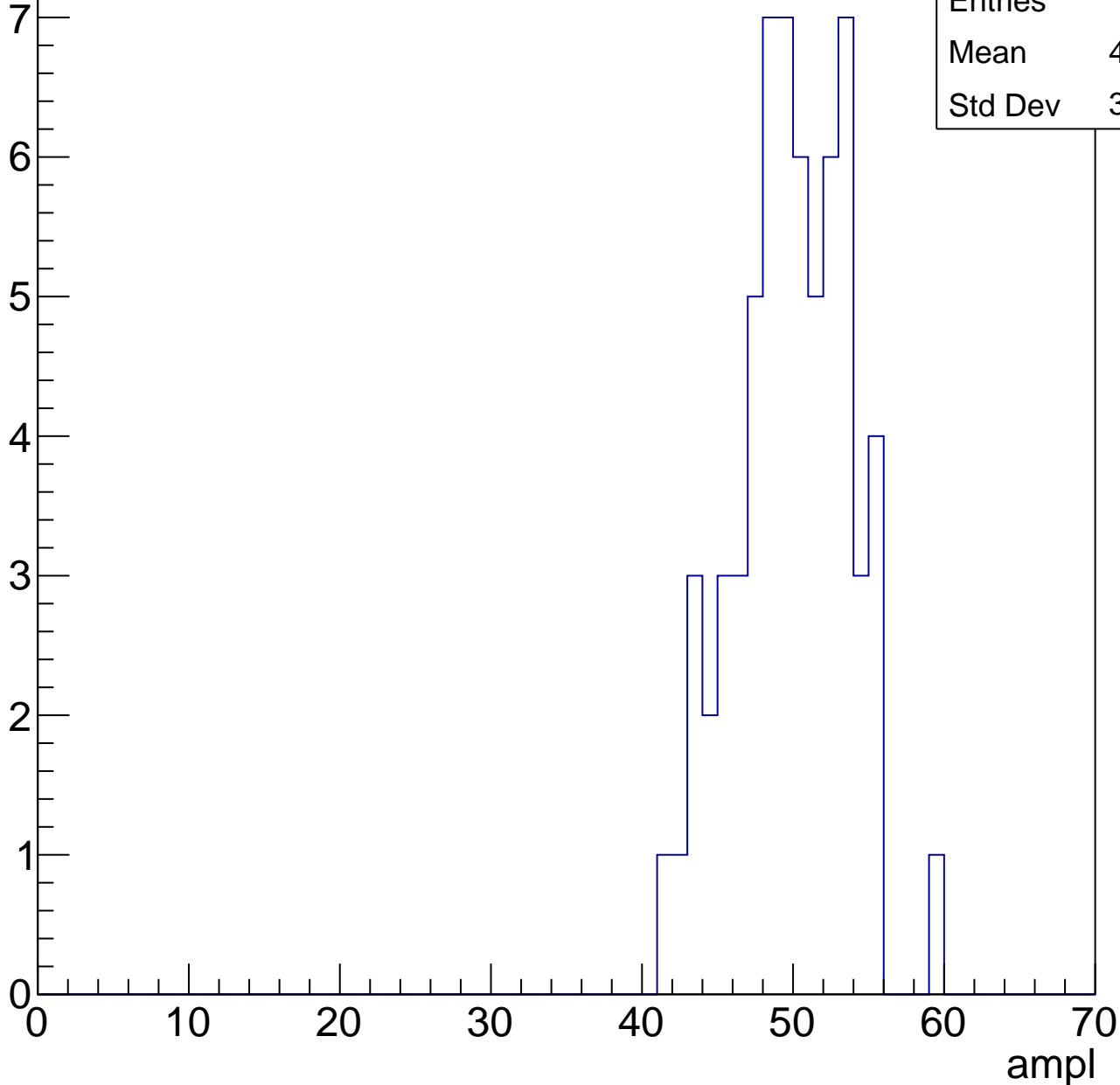


B0L001S, U19-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.47
Std Dev	3.687



B0L001S, U19-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	55.46
Std Dev	7.544

Entry

10

8

6

4

2

0

0

10

20

30

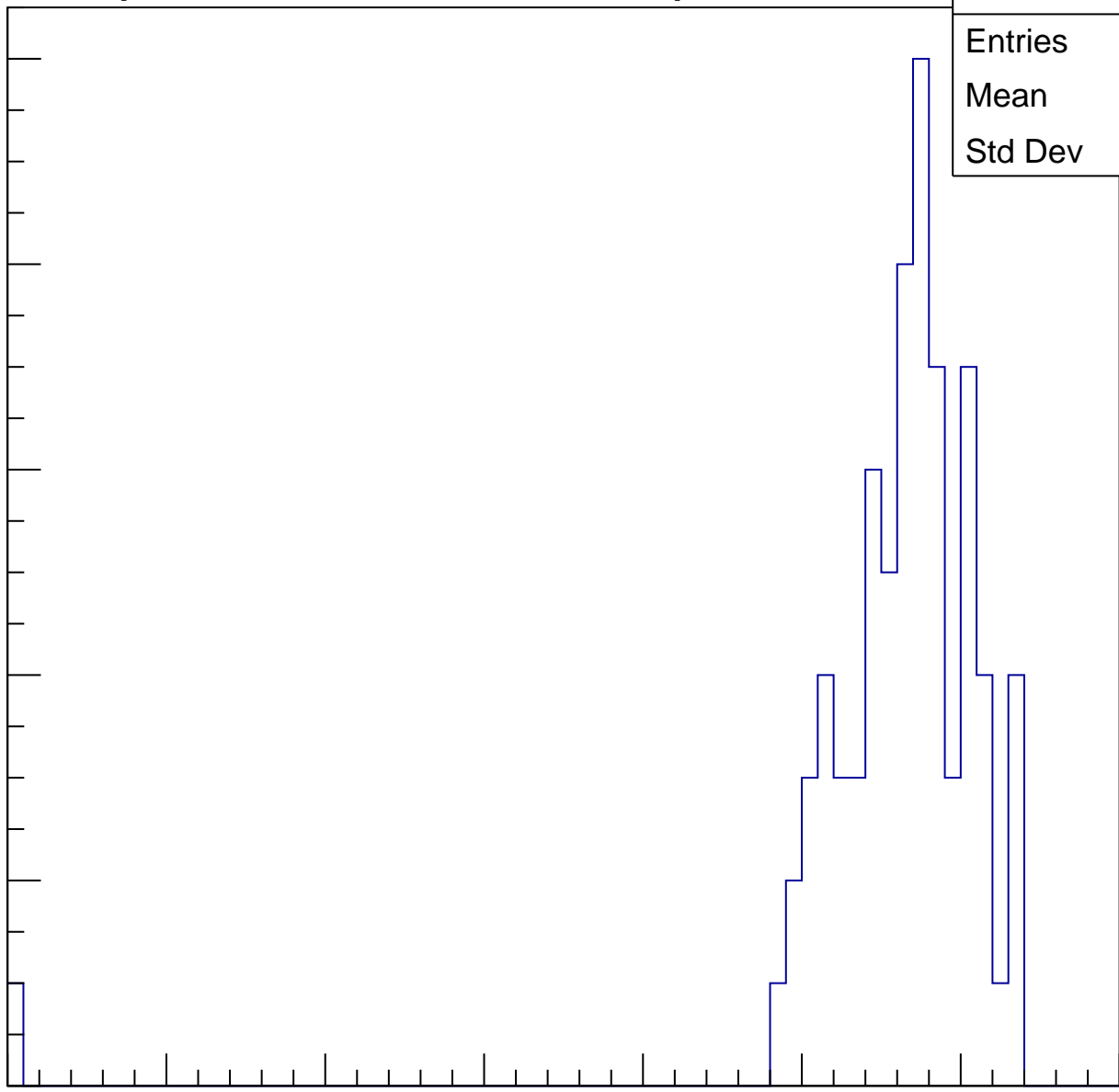
40

50

60

70

ampl

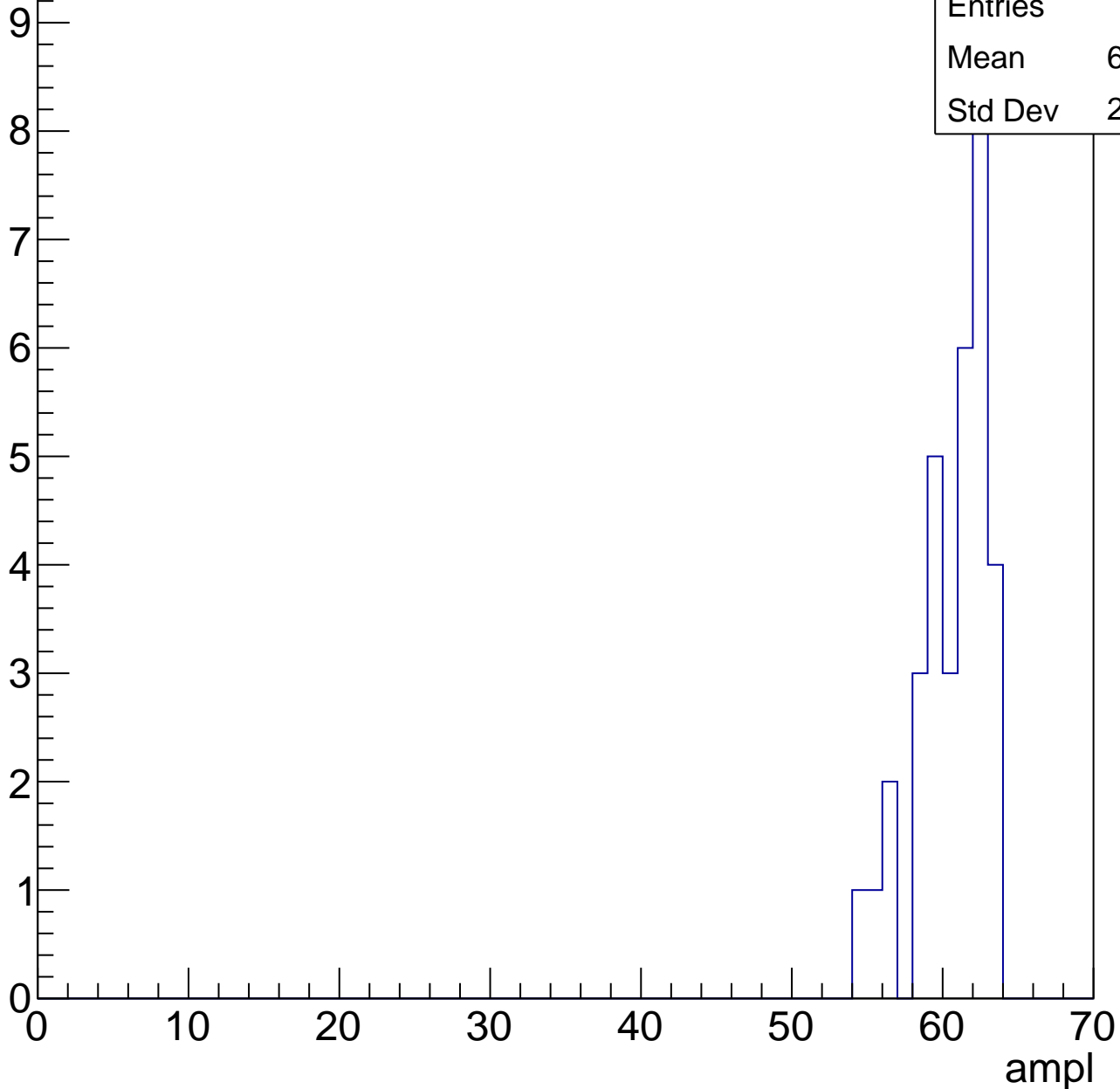


B0L001S, U19-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	60.18
Std Dev	2.345



B0L001S, U19-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch89, adc0

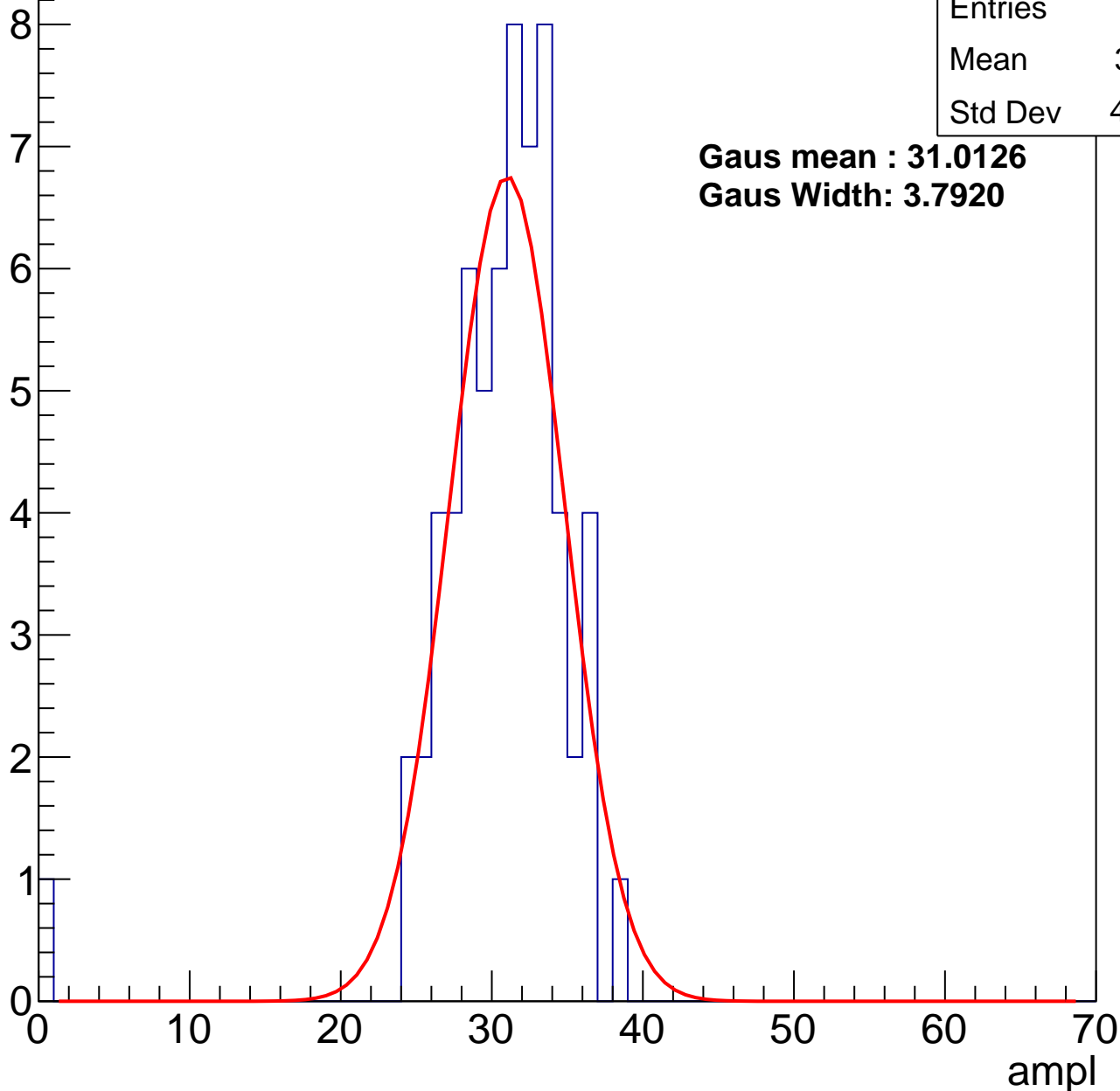
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.11
Std Dev	4.969

Gaus mean : 31.0126

Gaus Width: 3.7920



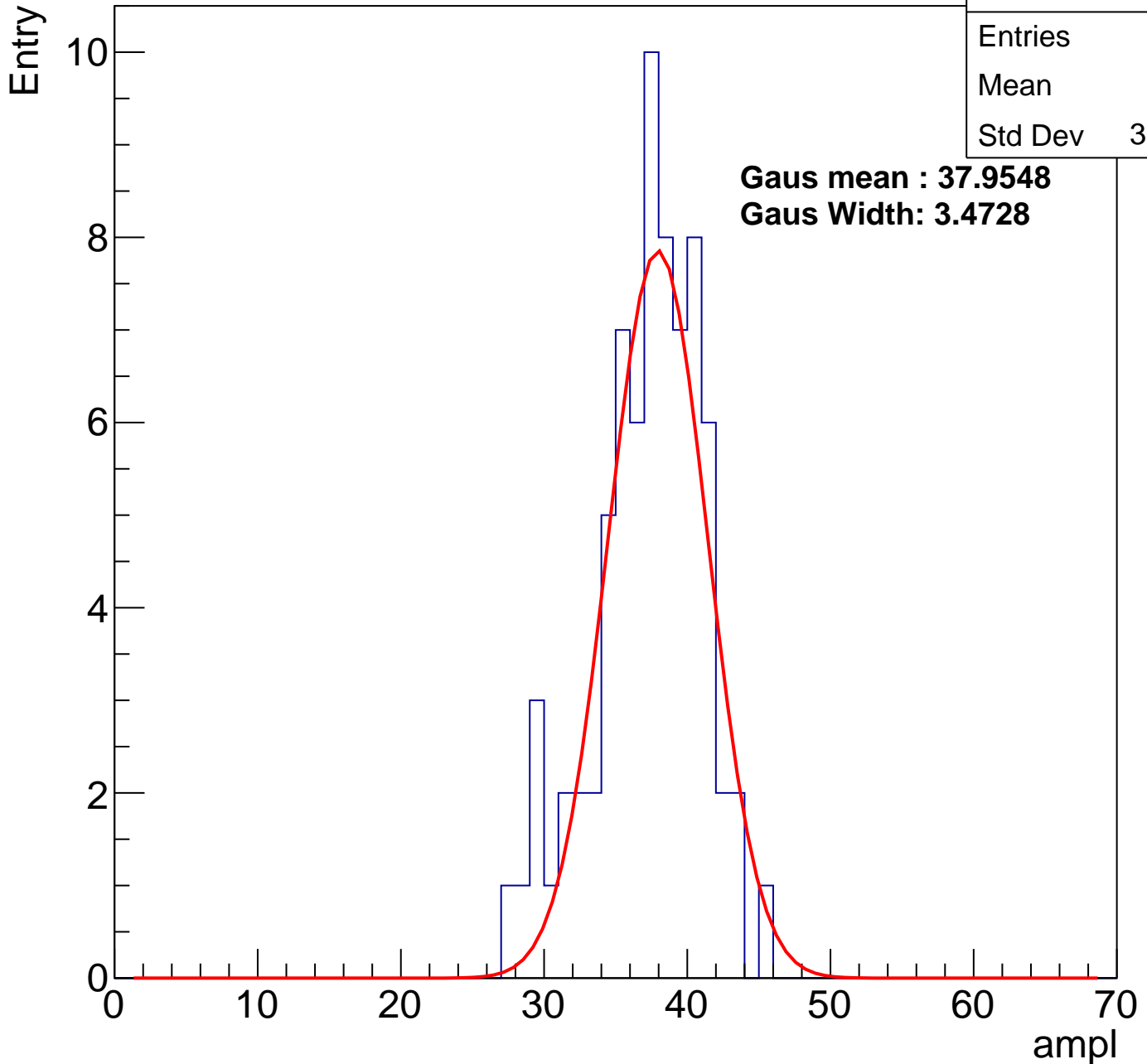
B0L001S, U19-ch89, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	36.8
Std Dev	3.752

Gaus mean : 37.9548

Gaus Width: 3.4728



B0L001S, U19-ch89, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	58
Mean	43.66
Std Dev	3.288

Gaus mean : 43.8890

Gaus Width: 2.6390

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

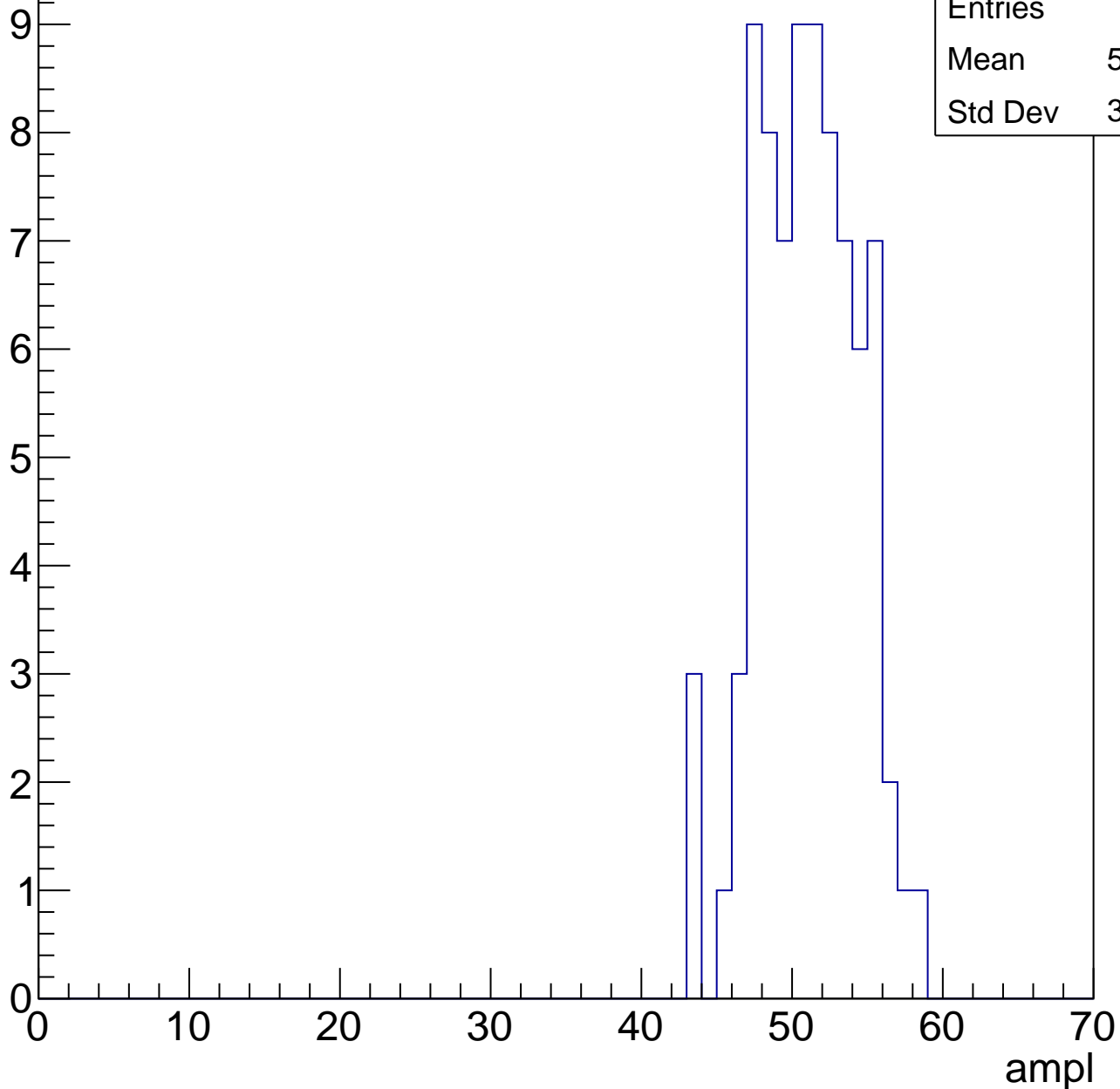


B0L001S, U19-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

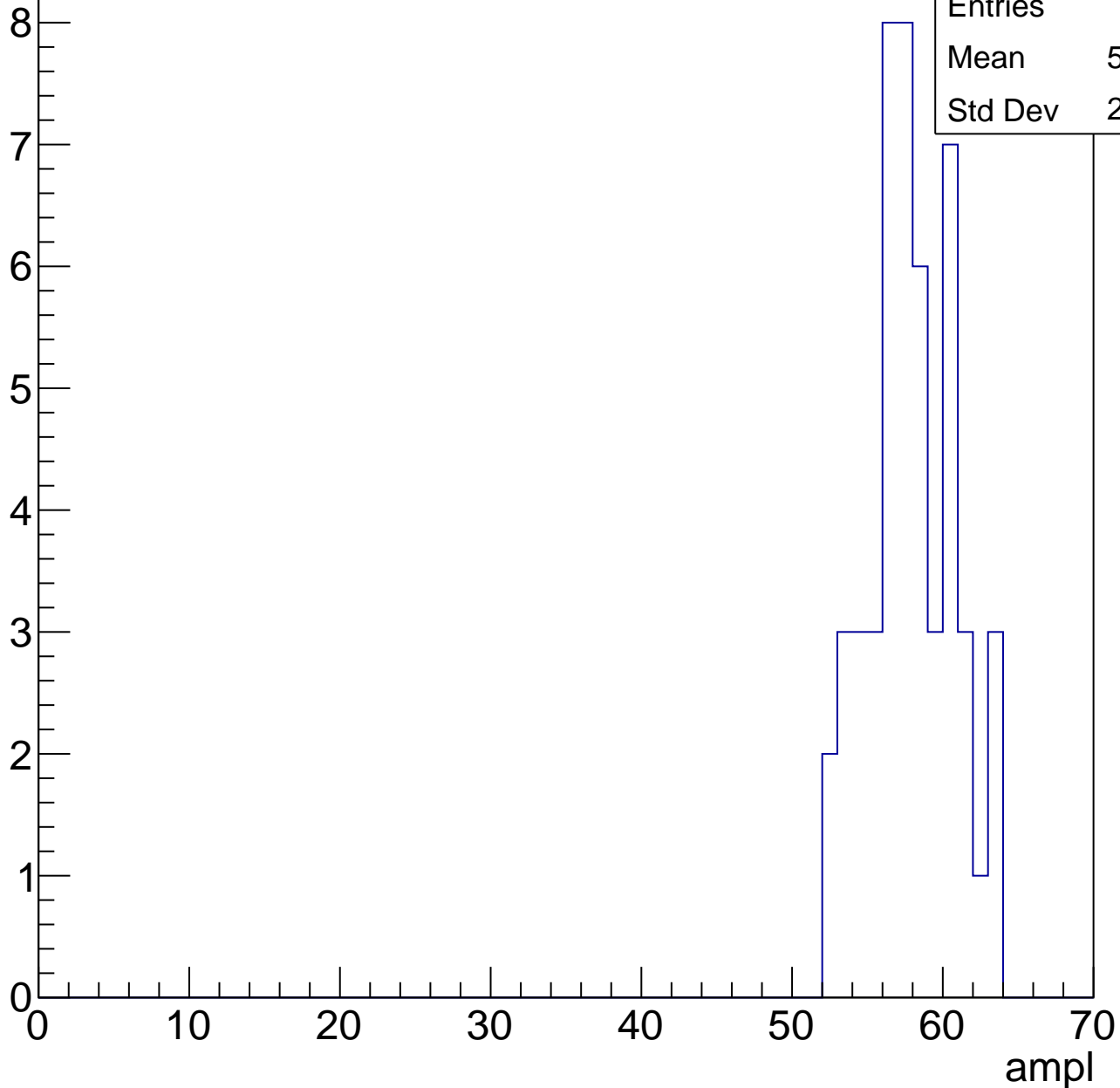
Entries	81
Mean	50.54
Std Dev	3.289



B0L001S, U19-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

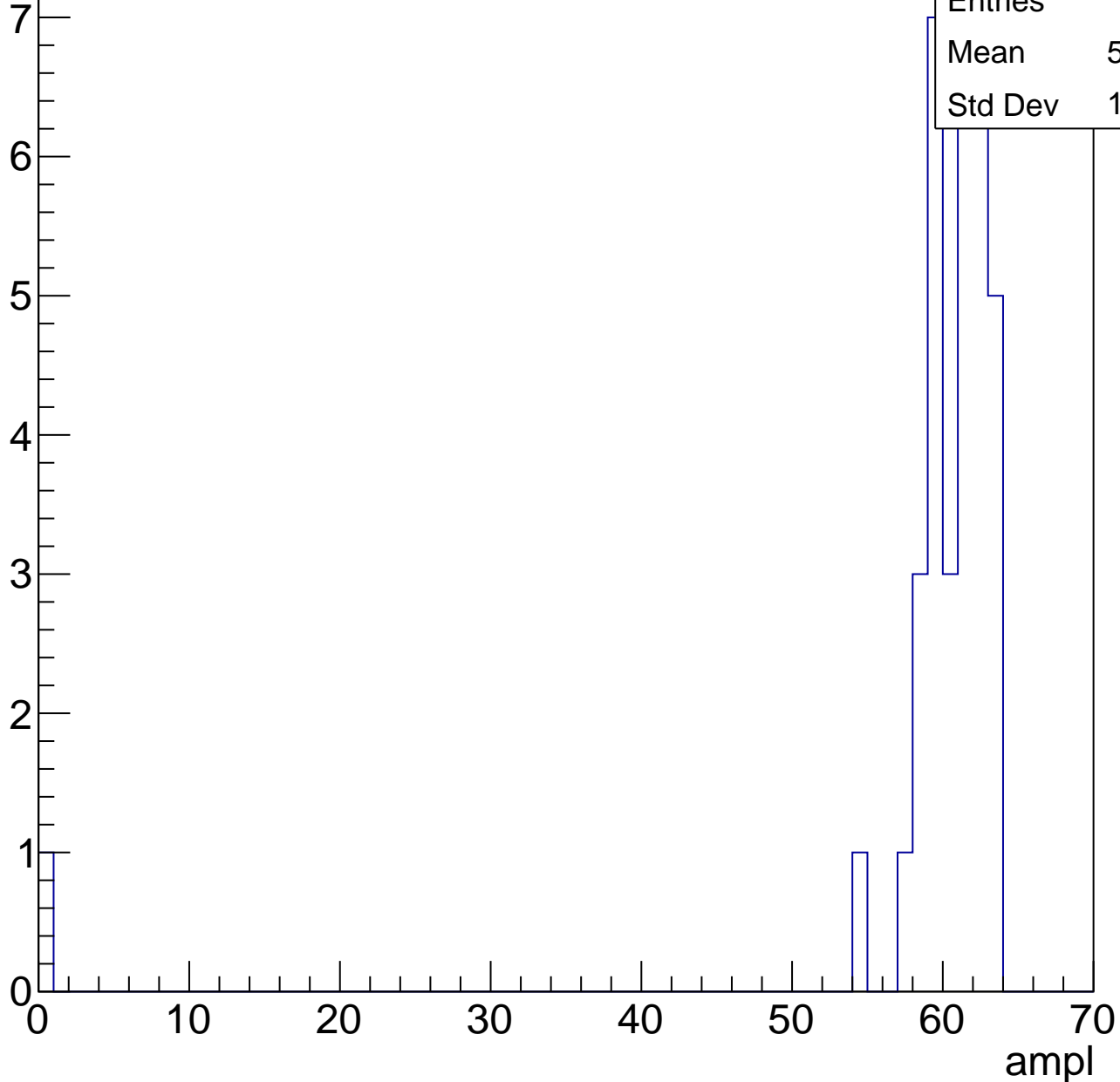


Entries	50
Mean	57.46
Std Dev	2.816

B0L001S, U19-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	62.5
Std Dev	0.5

B0L001S, U19-ch89, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch90, adc0

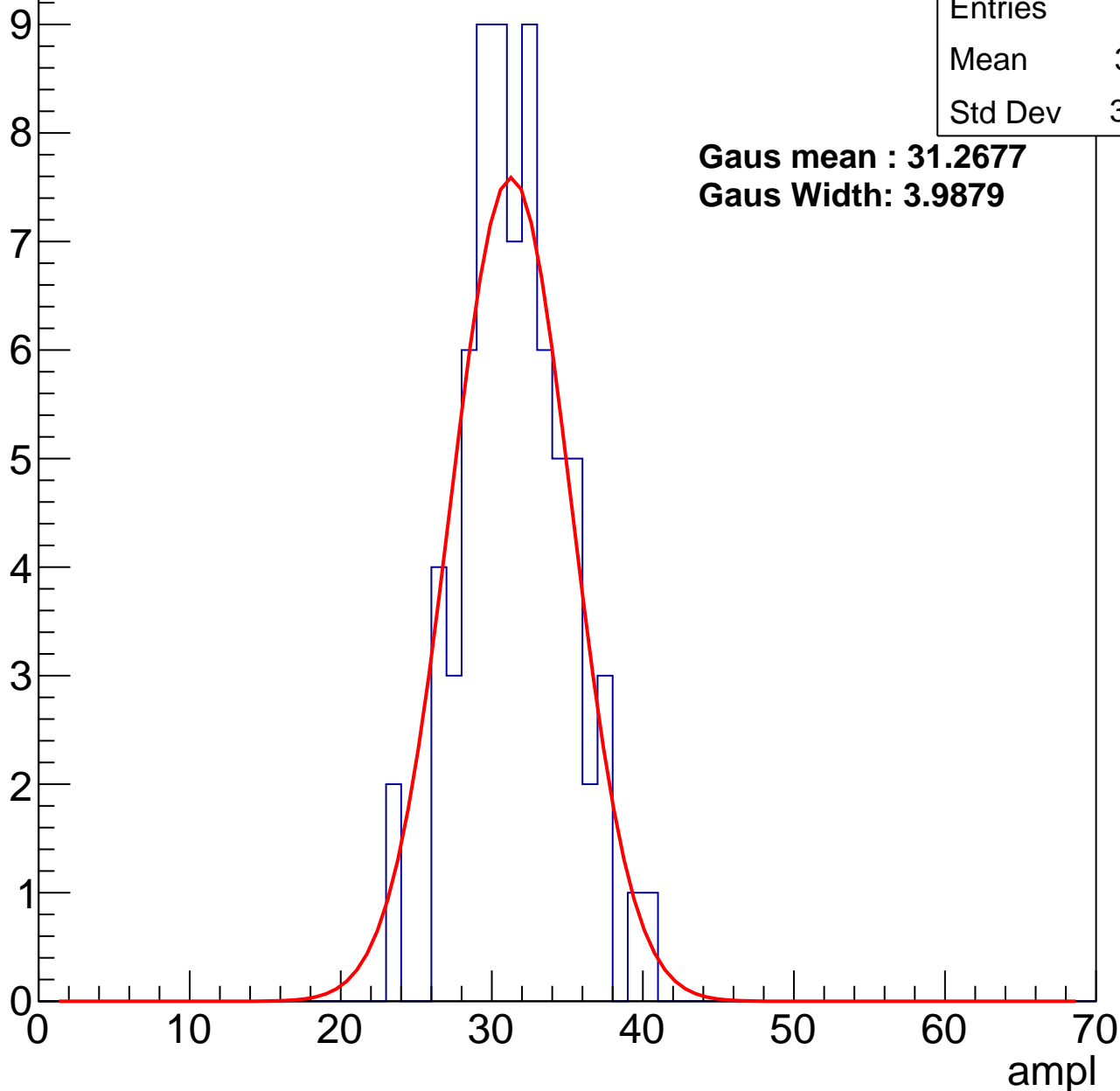
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	31.11
Std Dev	3.406

Gaus mean : 31.2677

Gaus Width: 3.9879



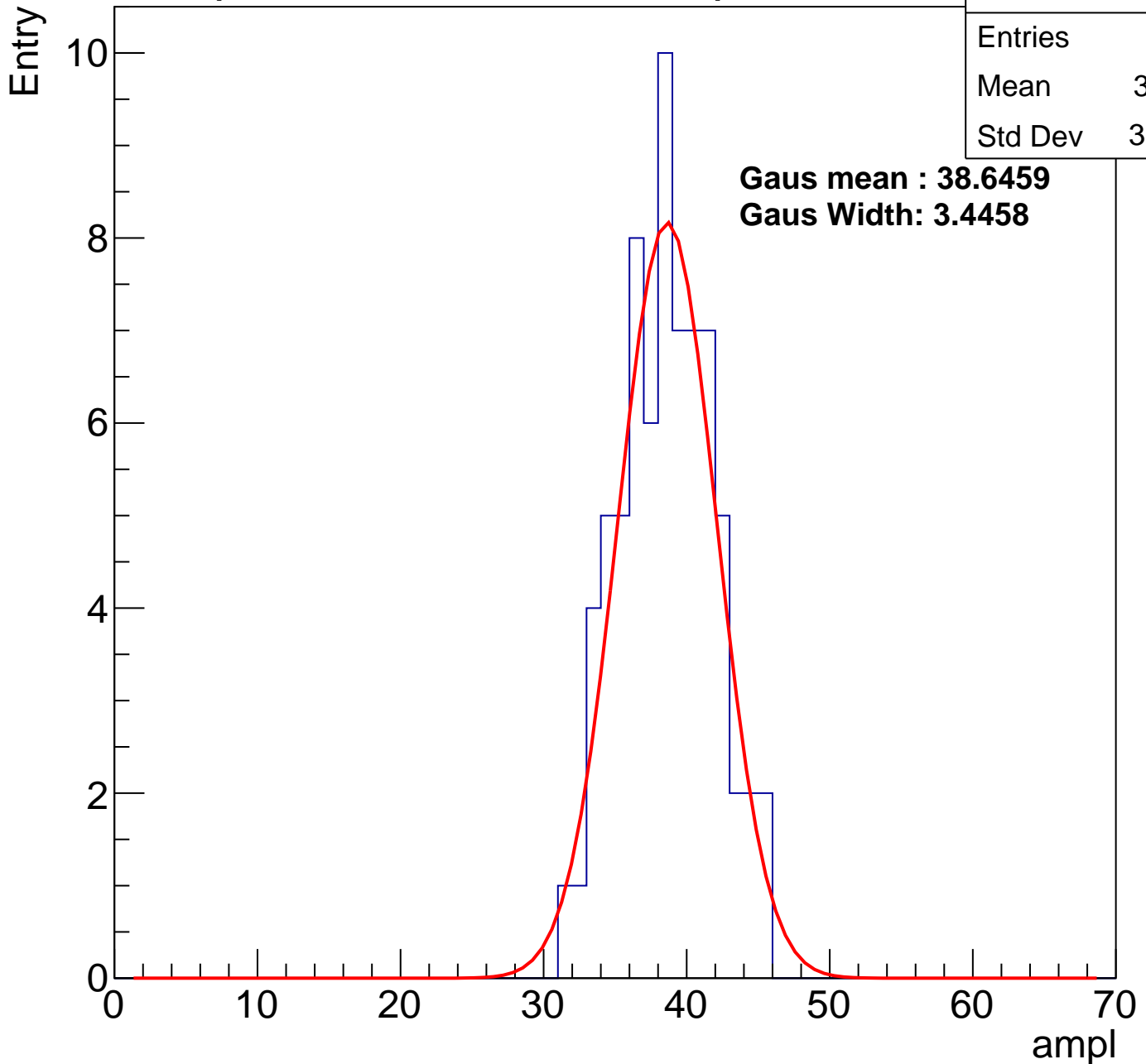
B0L001S, U19-ch90, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	38.11
Std Dev	3.217

Gaus mean : 38.6459

Gaus Width: 3.4458



B0L001S, U19-ch90, adc2

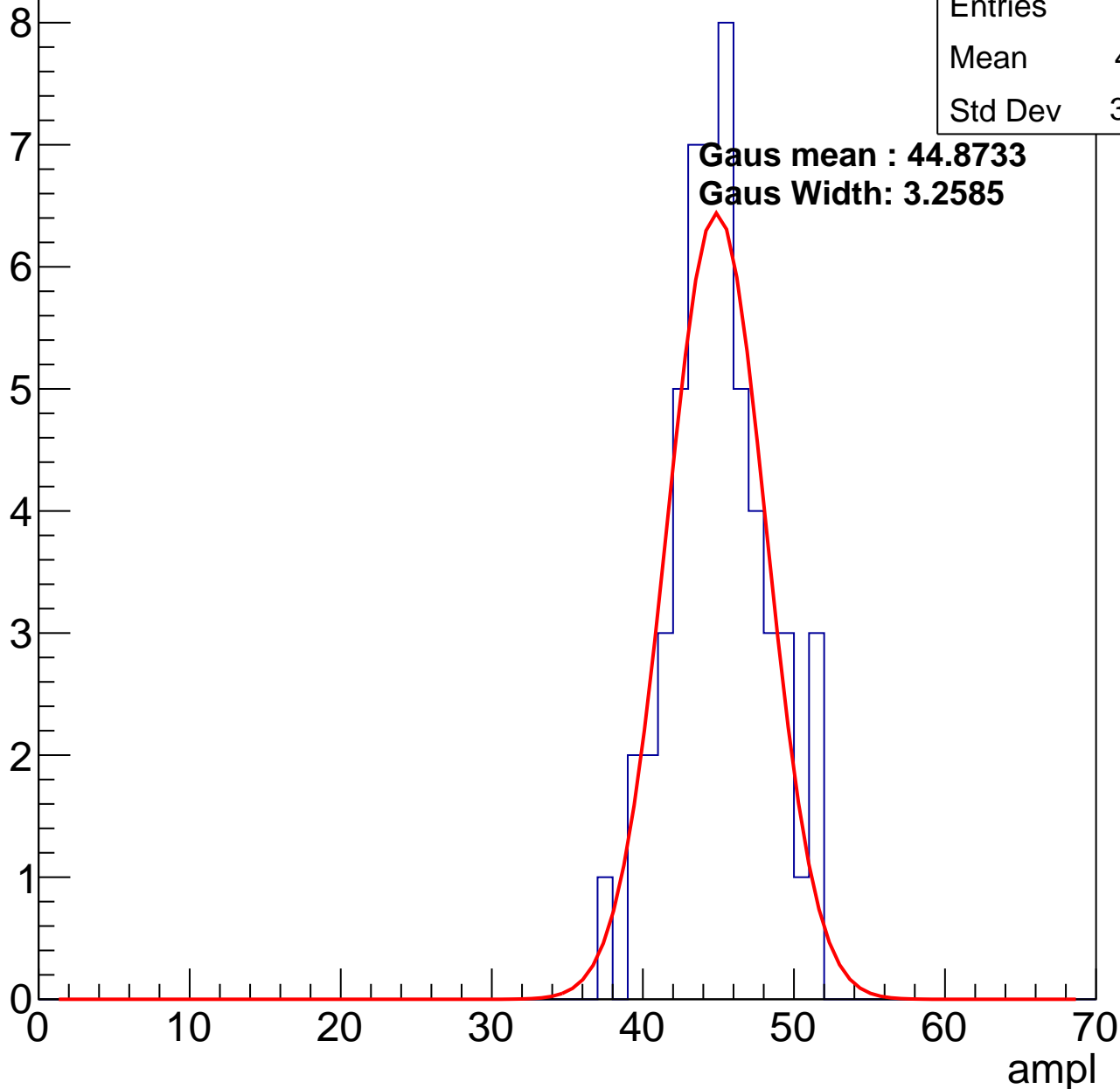
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	44.61
Std Dev	3.147

Gaus mean : 44.8733

Gaus Width: 3.2585

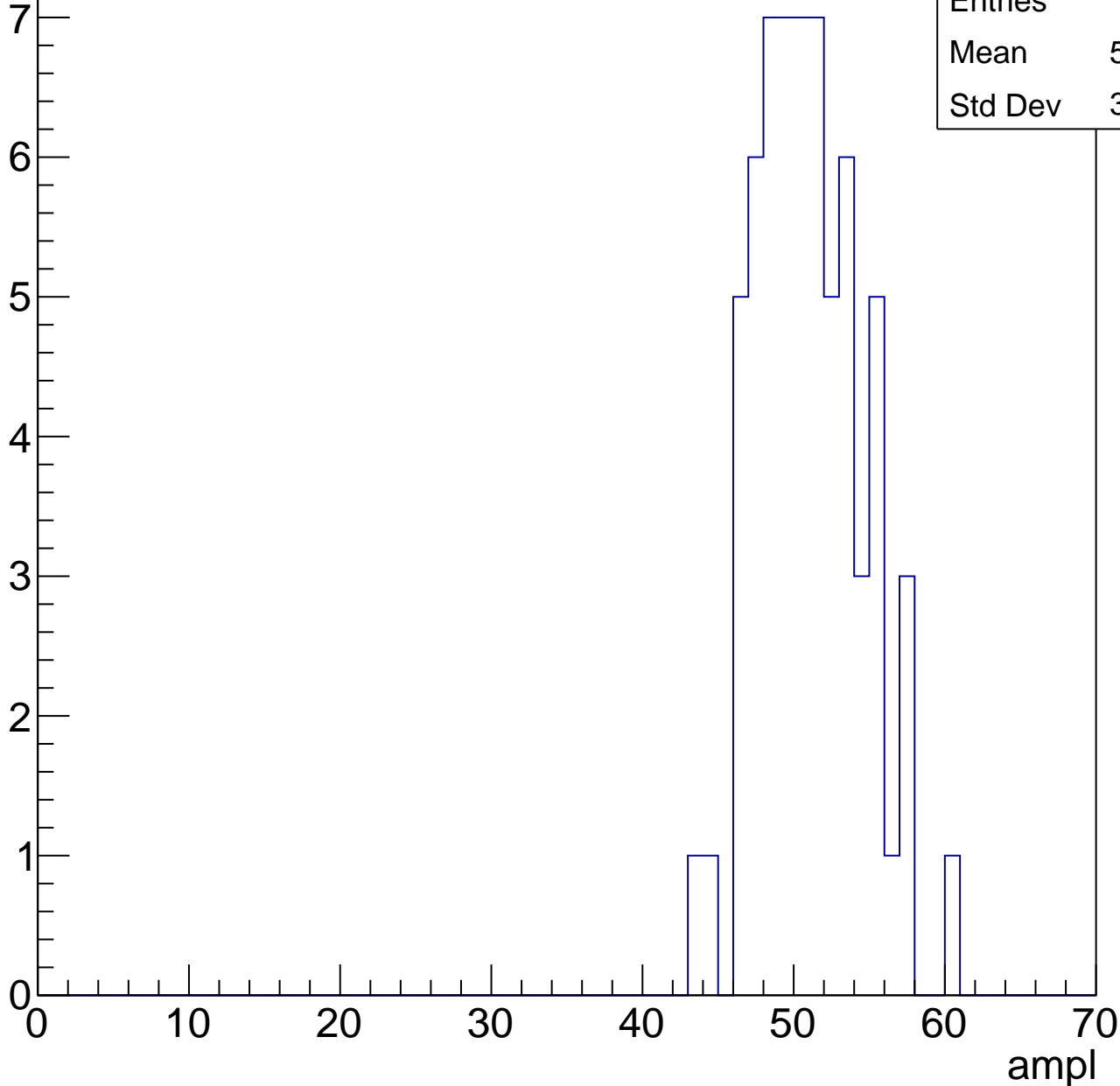


B0L001S, U19-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

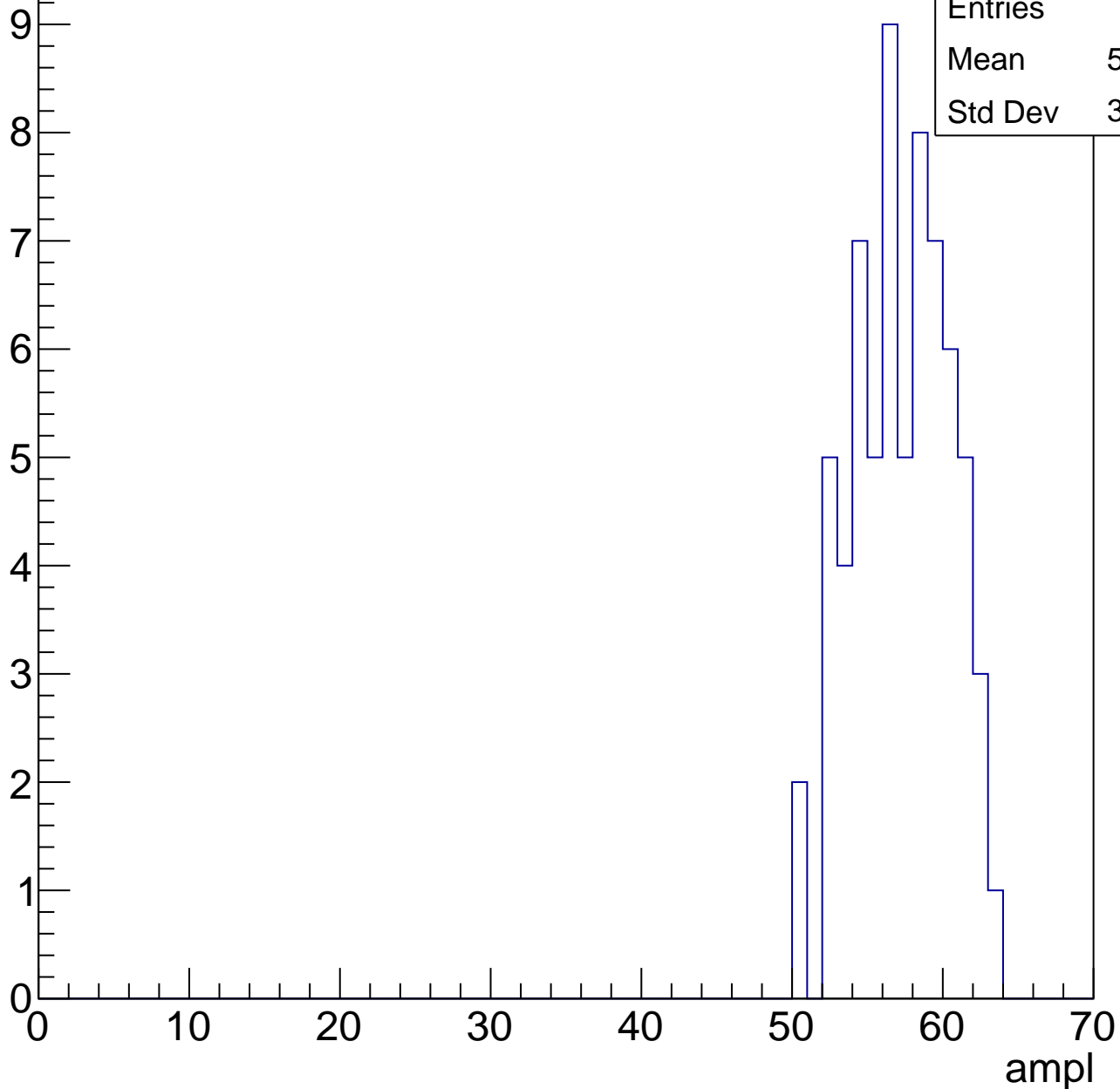
Entries	65
Mean	50.57
Std Dev	3.442



B0L001S, U19-ch90, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

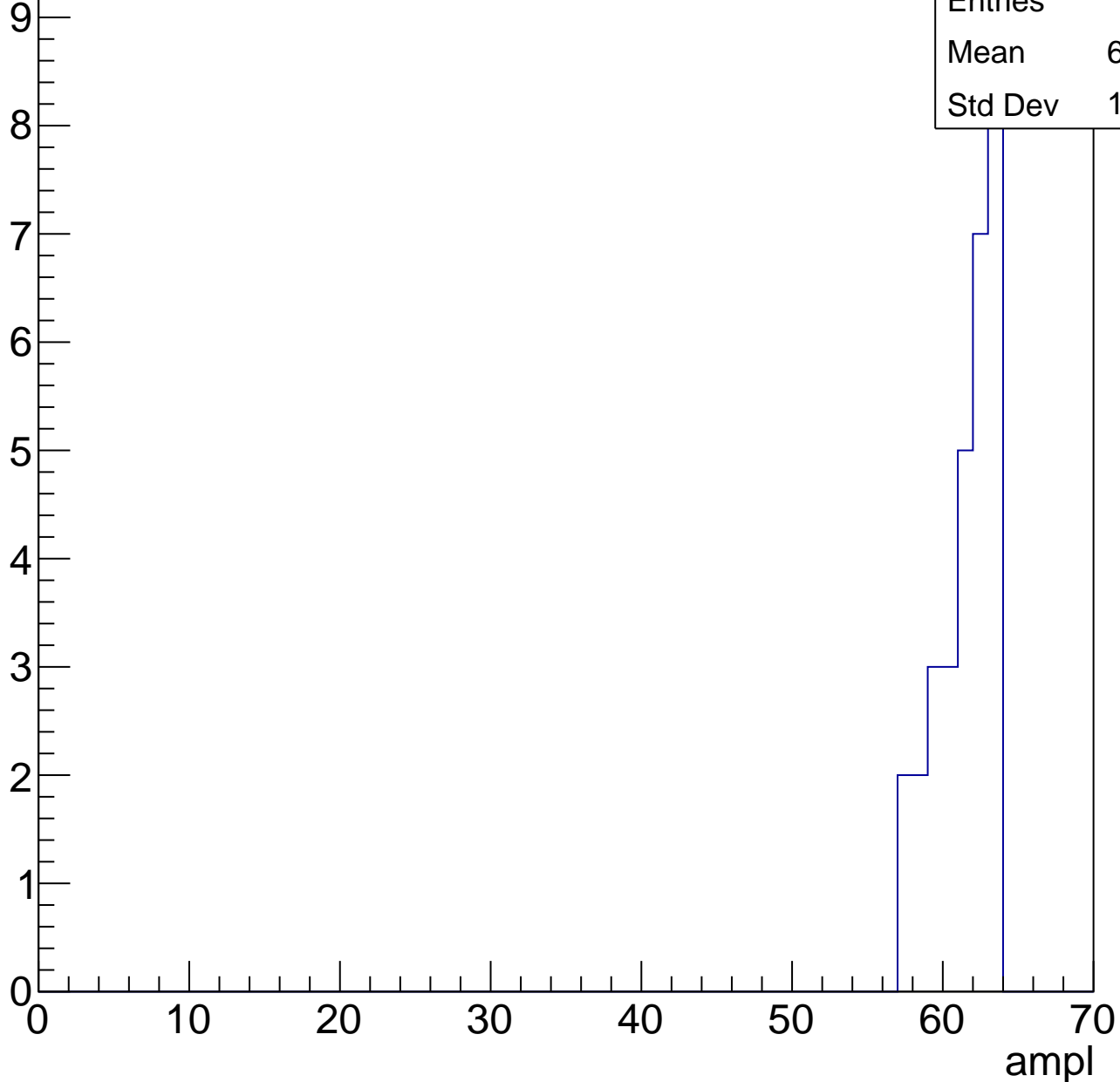


Entries	67
Mean	56.79
Std Dev	3.132

B0L001S, U19-ch90, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U19-ch91, adc0

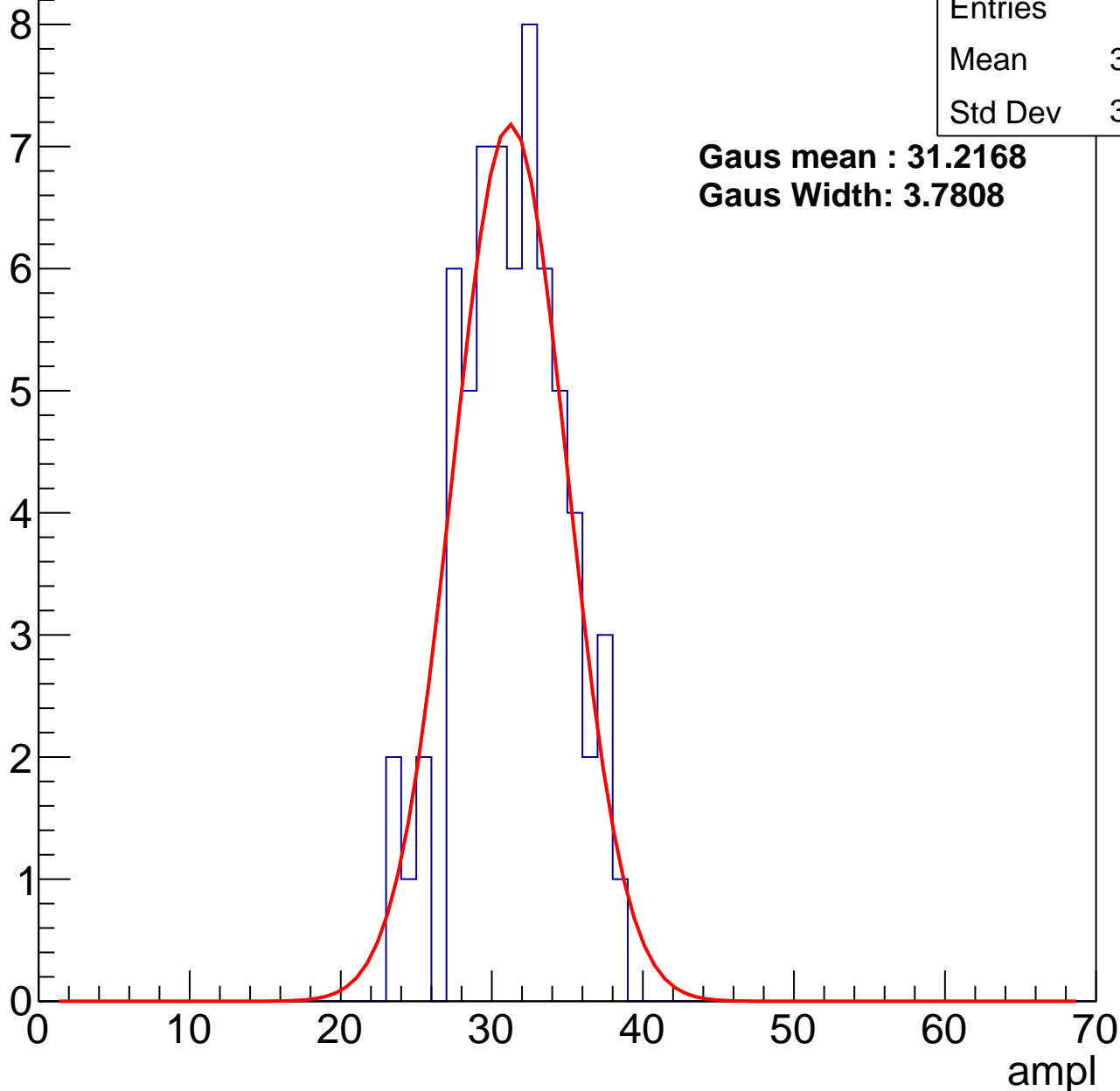
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.86
Std Dev	3.446

Gaus mean : 31.2168

Gaus Width: 3.7808



B0L001S, U19-ch91, adc1

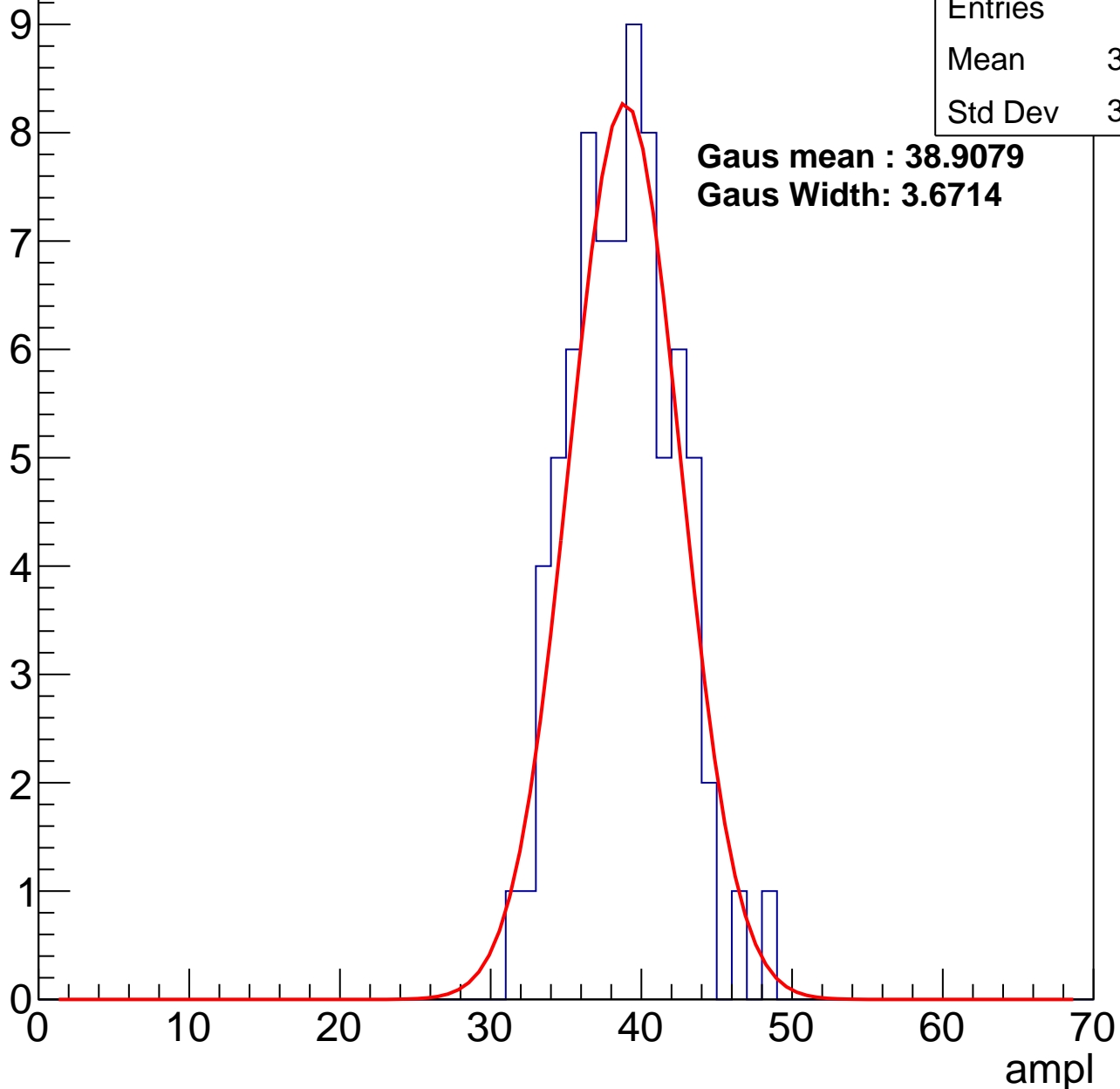
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	38.33
Std Dev	3.427

Gaus mean : 38.9079

Gaus Width: 3.6714



B0L001S, U19-ch91, adc2

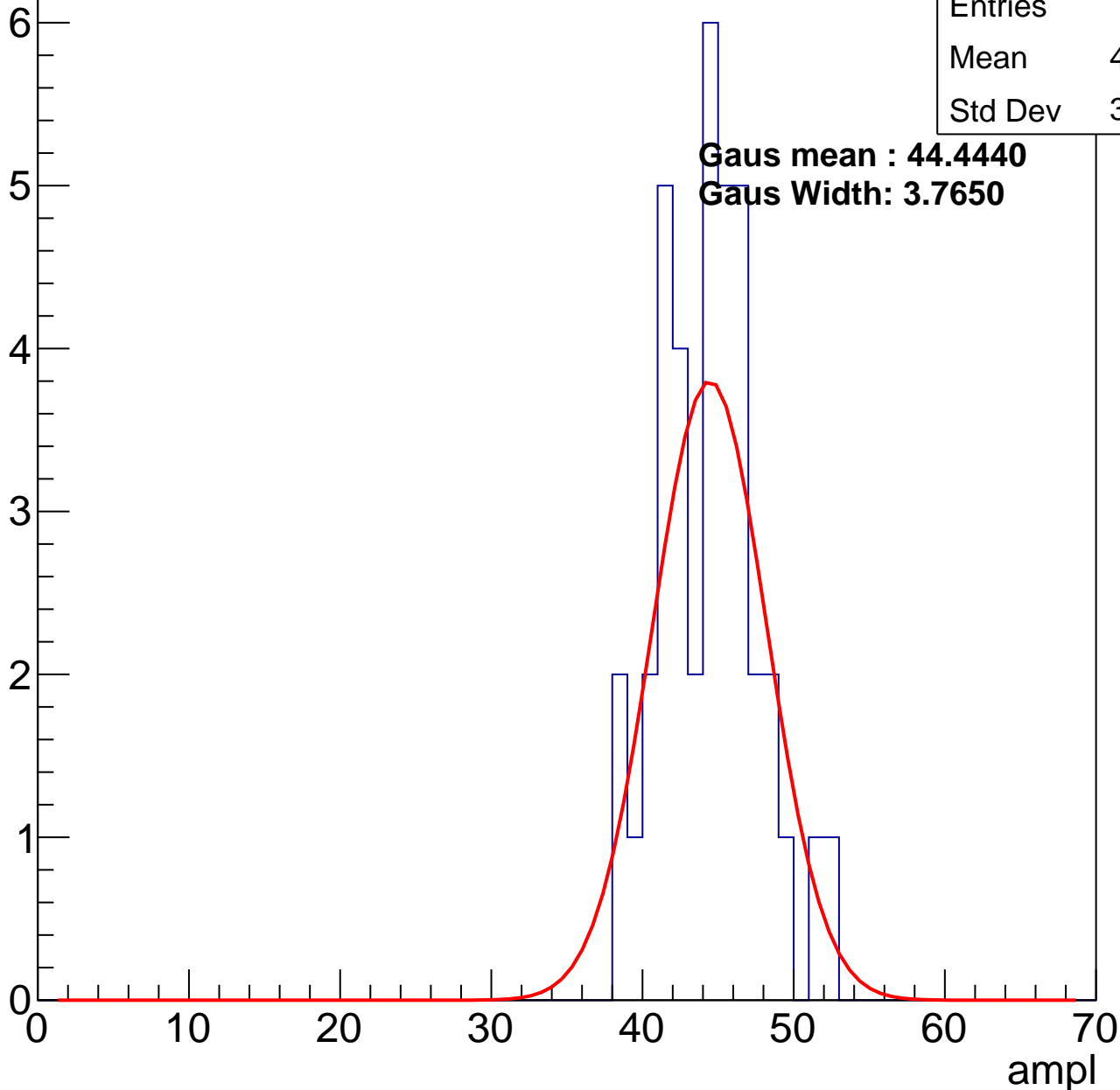
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	43.97
Std Dev	3.222

Gaus mean : 44.4440

Gaus Width: 3.7650

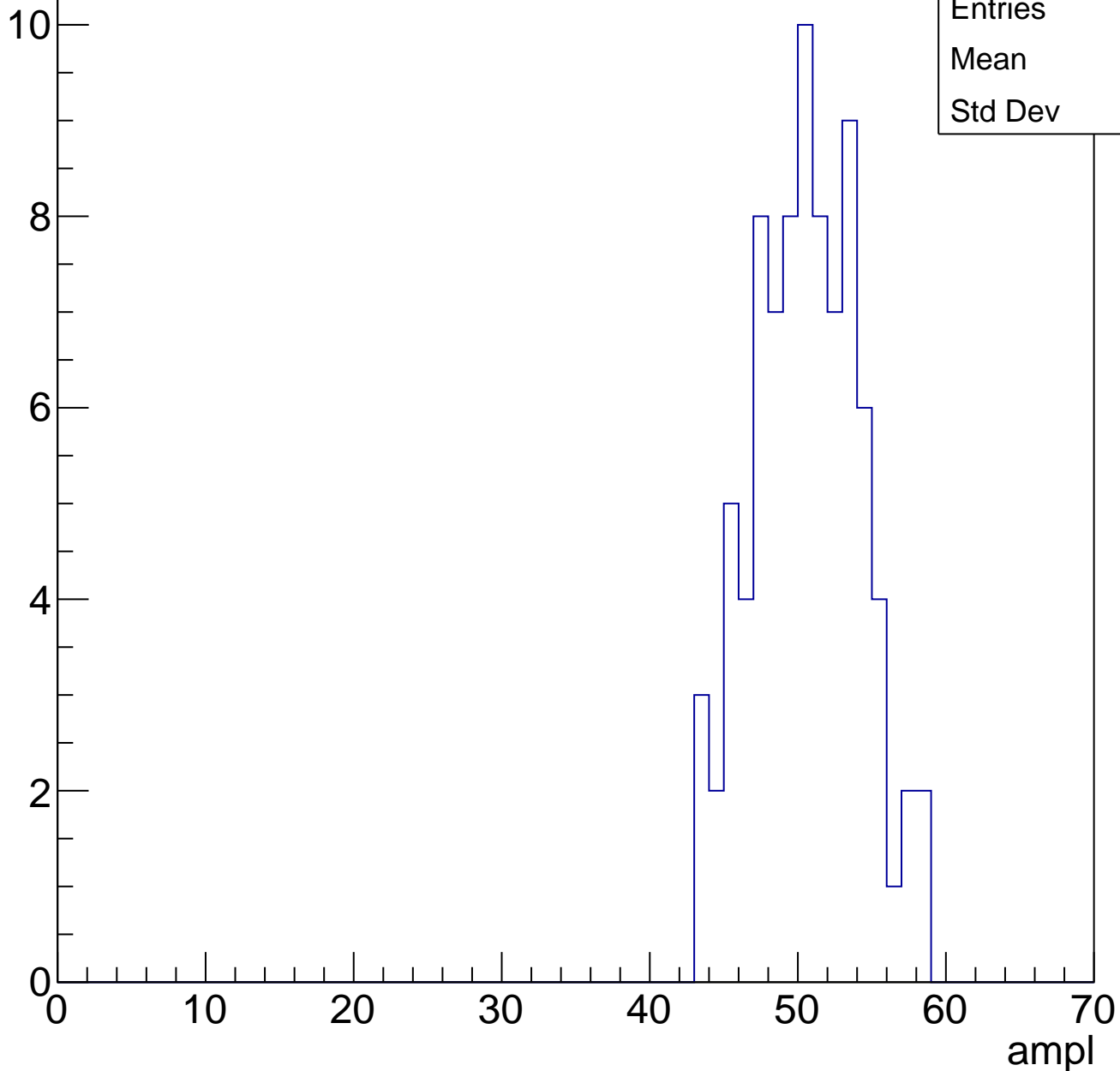


B0L001S, U19-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	50.1
Std Dev	3.56

Entry

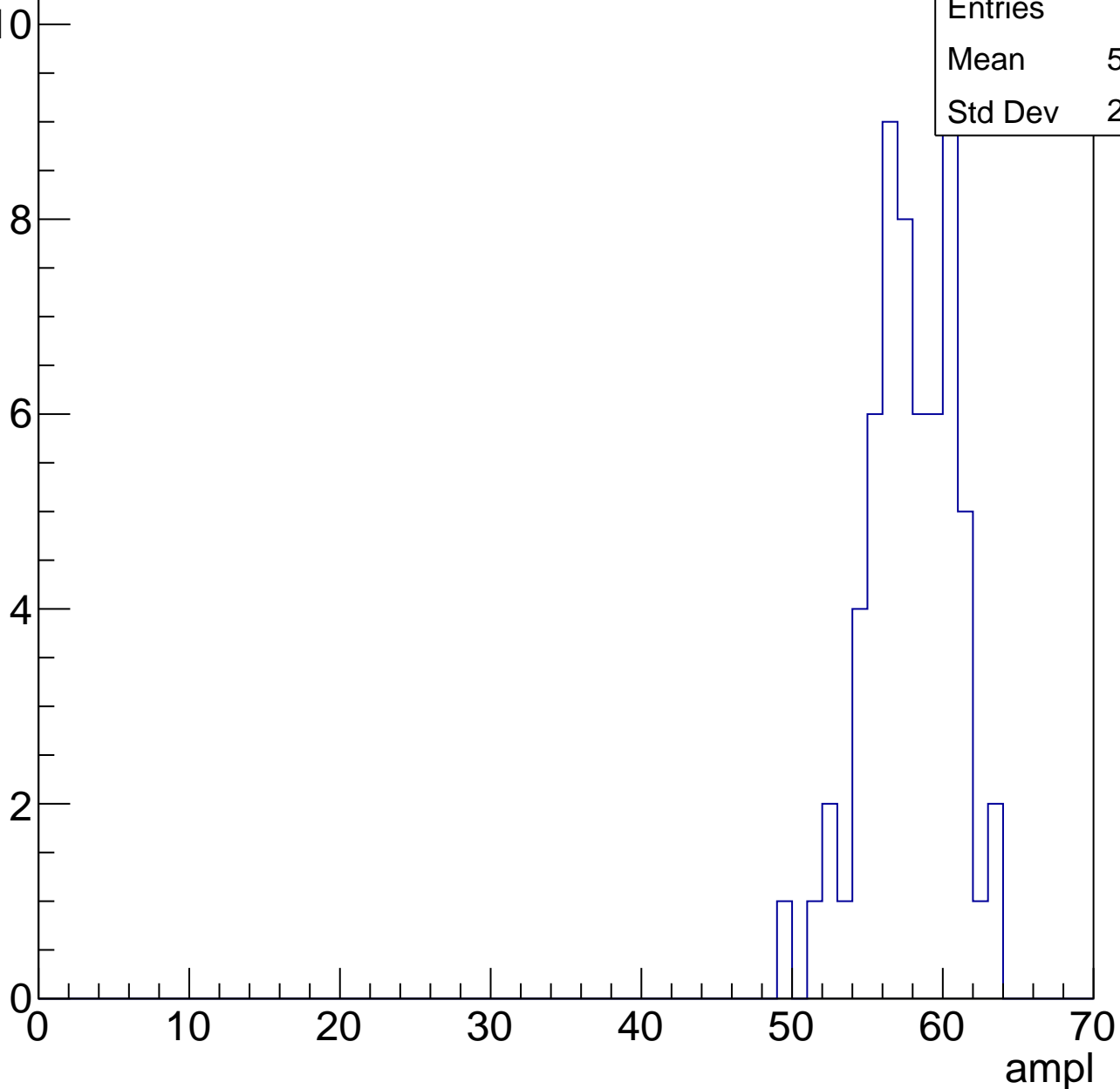


B0L001S, U19-ch91, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	57.39
Std Dev	2.904

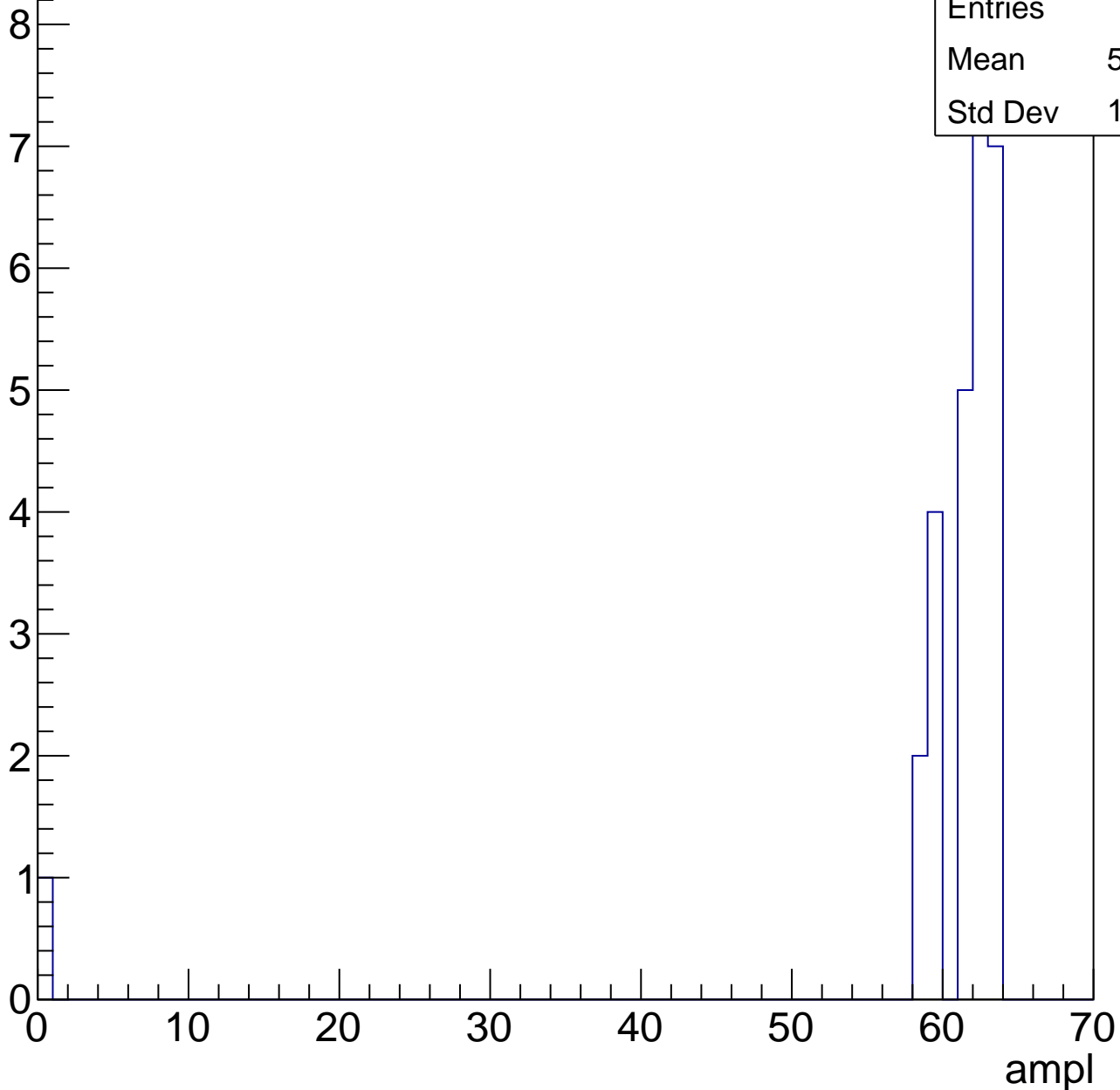


B0L001S, U19-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	59.04
Std Dev	11.69



B0L001S, U19-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch92, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	29.68
Std Dev	4.577

Gaus mean : 30.3230

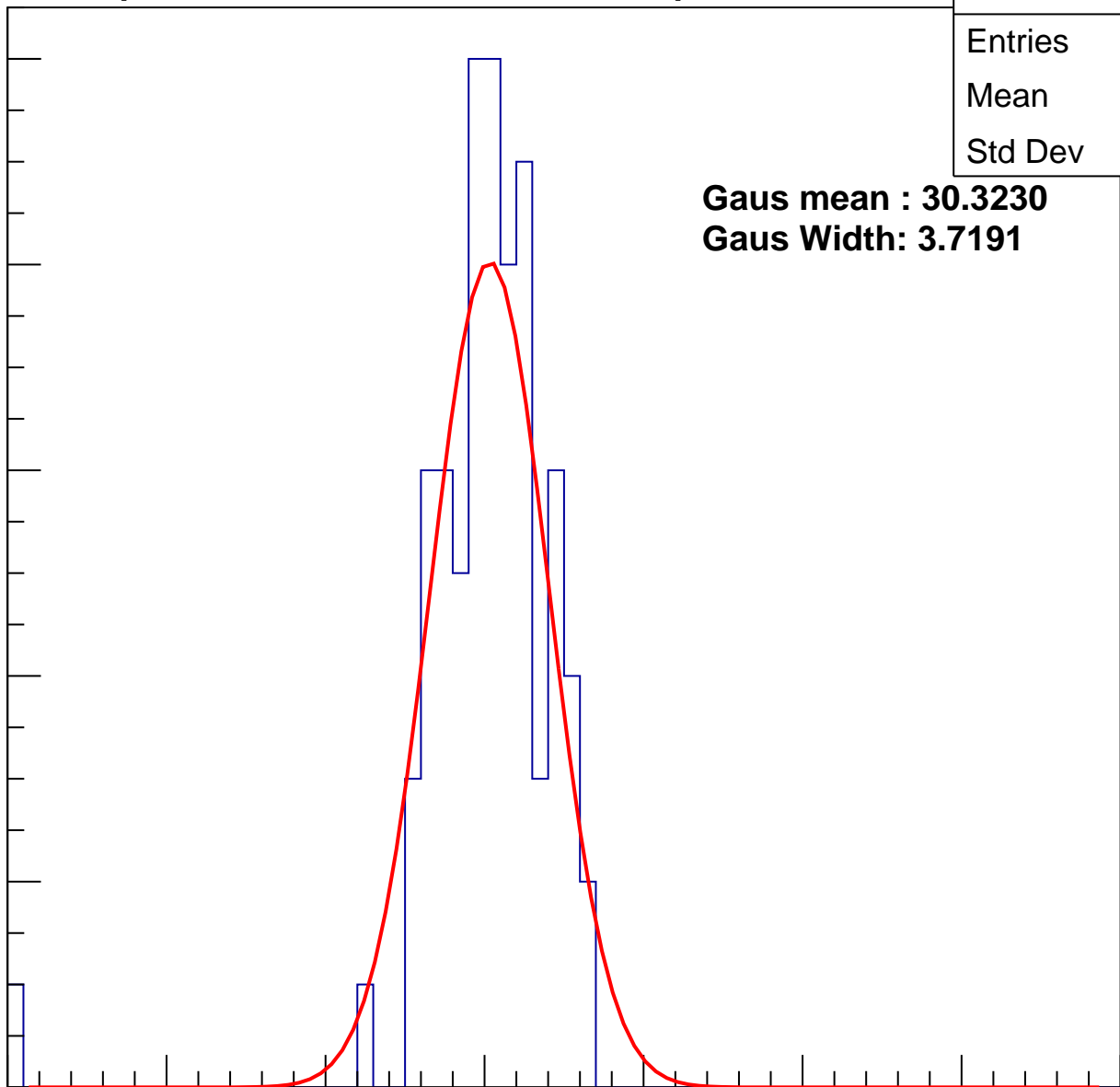
Gaus Width: 3.7191

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



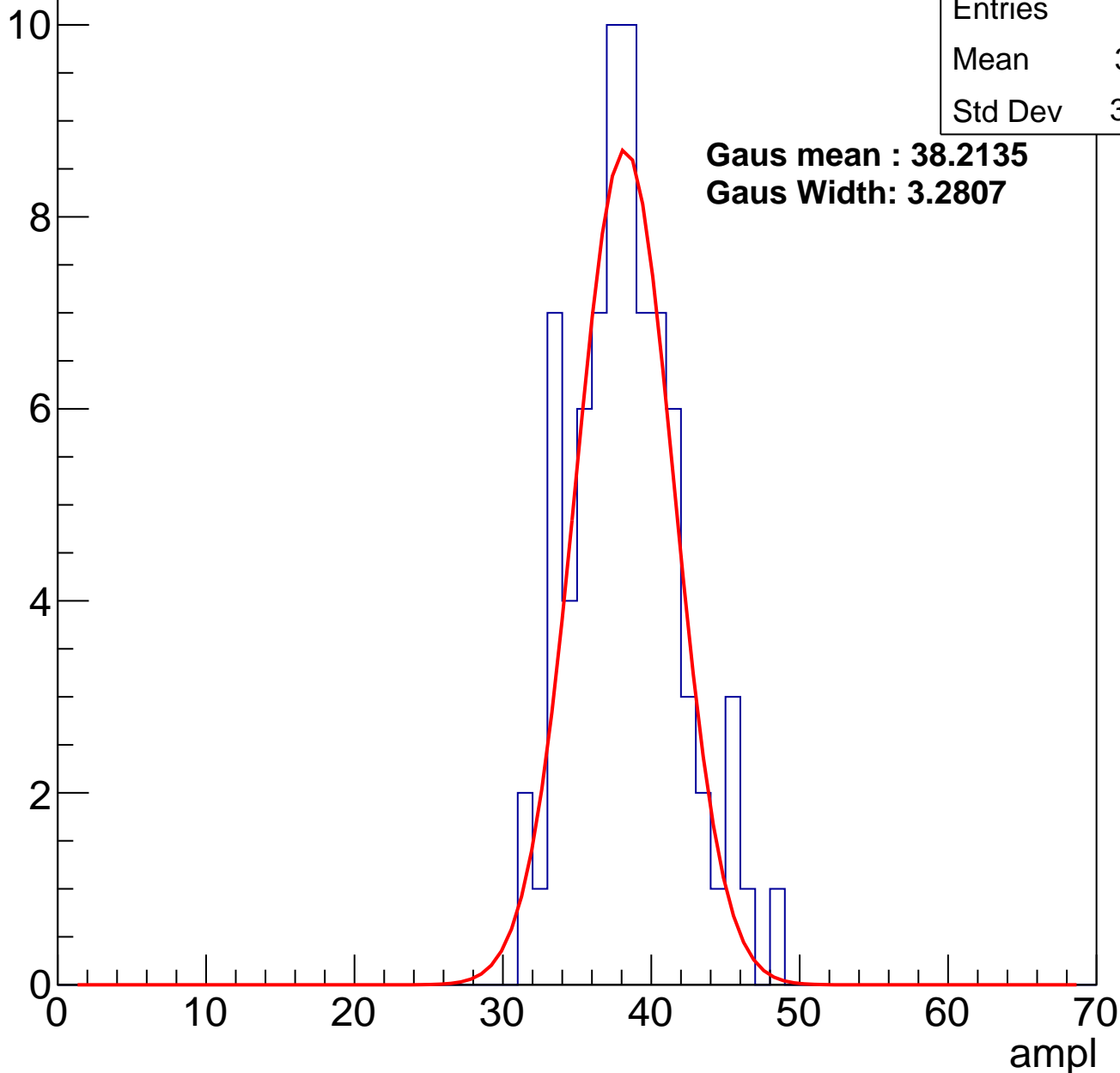
B0L001S, U19-ch92, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	37.91
Std Dev	3.578

Gaus mean : 38.2135
Gaus Width: 3.2807

Entry



B0L001S, U19-ch92, adc2

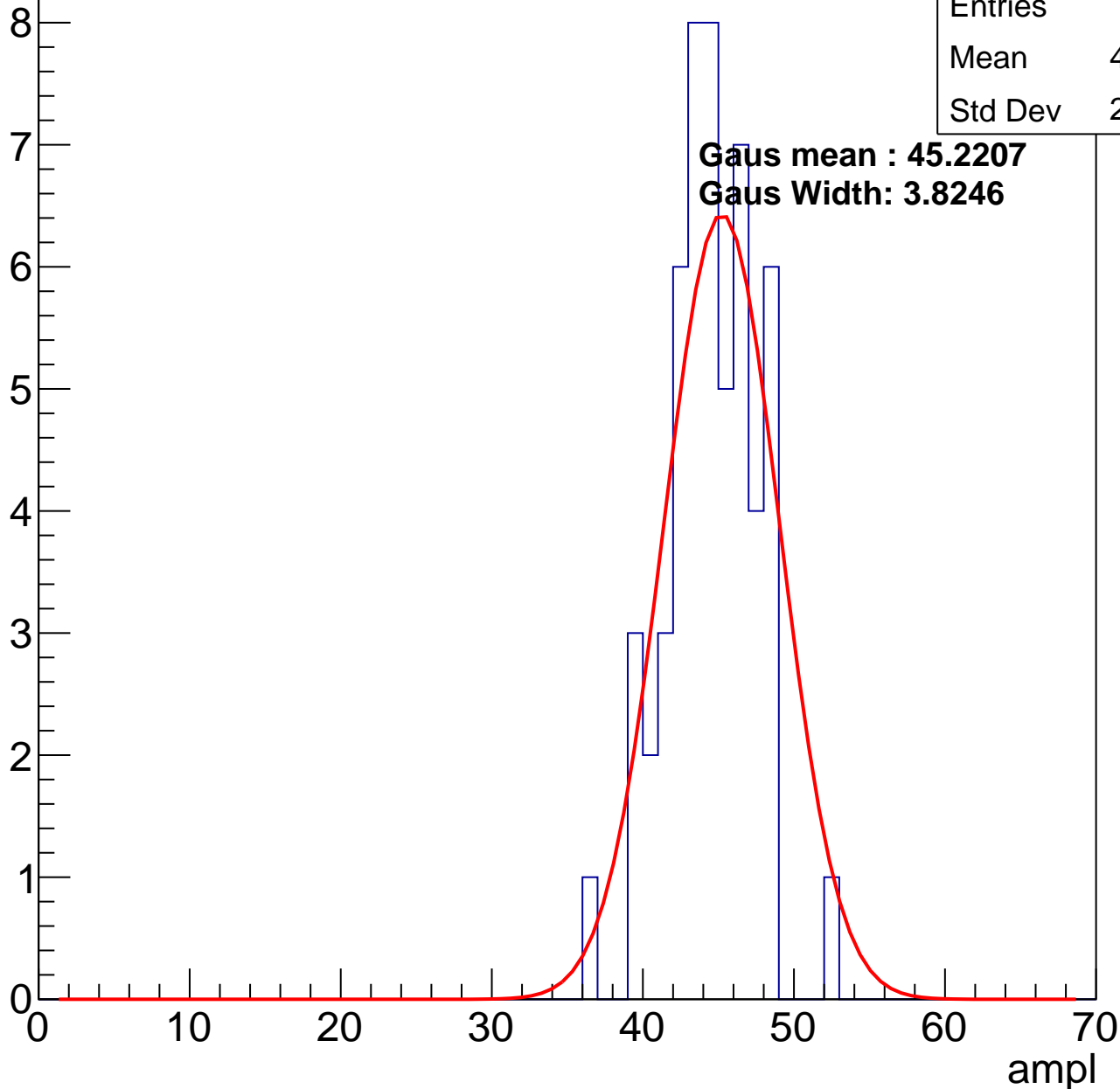
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	44.06
Std Dev	2.915

Gaus mean : 45.2207

Gaus Width: 3.8246



B0L001S, U19-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	50.39
Std Dev	3.705

Entry

10

8

6

4

2

0

0

10

20

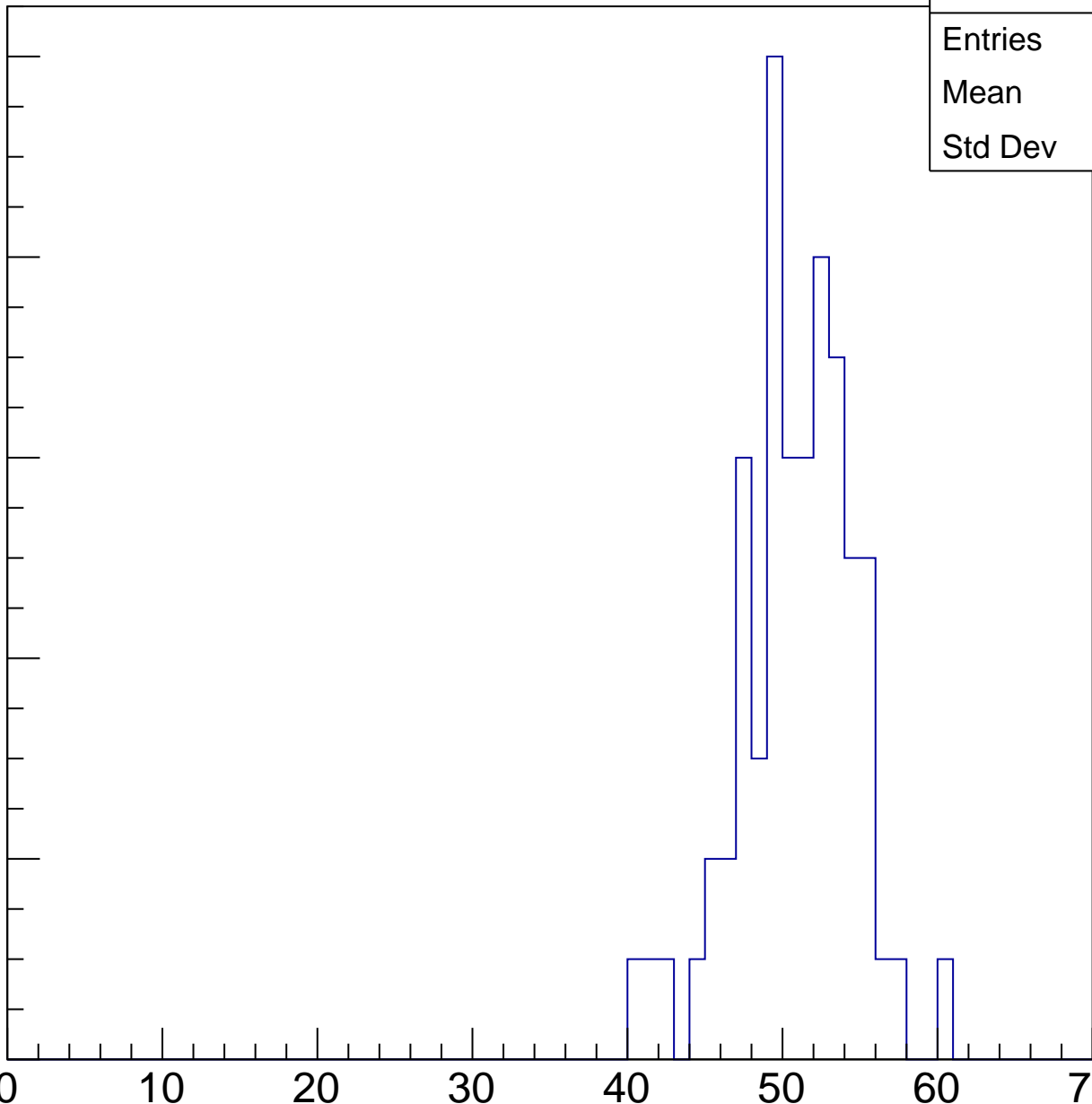
30

40

50

60

ampl

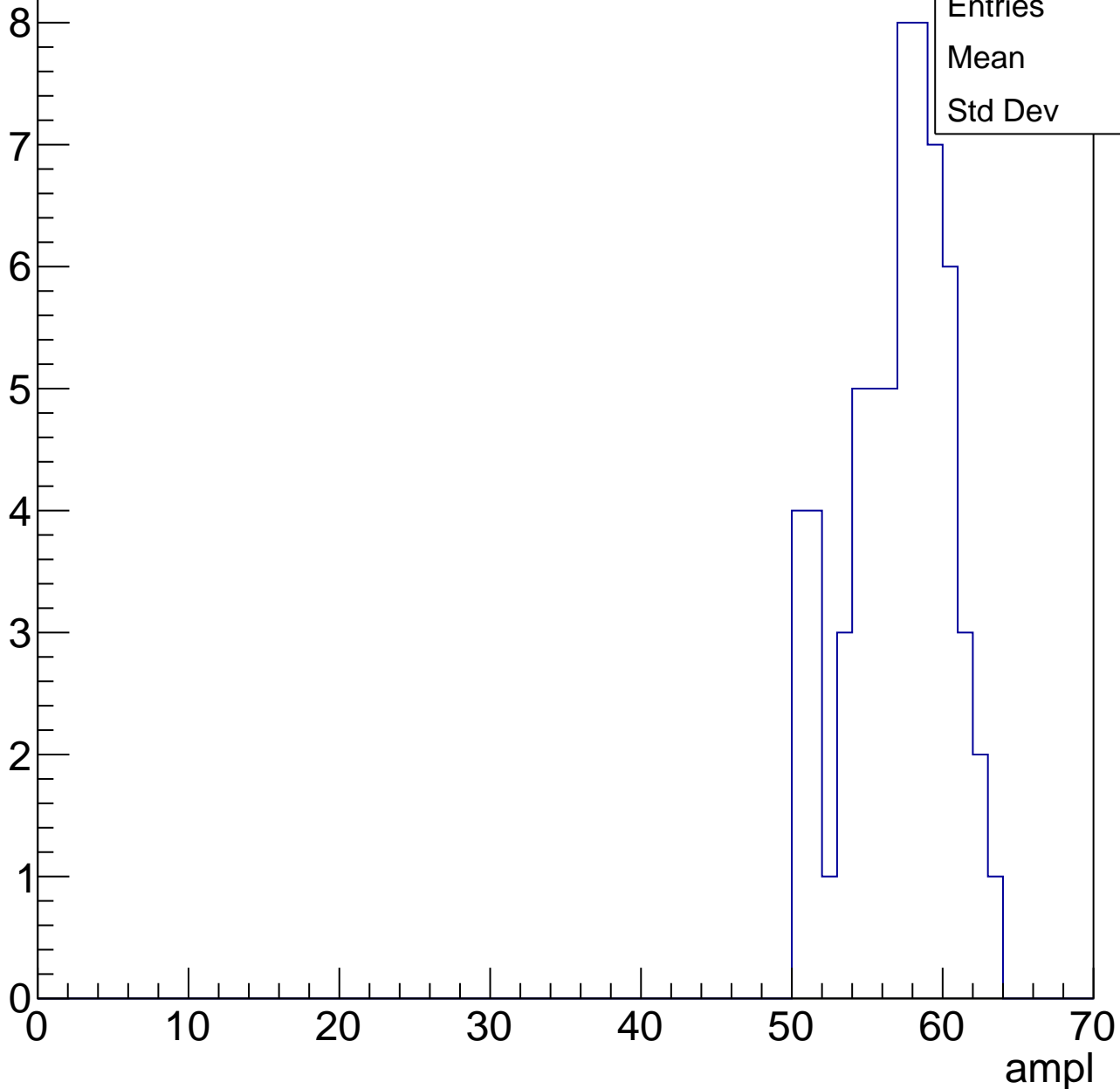


B0L001S, U19-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.5
Std Dev	3.34

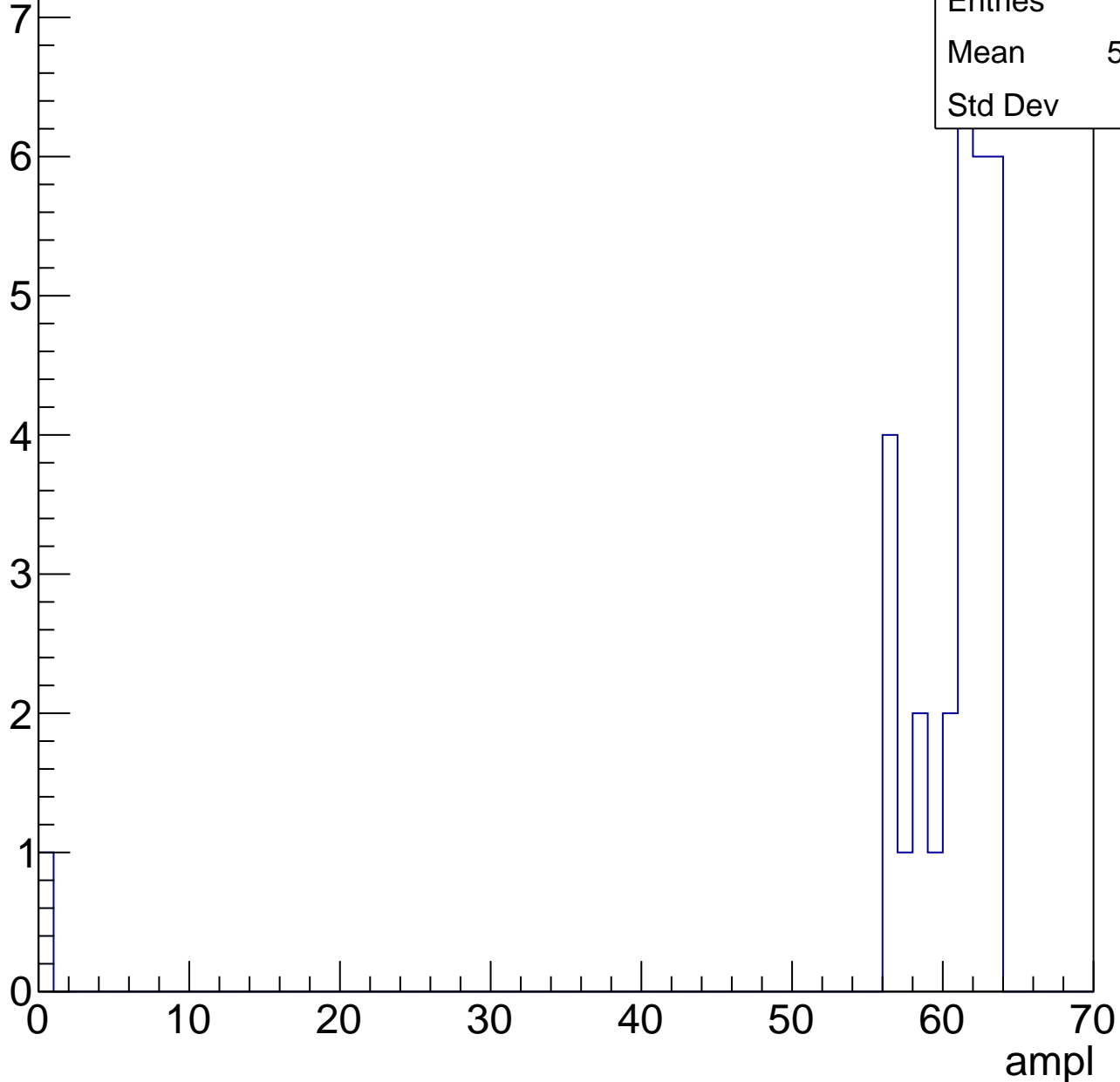


B0L001S, U19-ch92, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.43
Std Dev	11.1



B0L001S, U19-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

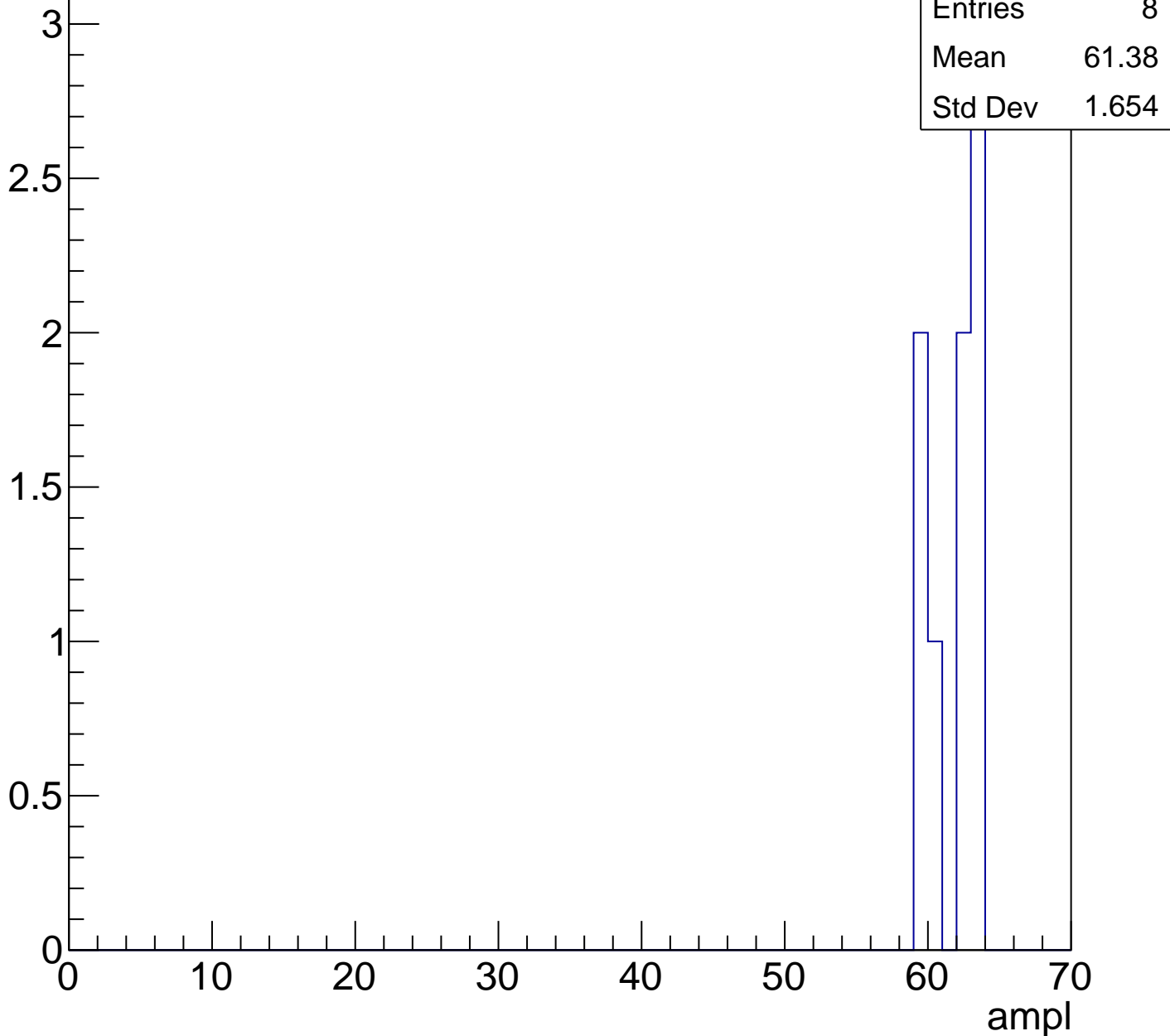
8

Mean

61.38

Std Dev

1.654



B0L001S, U19-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch93, adc0

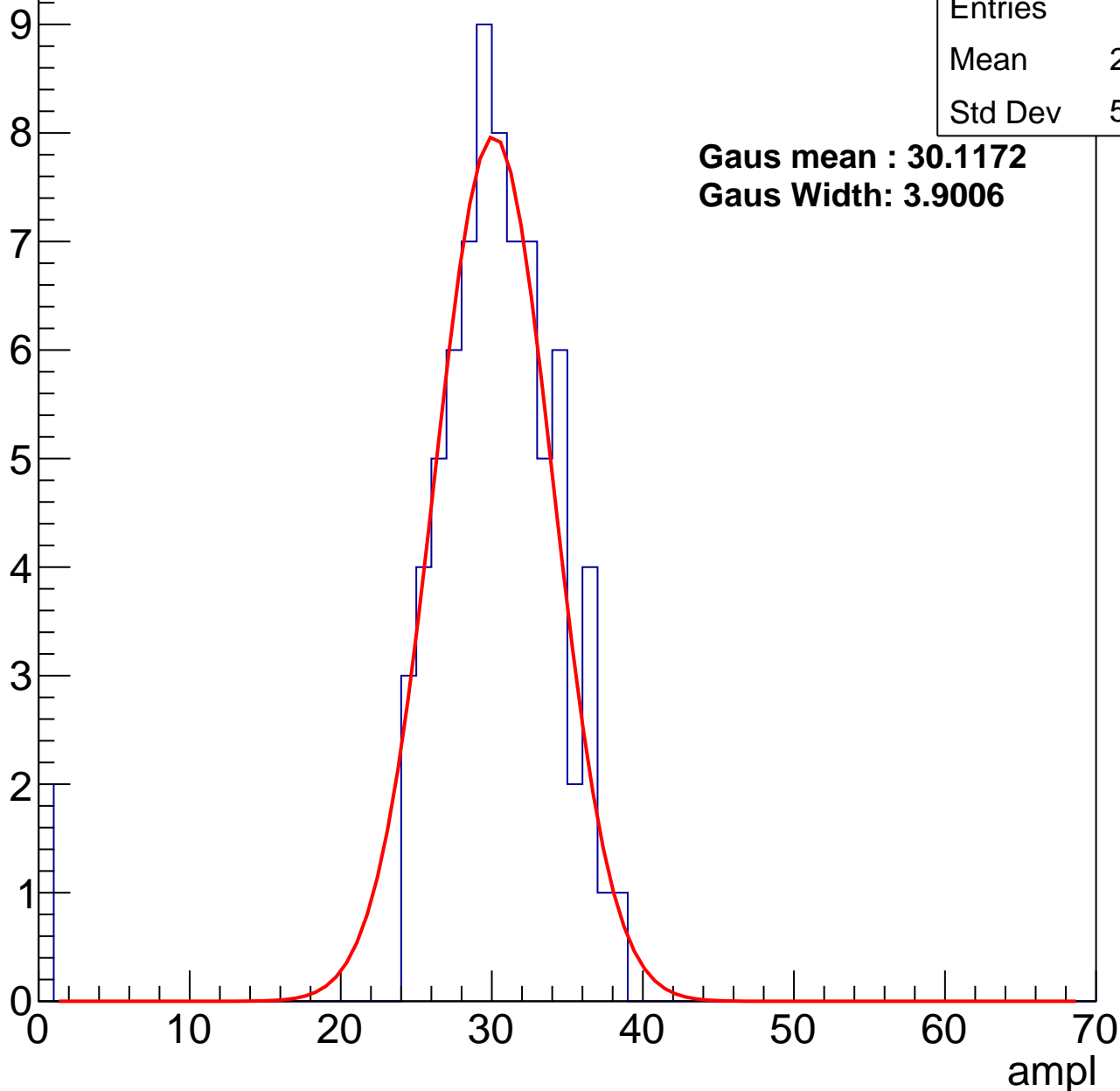
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	29.35
Std Dev	5.844

Gaus mean : 30.1172

Gaus Width: 3.9006



B0L001S, U19-ch93, adc1

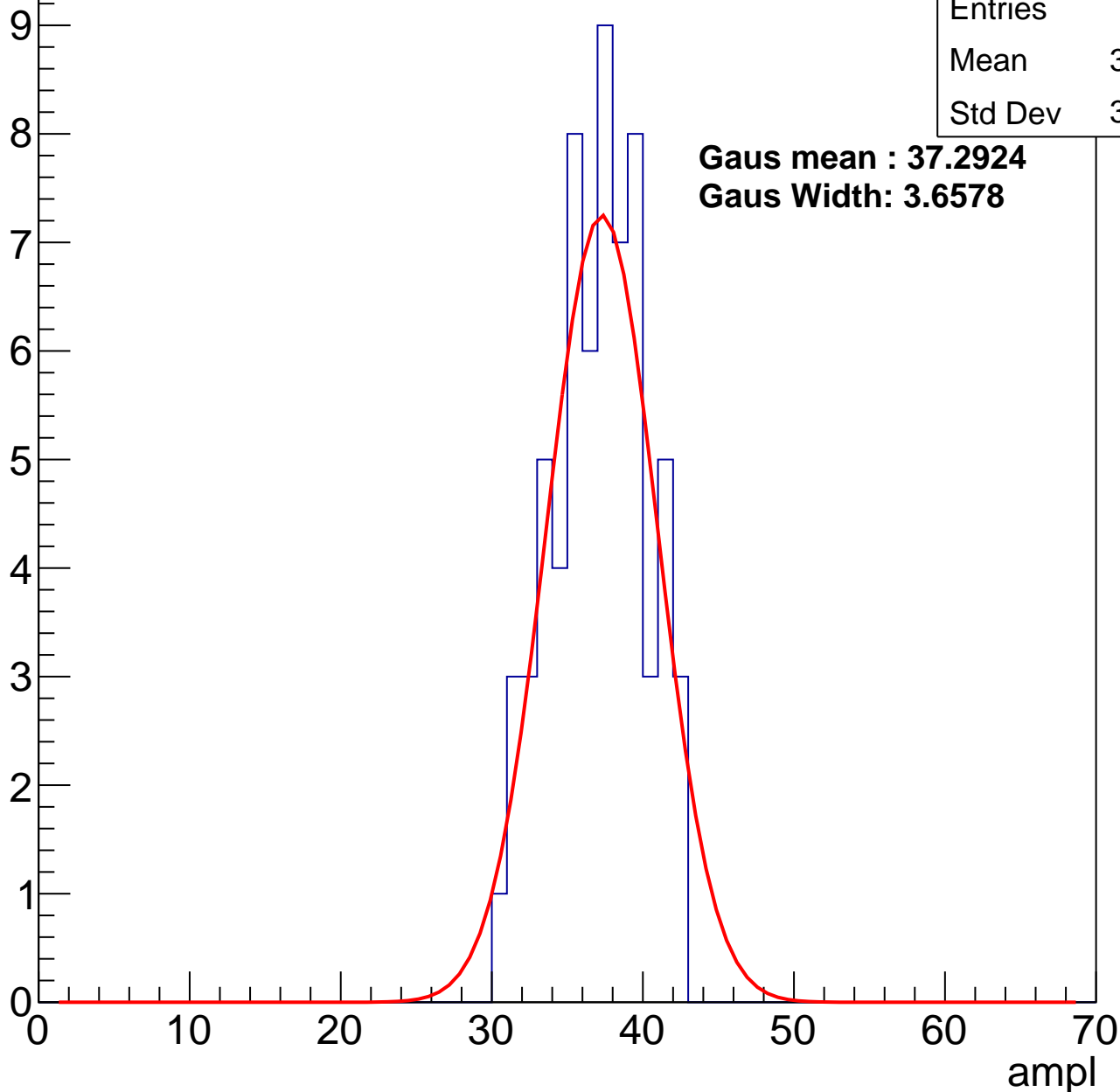
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.58
Std Dev	3.028

Gaus mean : 37.2924

Gaus Width: 3.6578



B0L001S, U19-ch93, adc2

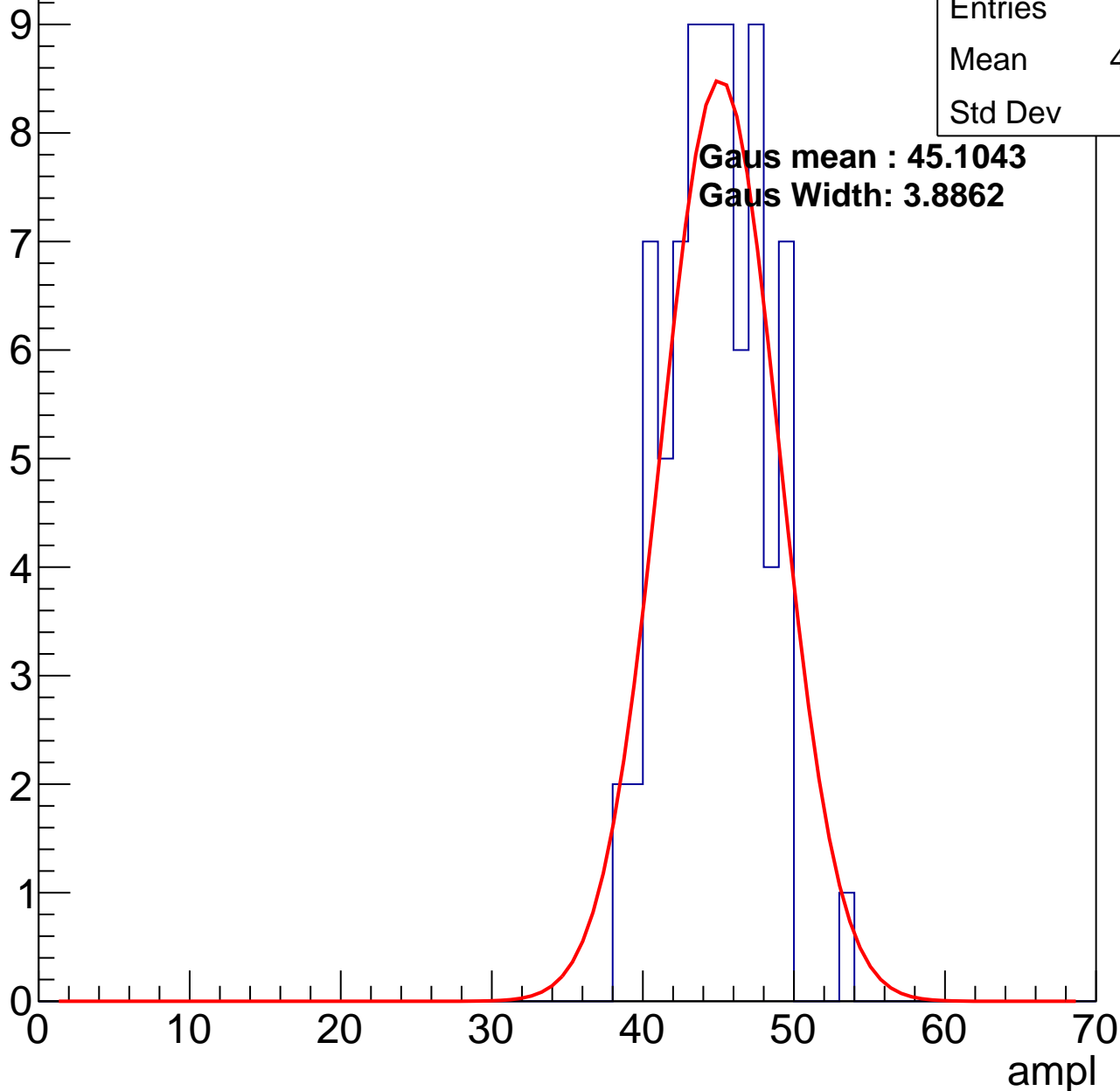
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.26
Std Dev	3.11

Gaus mean : 45.1043

Gaus Width: 3.8862

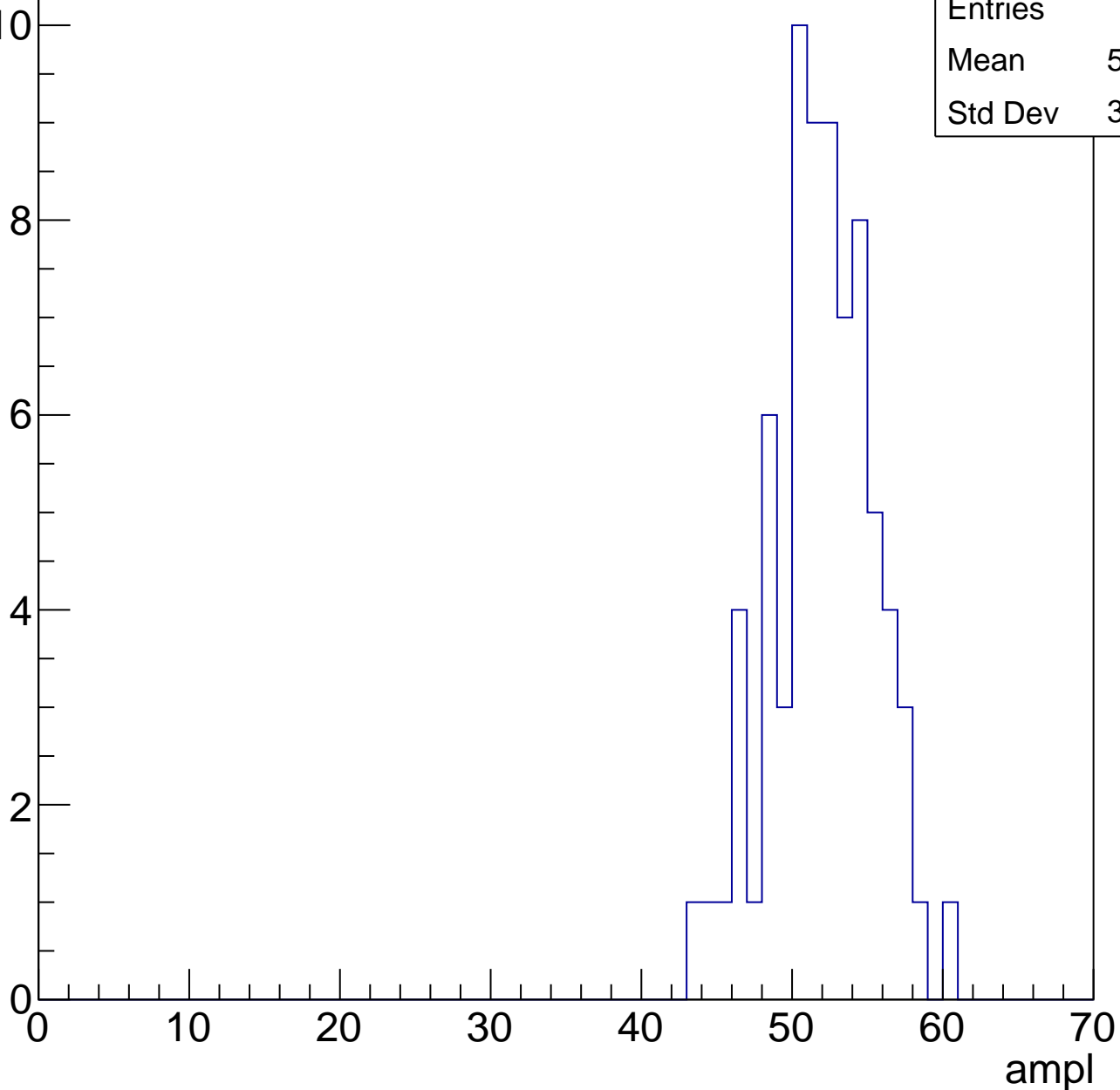


B0L001S, U19-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

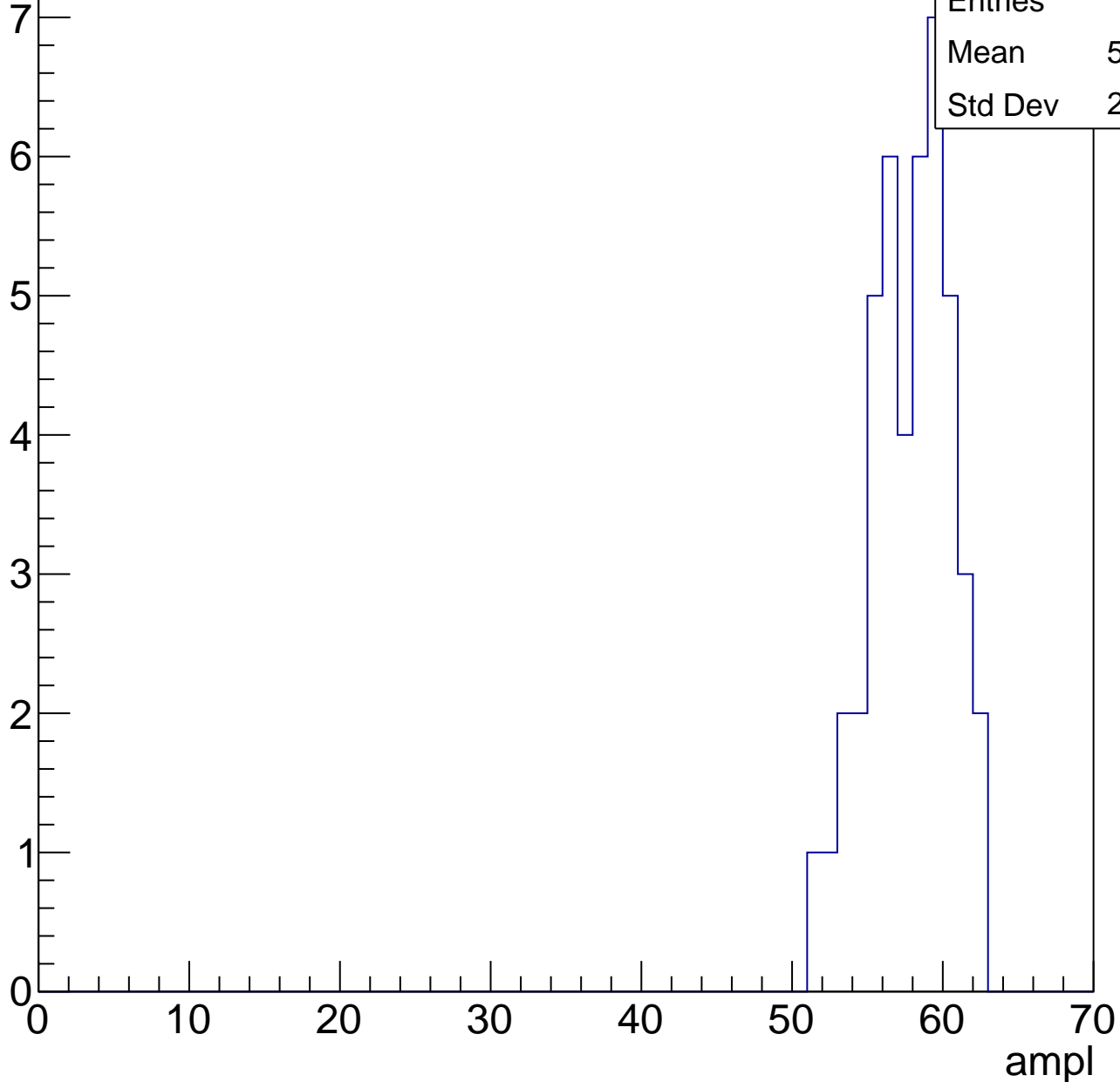
Entries	74
Mean	51.57
Std Dev	3.378



B0L001S, U19-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

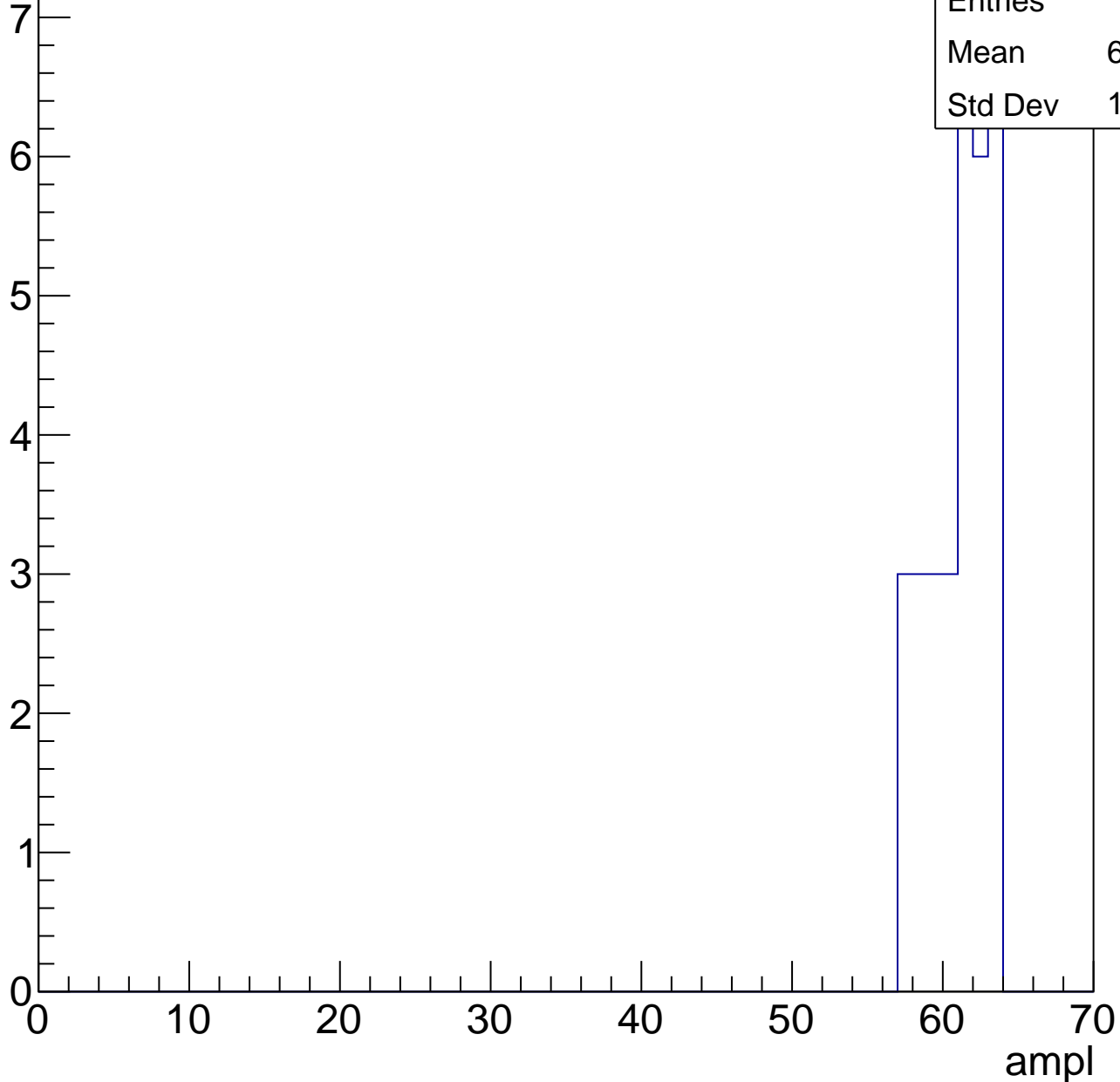
Entry



B0L001S, U19-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

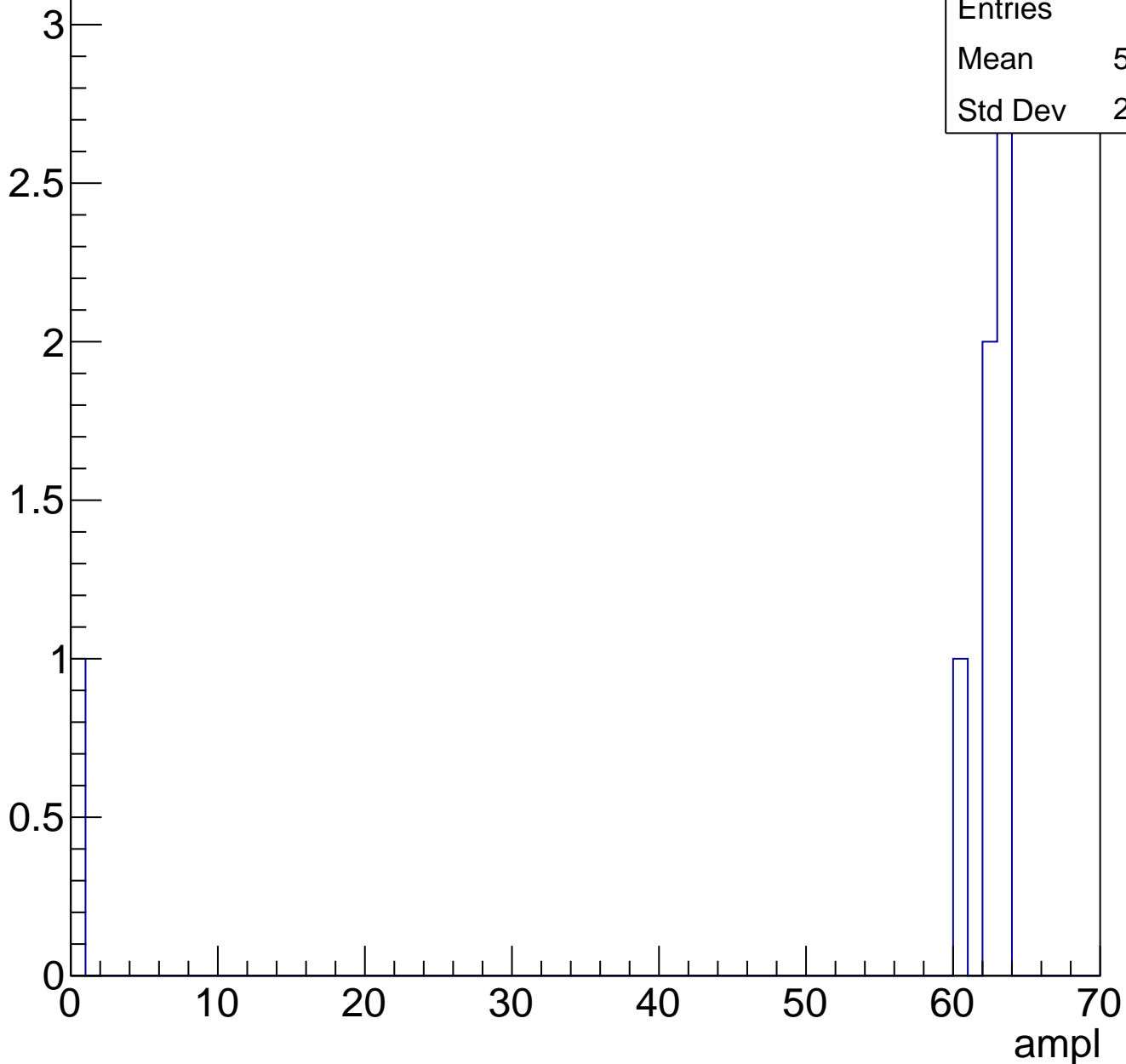


Entries	32
Mean	60.69
Std Dev	1.944

B0L001S, U19-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch94, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	27.65
Std Dev	4.43

Gaus mean : 28.0962

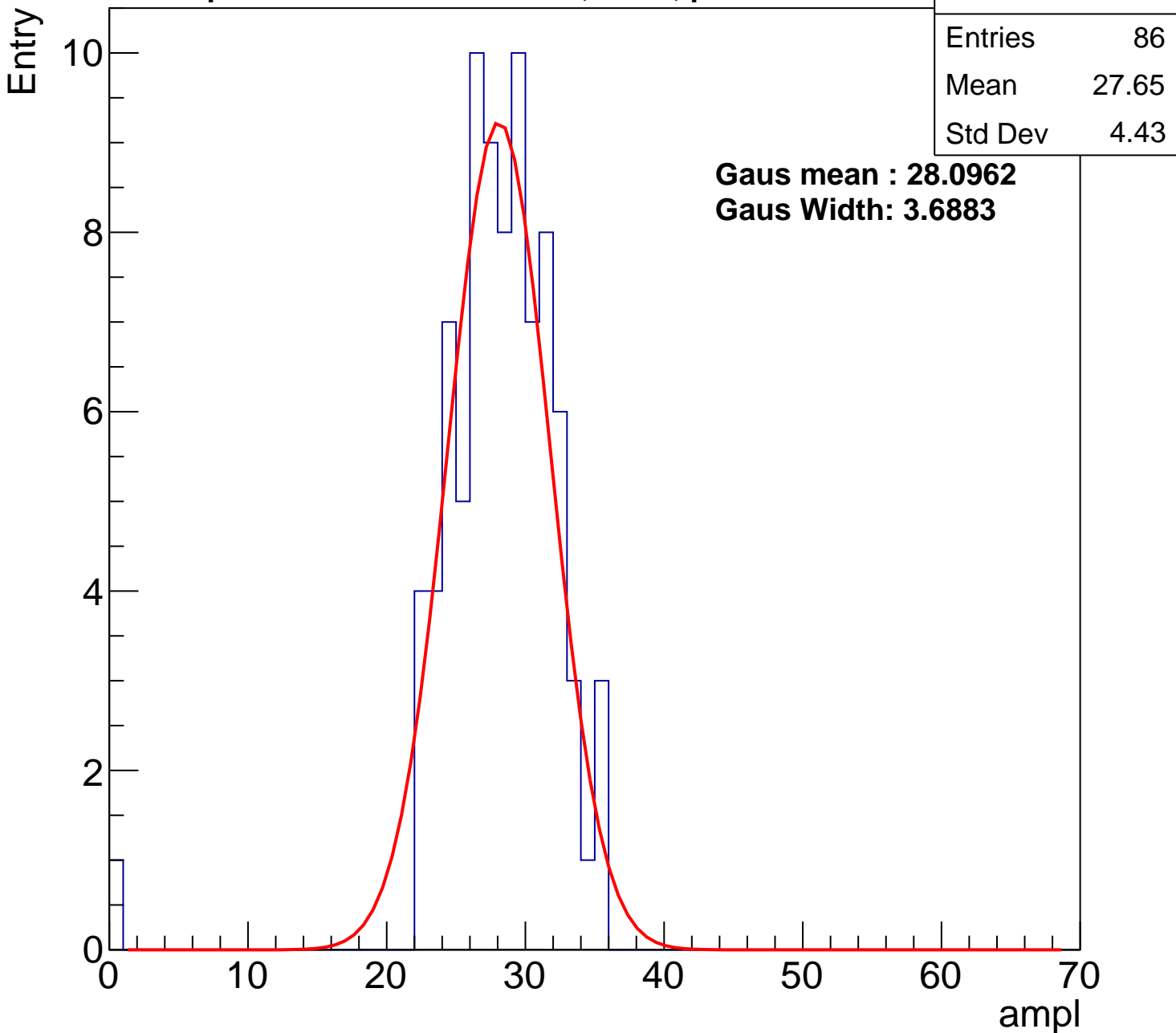
Gaus Width: 3.6883

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch94, adc1

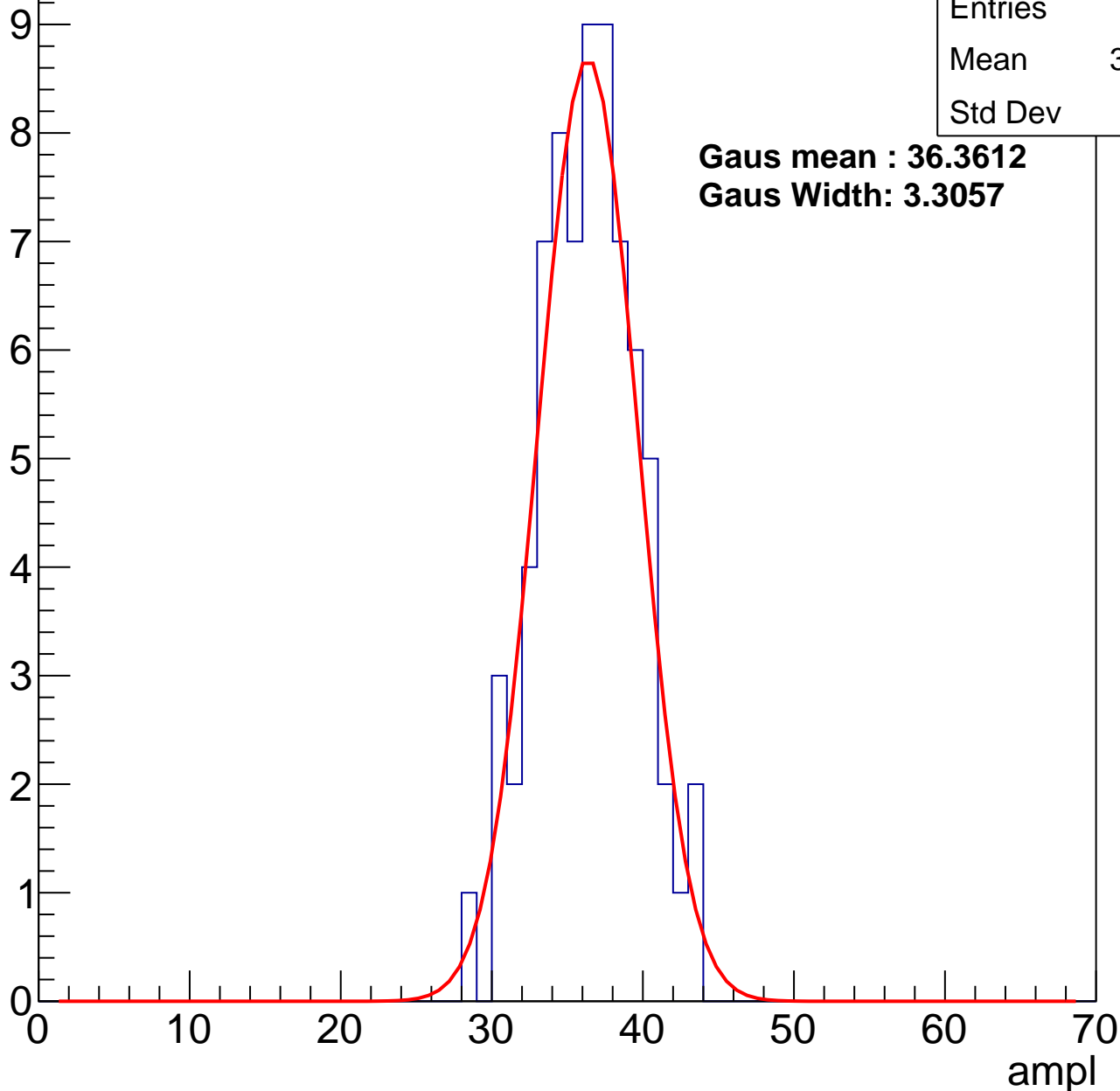
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	35.93
Std Dev	3.19

Gaus mean : 36.3612

Gaus Width: 3.3057



B0L001S, U19-ch94, adc2

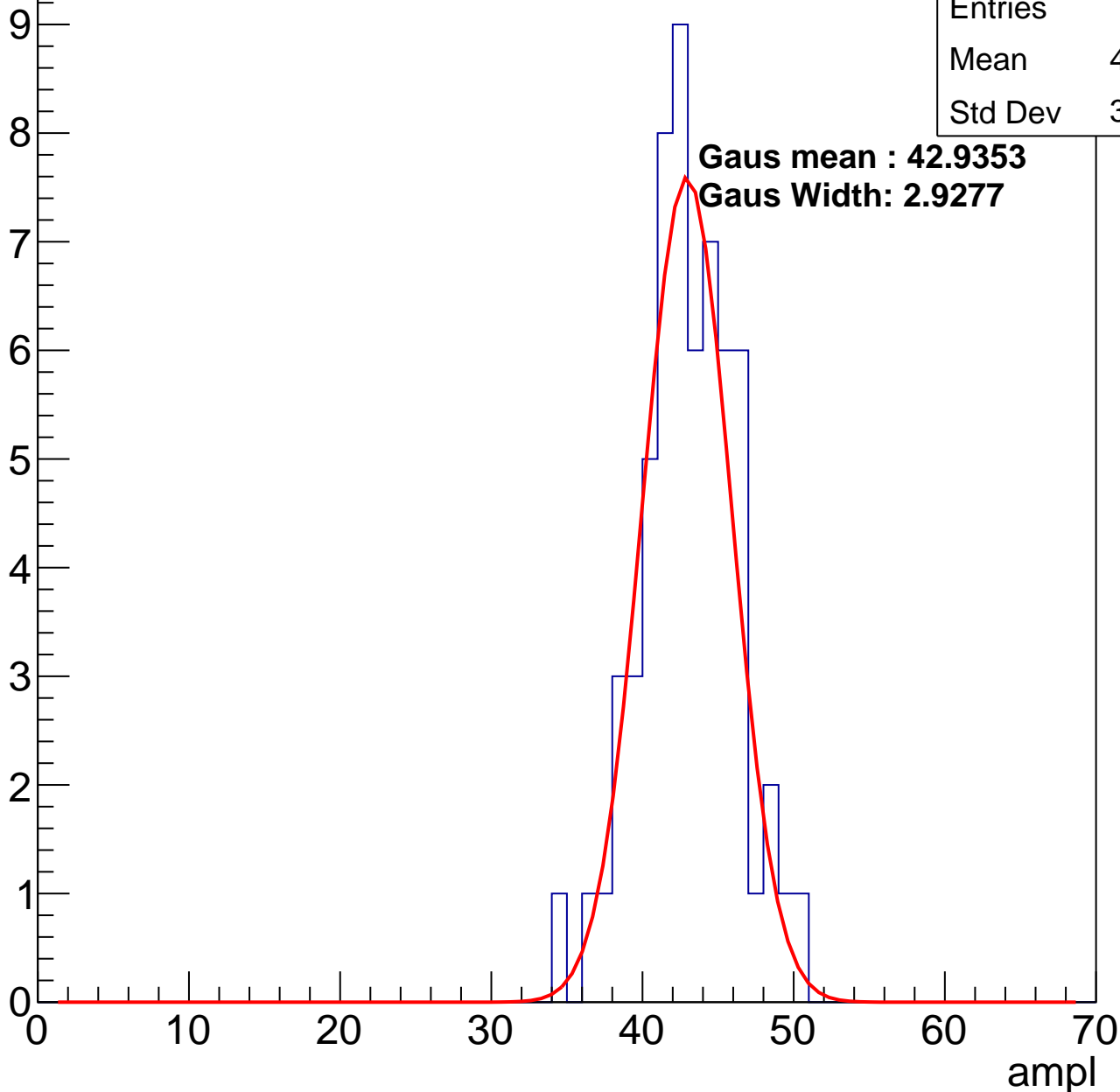
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	42.59
Std Dev	3.138

Gaus mean : 42.9353

Gaus Width: 2.9277

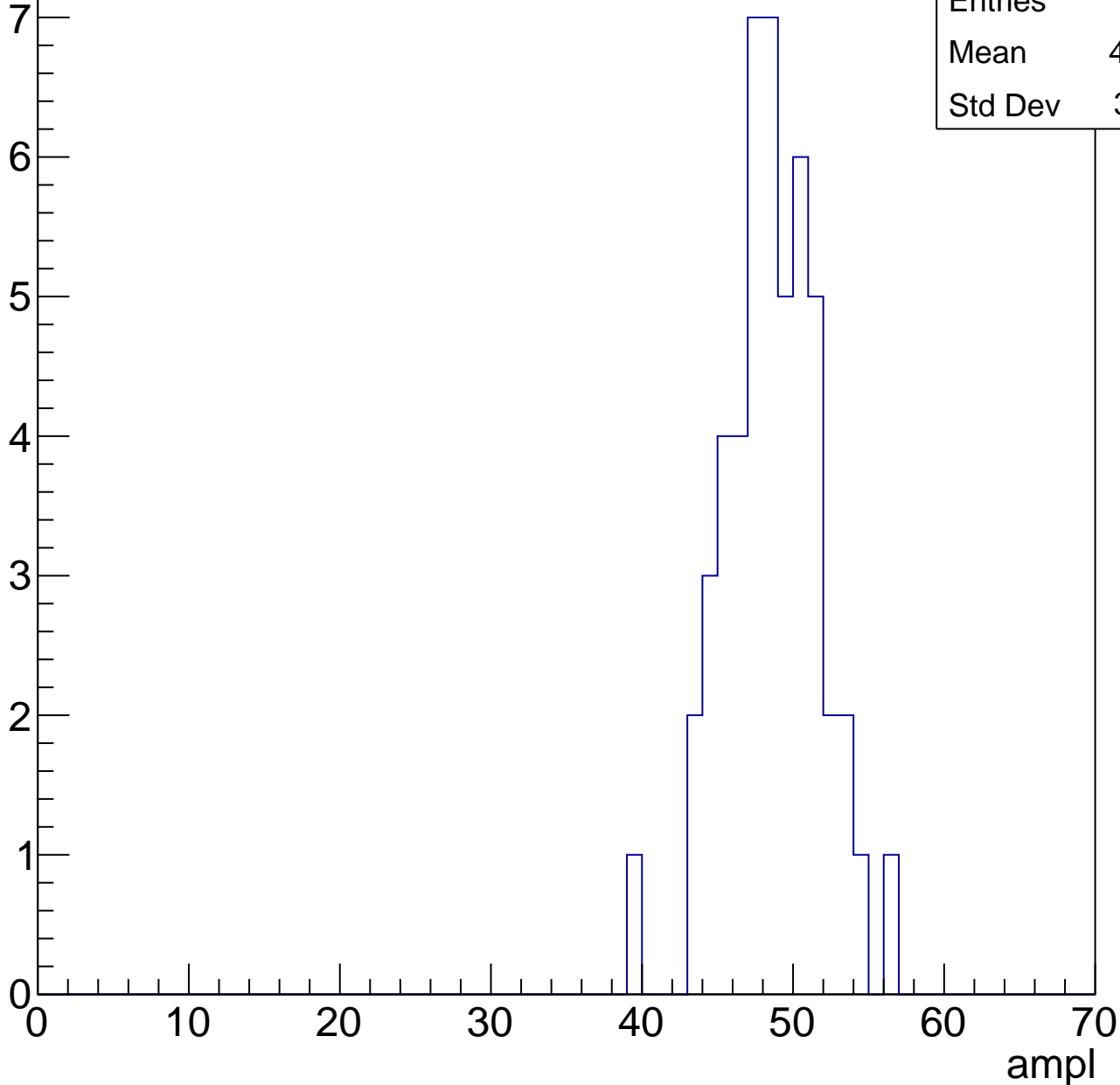


B0L001S, U19-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

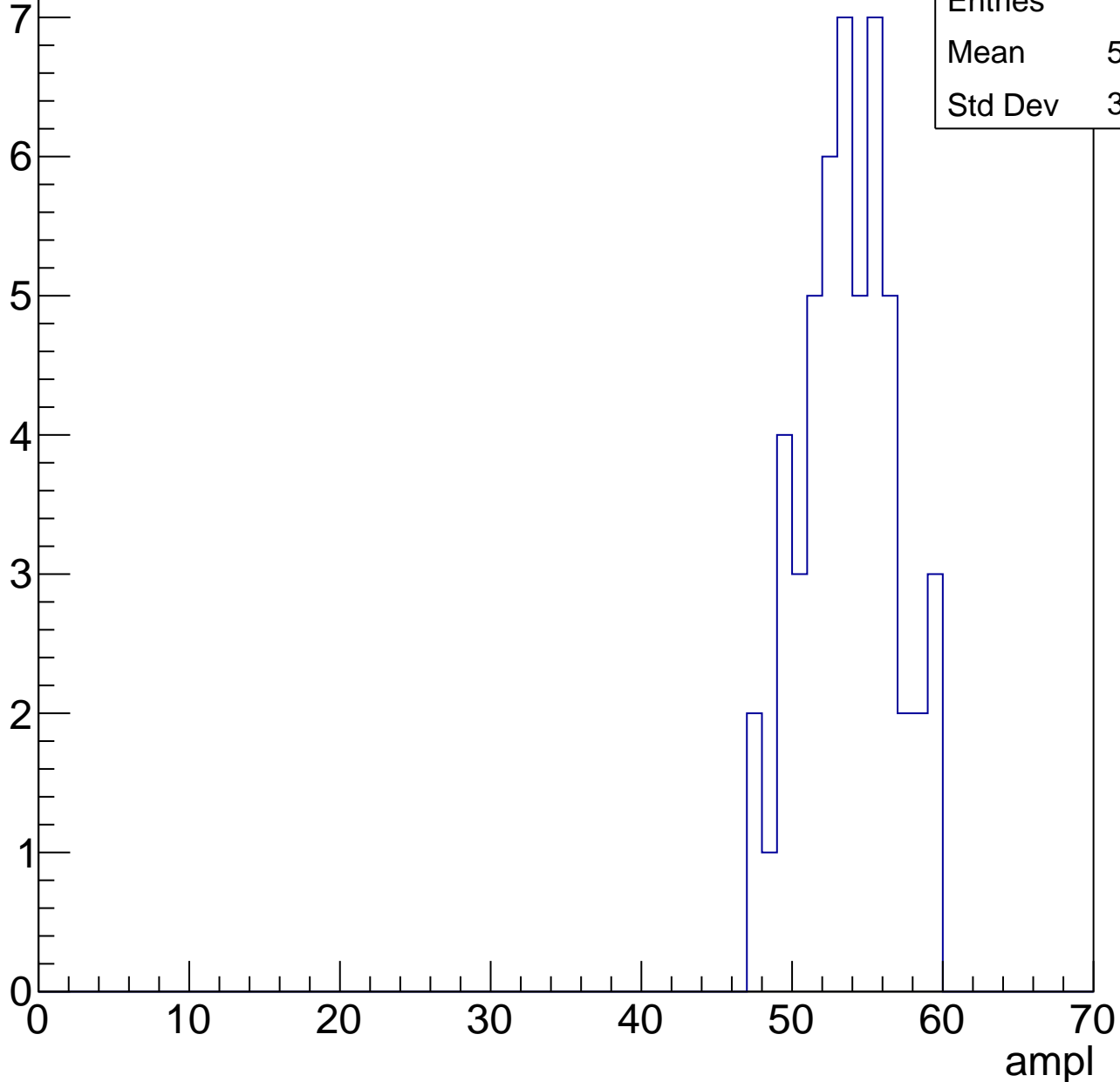
Entries	50
Mean	48.12
Std Dev	3.141



B0L001S, U19-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	52
Mean	53.23
Std Dev	3.036

B0L001S, U19-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	57.94
Std Dev	7.572

Entry

10

8

6

4

2

0

0

10

20

30

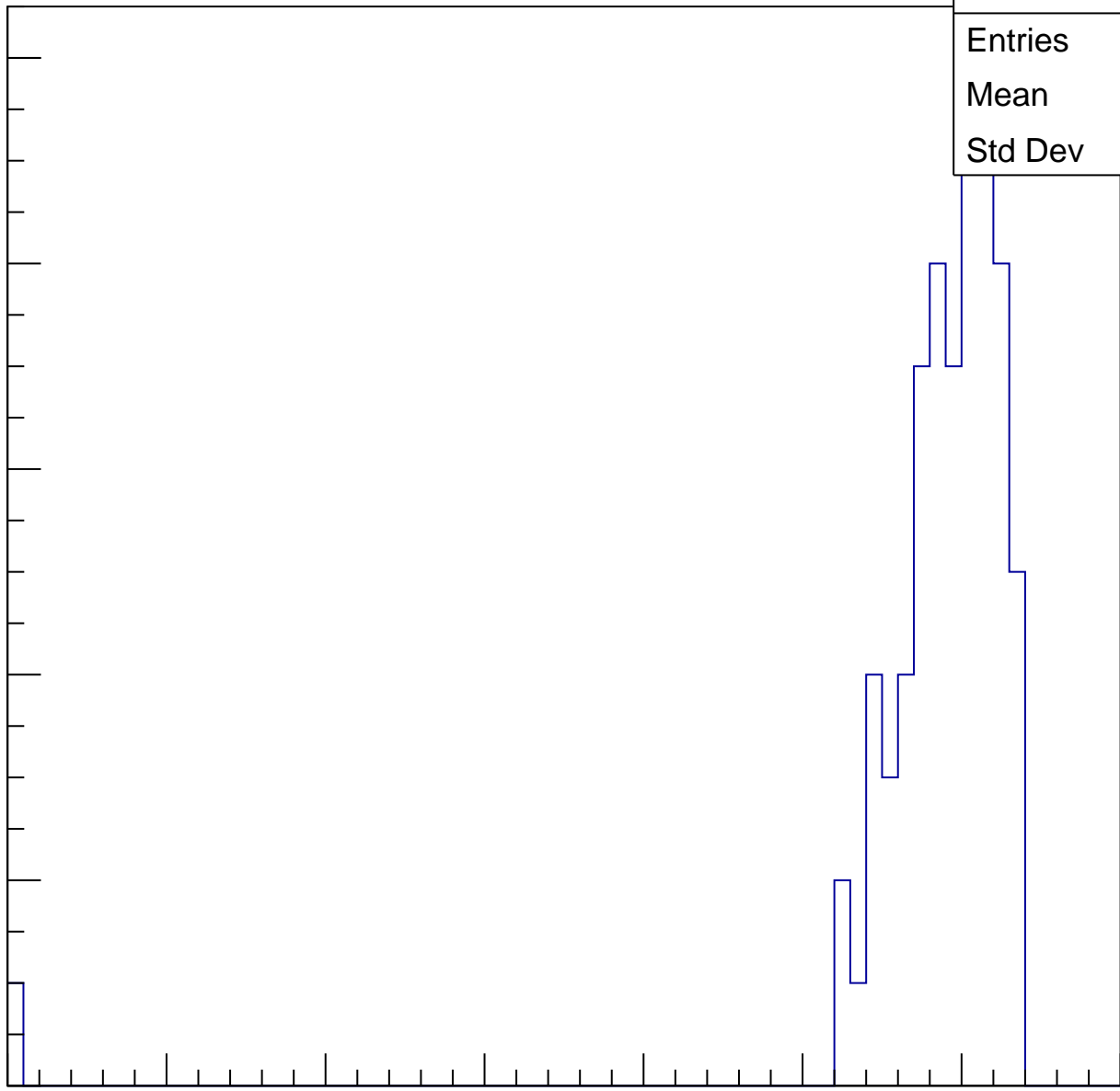
40

50

60

70

ampl

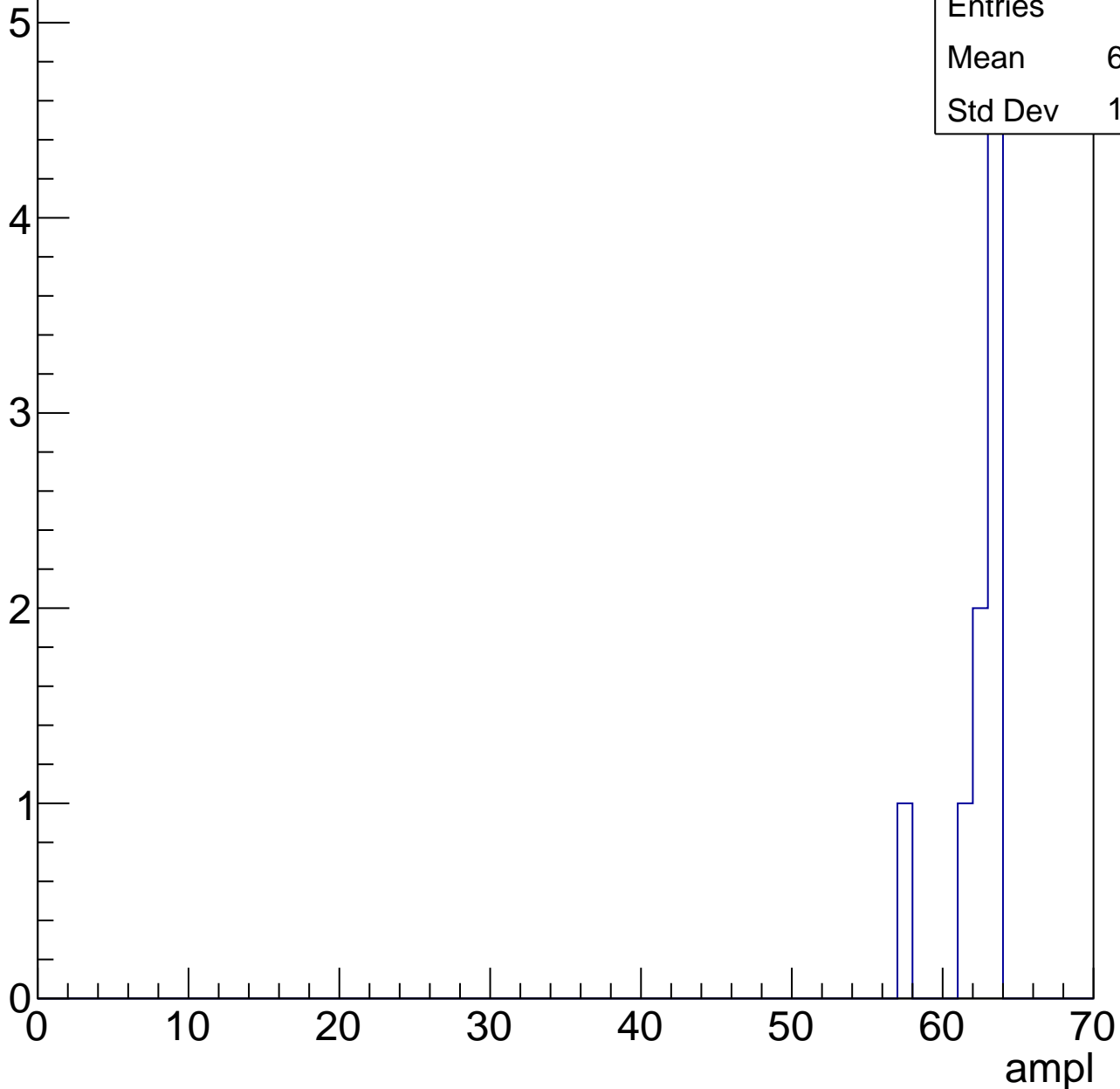


B0L001S, U19-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	61.89
Std Dev	1.853



B0L001S, U19-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch95, adc0

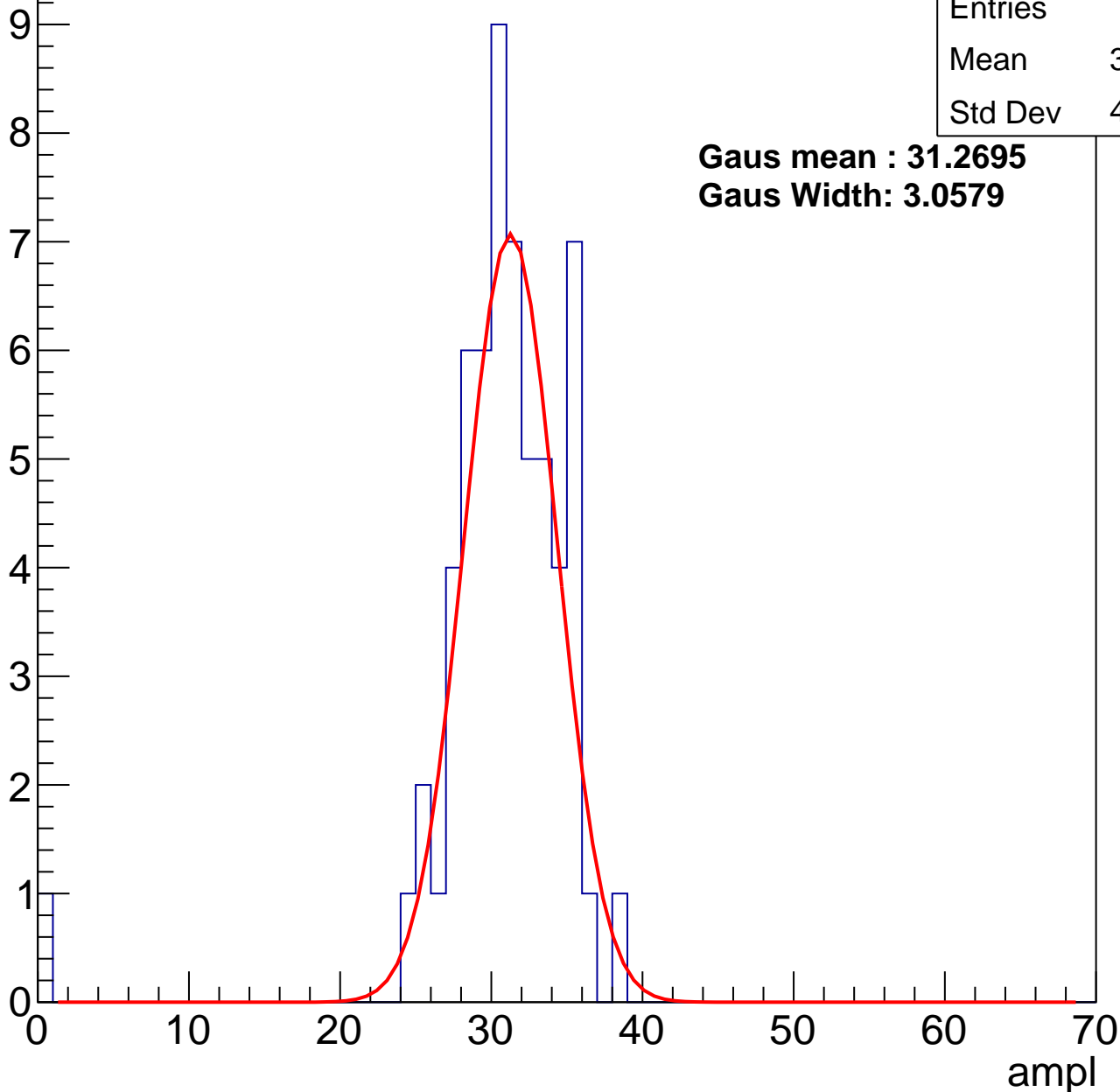
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.28
Std Dev	4.957

Gaus mean : 31.2695

Gaus Width: 3.0579



B0L001S, U19-ch95, adc1

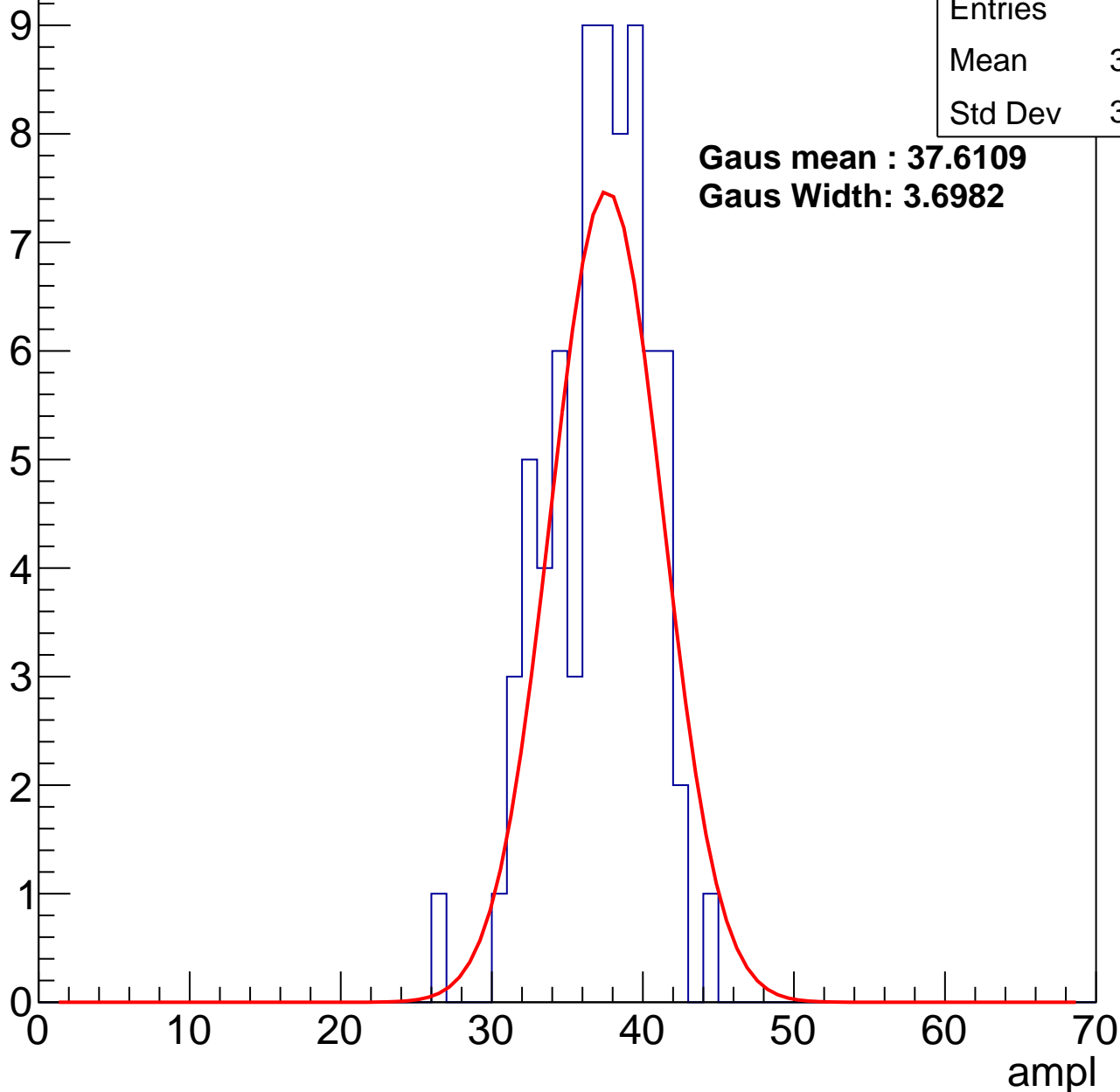
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	36.66
Std Dev	3.365

Gaus mean : 37.6109

Gaus Width: 3.6982



B0L001S, U19-ch95, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	43.85
Std Dev	2.66

Gaus mean : 44.4264

Gaus Width: 2.6604

Entry

10

8

6

4

2

0

0

10

20

30

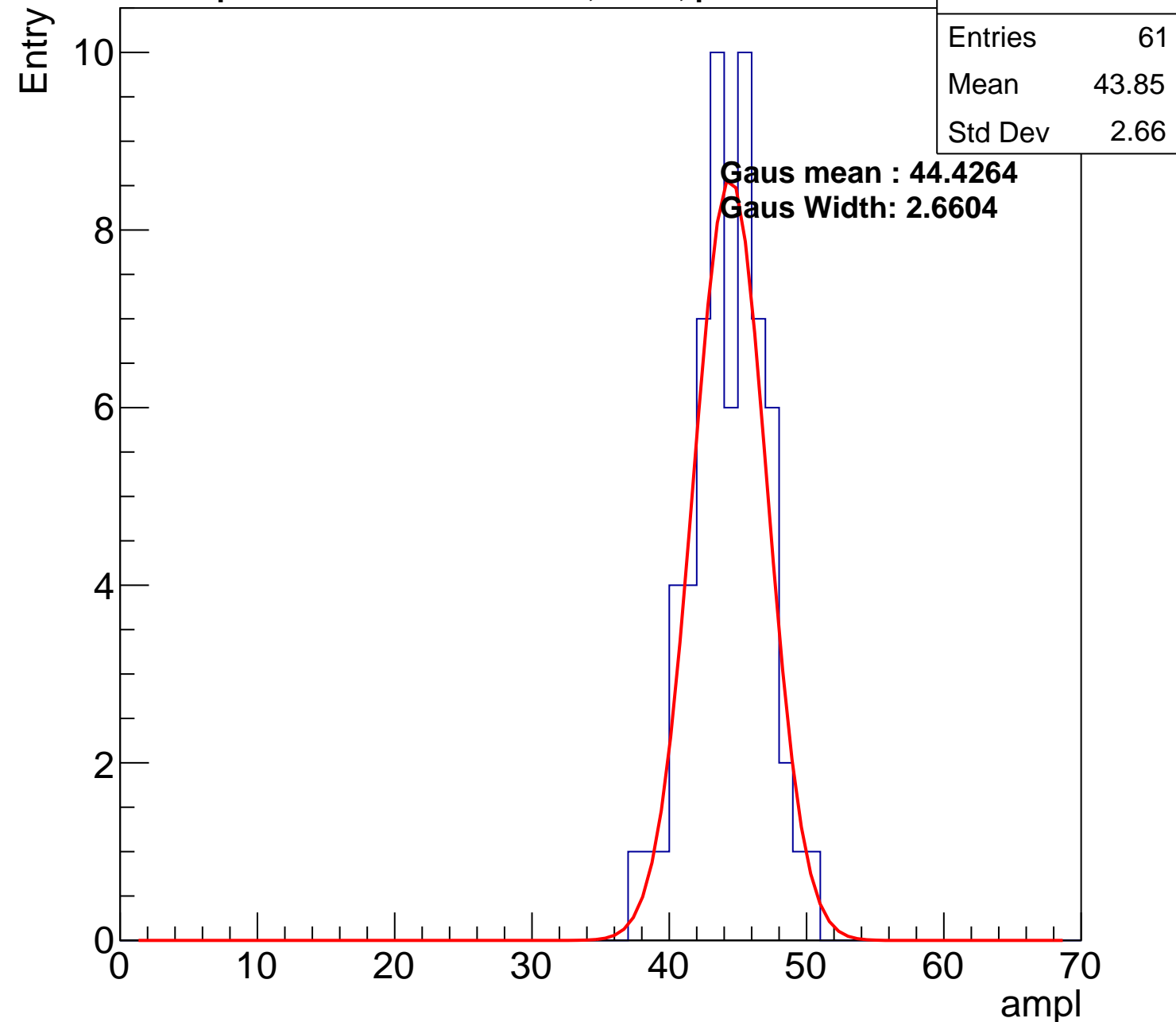
40

50

60

70

ampl

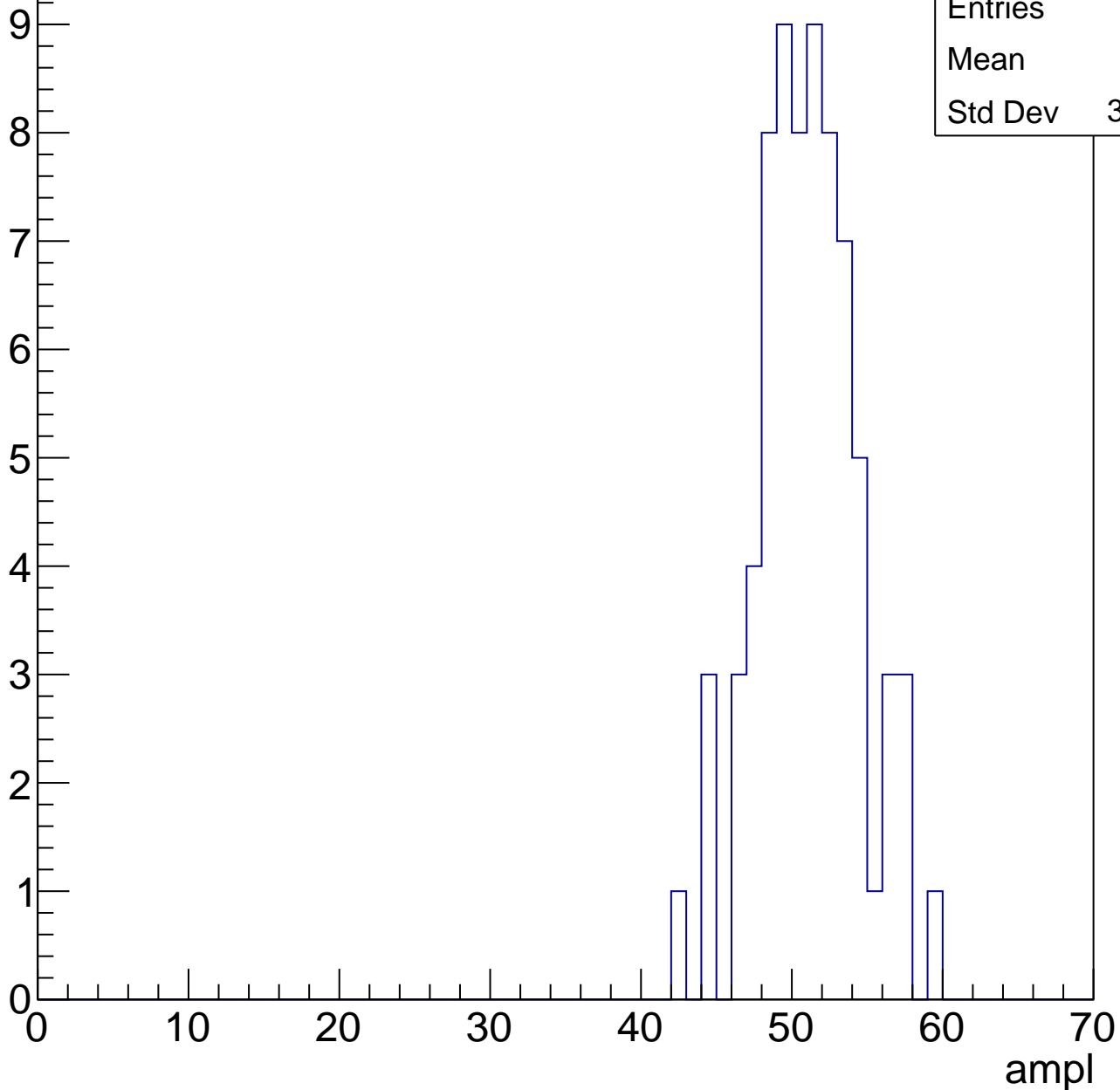


B0L001S, U19-ch95, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	50.6
Std Dev	3.342

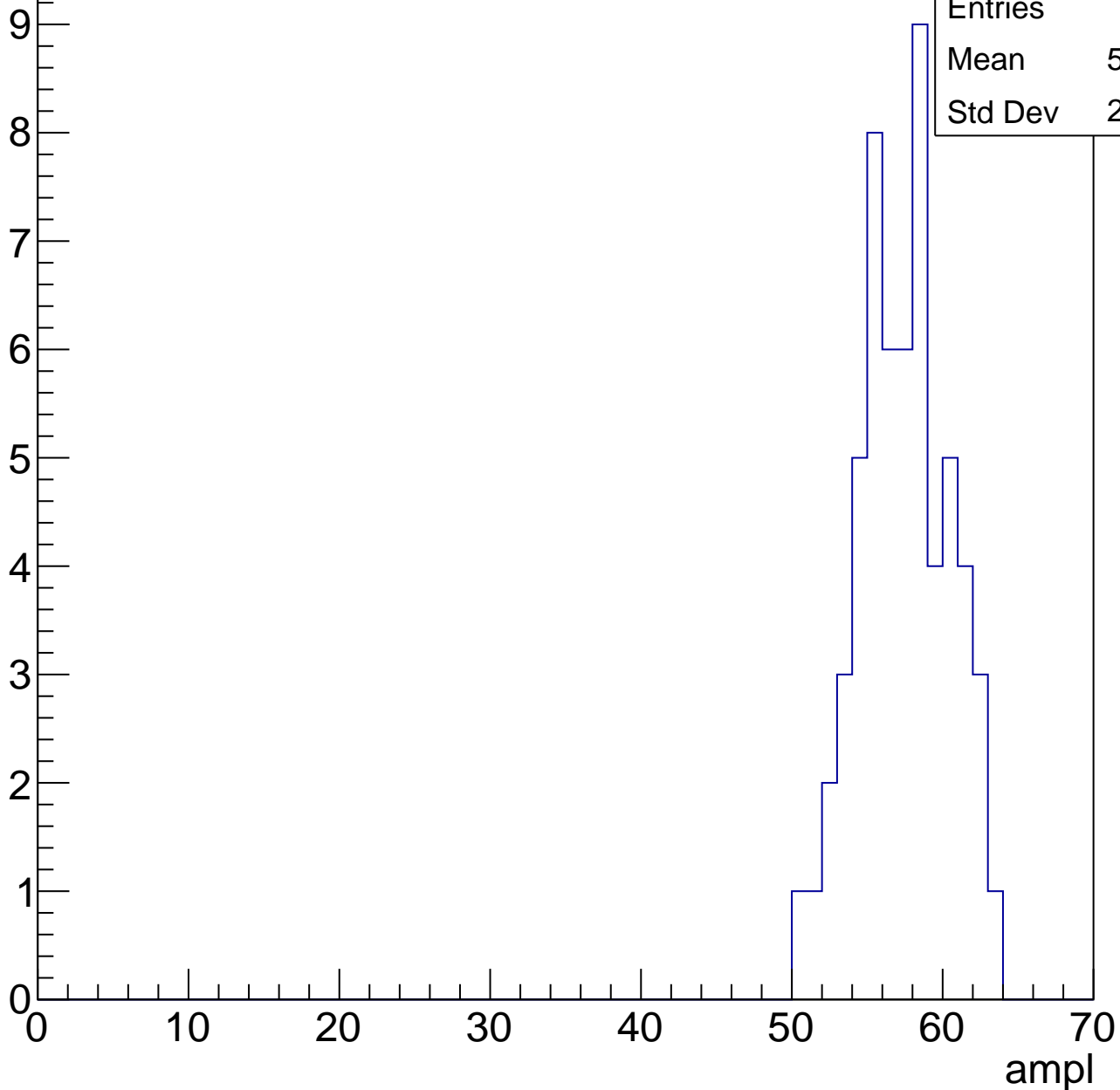


B0L001S, U19-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.95
Std Dev	2.968

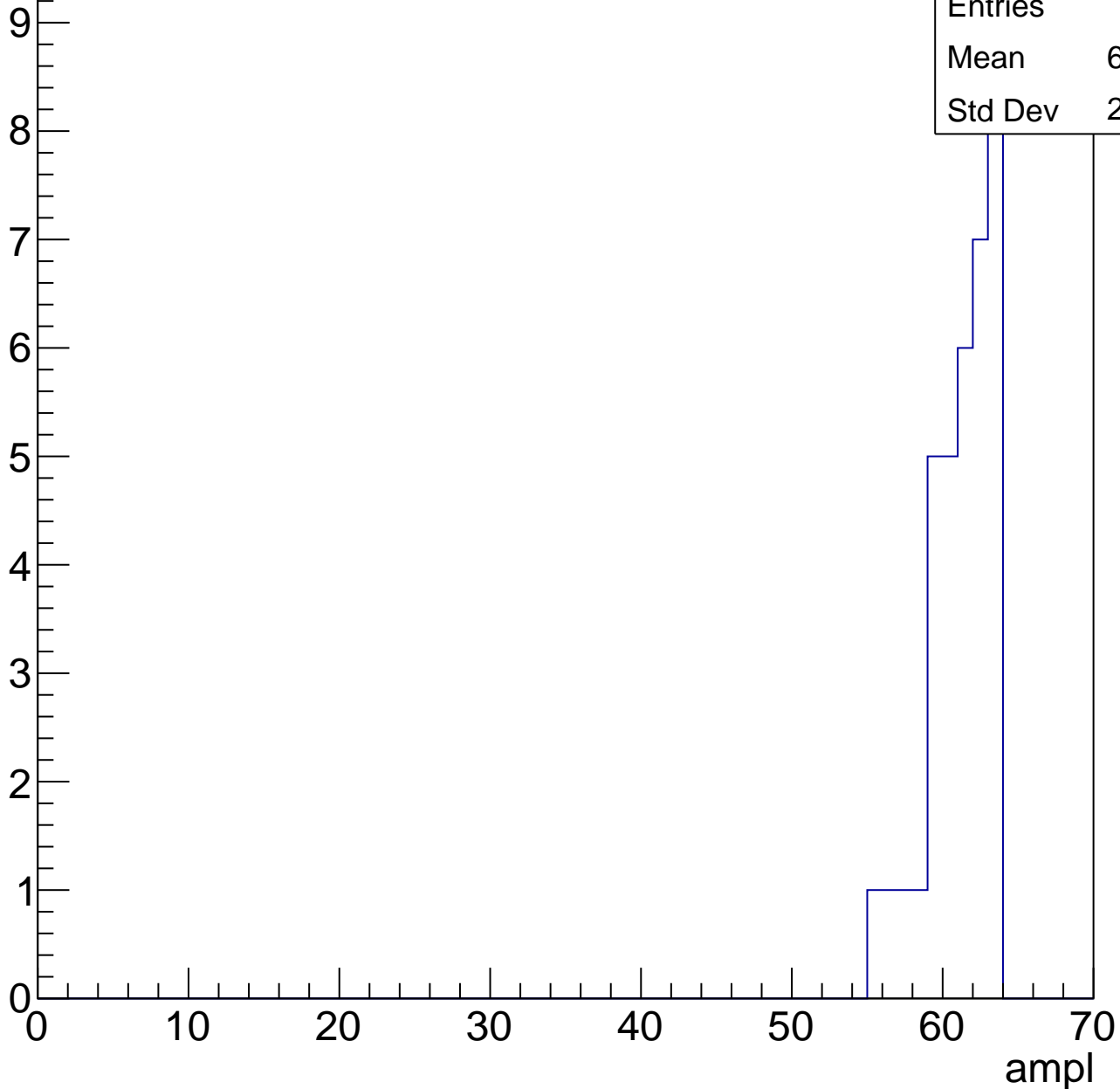


B0L001S, U19-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	60.78
Std Dev	2.056



B0L001S, U19-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch96, adc0

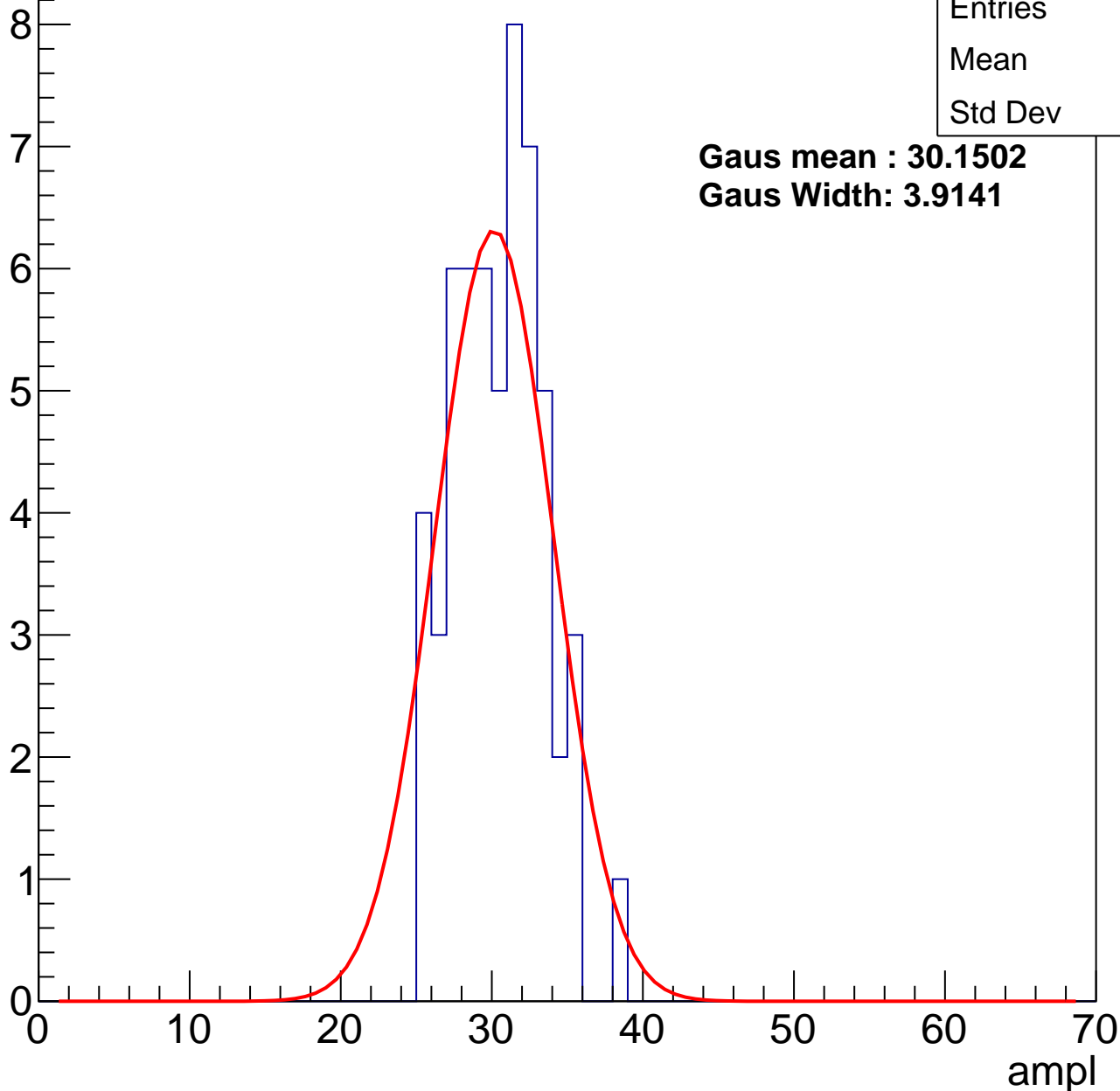
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	30
Std Dev	2.94

Gaus mean : 30.1502

Gaus Width: 3.9141



B0L001S, U19-ch96, adc1

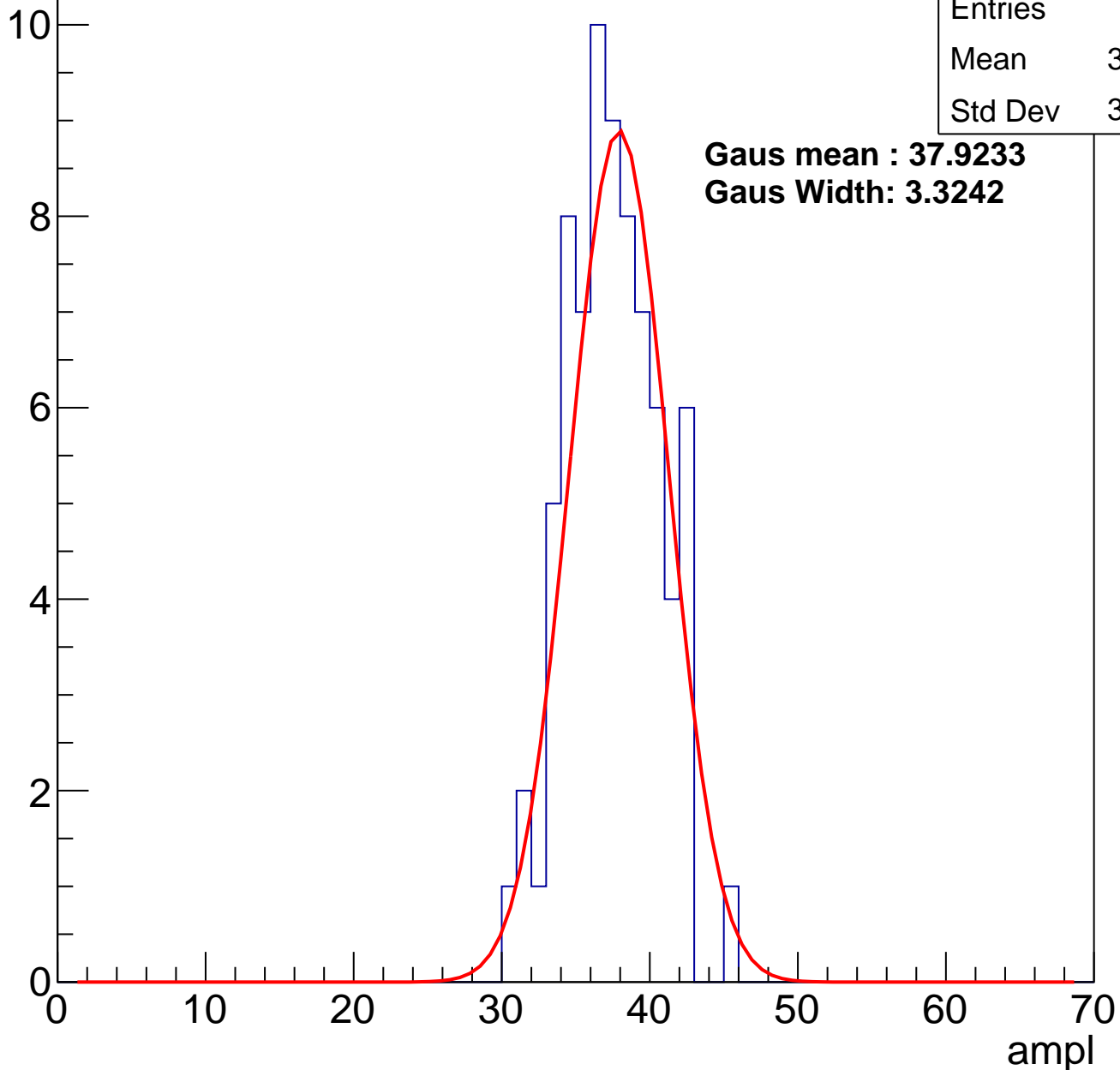
calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	37.03
Std Dev	3.064

Gaus mean : 37.9233

Gaus Width: 3.3242

Entry



B0L001S, U19-ch96, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	59
Mean	43.86
Std Dev	3.249

Gaus mean : 44.2758

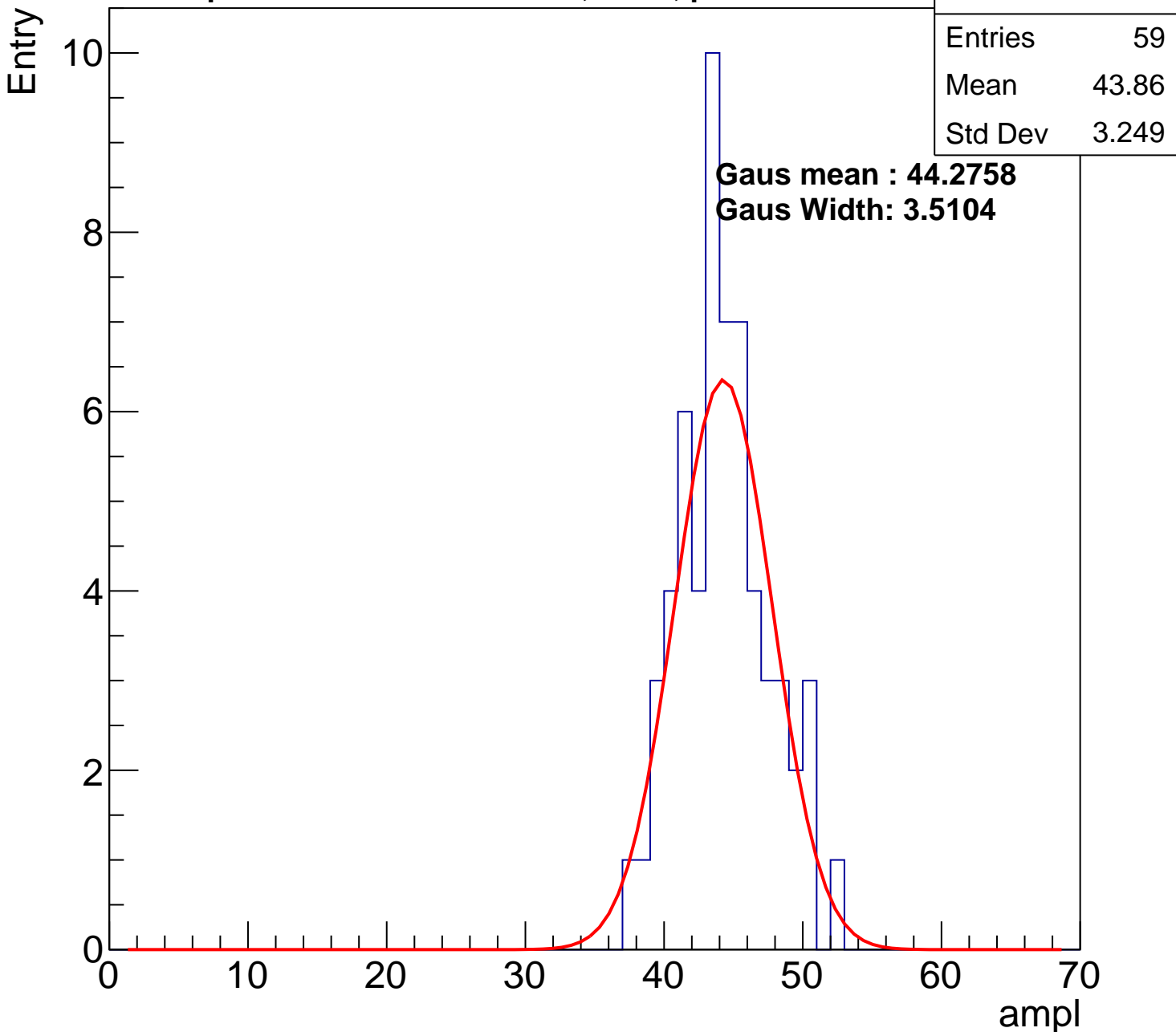
Gaus Width: 3.5104

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

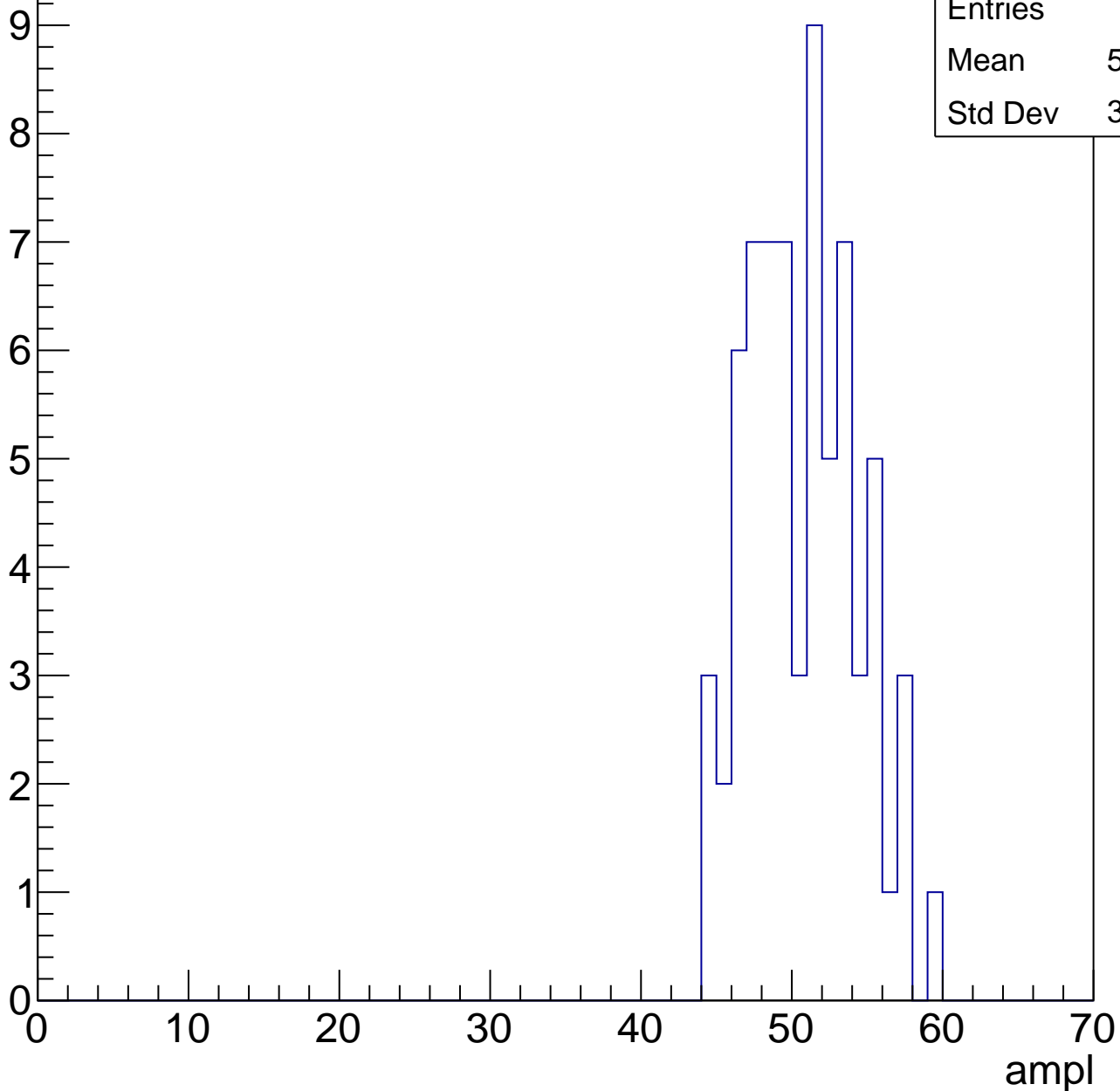


B0L001S, U19-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.28
Std Dev	3.563

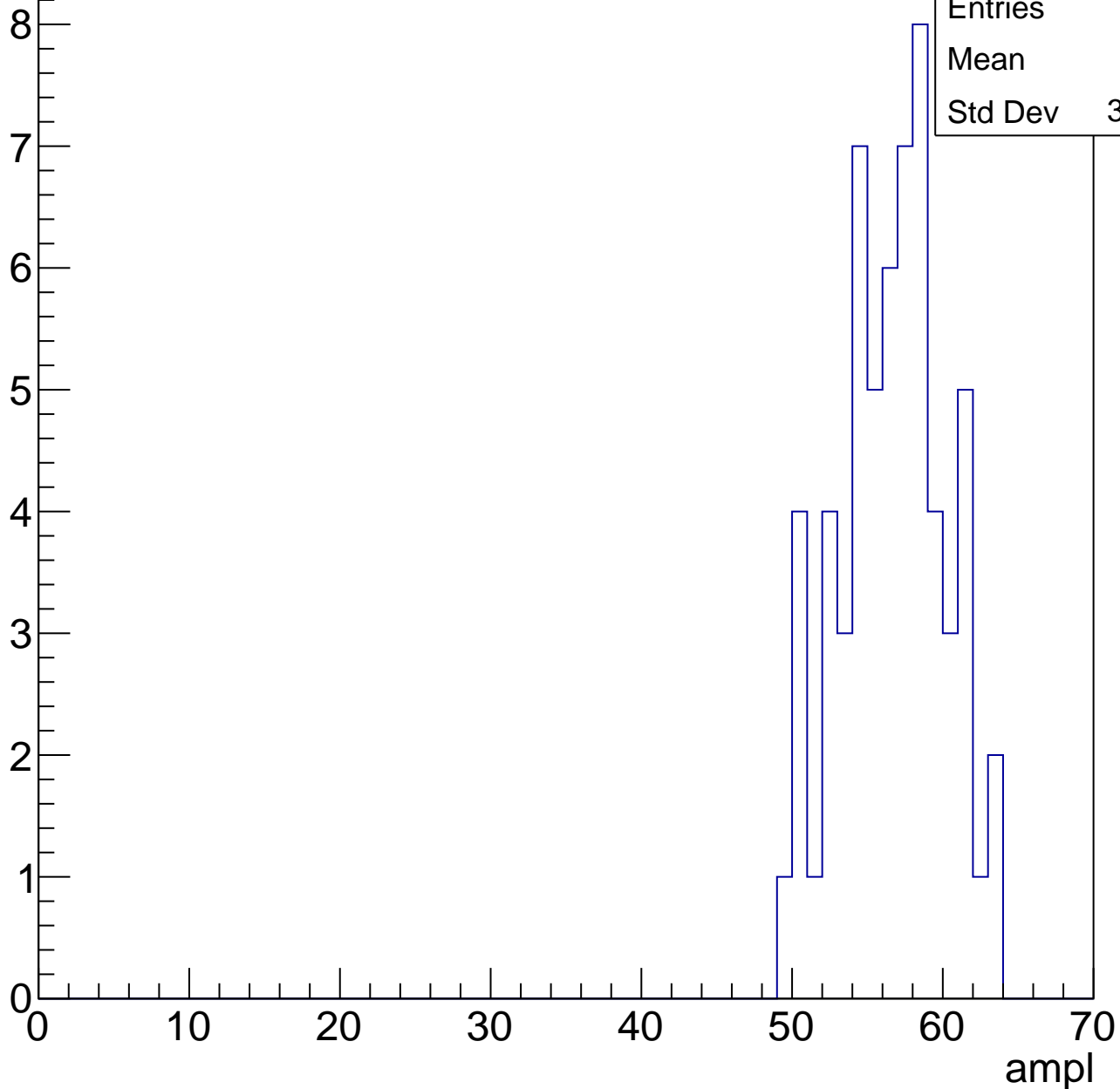


B0L001S, U19-ch96, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	56.2
Std Dev	3.439

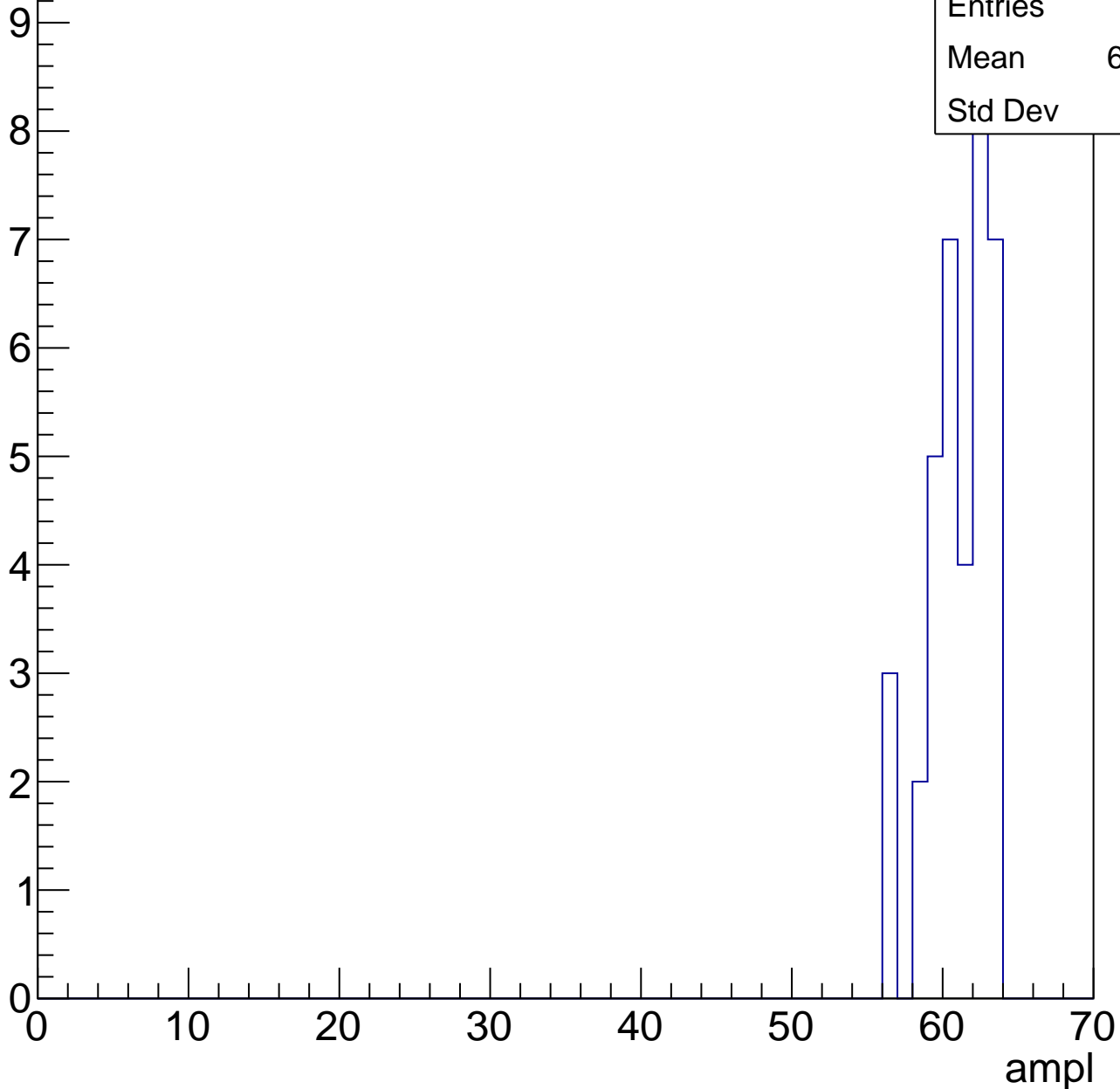


B0L001S, U19-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	60.59
Std Dev	2.02



B0L001S, U19-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

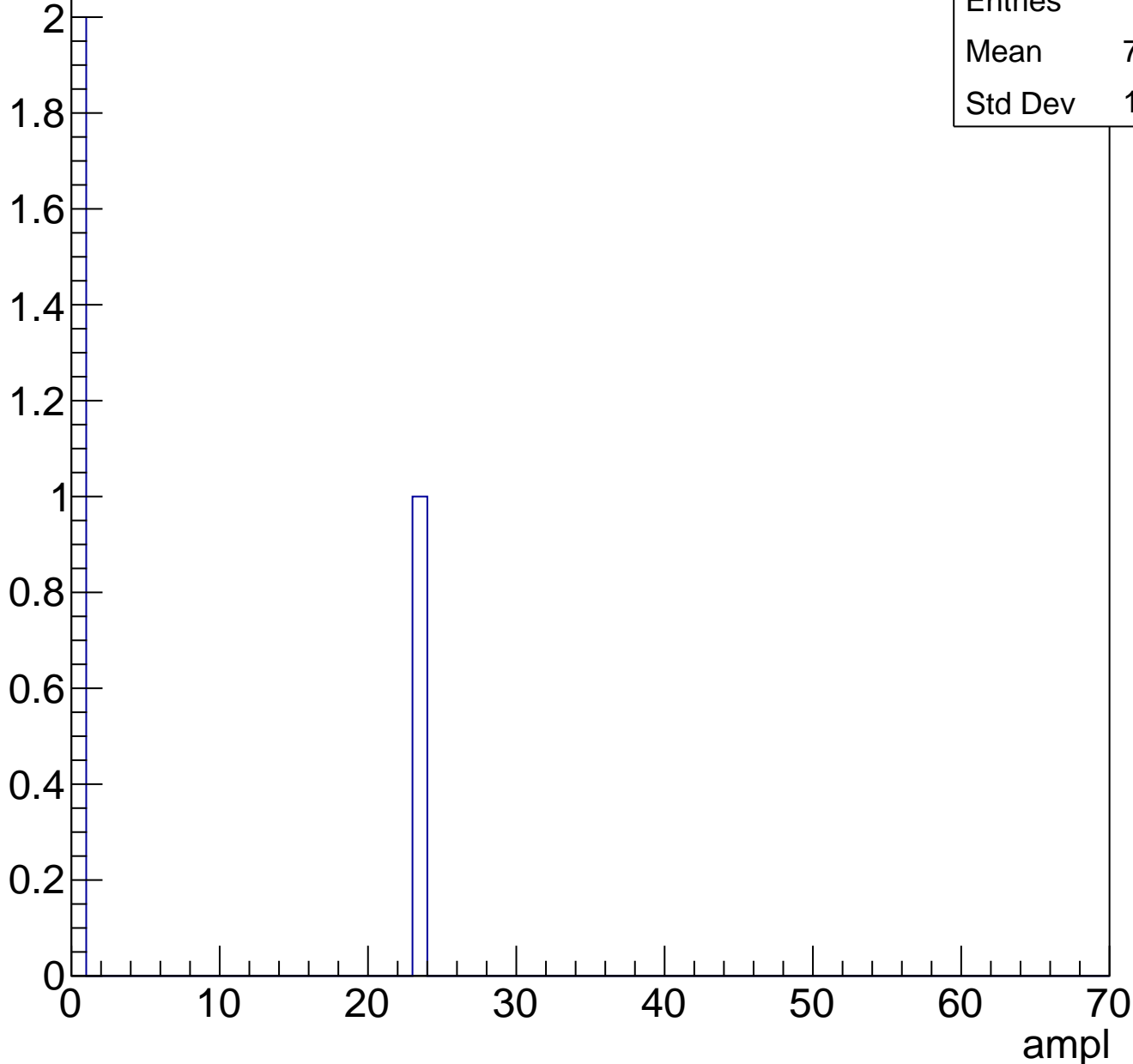
Entry



B0L001S, U19-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L001S, U19-ch97, adc0

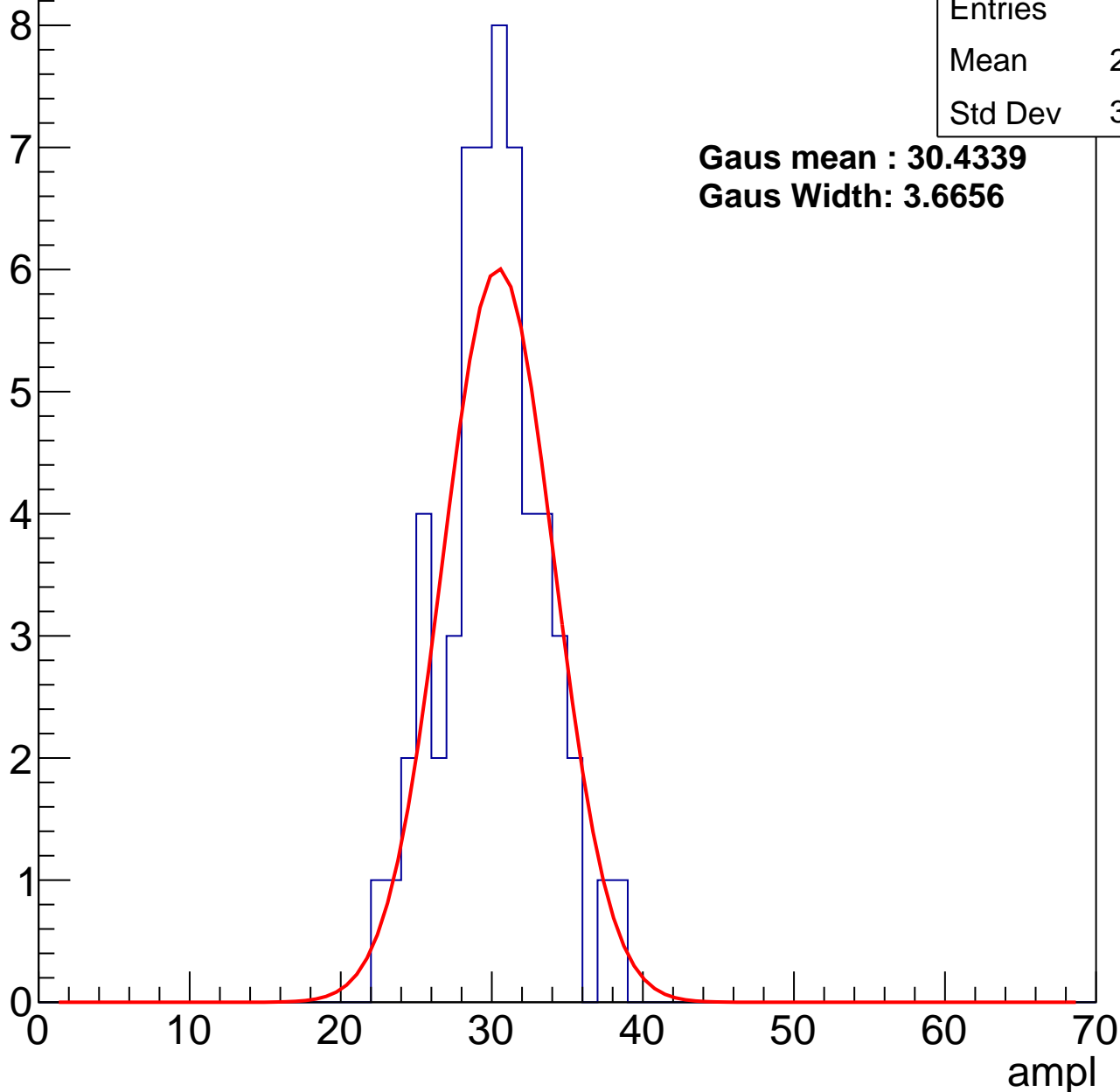
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	29.63
Std Dev	3.354

Gaus mean : 30.4339

Gaus Width: 3.6656



B0L001S, U19-ch97, adc1

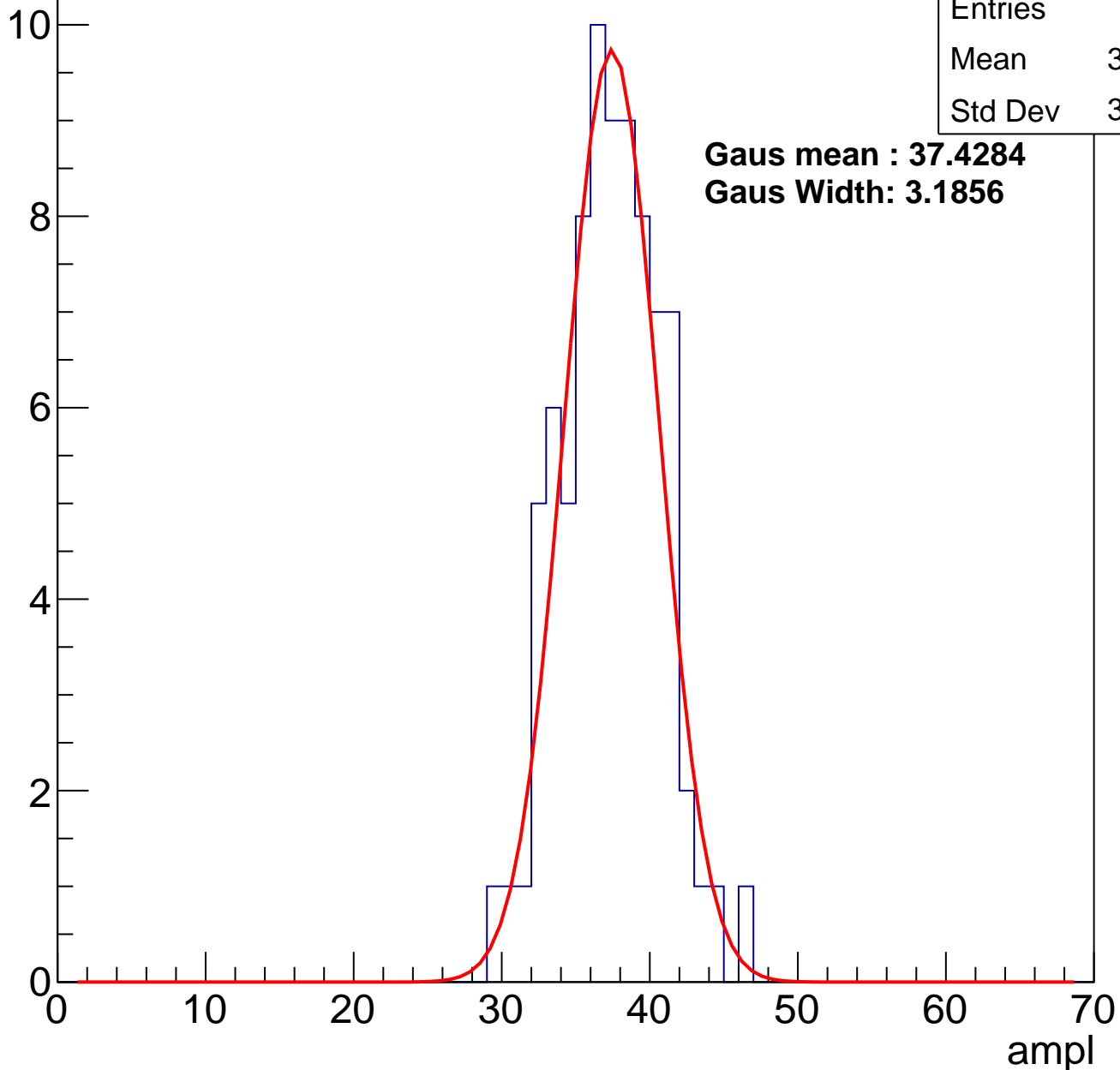
calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	36.94
Std Dev	3.292

Gaus mean : 37.4284

Gaus Width: 3.1856

Entry



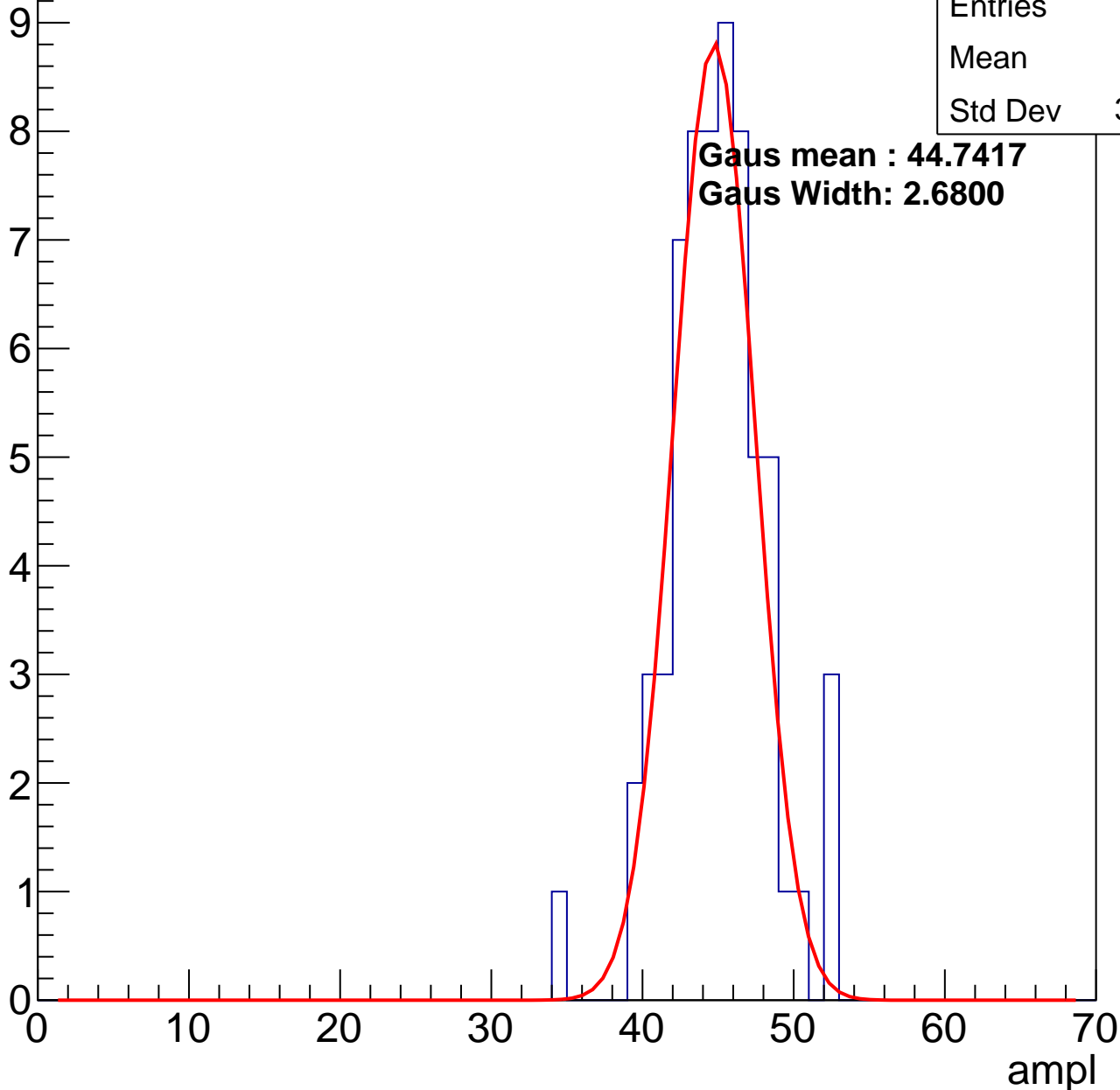
B0L001S, U19-ch97, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	44.5
Std Dev	3.221

Gaus mean : 44.7417
Gaus Width: 2.6800

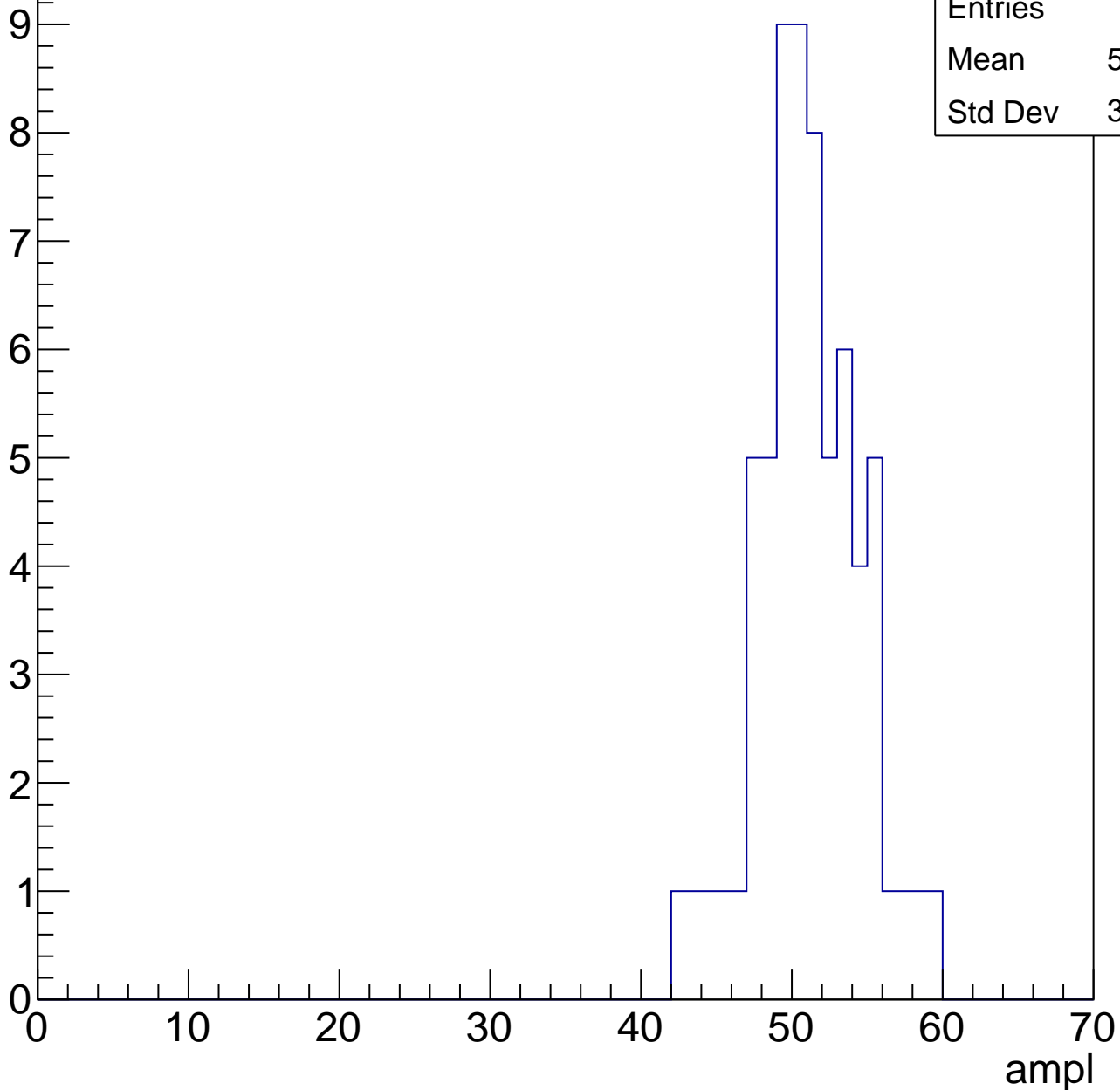


B0L001S, U19-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

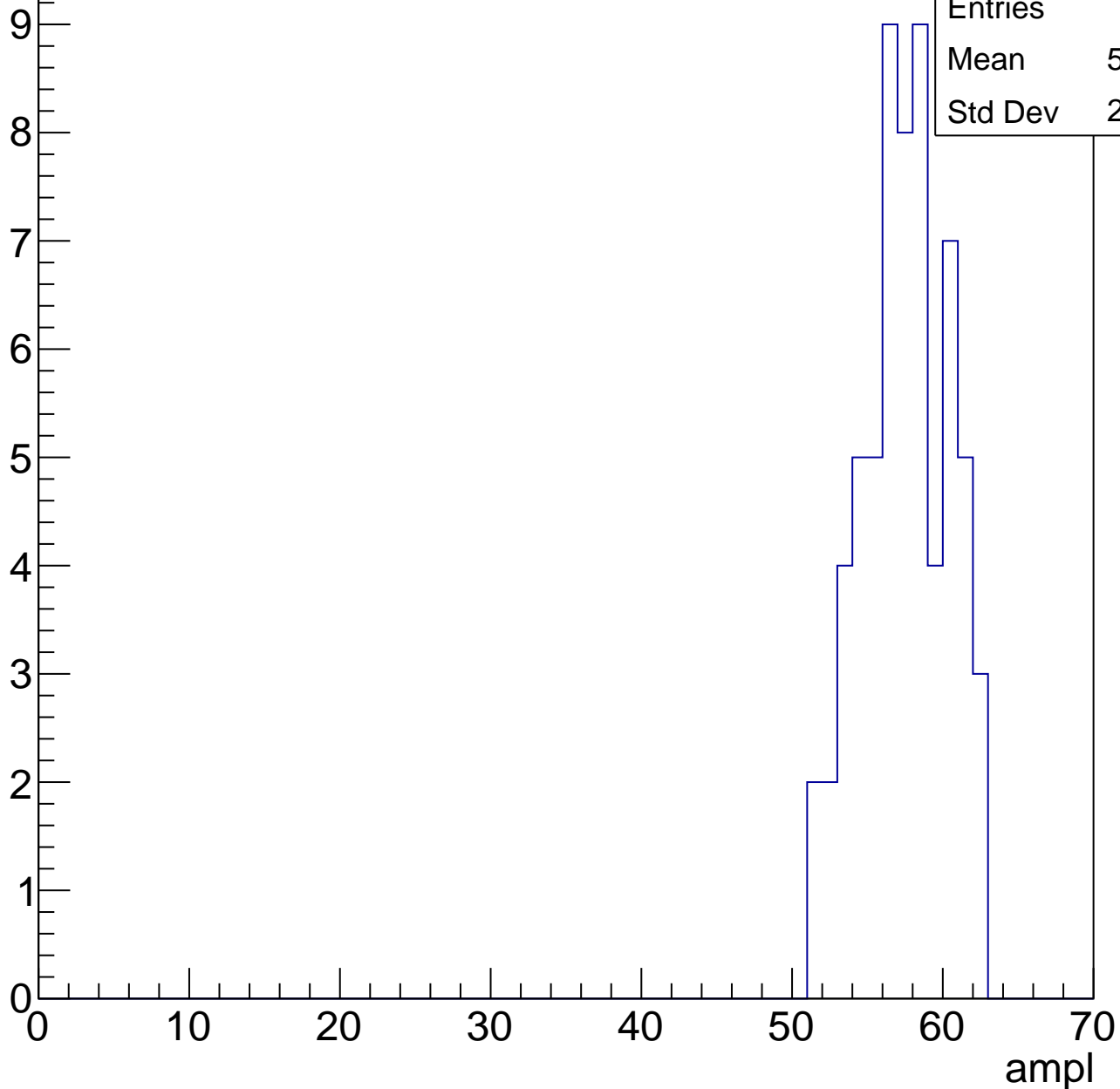
Entries	65
Mean	50.66
Std Dev	3.366



B0L001S, U19-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



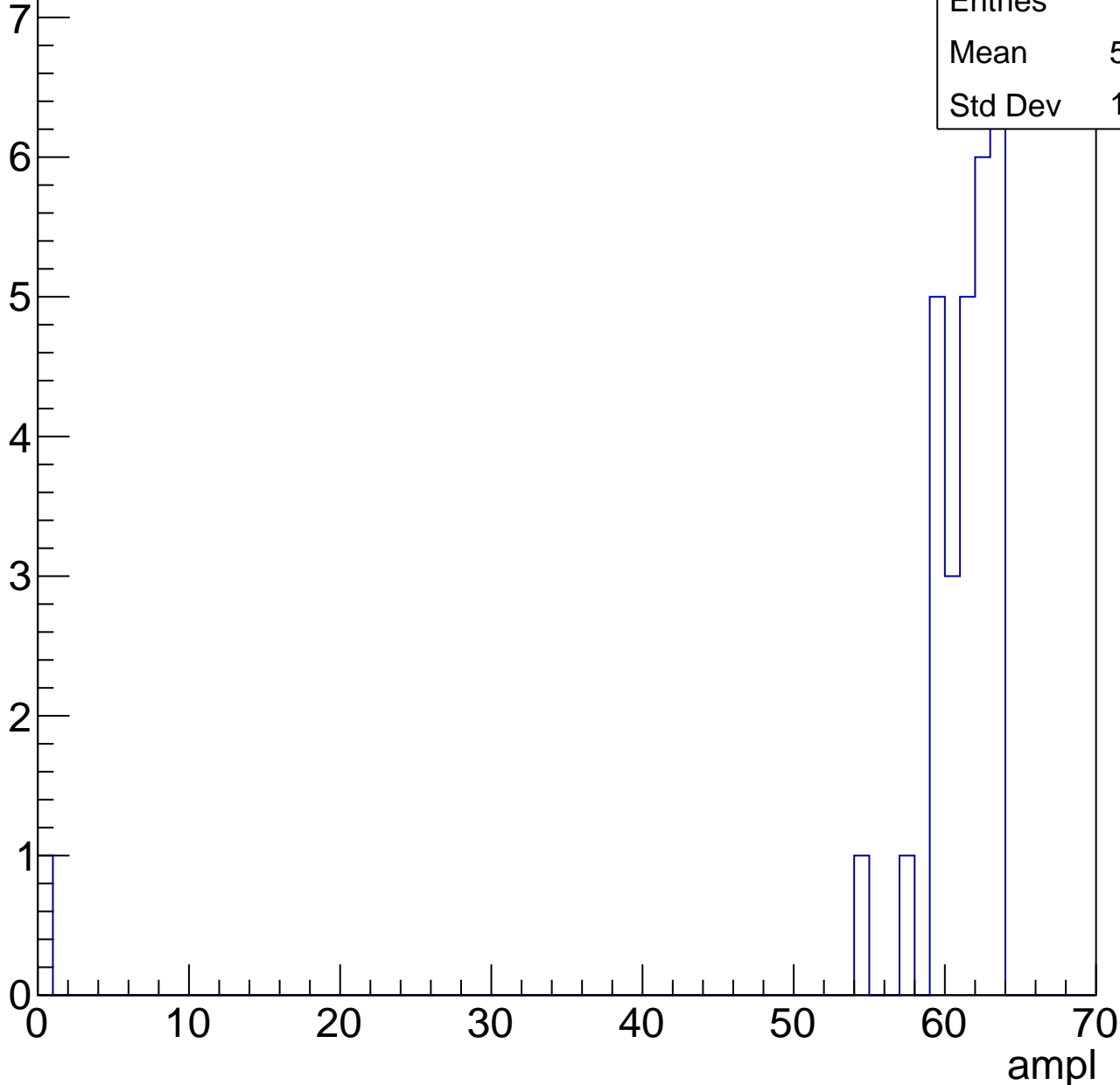
Entries	63
Mean	57.02
Std Dev	2.826

B0L001S, U19-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.76
Std Dev	11.29



B0L001S, U19-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	62.75
Std Dev	0.433

B0L001S, U19-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch98, adc0

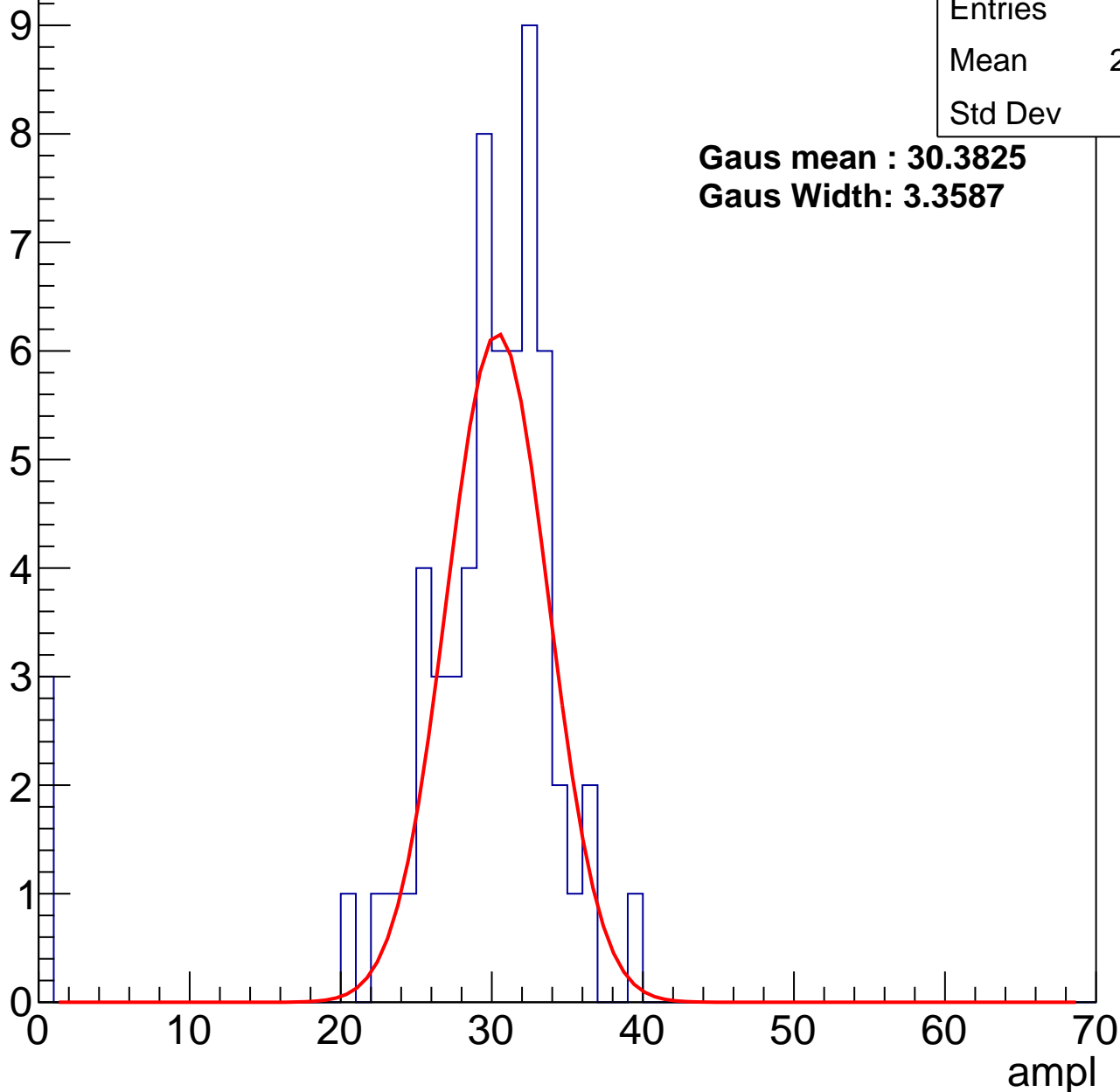
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	28.35
Std Dev	7.28

Gaus mean : 30.3825

Gaus Width: 3.3587



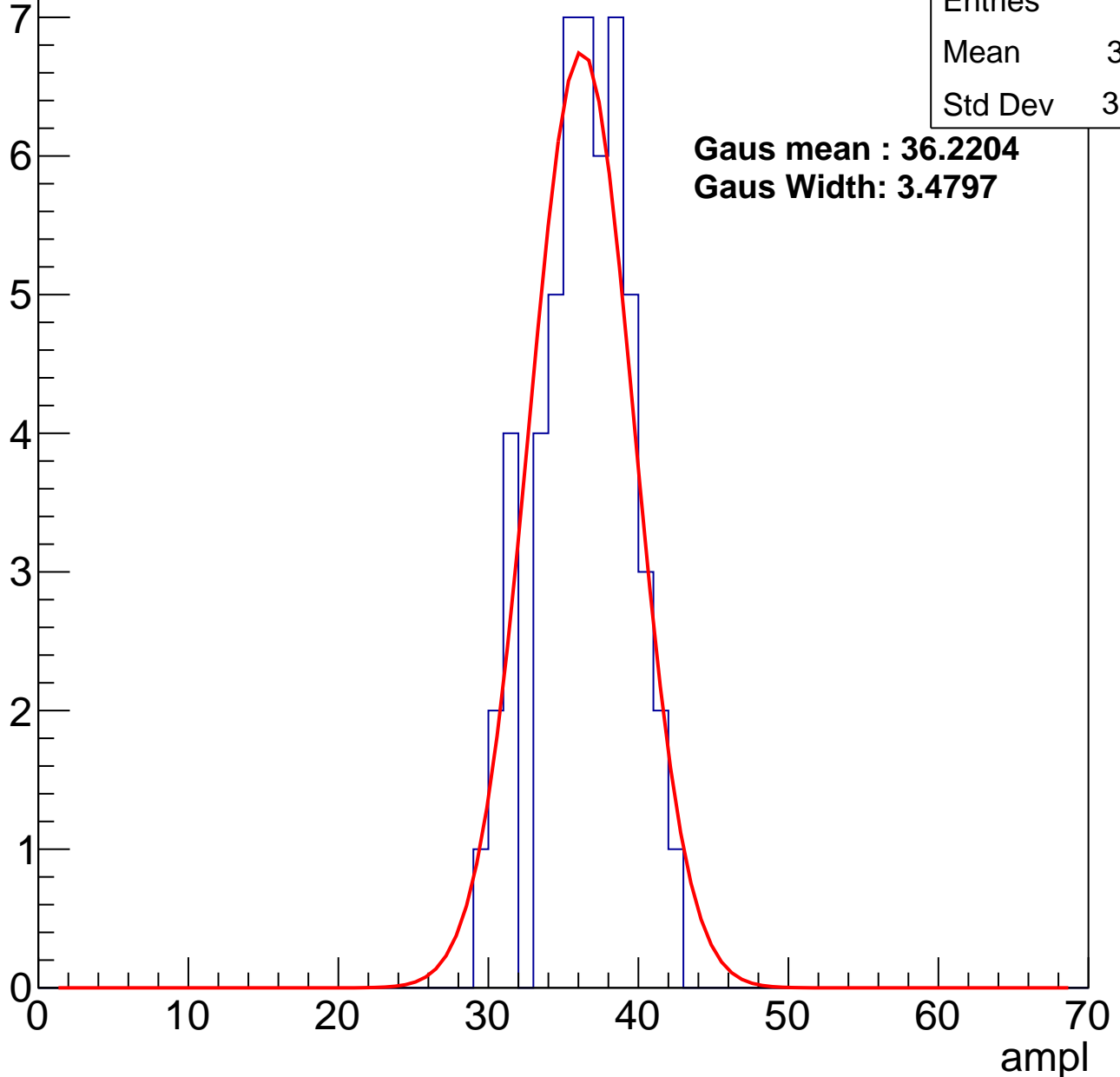
B0L001S, U19-ch98, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	35.91
Std Dev	3.032

Gaus mean : 36.2204
Gaus Width: 3.4797



B0L001S, U19-ch98, adc2

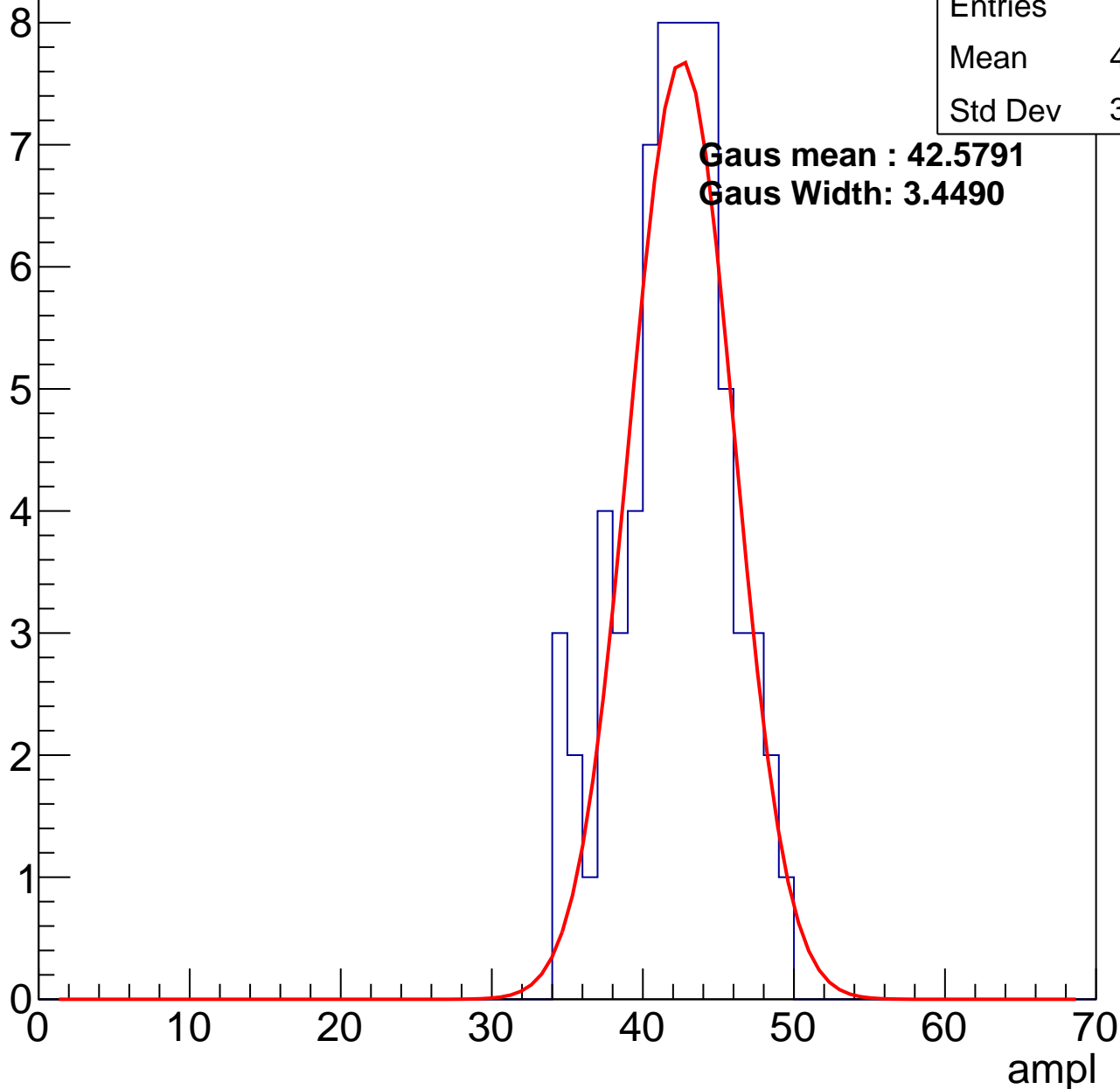
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	41.64
Std Dev	3.517

Gaus mean : 42.5791

Gaus Width: 3.4490

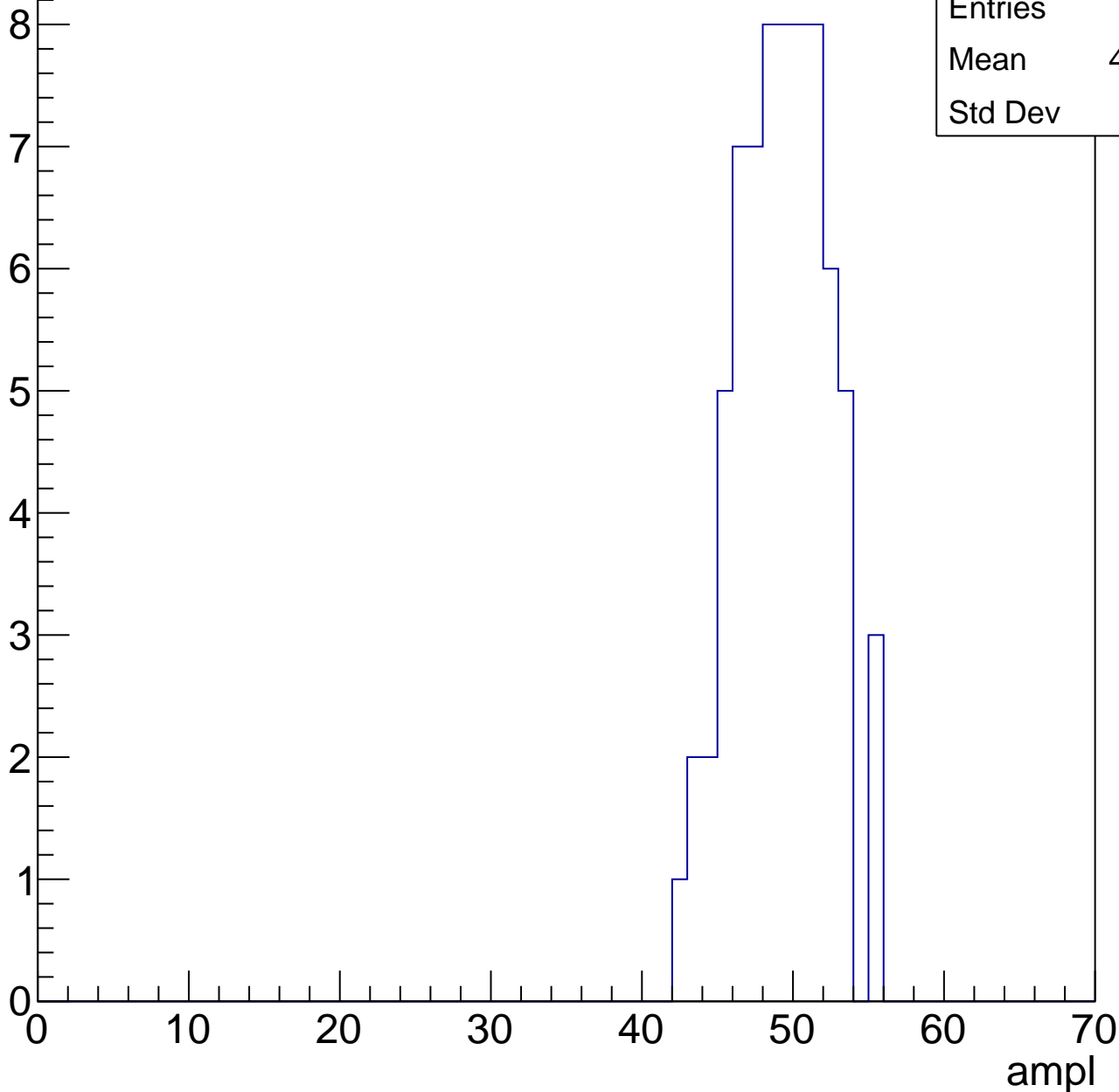


B0L001S, U19-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

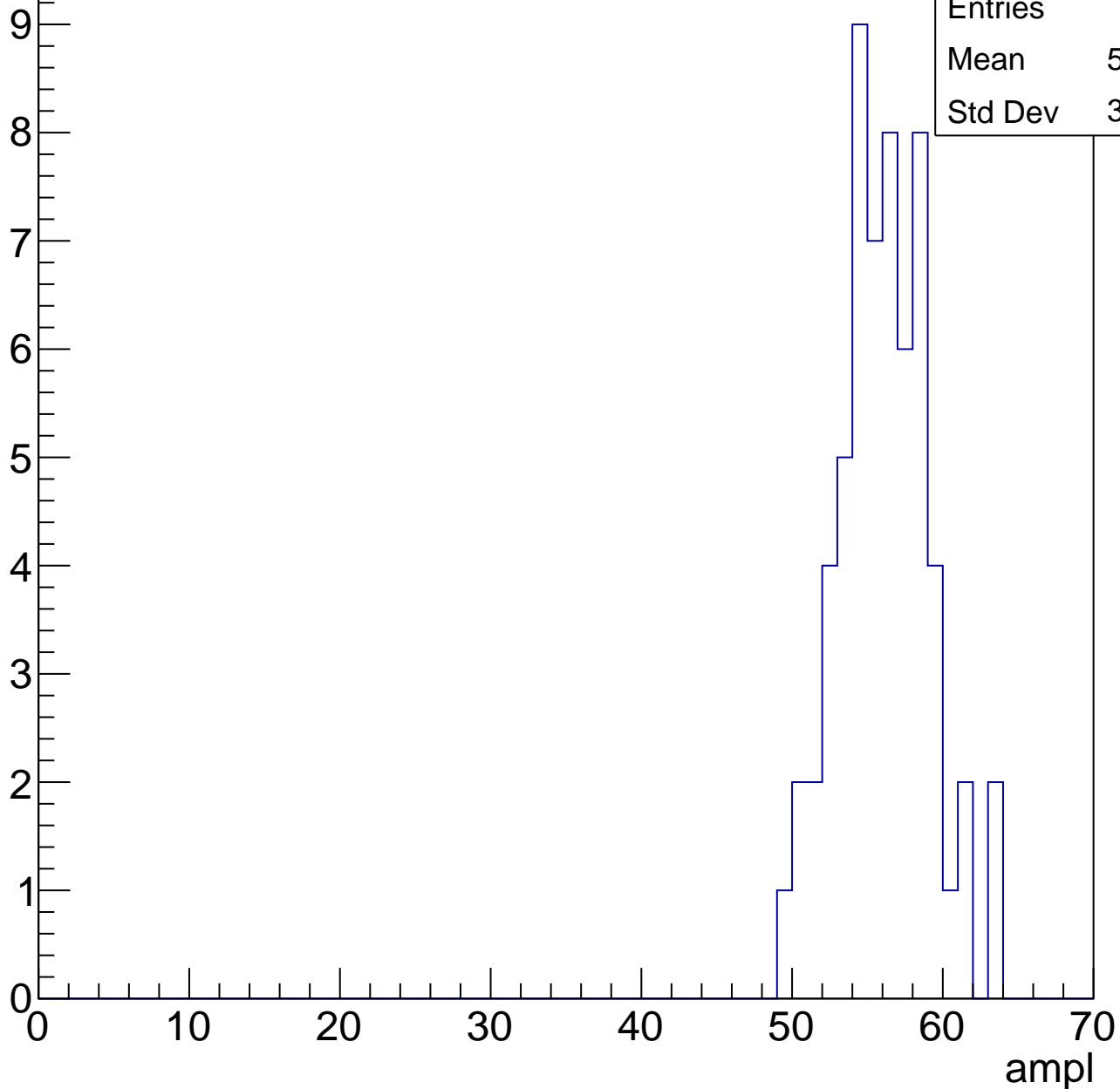
Entries	70
Mean	48.83
Std Dev	3



B0L001S, U19-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



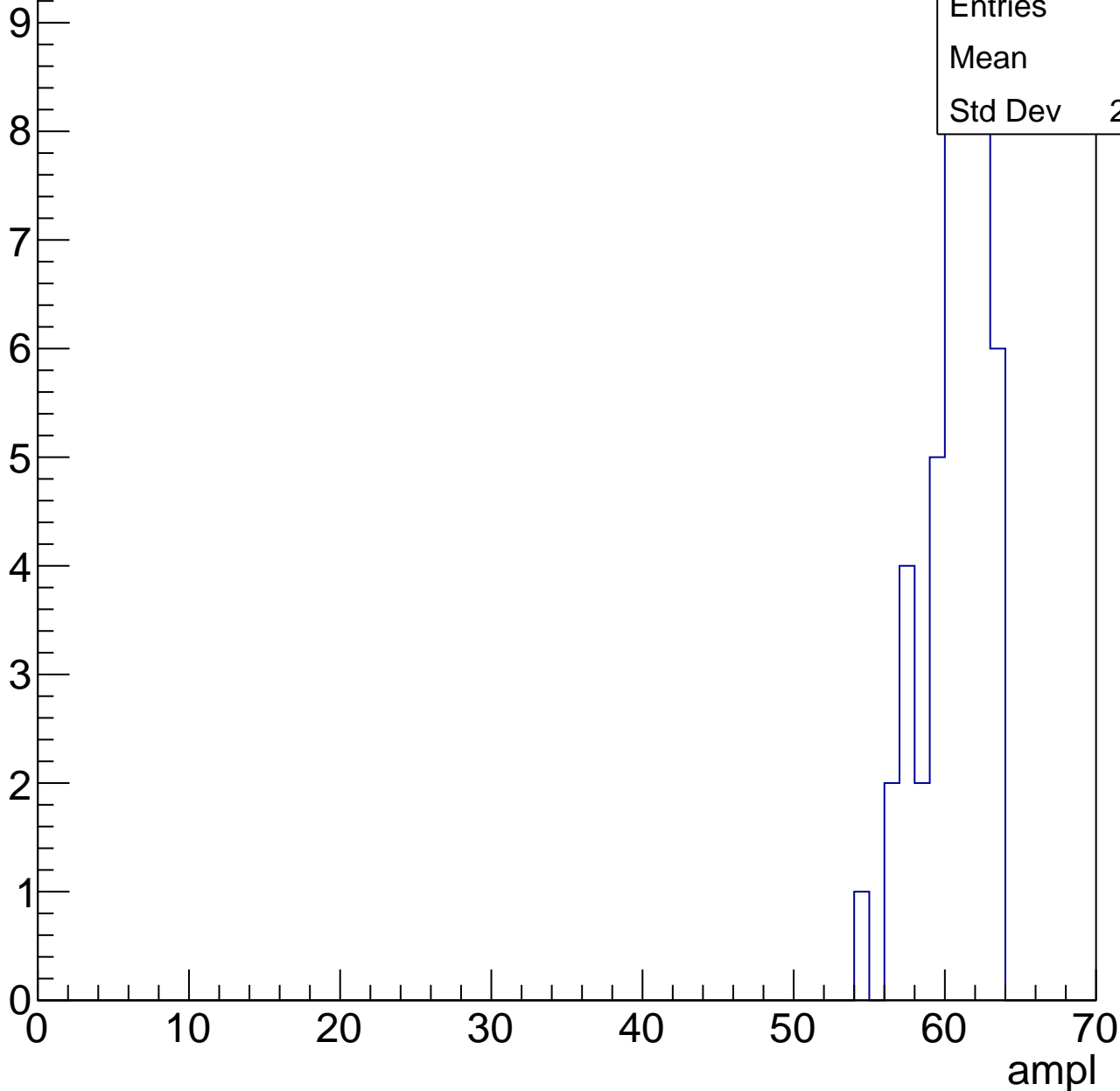
Entries	61
Mean	55.62
Std Dev	3.009

B0L001S, U19-ch98, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	60.2
Std Dev	2.177



B0L001S, U19-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

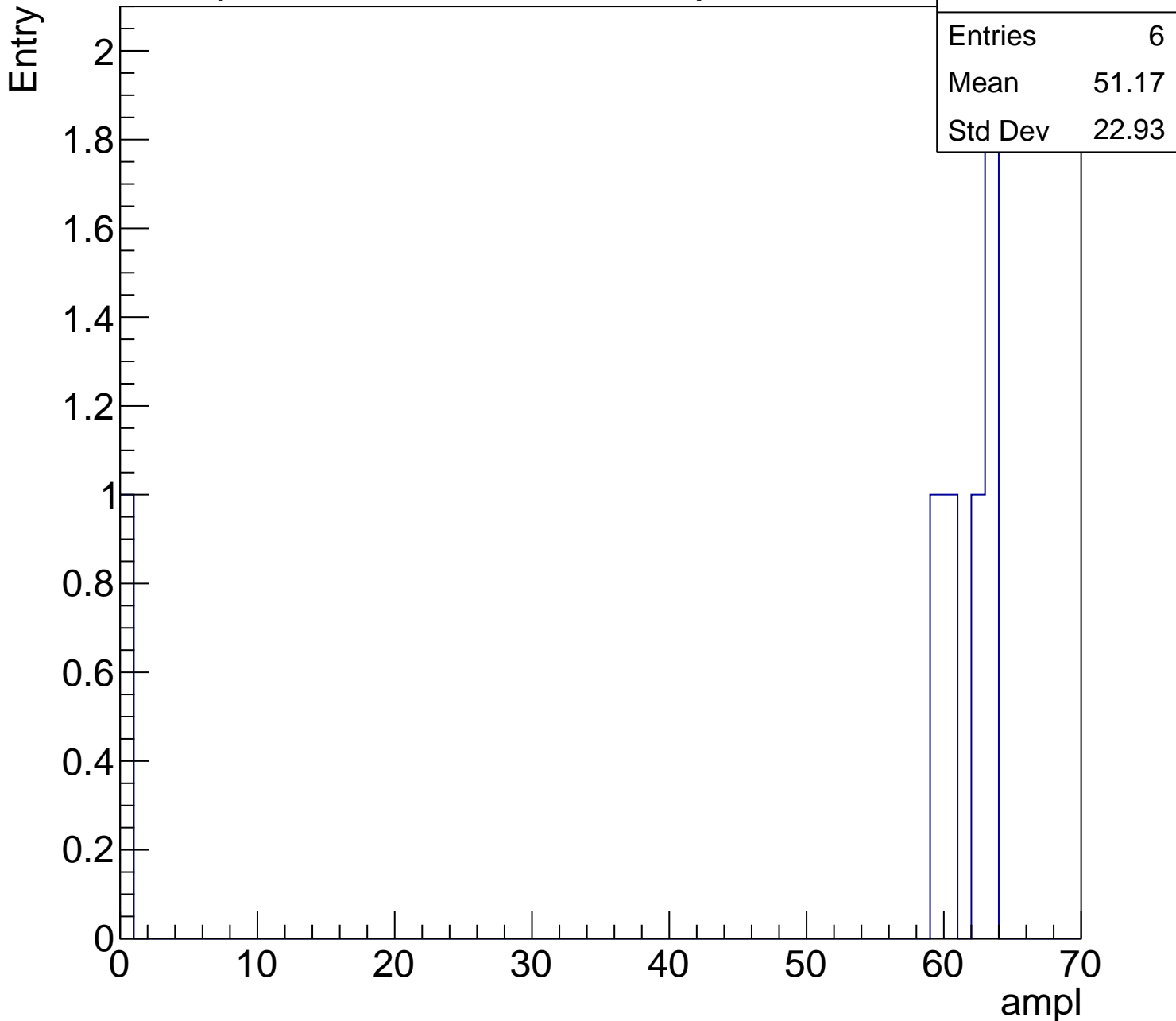
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.93

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch99, adc0

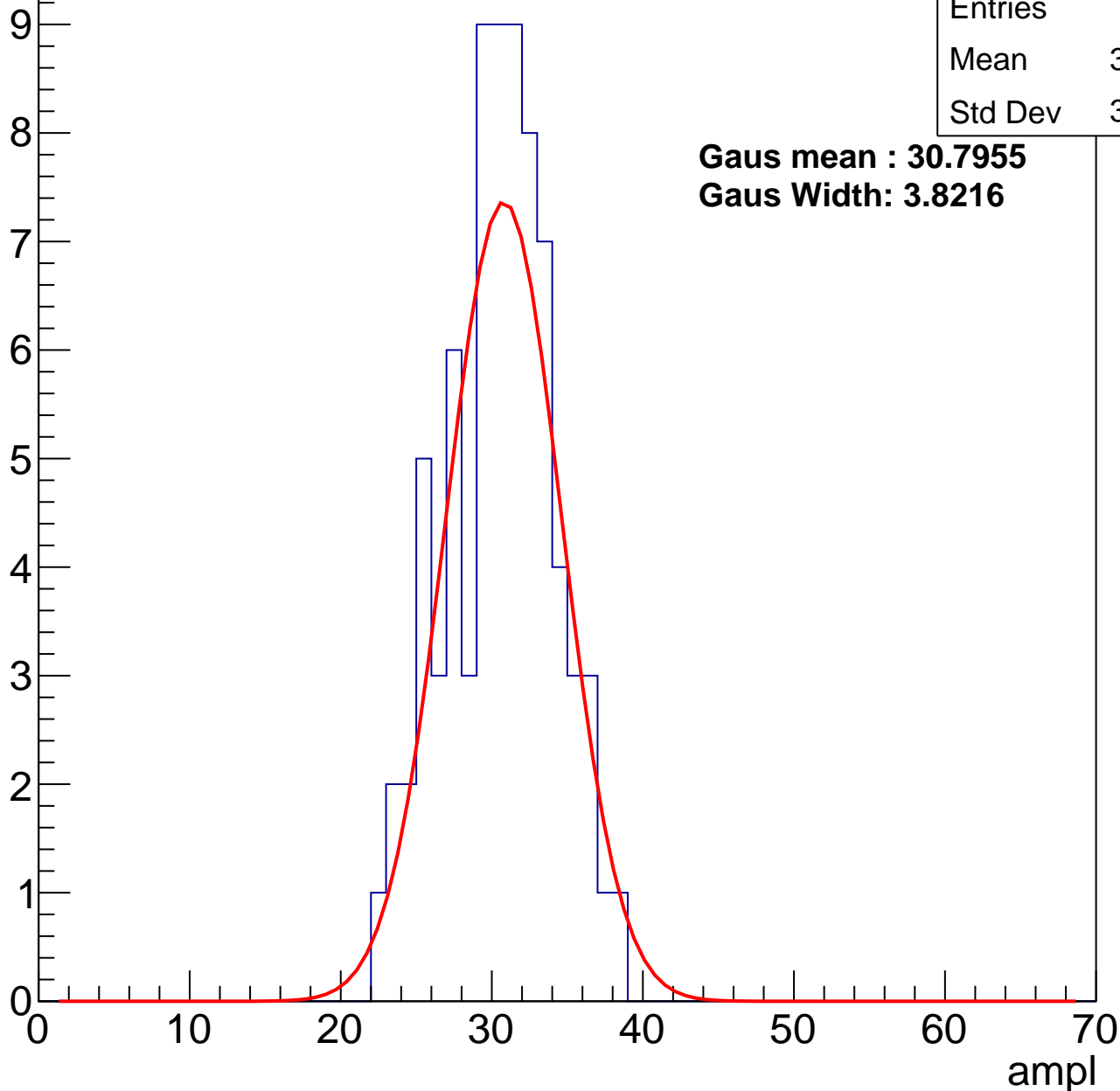
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	30.08
Std Dev	3.527

Gaus mean : 30.7955

Gaus Width: 3.8216



B0L001S, U19-ch99, adc1

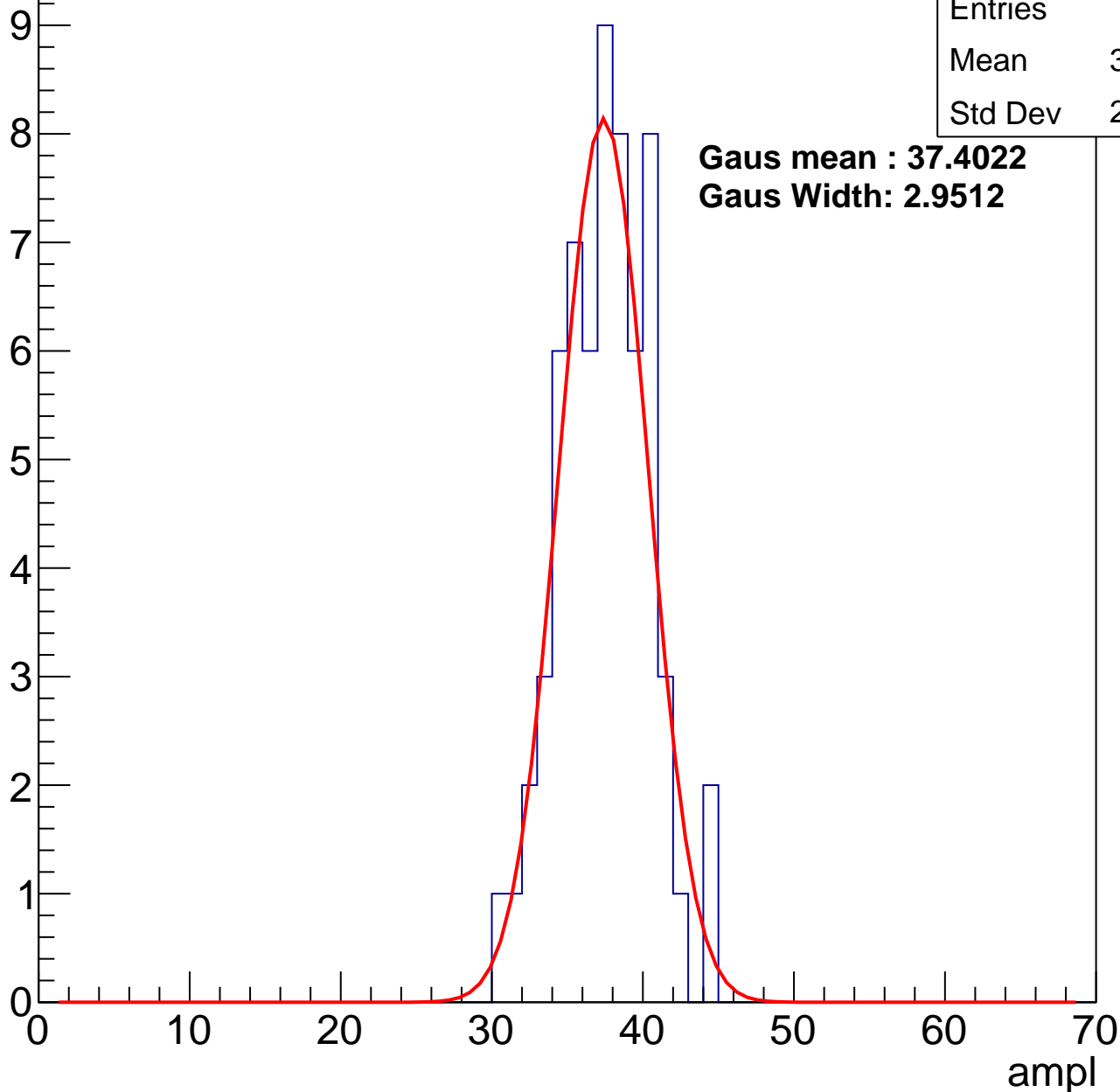
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.03
Std Dev	2.944

Gaus mean : 37.4022

Gaus Width: 2.9512



B0L001S, U19-ch99, adc2

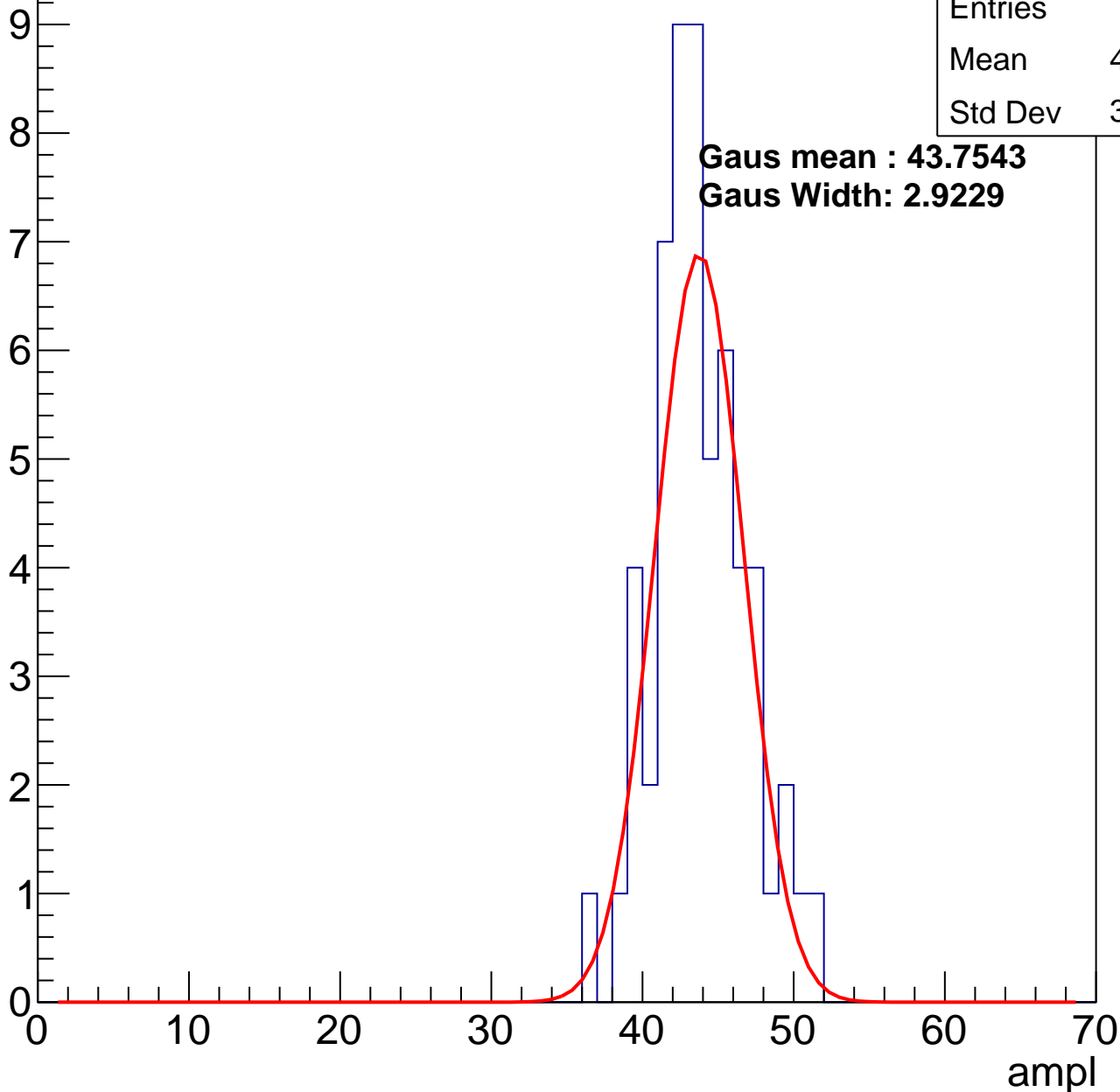
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.35
Std Dev	3.035

Gaus mean : 43.7543

Gaus Width: 2.9229

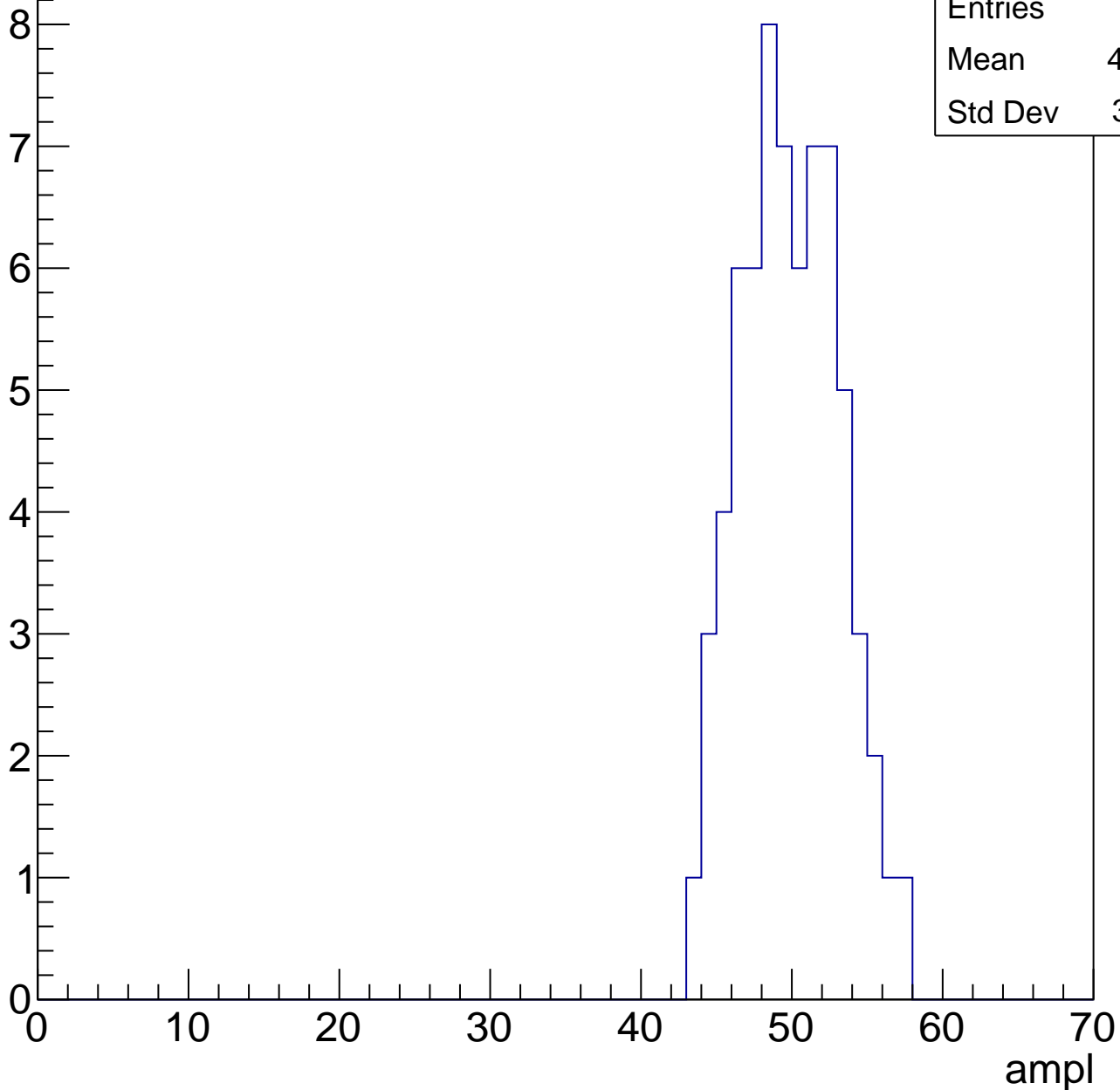


B0L001S, U19-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

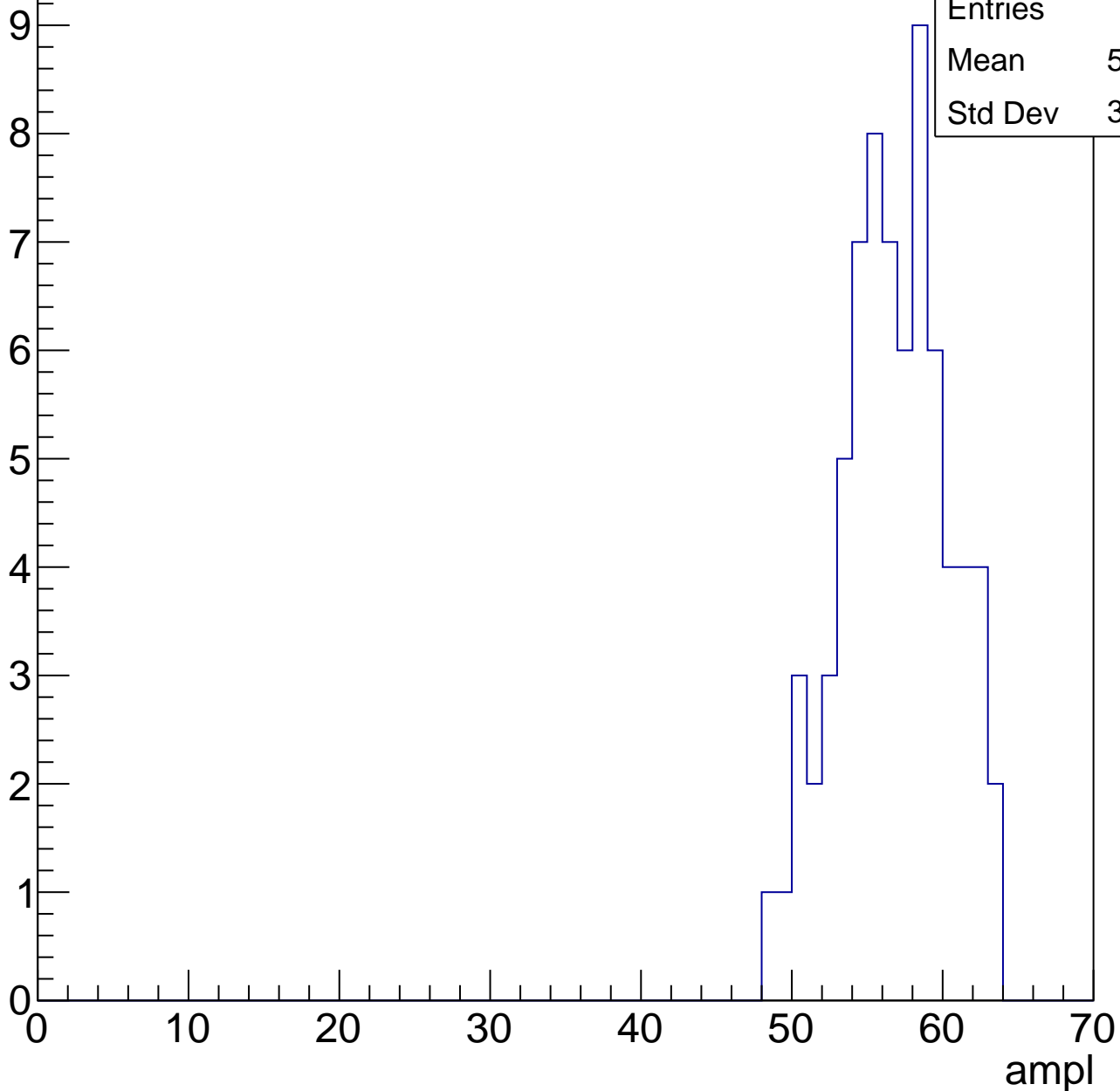
Entries	67
Mean	49.42
Std Dev	3.201



B0L001S, U19-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



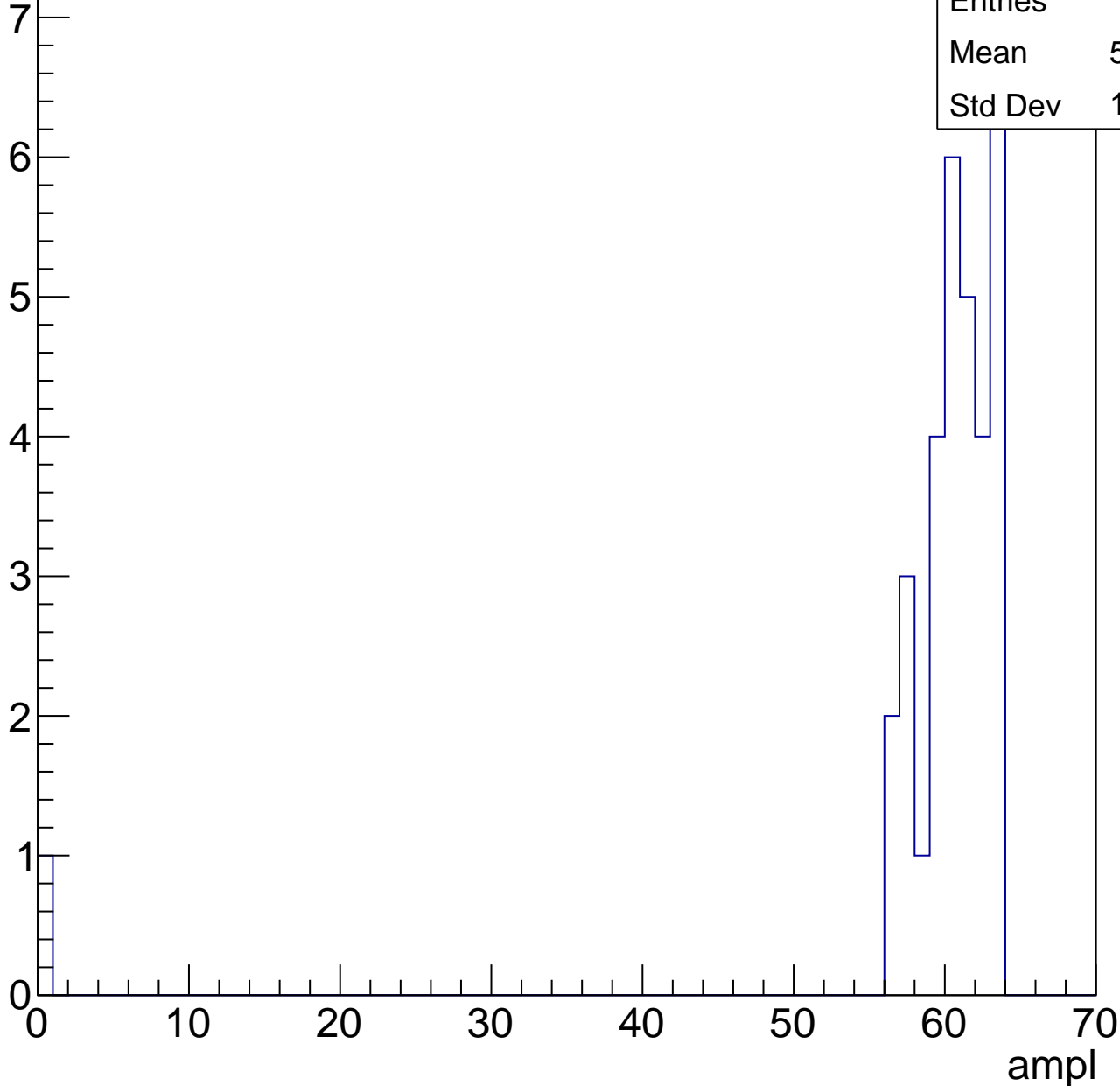
Entries	72
Mean	56.33
Std Dev	3.524

B0L001S, U19-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.52
Std Dev	10.56



B0L001S, U19-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	62
Std Dev	0.7071

B0L001S, U19-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch100, adc0

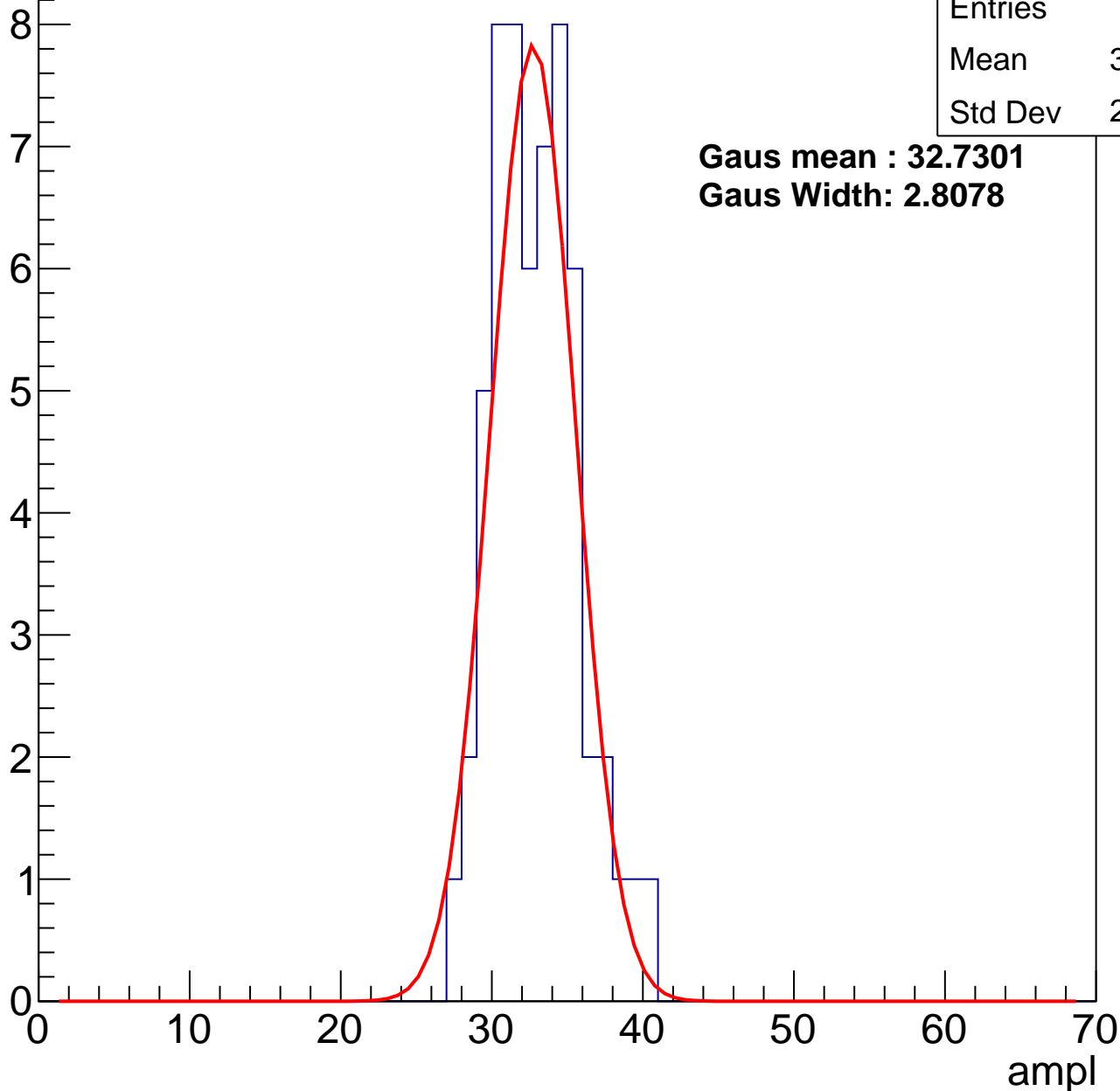
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	32.48
Std Dev	2.793

Gaus mean : 32.7301

Gaus Width: 2.8078



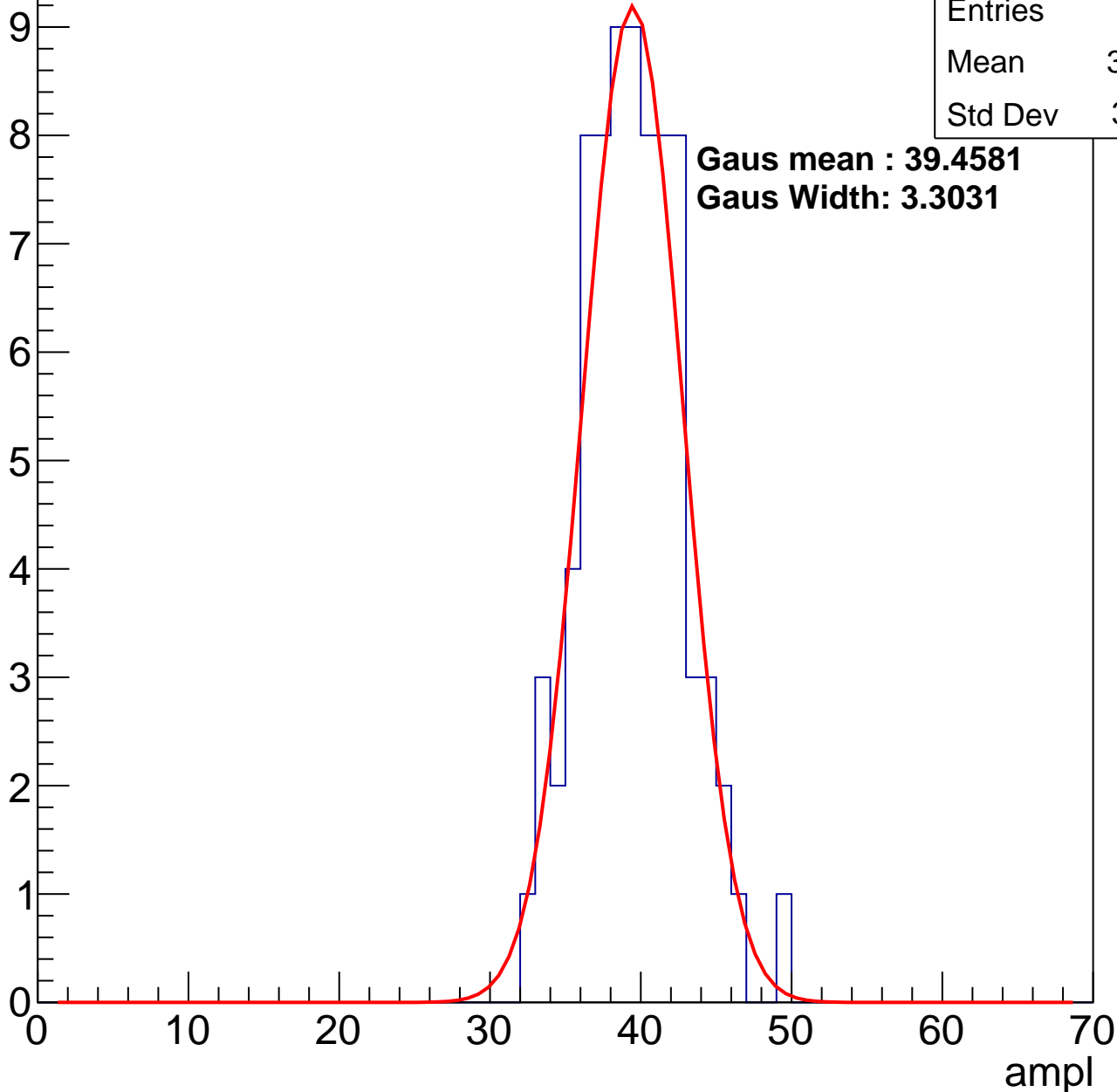
B0L001S, U19-ch100, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	39.05
Std Dev	3.281

Gaus mean : 39.4581
Gaus Width: 3.3031



B0L001S, U19-ch100, adc2

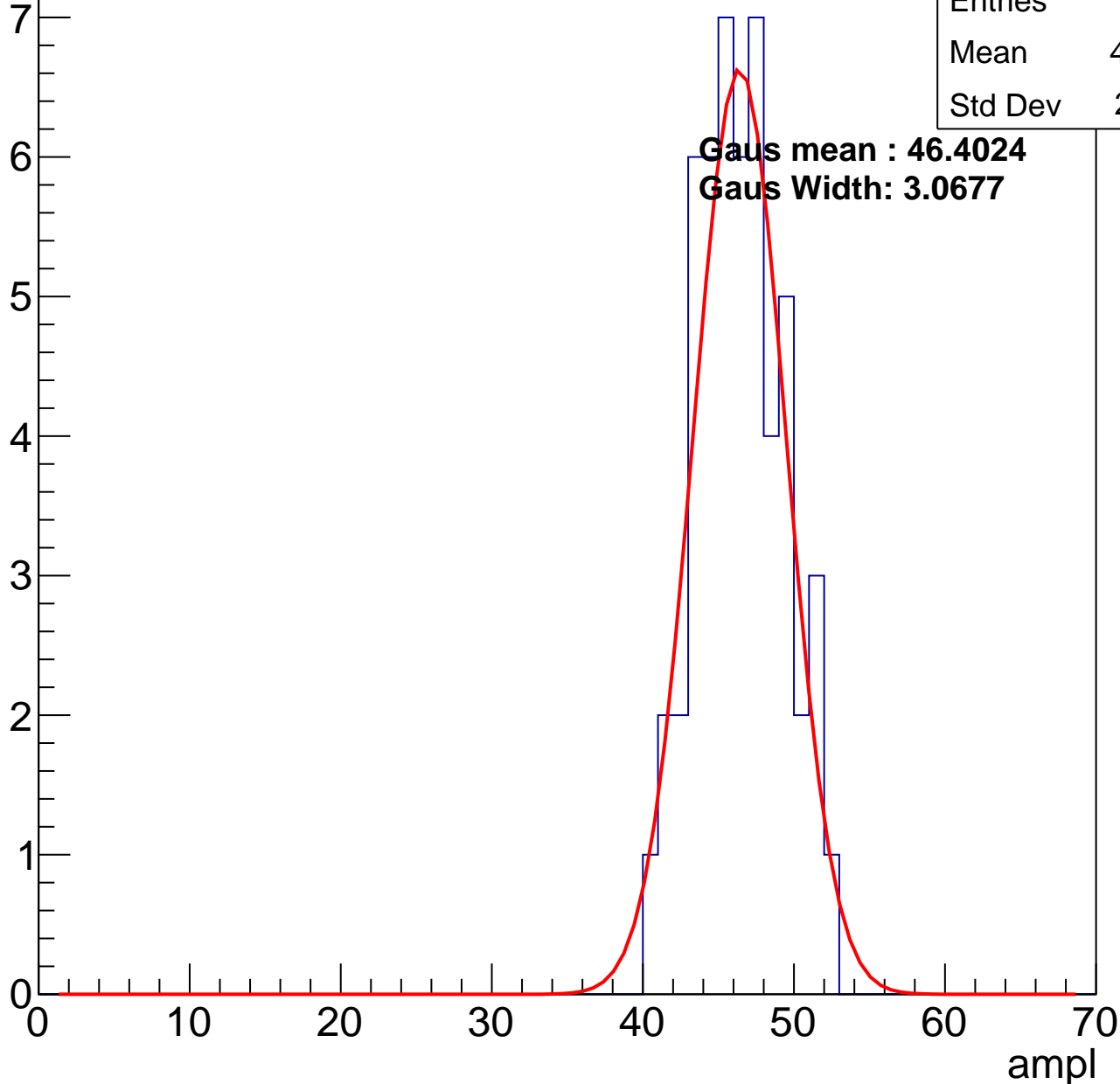
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	45.96
Std Dev	2.821

Gaus mean : 46.4024

Gaus Width: 3.0677

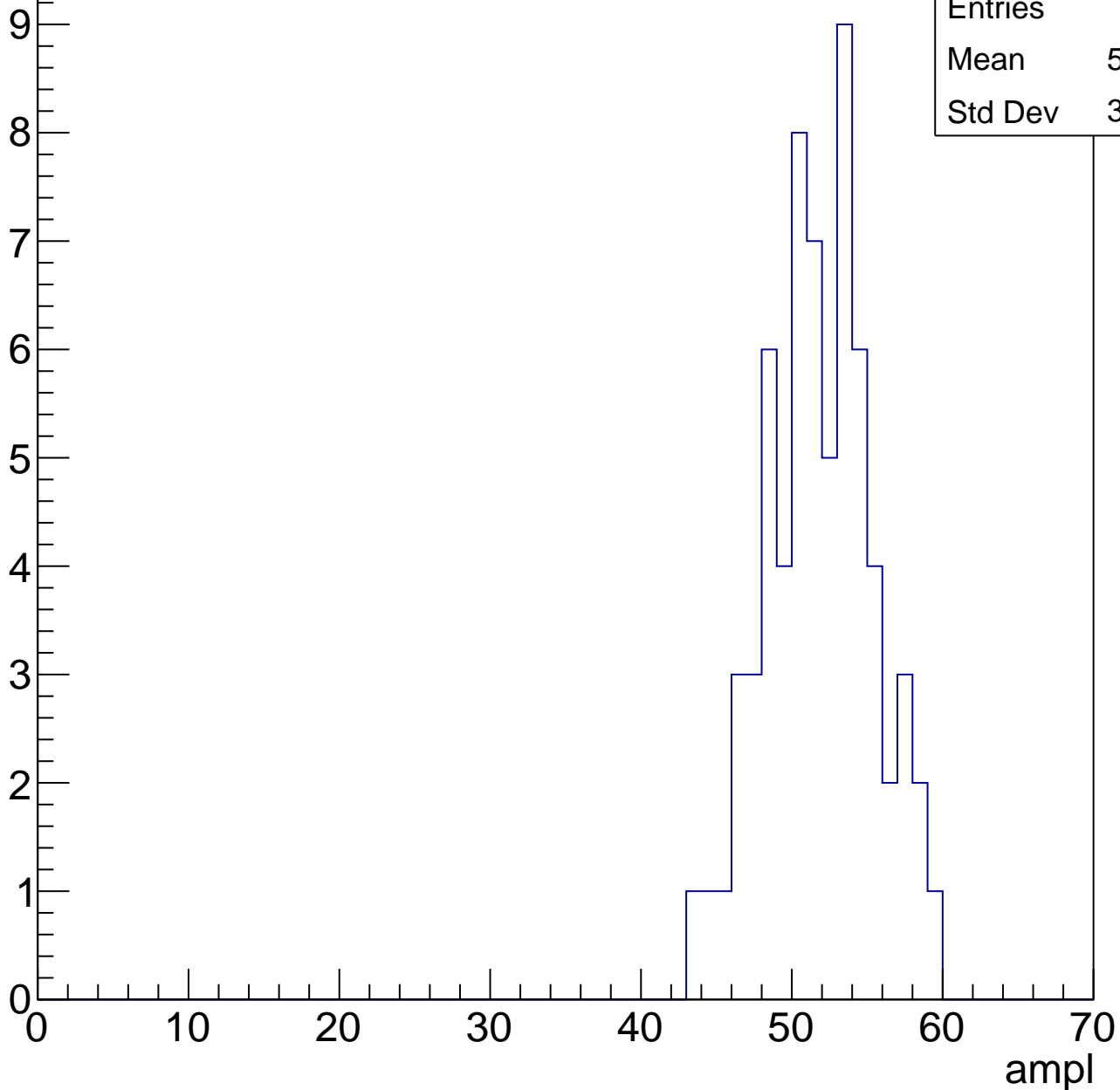


B0L001S, U19-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	51.38
Std Dev	3.524

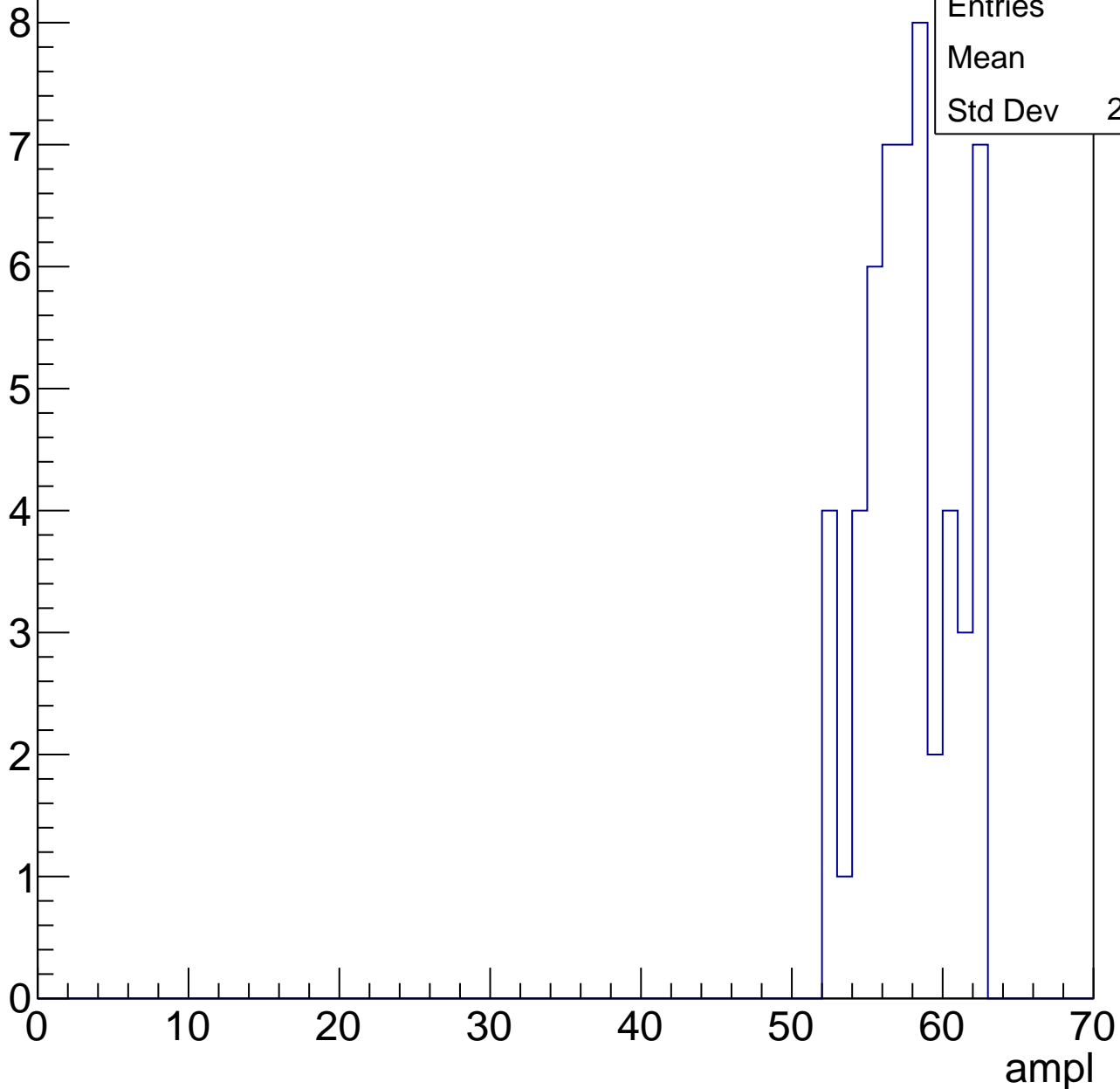


B0L001S, U19-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	57.3
Std Dev	2.924

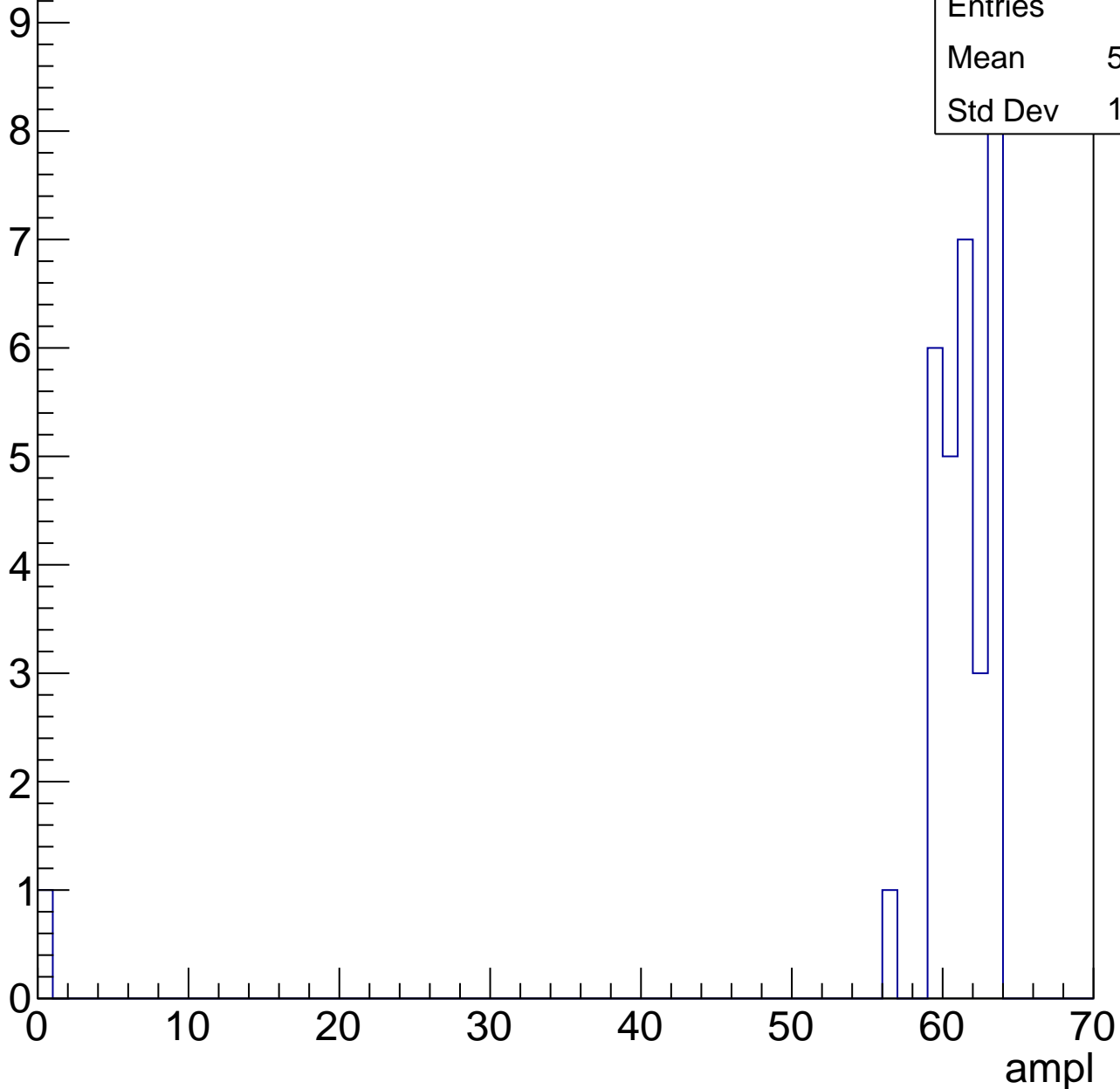


B0L001S, U19-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

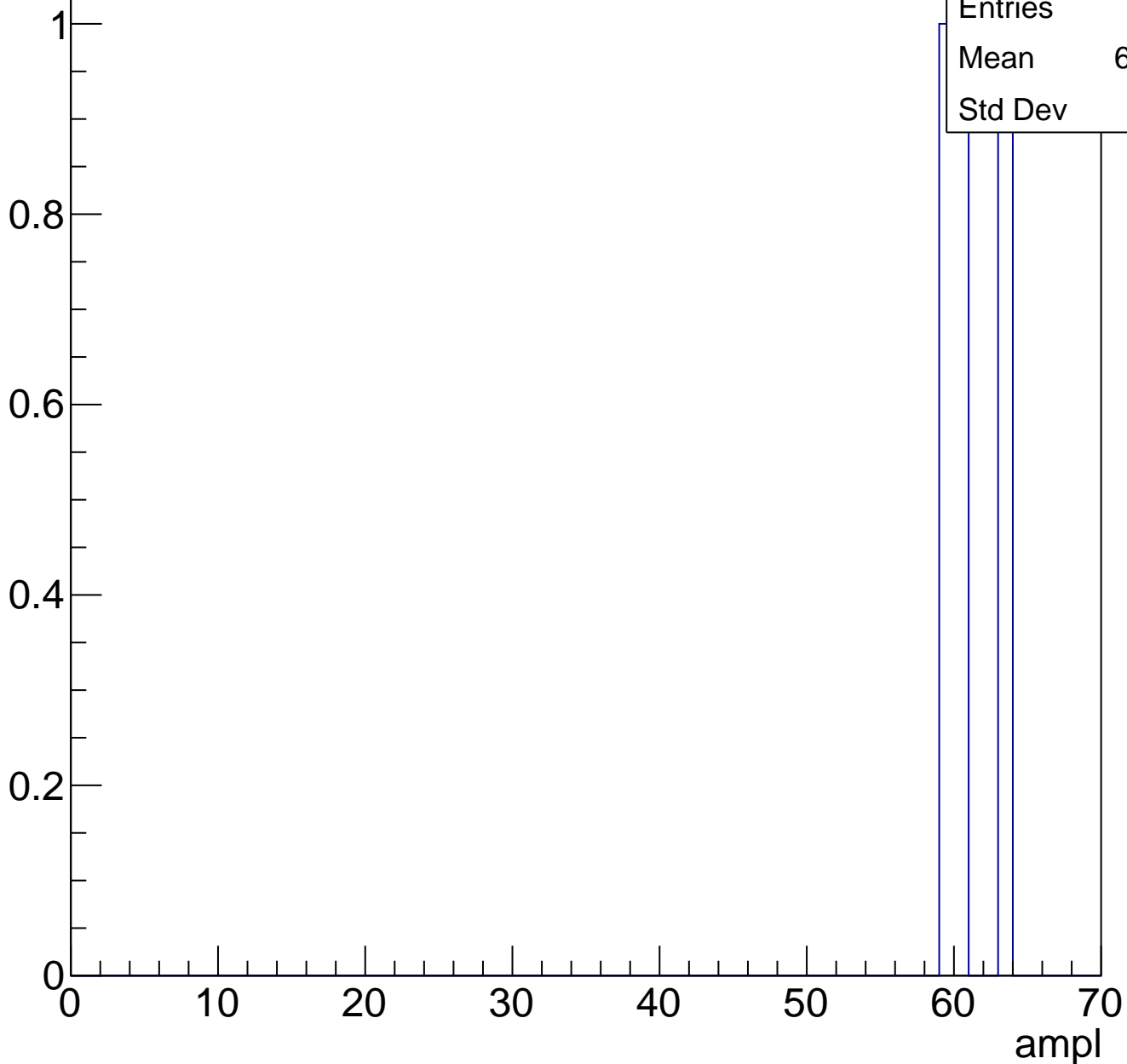
Entries	32
Mean	59.06
Std Dev	10.74



B0L001S, U19-ch100, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch101, adc0

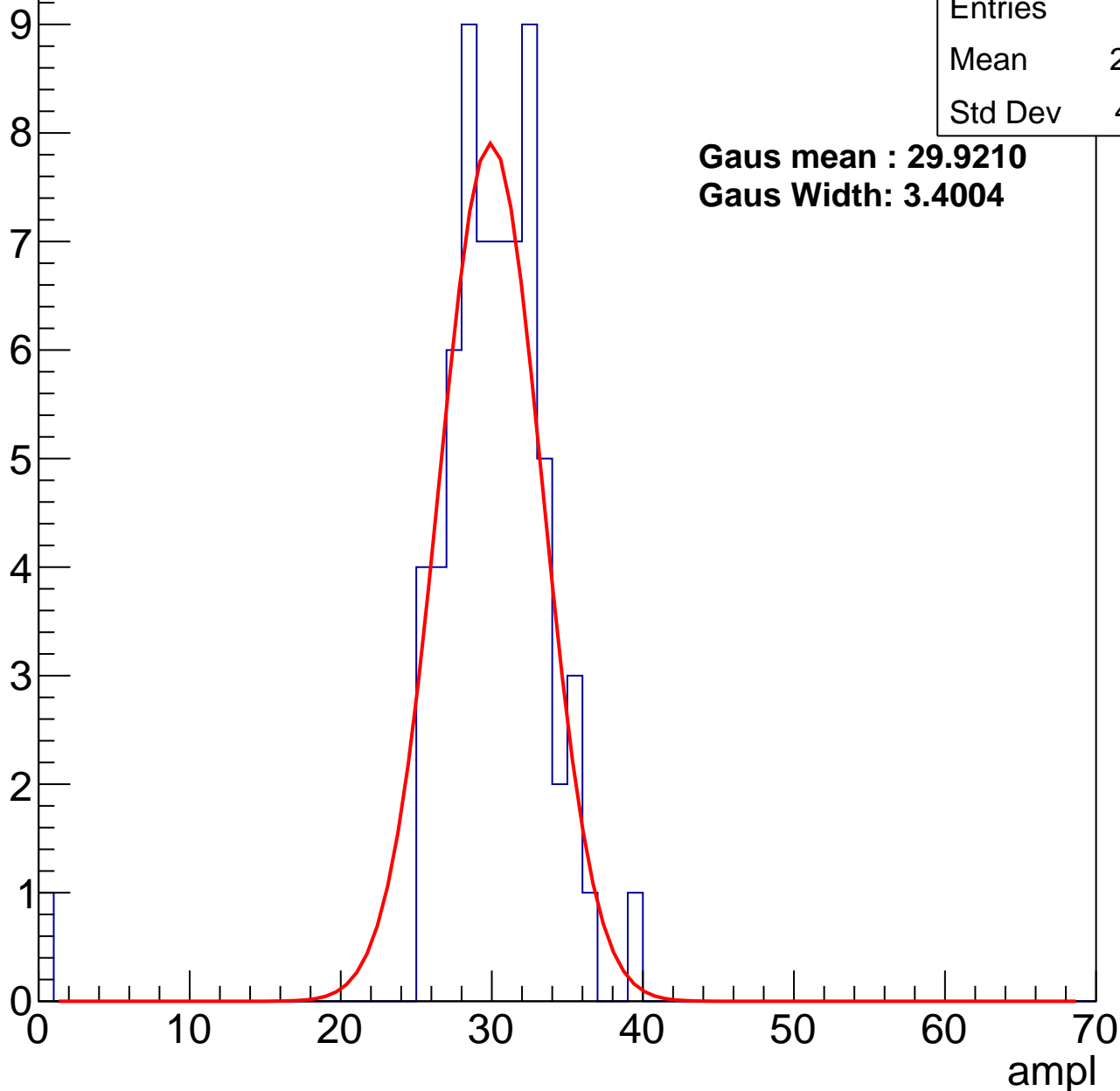
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	29.53
Std Dev	4.701

Gaus mean : 29.9210

Gaus Width: 3.4004



B0L001S, U19-ch101, adc1

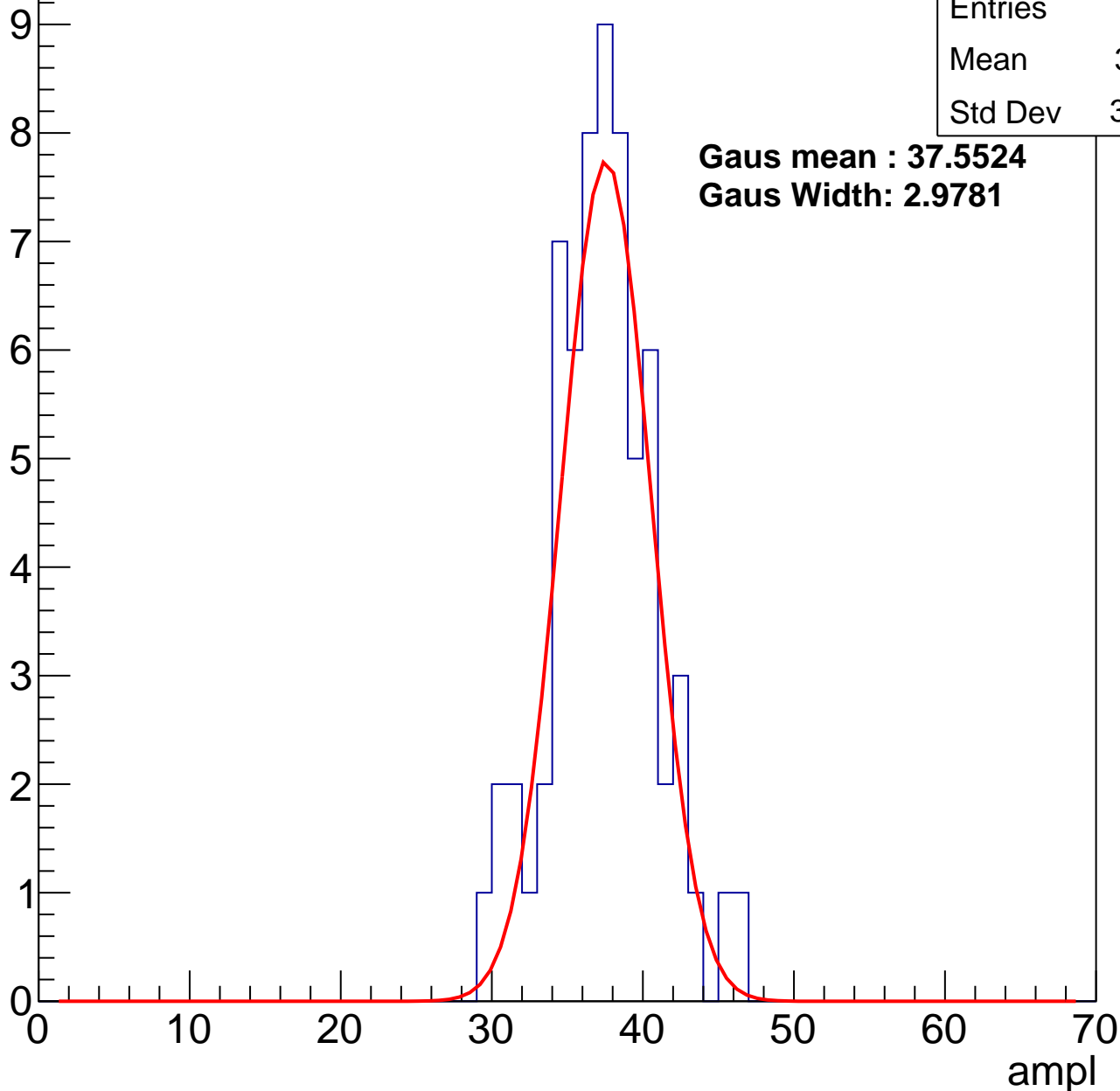
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.91
Std Dev	3.409

Gaus mean : 37.5524

Gaus Width: 2.9781



B0L001S, U19-ch101, adc2

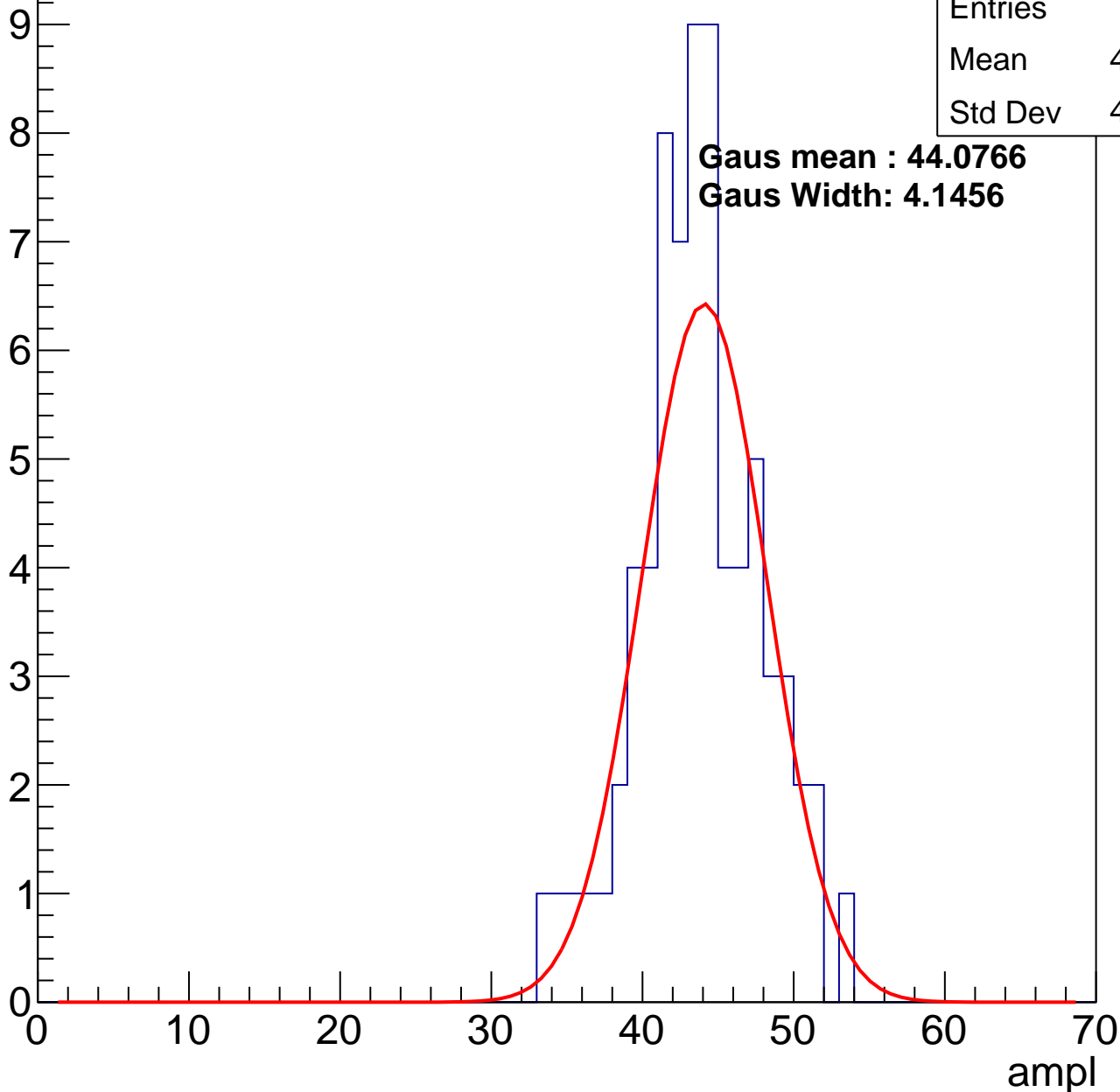
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.29
Std Dev	4.019

Gaus mean : 44.0766

Gaus Width: 4.1456

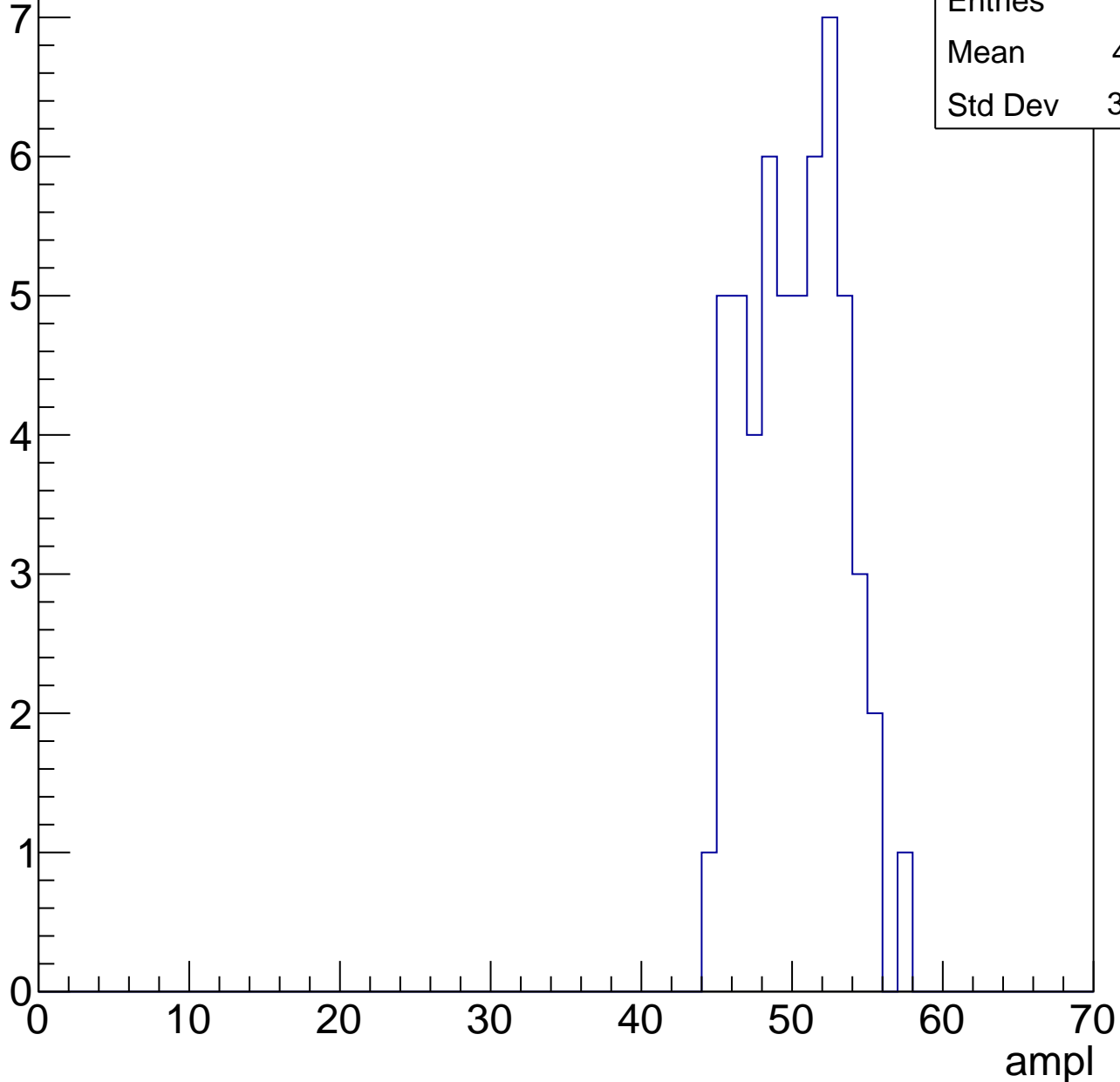


B0L001S, U19-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	49.71
Std Dev	3.096



B0L001S, U19-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

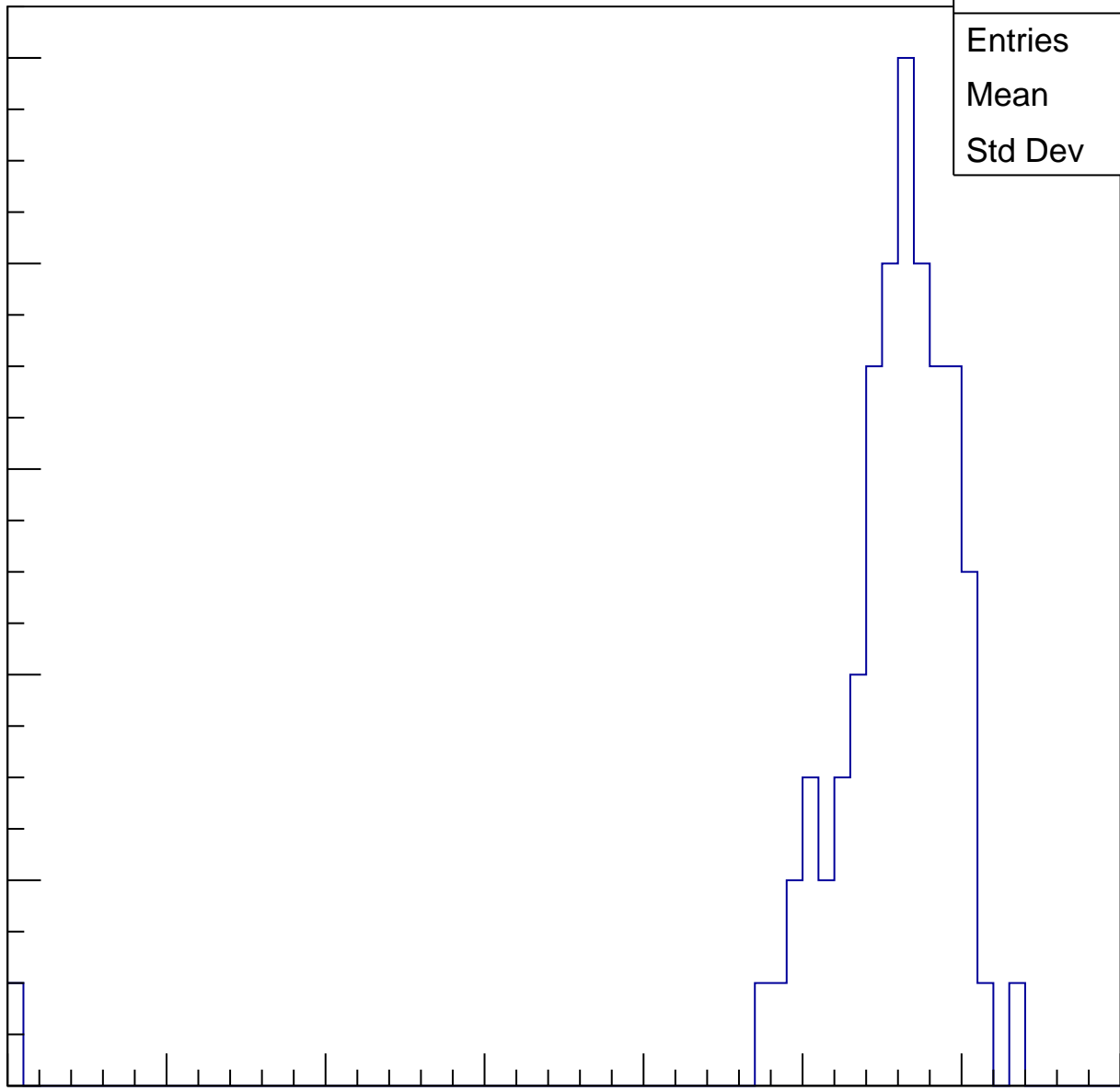
Entries	71
Mean	54.79
Std Dev	7.321

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

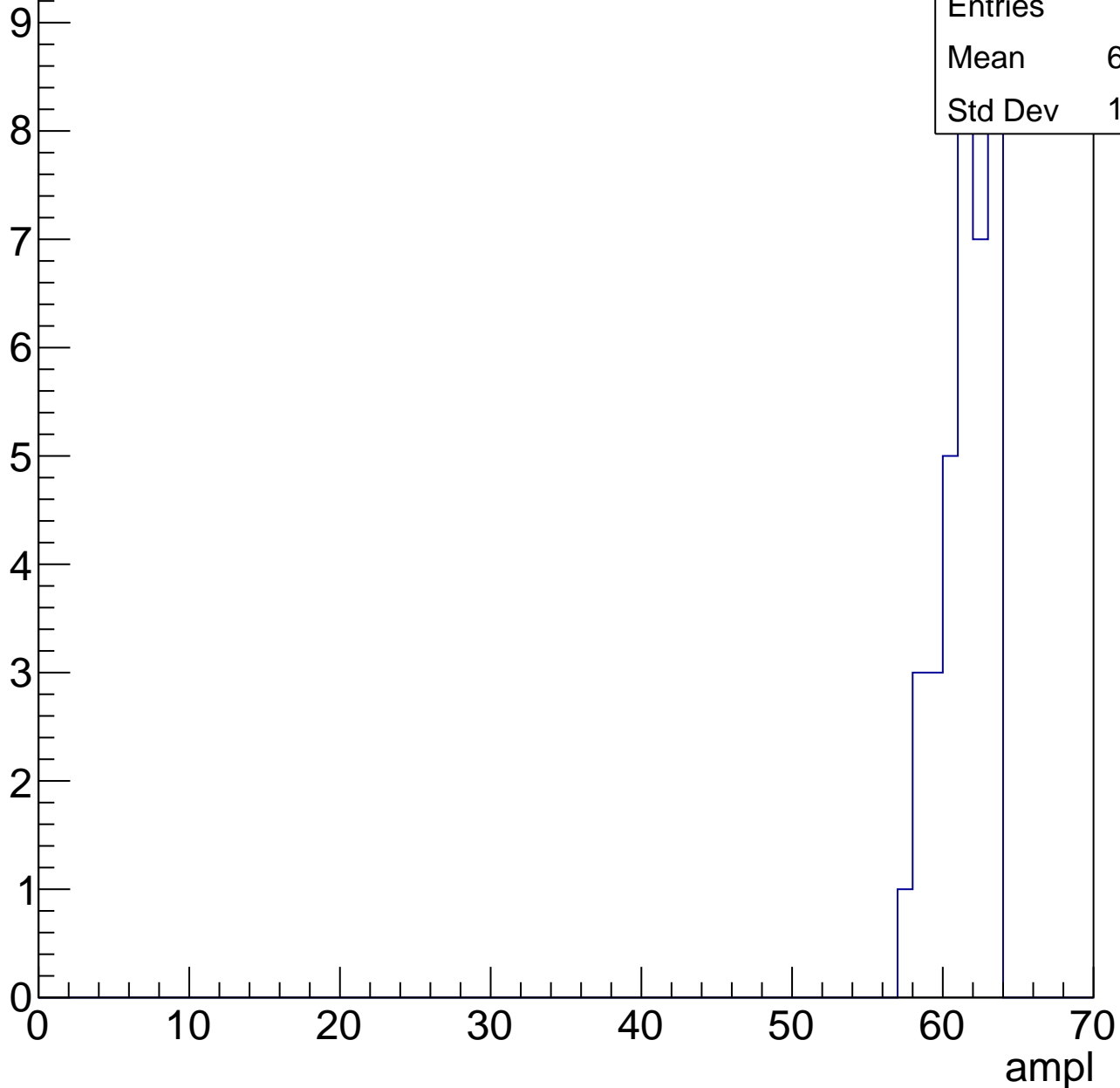
ampl



B0L001S, U19-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch102, adc0

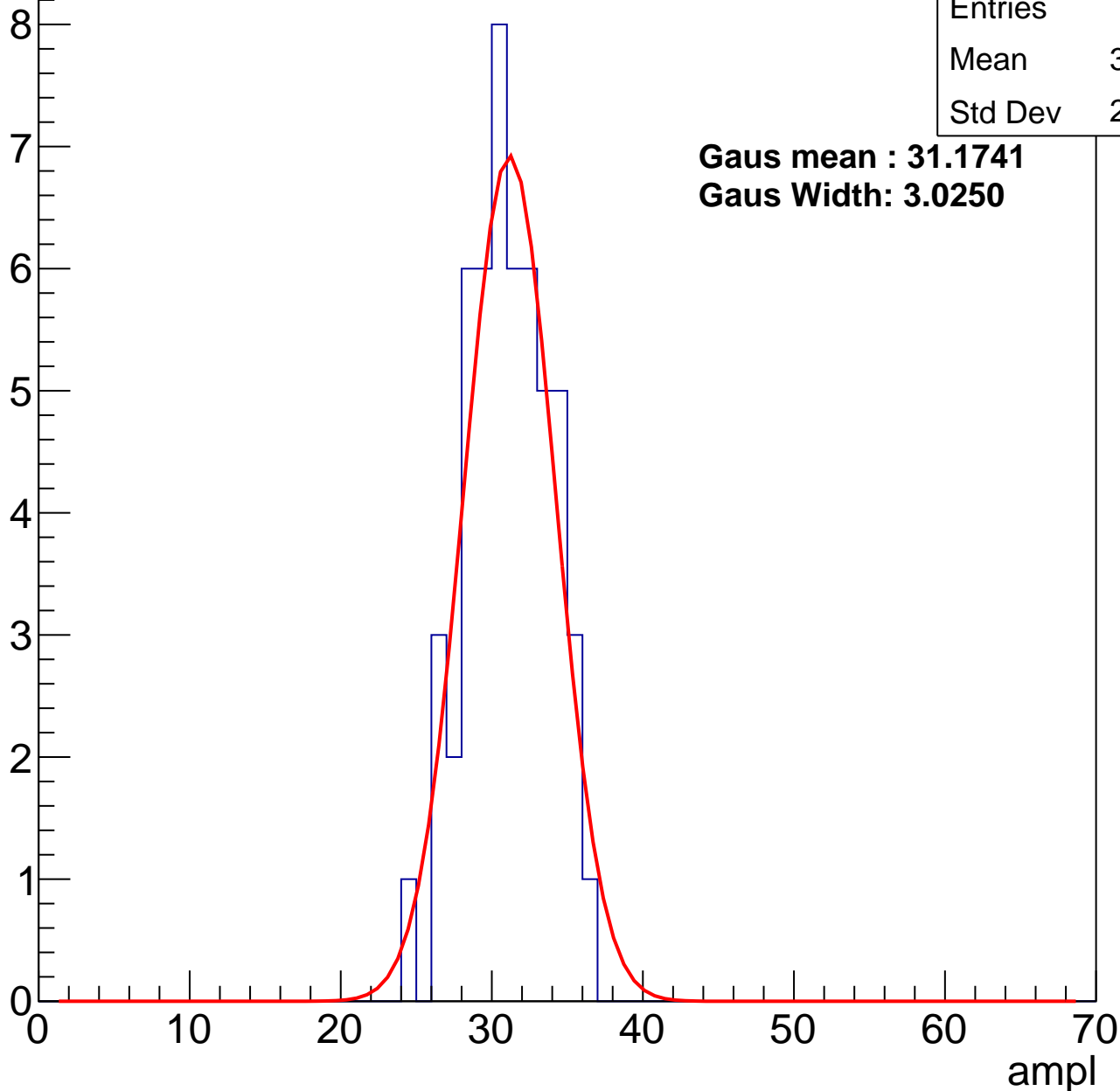
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	30.62
Std Dev	2.697

Gaus mean : 31.1741

Gaus Width: 3.0250



B0L001S, U19-ch102, adc1

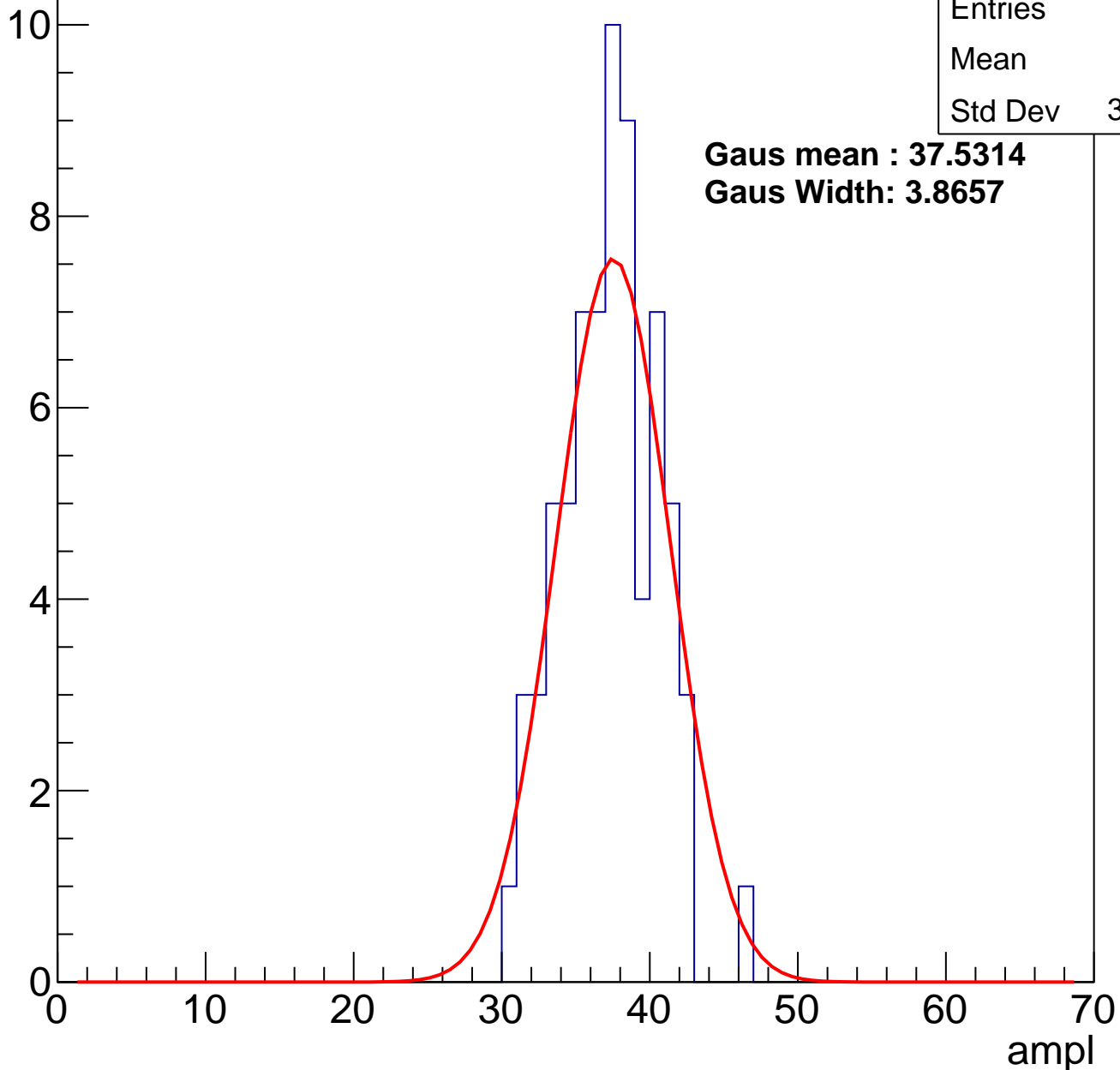
calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	36.8
Std Dev	3.192

Gaus mean : 37.5314

Gaus Width: 3.8657

Entry



B0L001S, U19-ch102, adc2

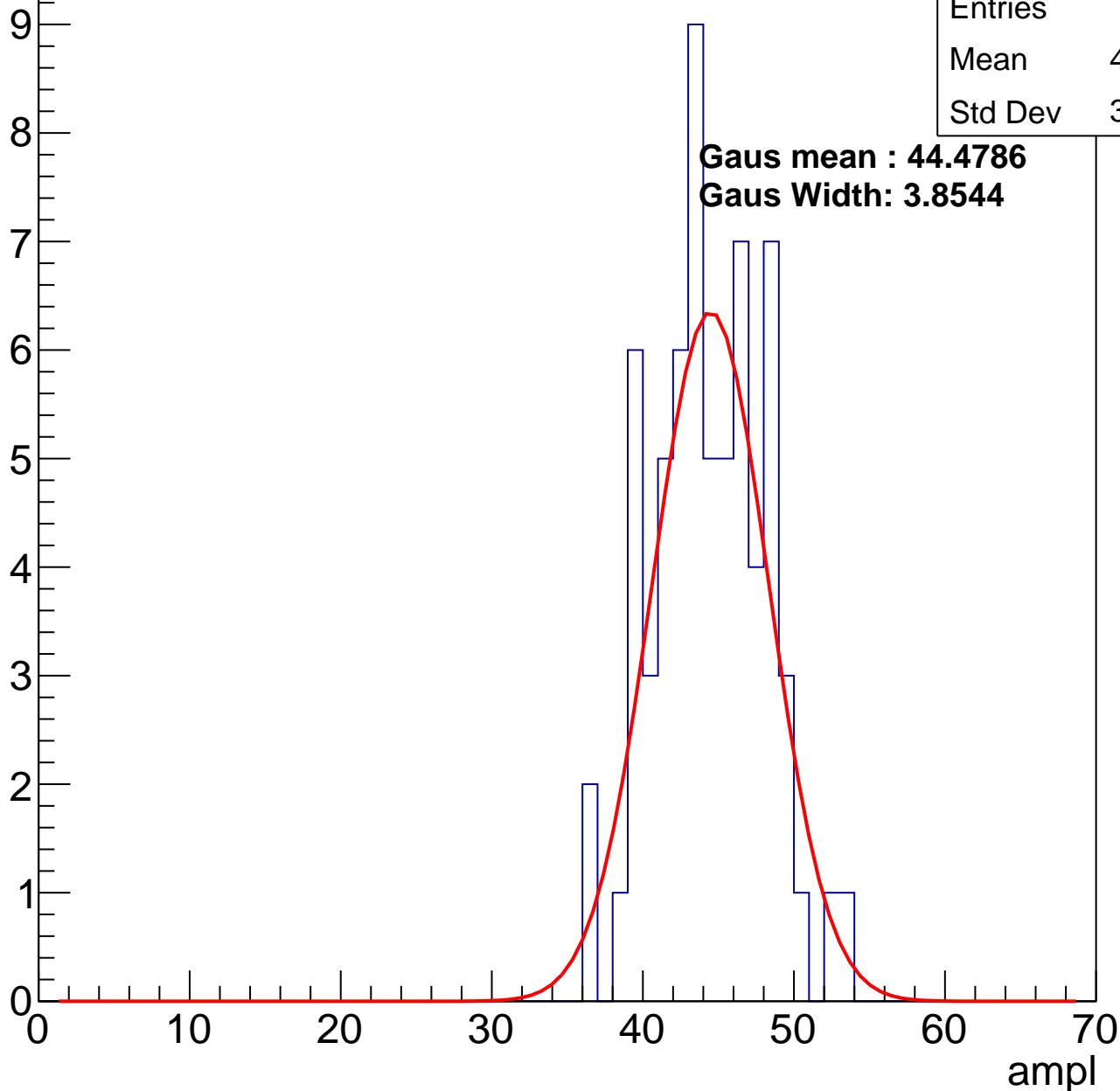
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.95
Std Dev	3.645

Gaus mean : 44.4786

Gaus Width: 3.8544

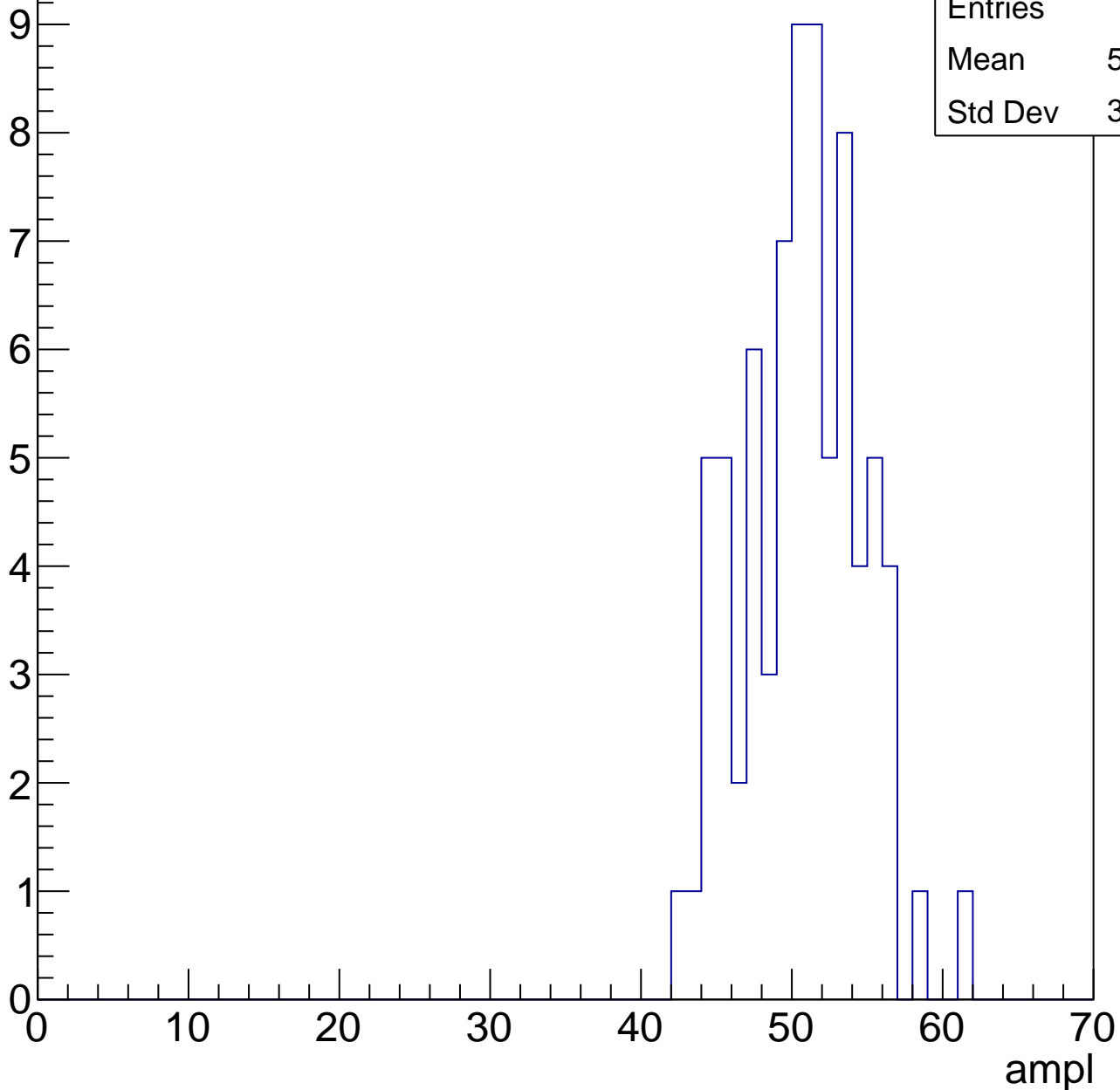


B0L001S, U19-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	50.24
Std Dev	3.869

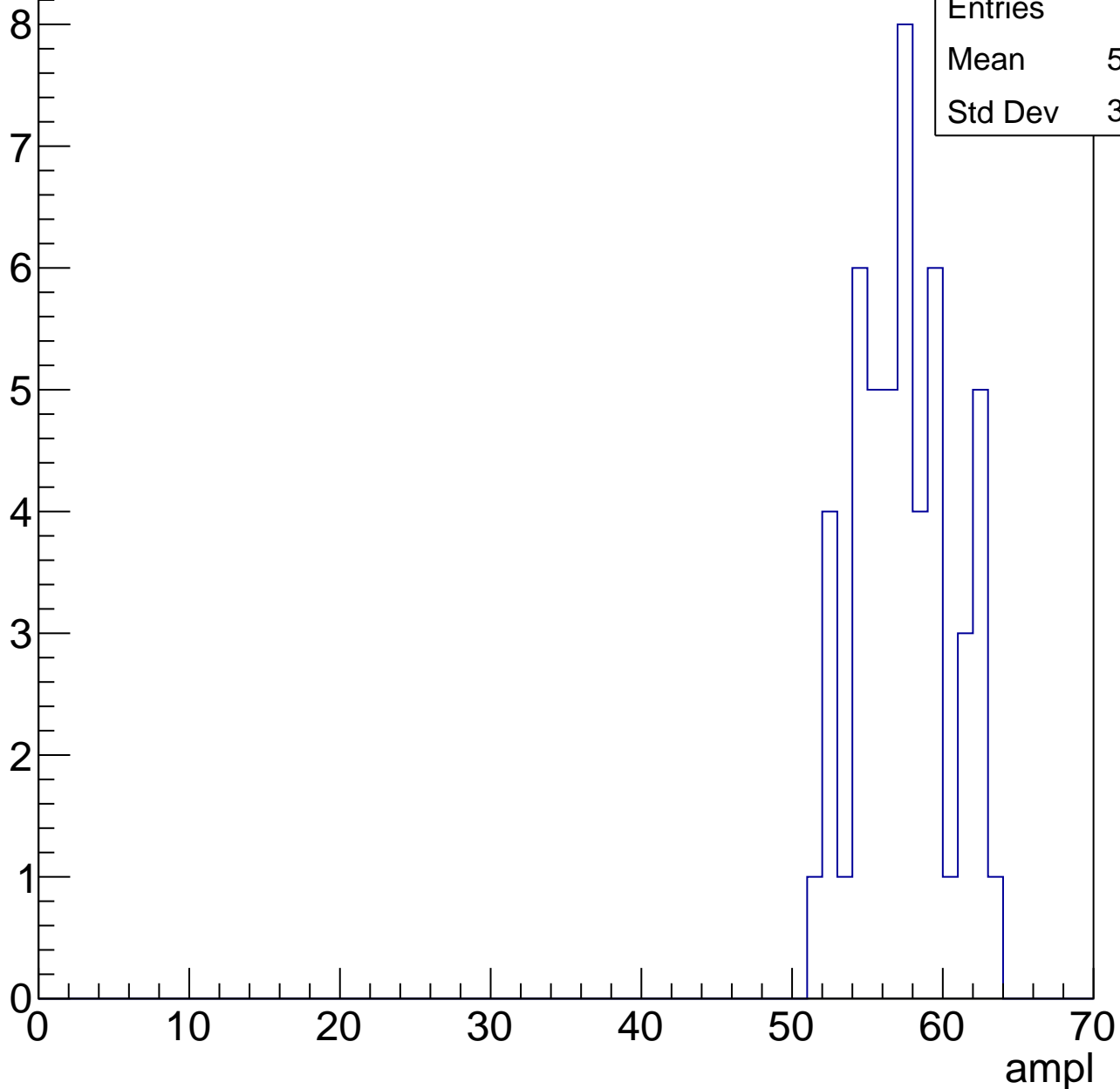


B0L001S, U19-ch102, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

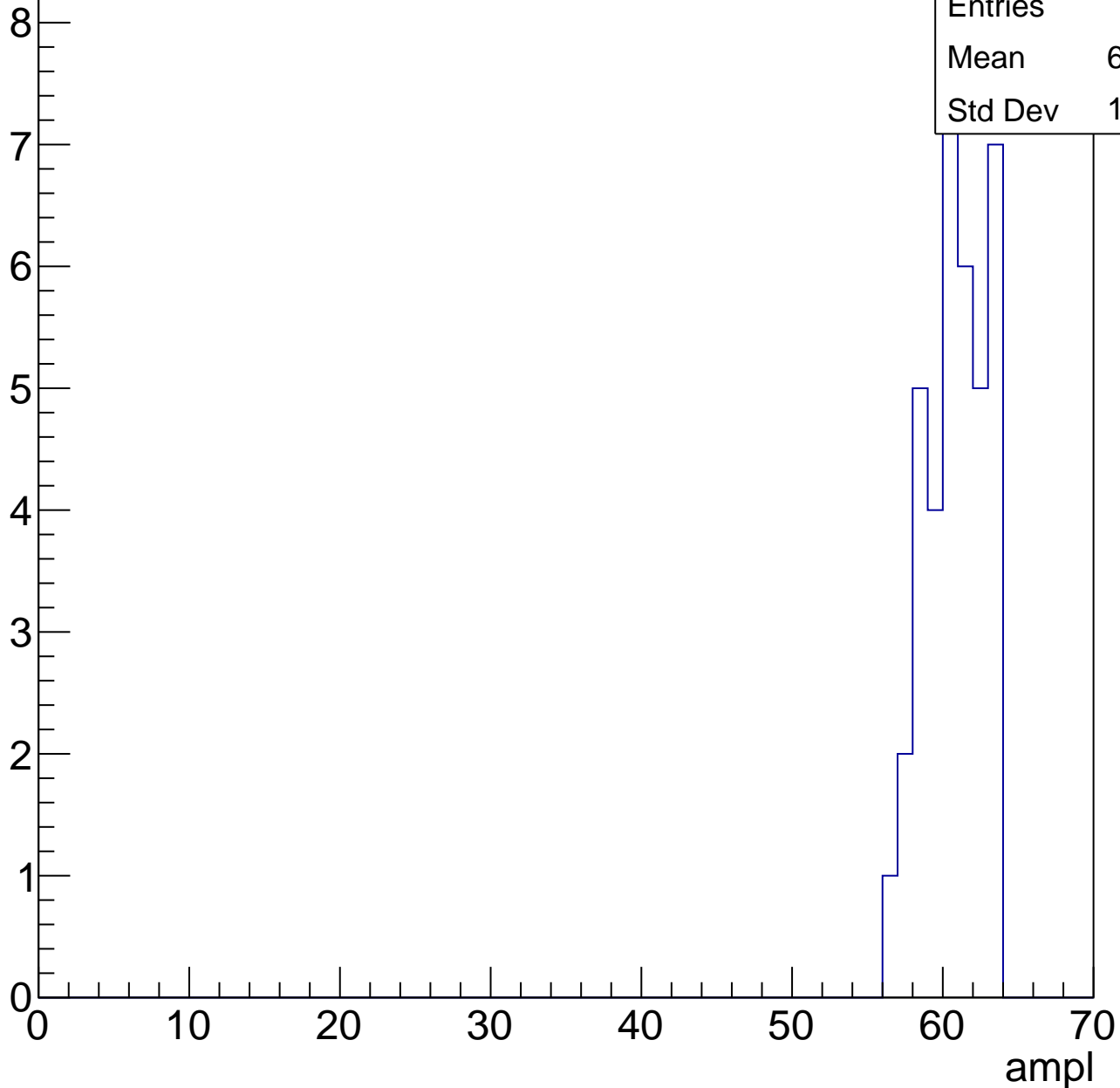
Entries	50
Mean	56.98
Std Dev	3.089



B0L001S, U19-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	8
Std Dev	11.31

B0L001S, U19-ch103, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	31.81
Std Dev	6.407

Gaus mean : 32.9425

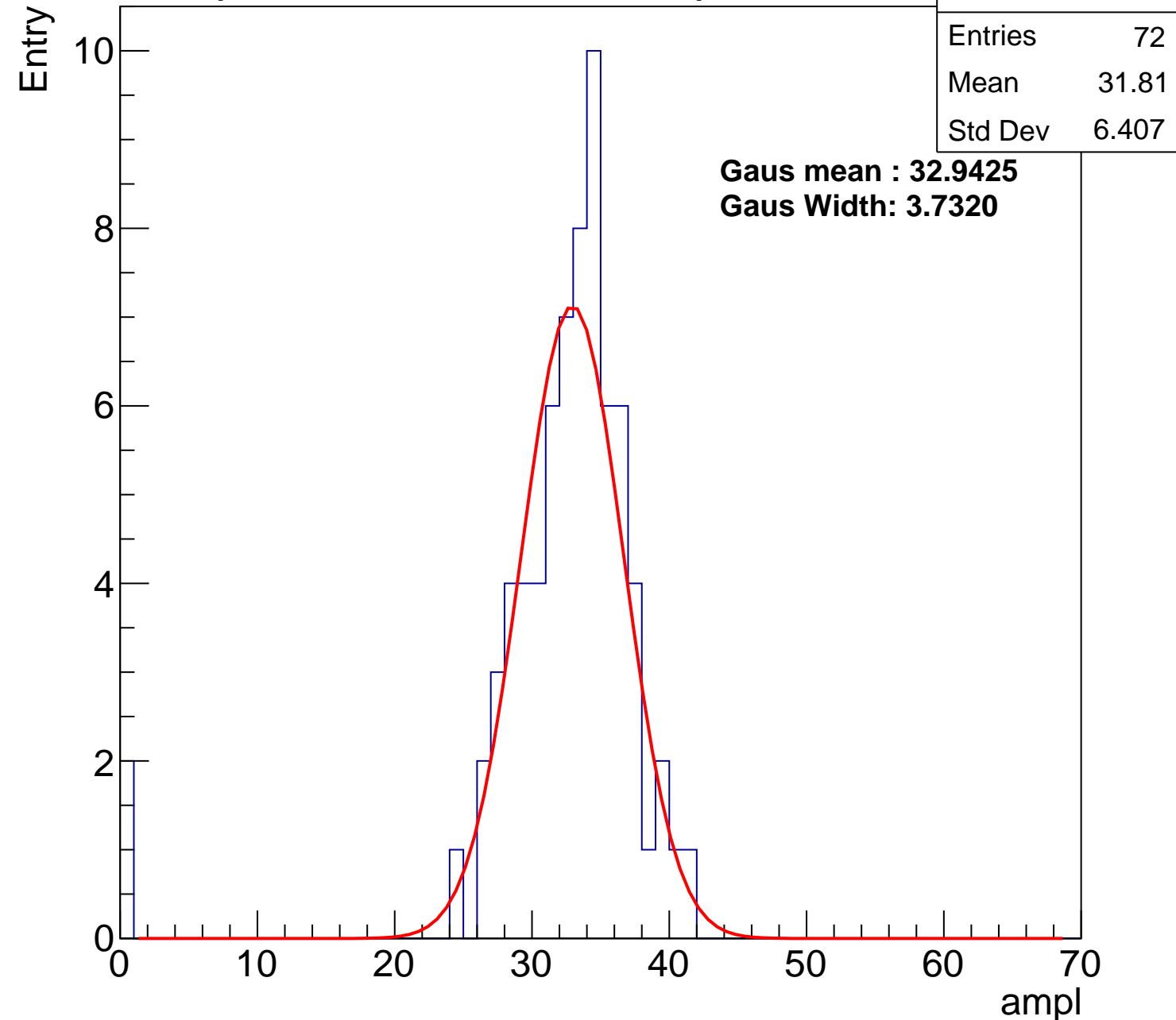
Gaus Width: 3.7320

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch103, adc1

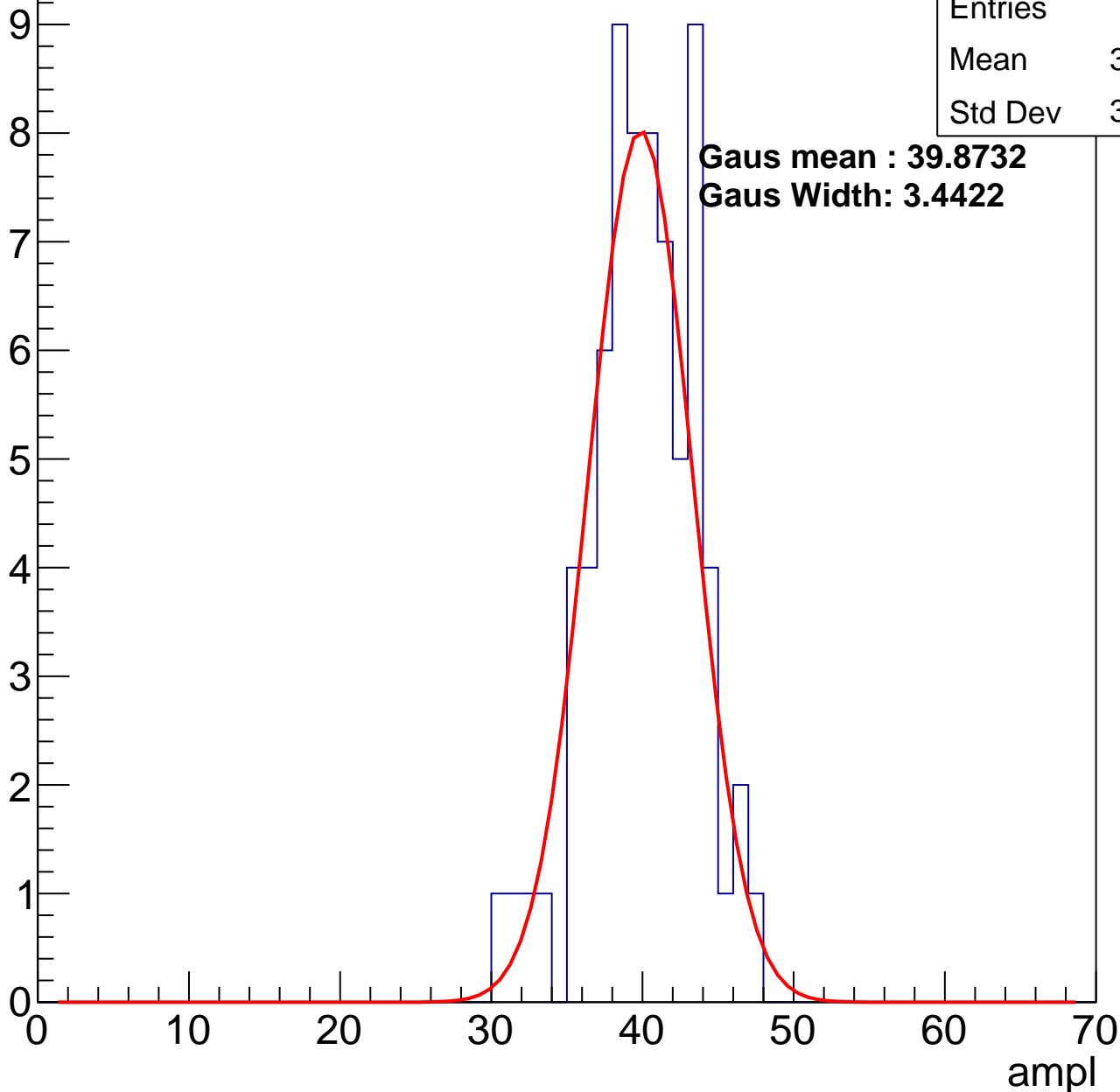
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	39.58
Std Dev	3.459

Gaus mean : 39.8732

Gaus Width: 3.4422



B0L001S, U19-ch103, adc2

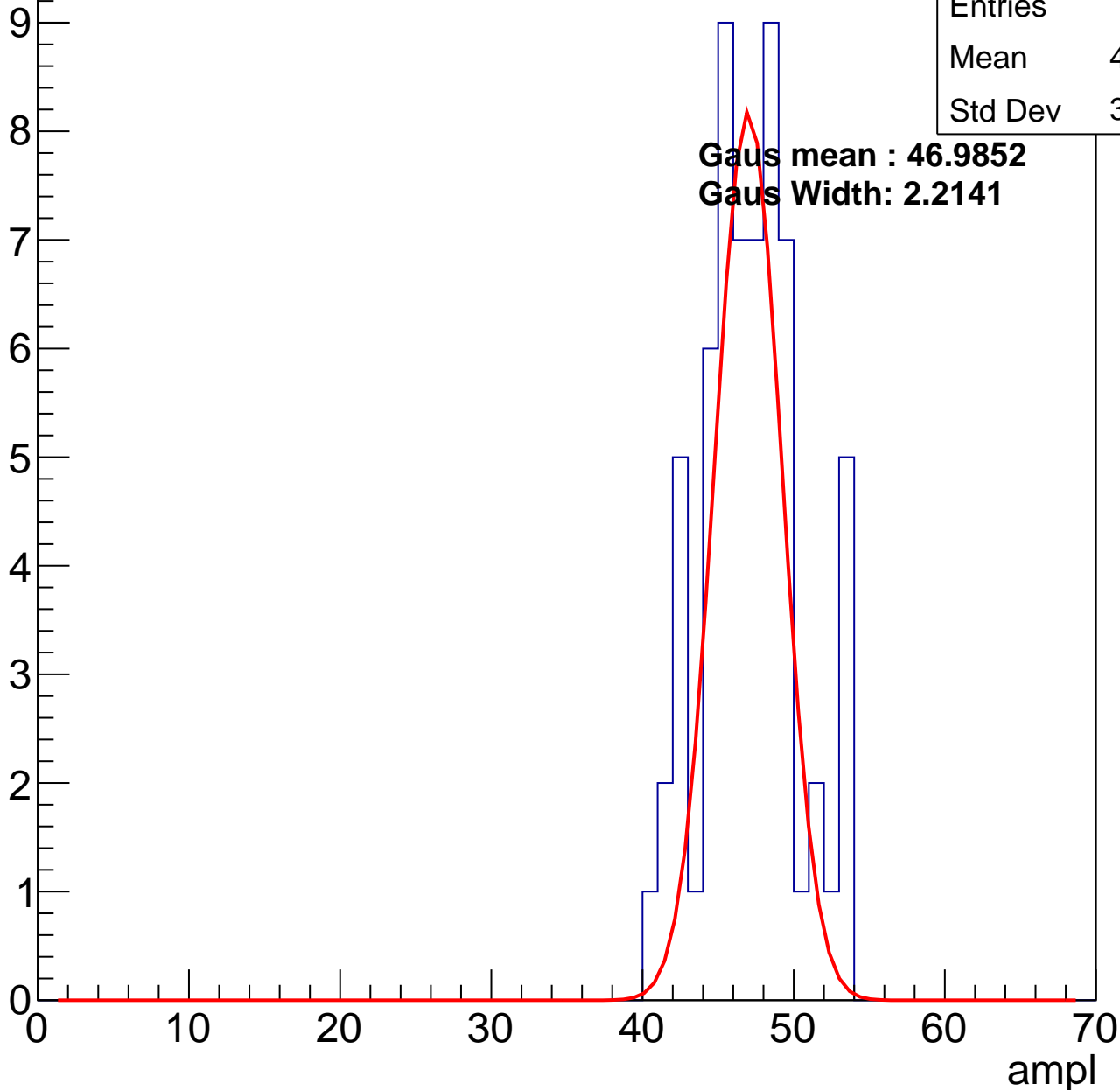
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	46.65
Std Dev	3.173

Gaus mean : 46.9852

Gaus Width: 2.2141

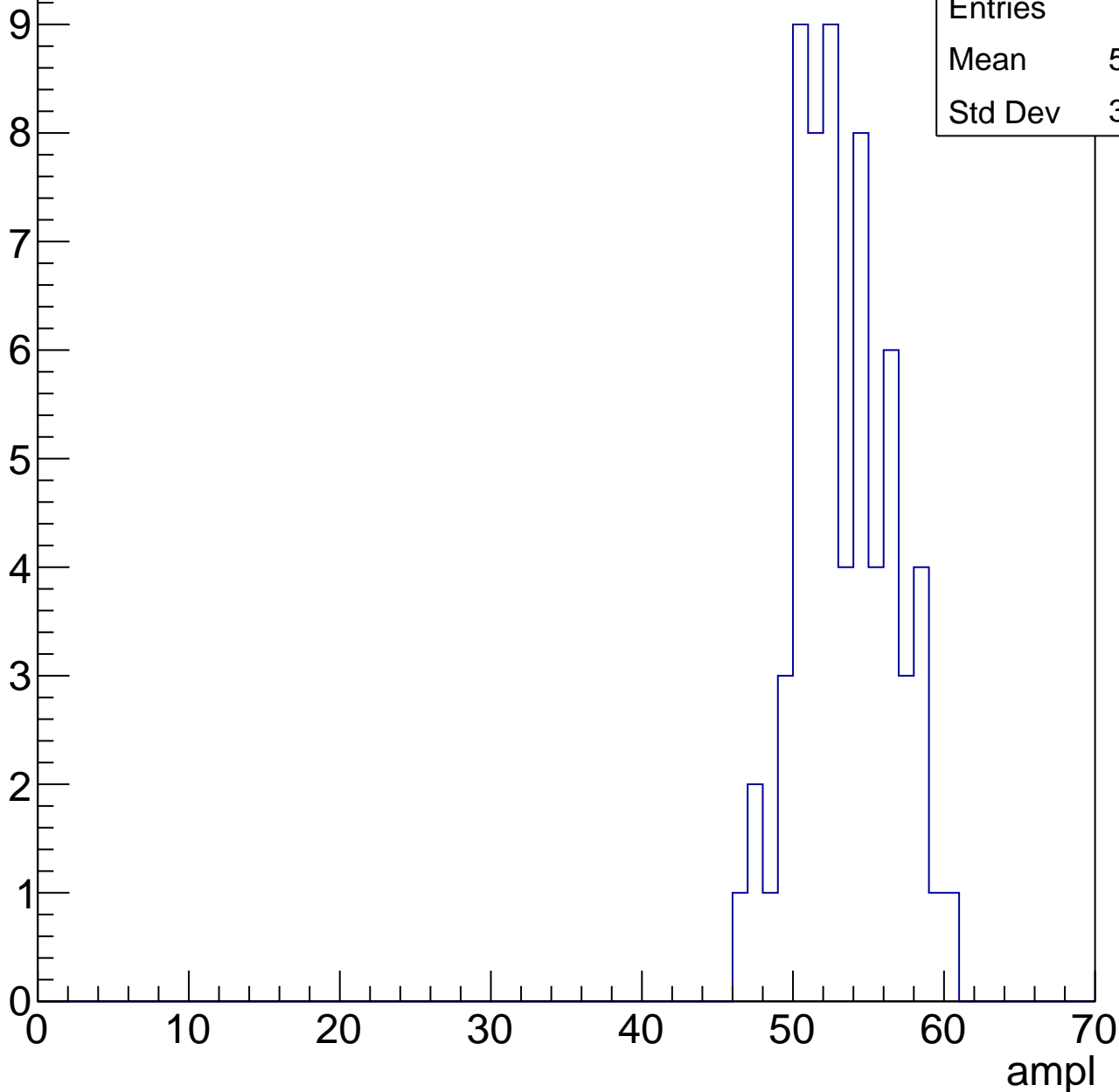


B0L001S, U19-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	52.86
Std Dev	3.127

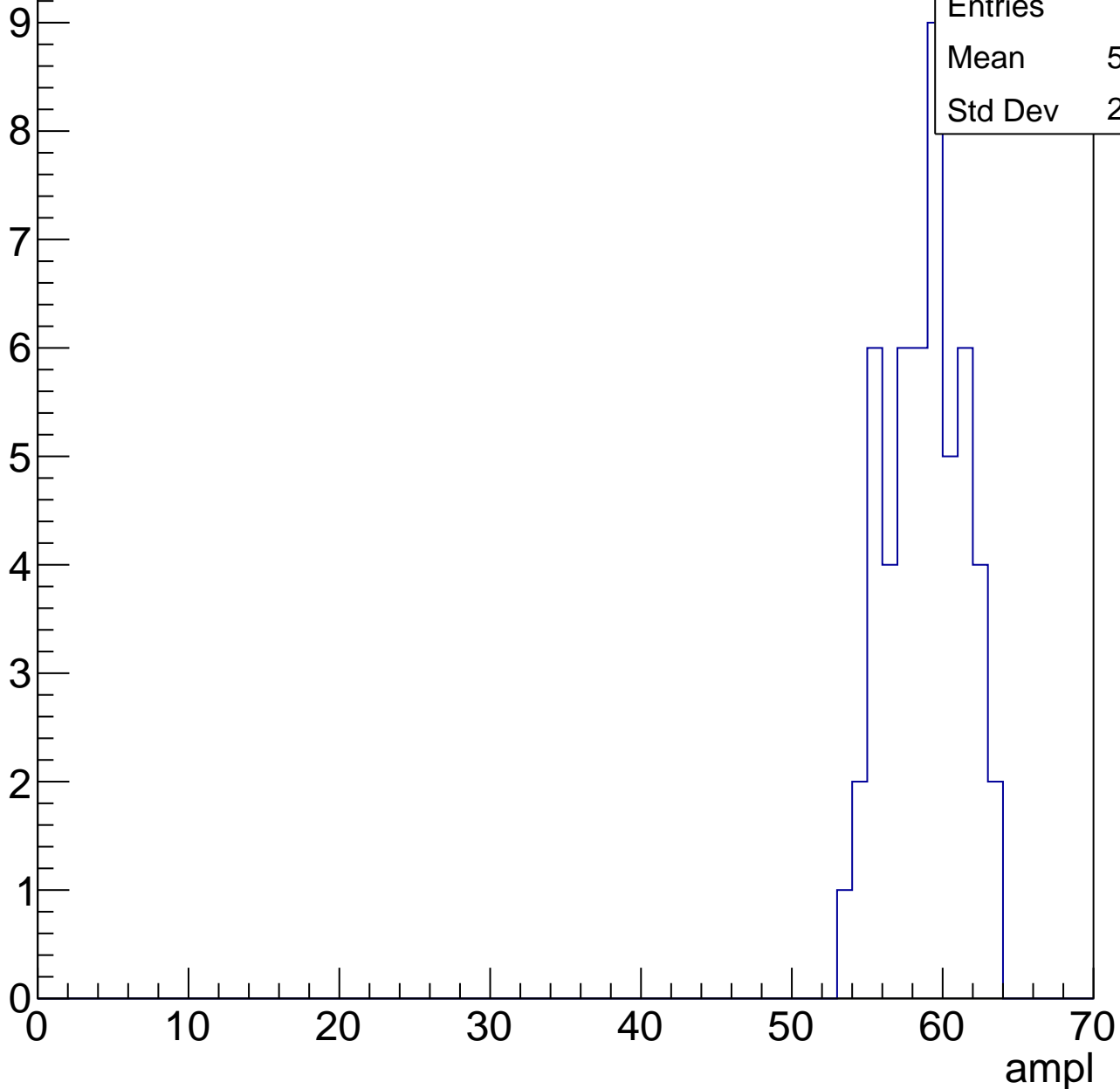


B0L001S, U19-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	58.35
Std Dev	2.519

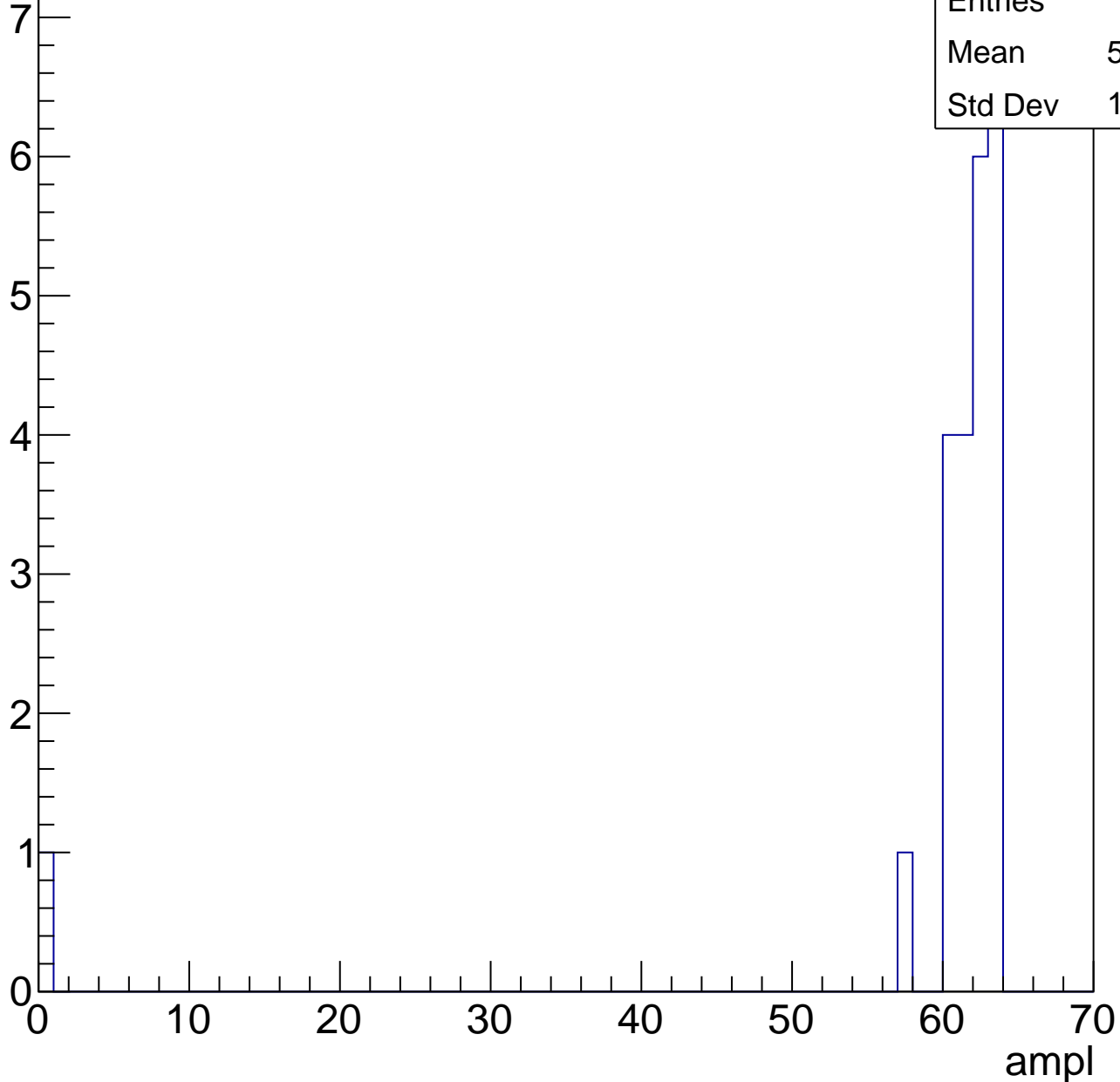


B0L001S, U19-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.87
Std Dev	12.63



B0L001S, U19-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch104, adc0

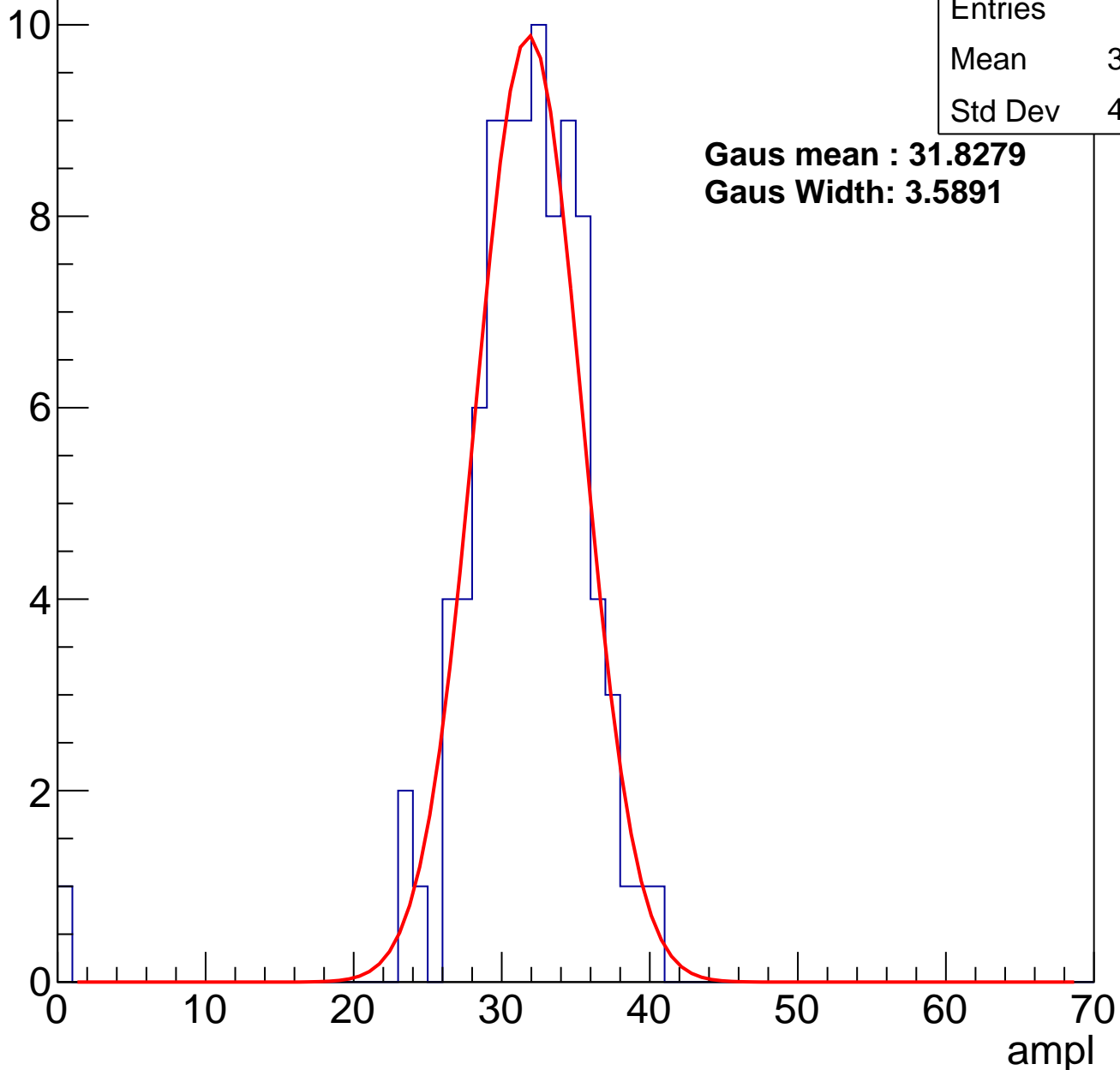
calib_packv5_042523_0143.root, FC#9, port A1

Entries	90
Mean	31.13
Std Dev	4.775

Gaus mean : 31.8279

Gaus Width: 3.5891

Entry



B0L001S, U19-ch104, adc1

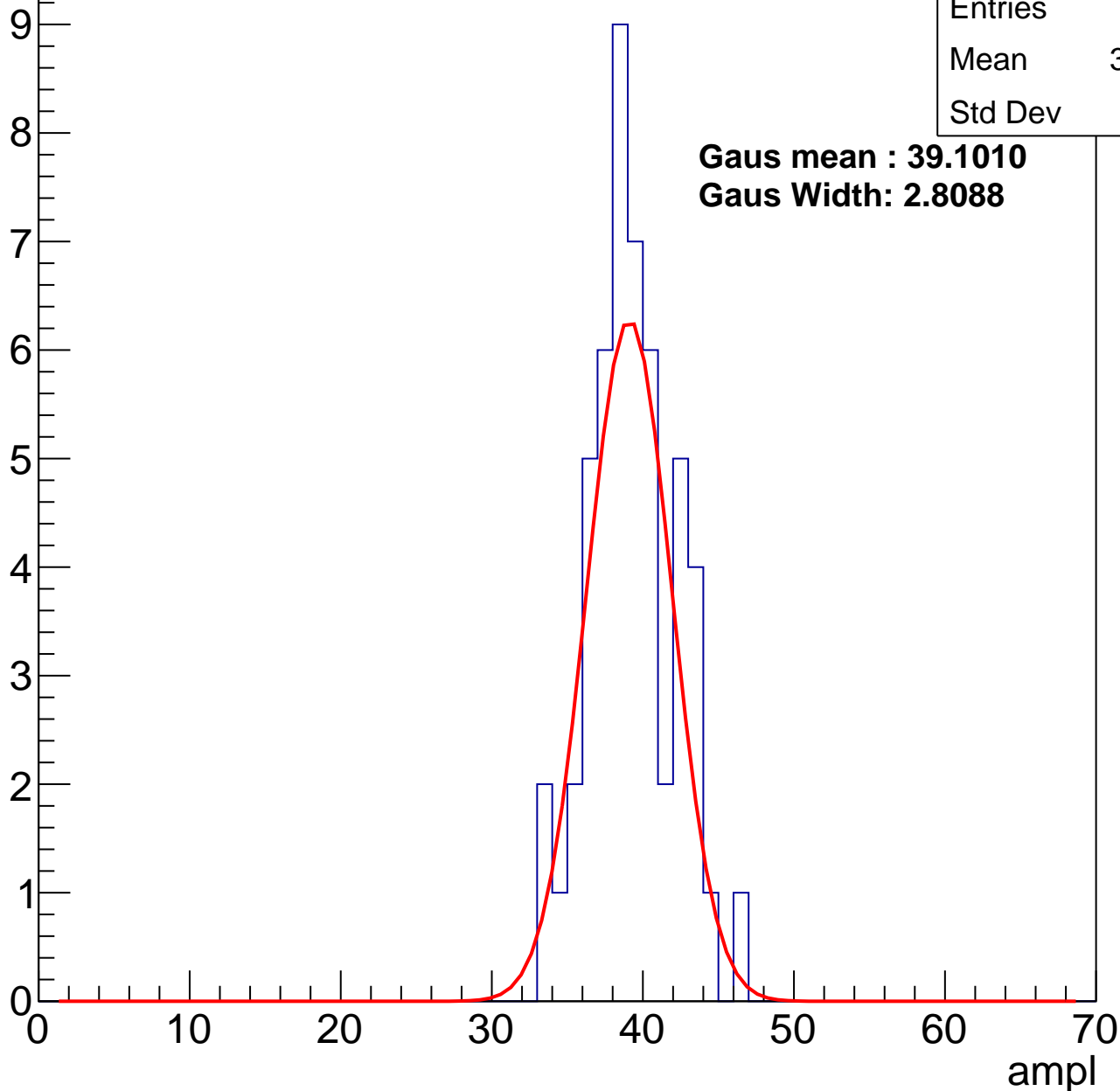
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	38.84
Std Dev	2.81

Gaus mean : 39.1010

Gaus Width: 2.8088



B0L001S, U19-ch104, adc2

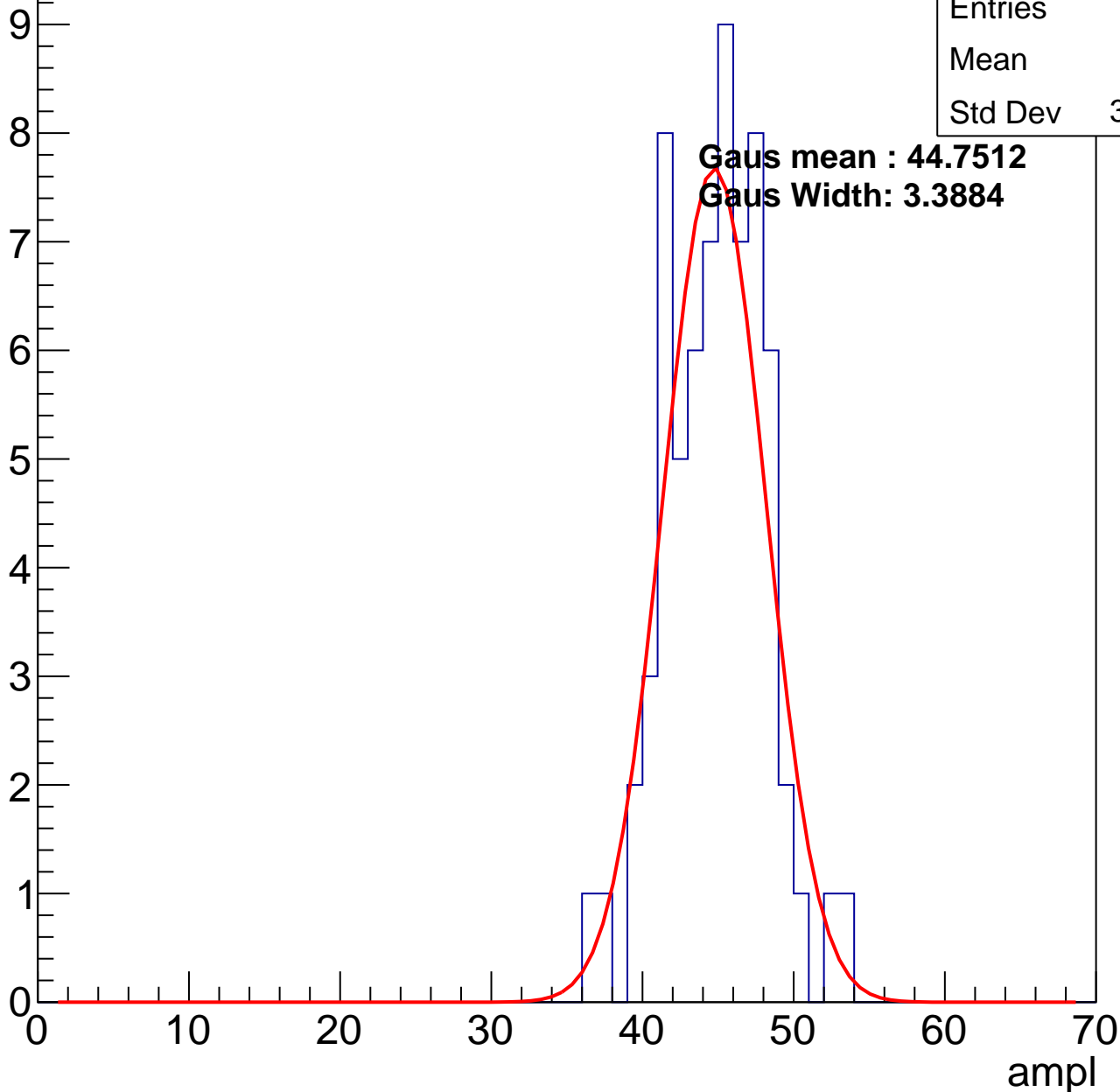
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.4
Std Dev	3.286

Gaus mean : 44.7512

Gaus Width: 3.3884

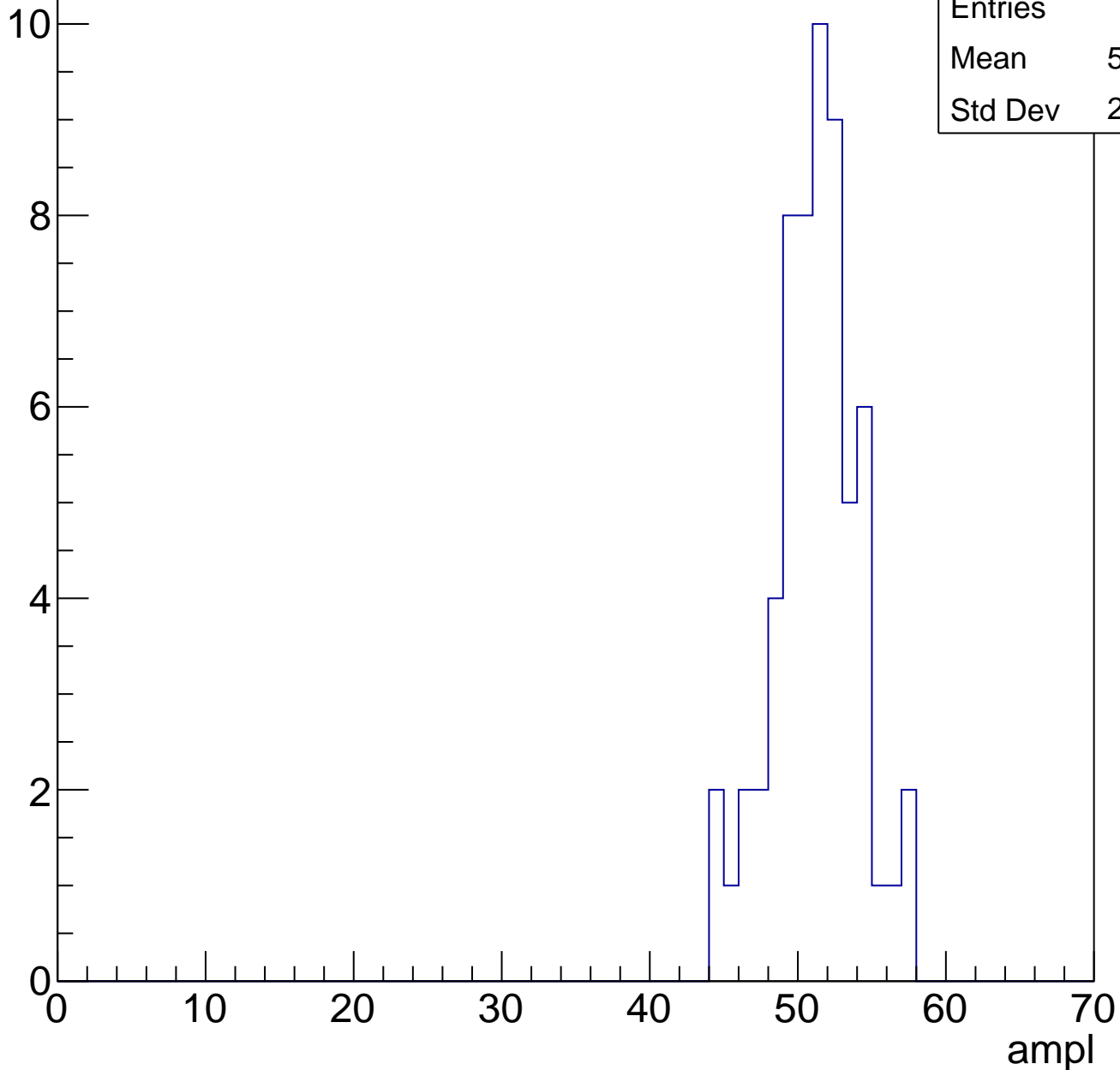


B0L001S, U19-ch104, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	50.74
Std Dev	2.816

Entry

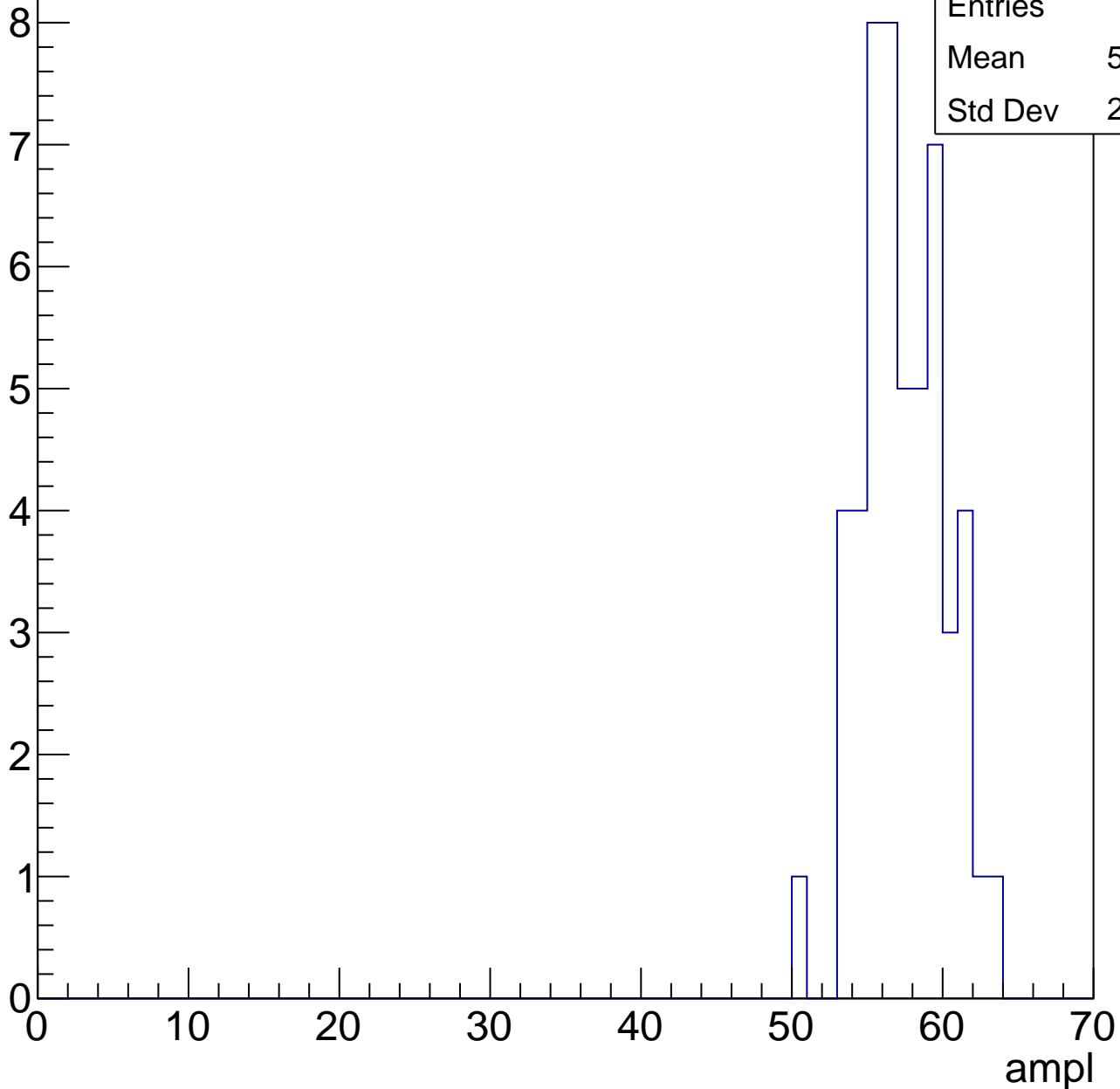


B0L001S, U19-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.92
Std Dev	2.707

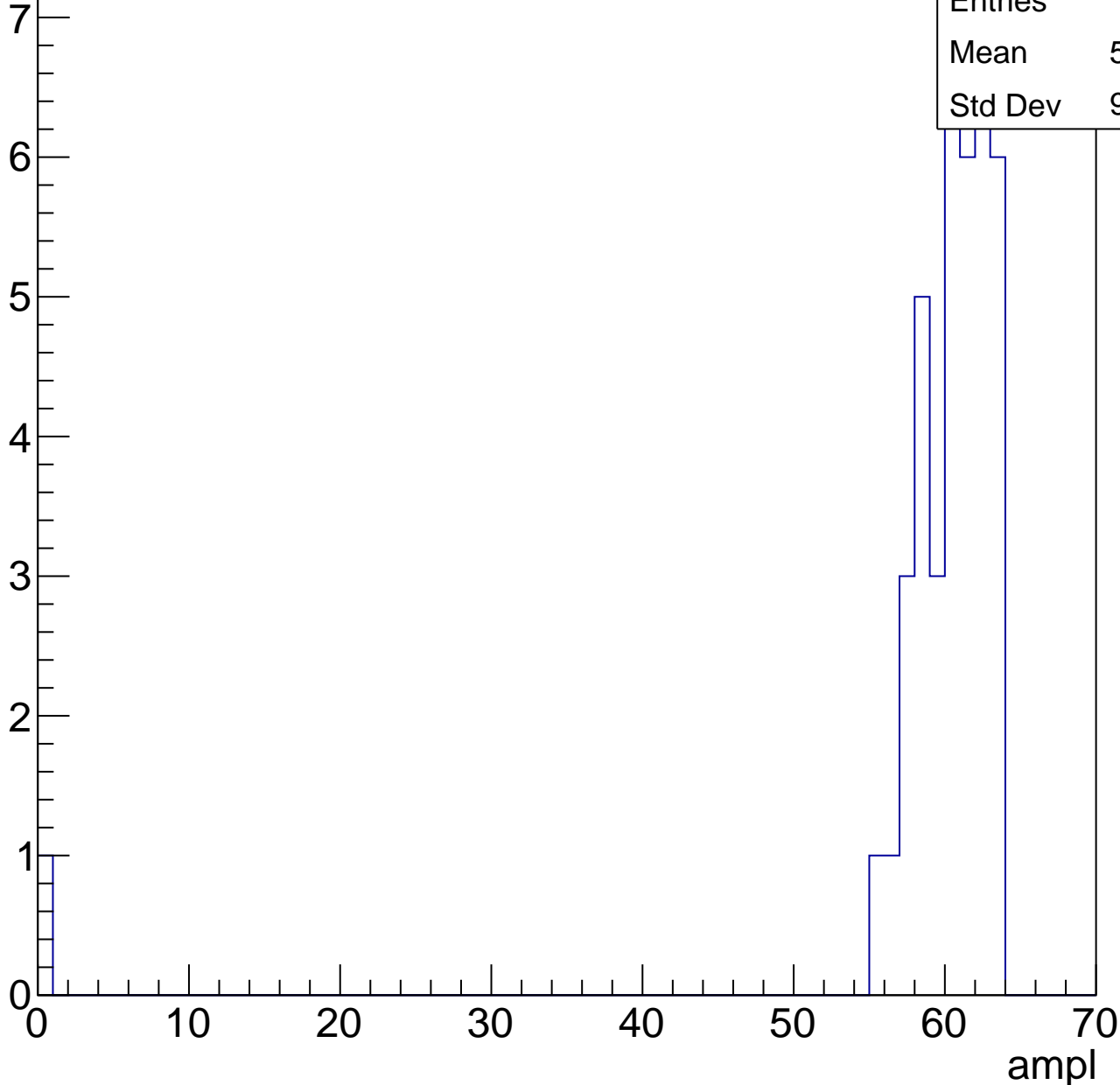


B0L001S, U19-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	58.67
Std Dev	9.629



B0L001S, U19-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch105, adc0

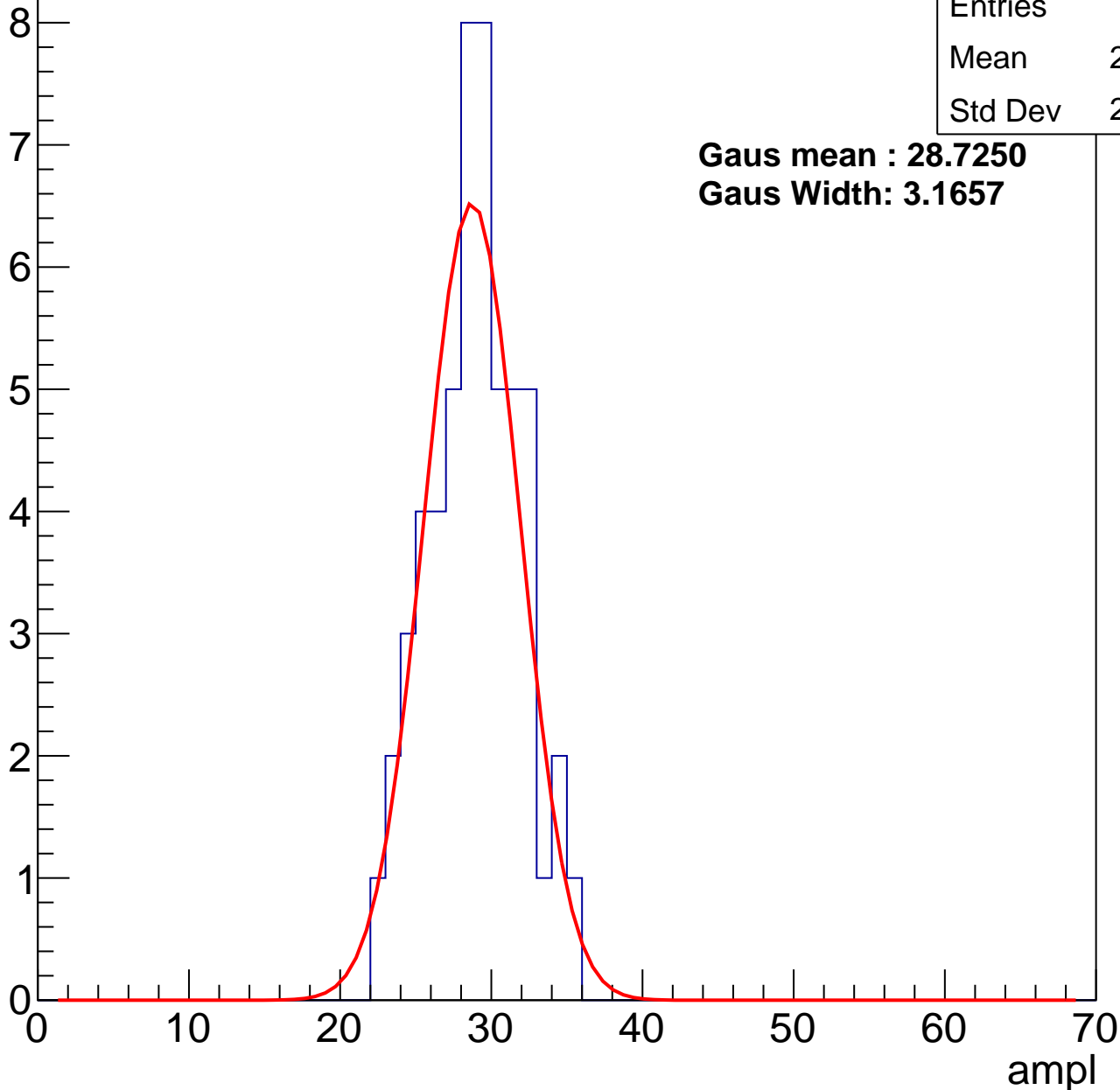
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	28.44
Std Dev	2.979

Gaus mean : 28.7250

Gaus Width: 3.1657



B0L001S, U19-ch105, adc1

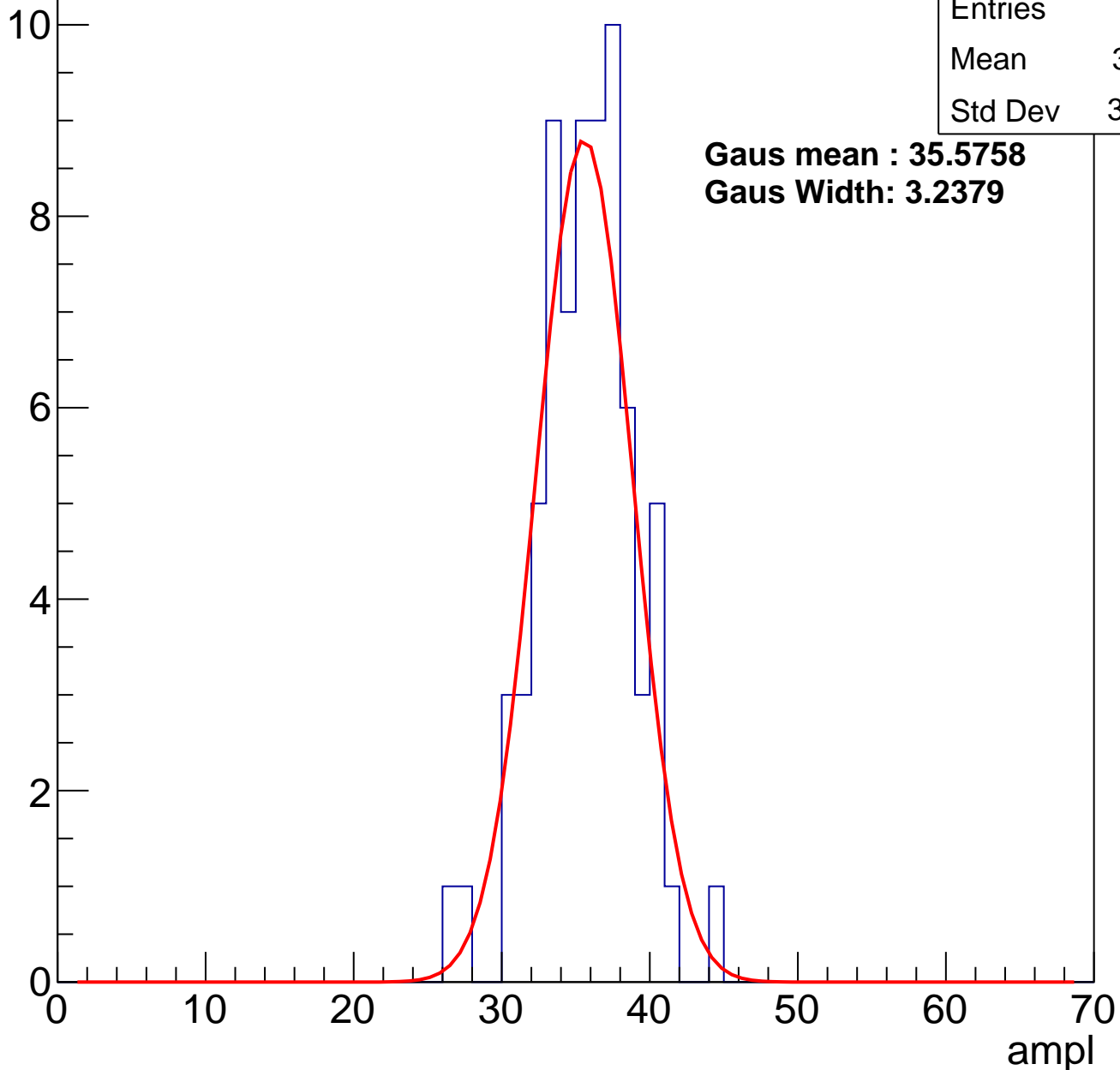
calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	35.21
Std Dev	3.197

Gaus mean : 35.5758

Gaus Width: 3.2379

Entry



B0L001S, U19-ch105, adc2

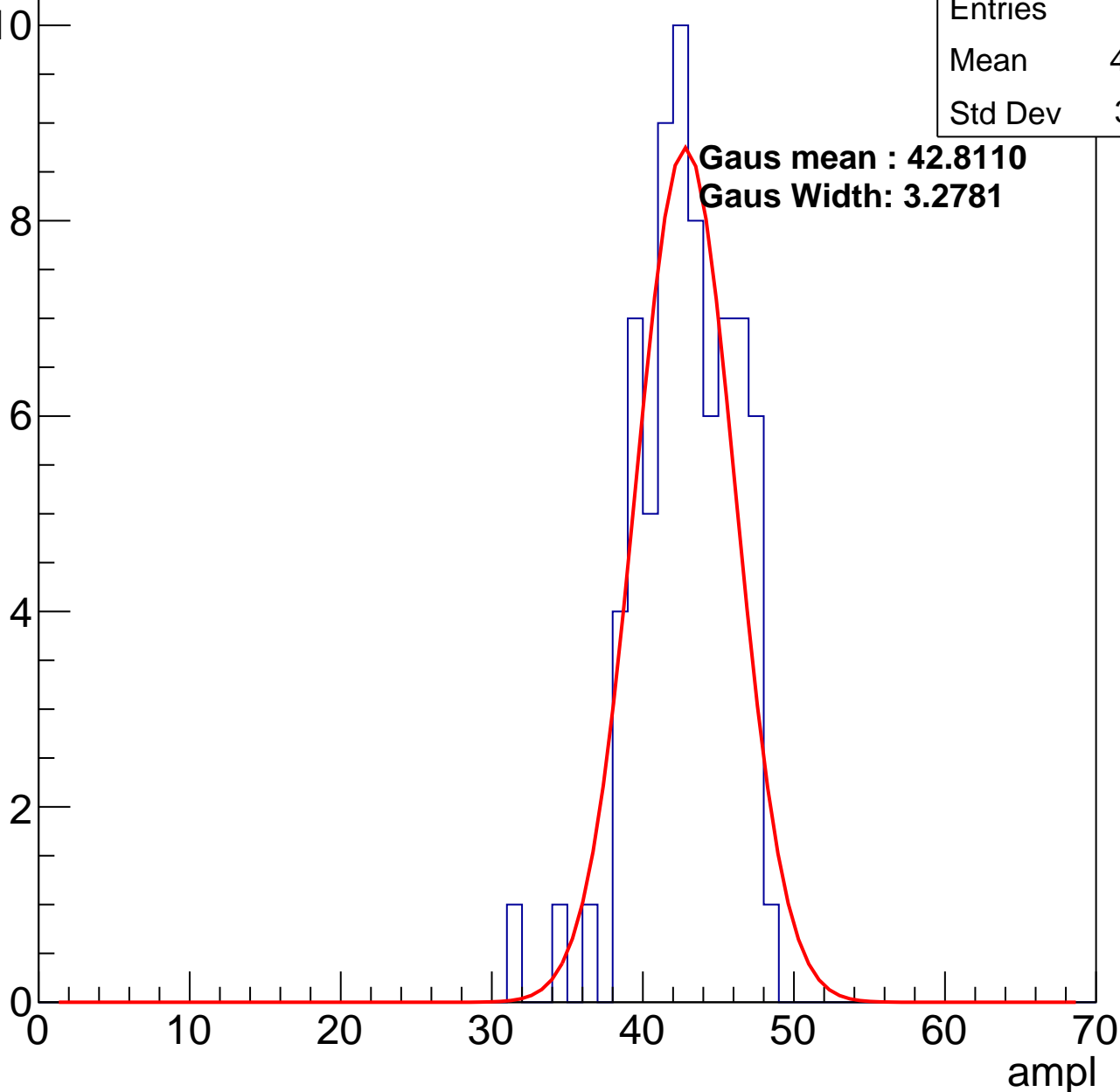
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	42.33
Std Dev	3.231

Gaus mean : 42.8110

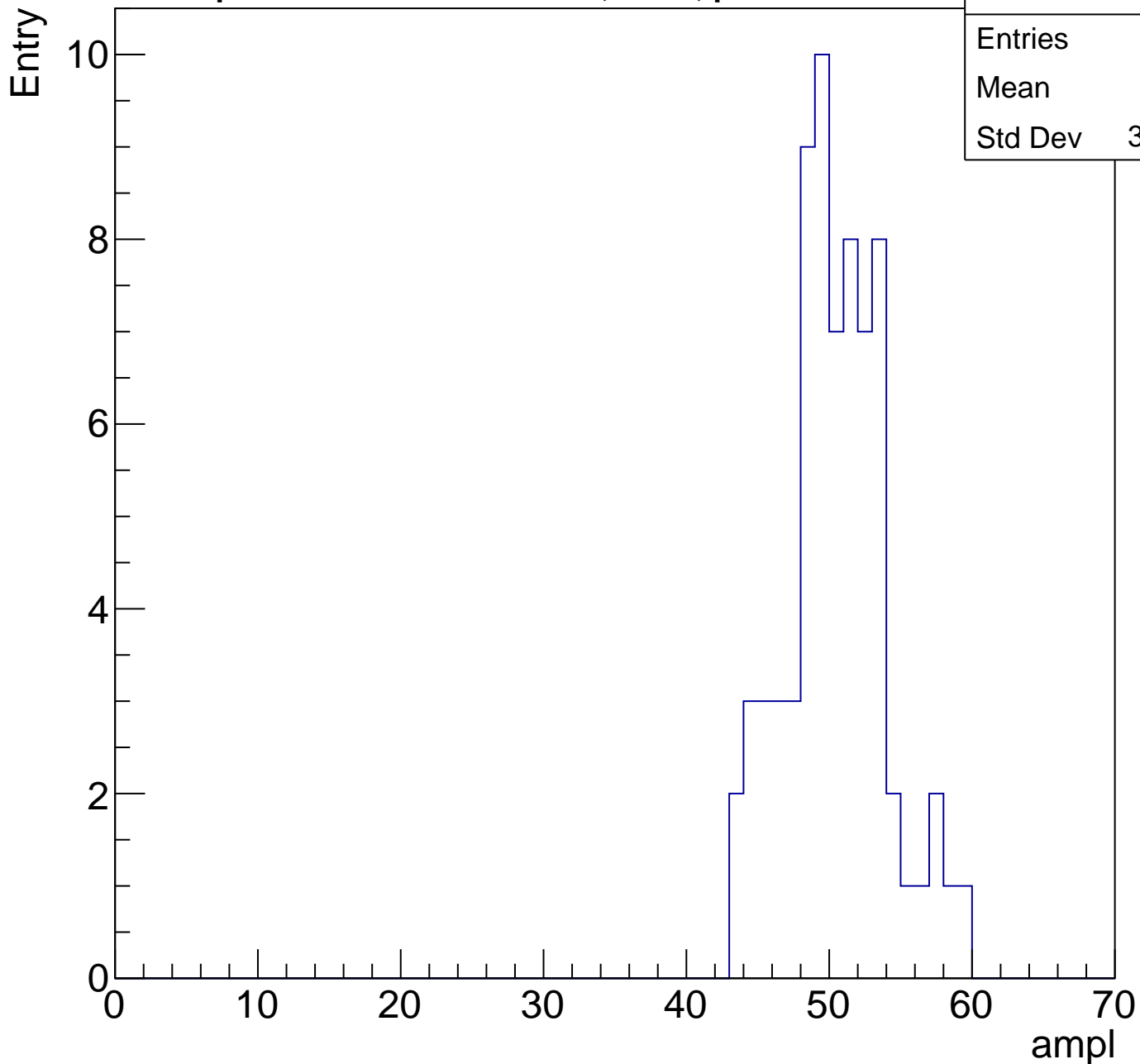
Gaus Width: 3.2781



B0L001S, U19-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	50
Std Dev	3.452

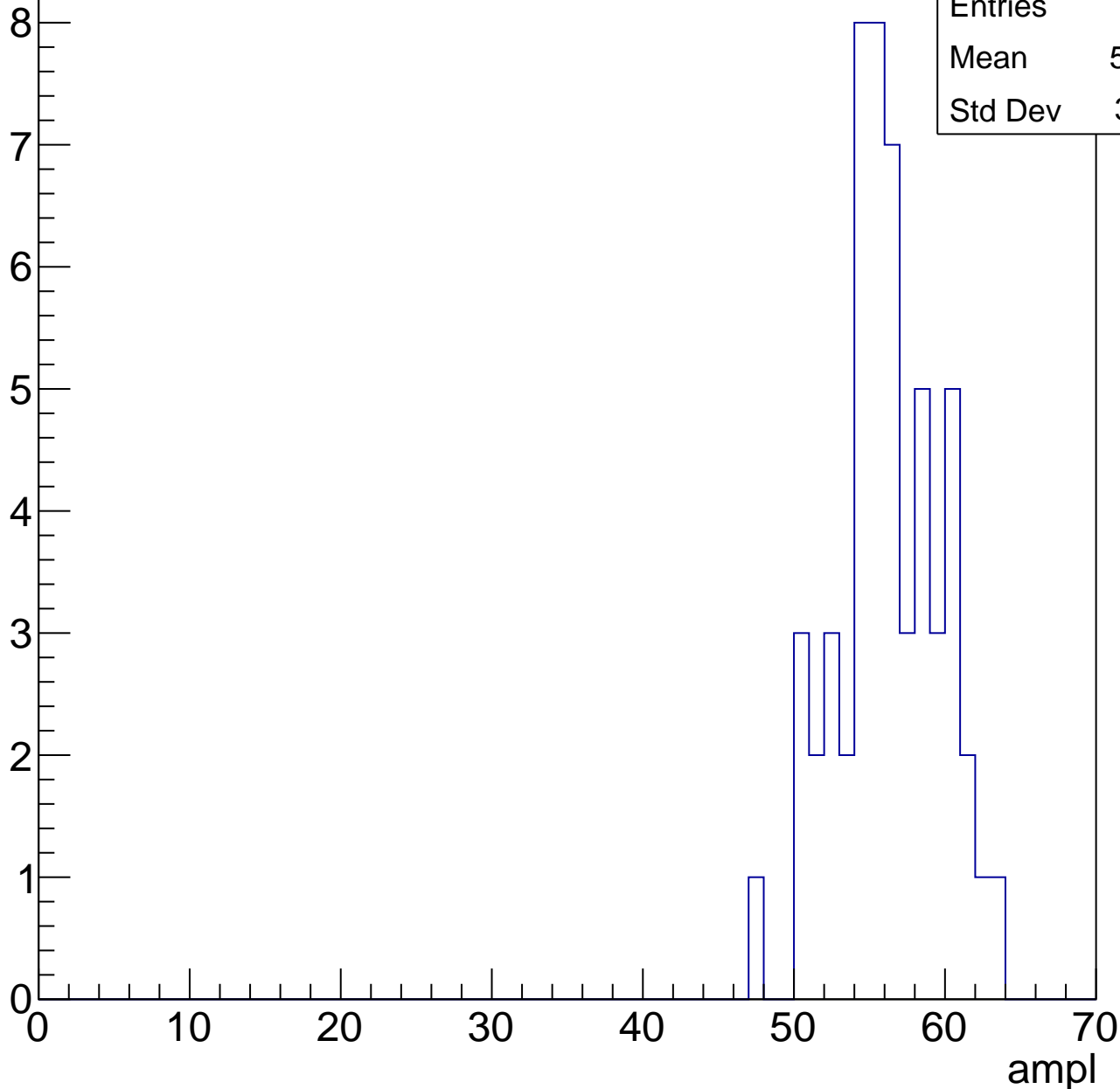


B0L001S, U19-ch105, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	55.74
Std Dev	3.351

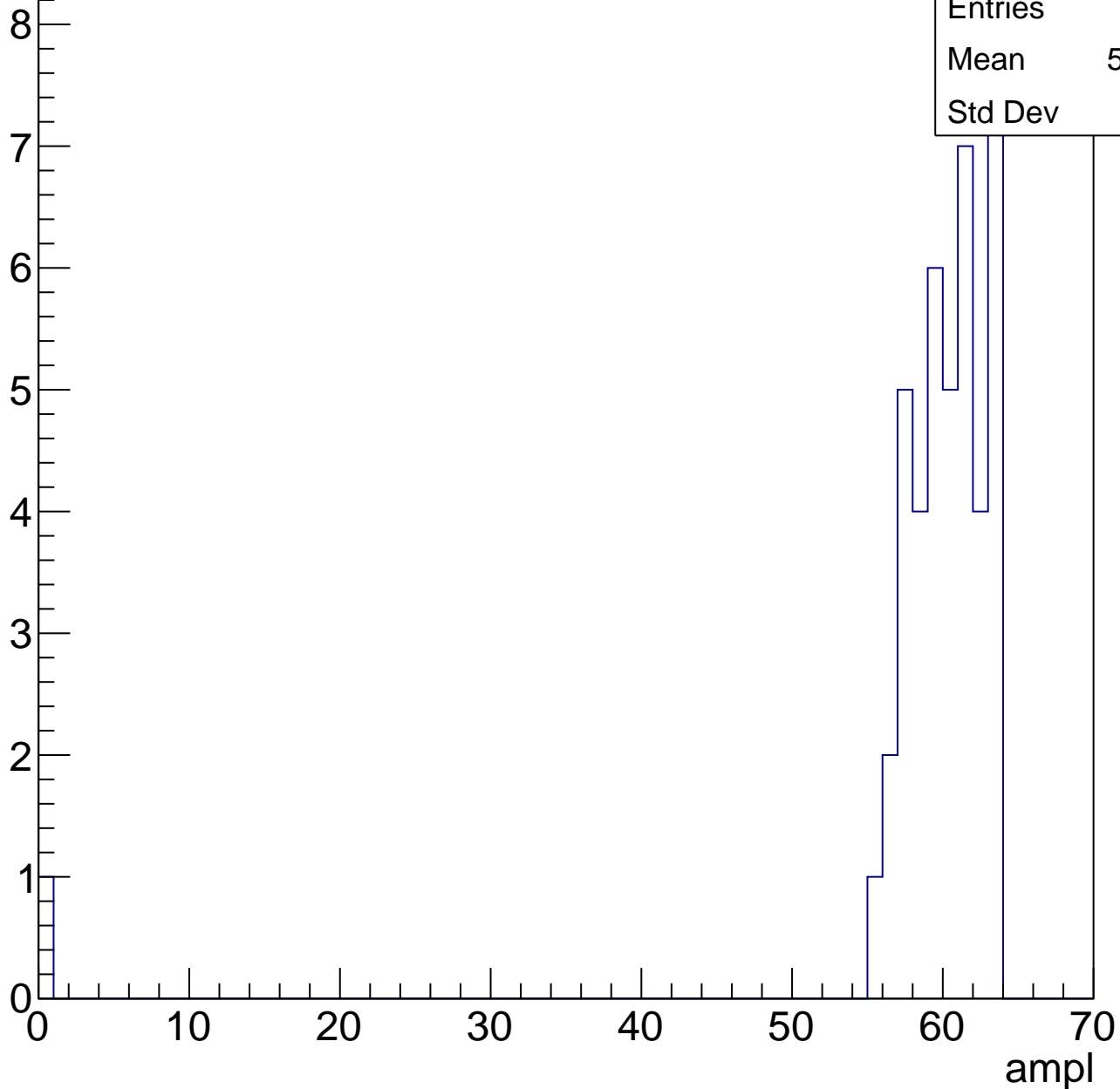


B0L001S, U19-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.53
Std Dev	9.31

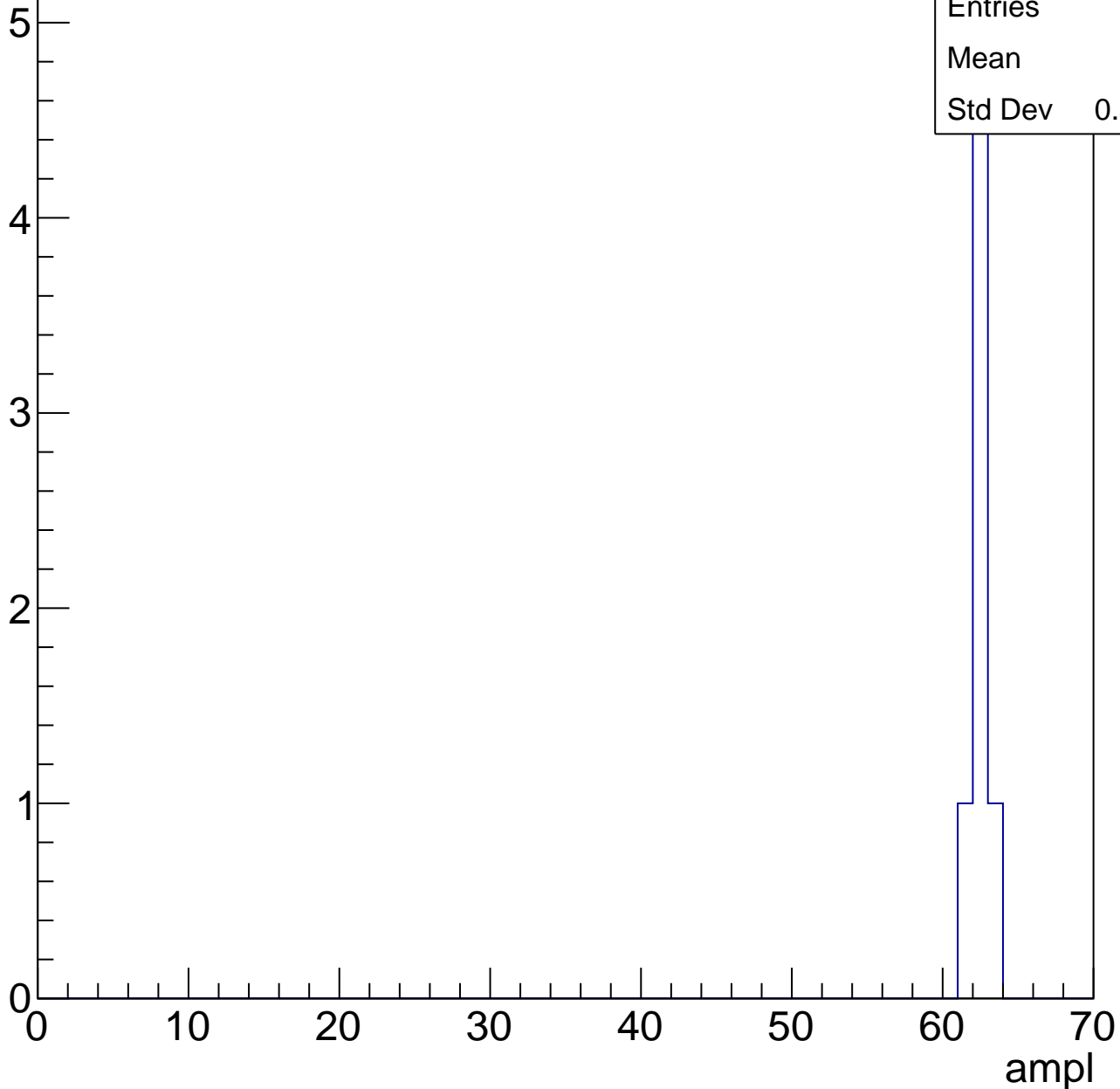


B0L001S, U19-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	7
Mean	62
Std Dev	0.5345



B0L001S, U19-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch106, adc0

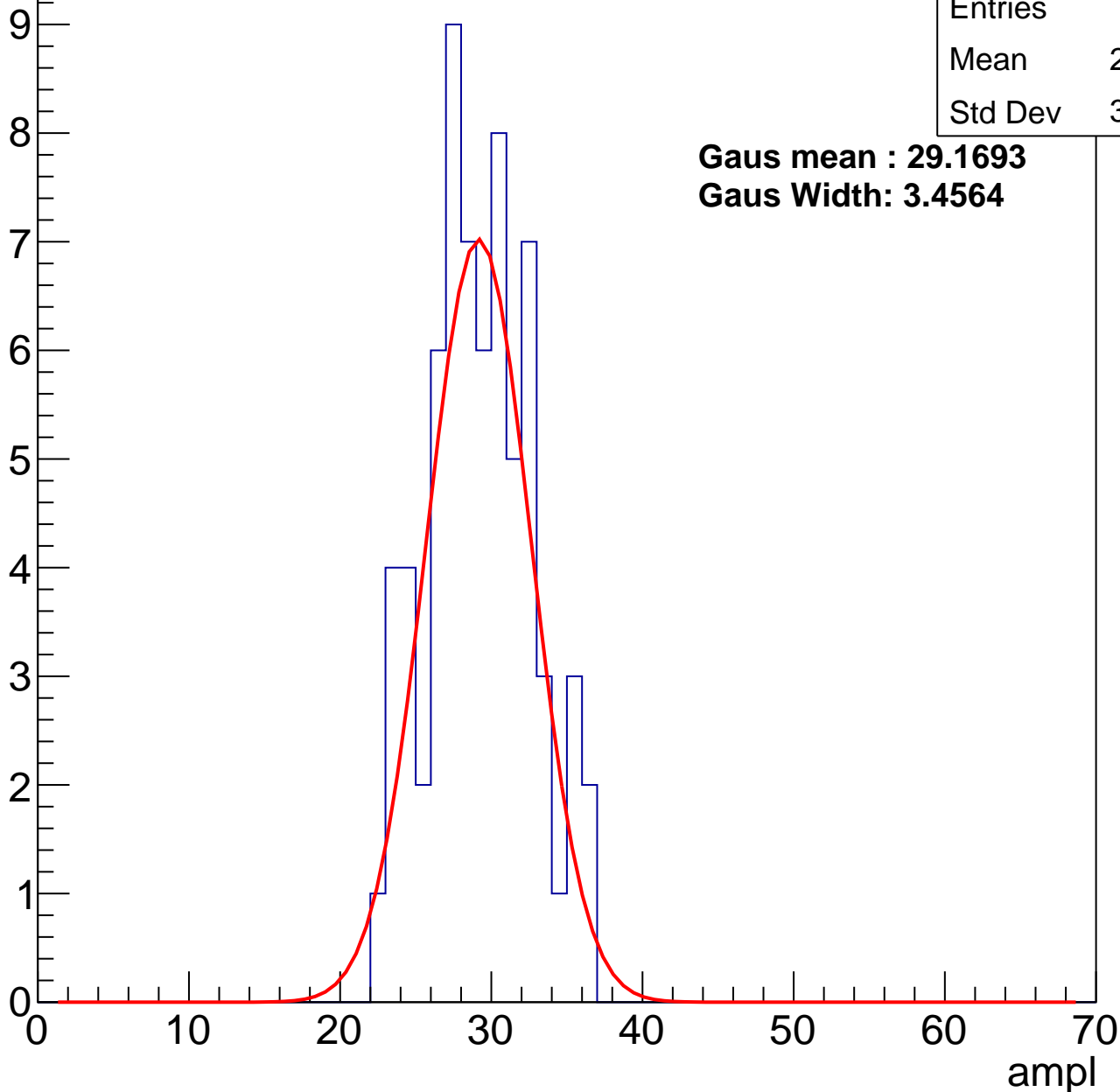
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	28.79
Std Dev	3.407

Gaus mean : 29.1693

Gaus Width: 3.4564



B0L001S, U19-ch106, adc1

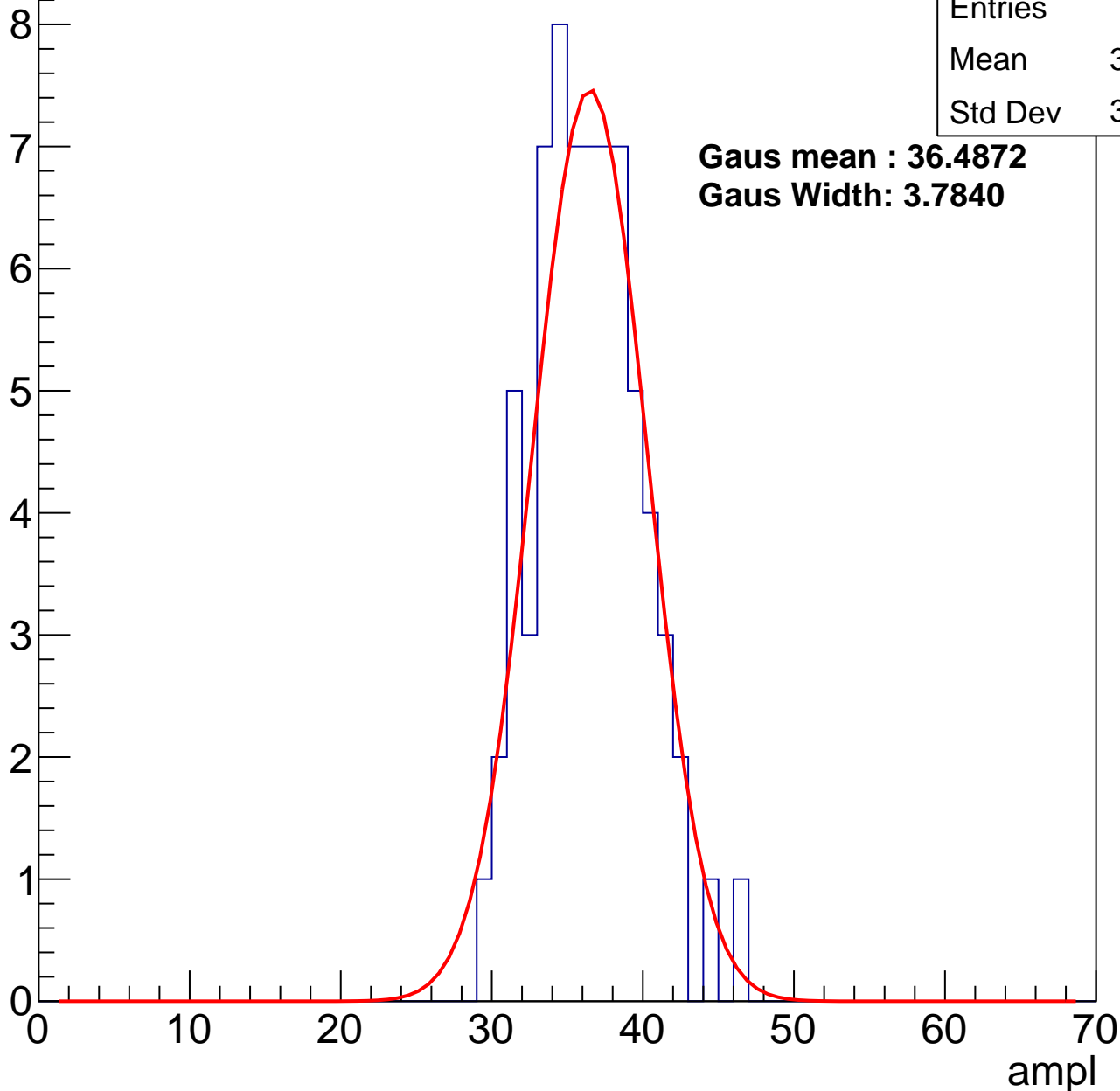
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	35.96
Std Dev	3.486

Gaus mean : 36.4872

Gaus Width: 3.7840



B0L001S, U19-ch106, adc2

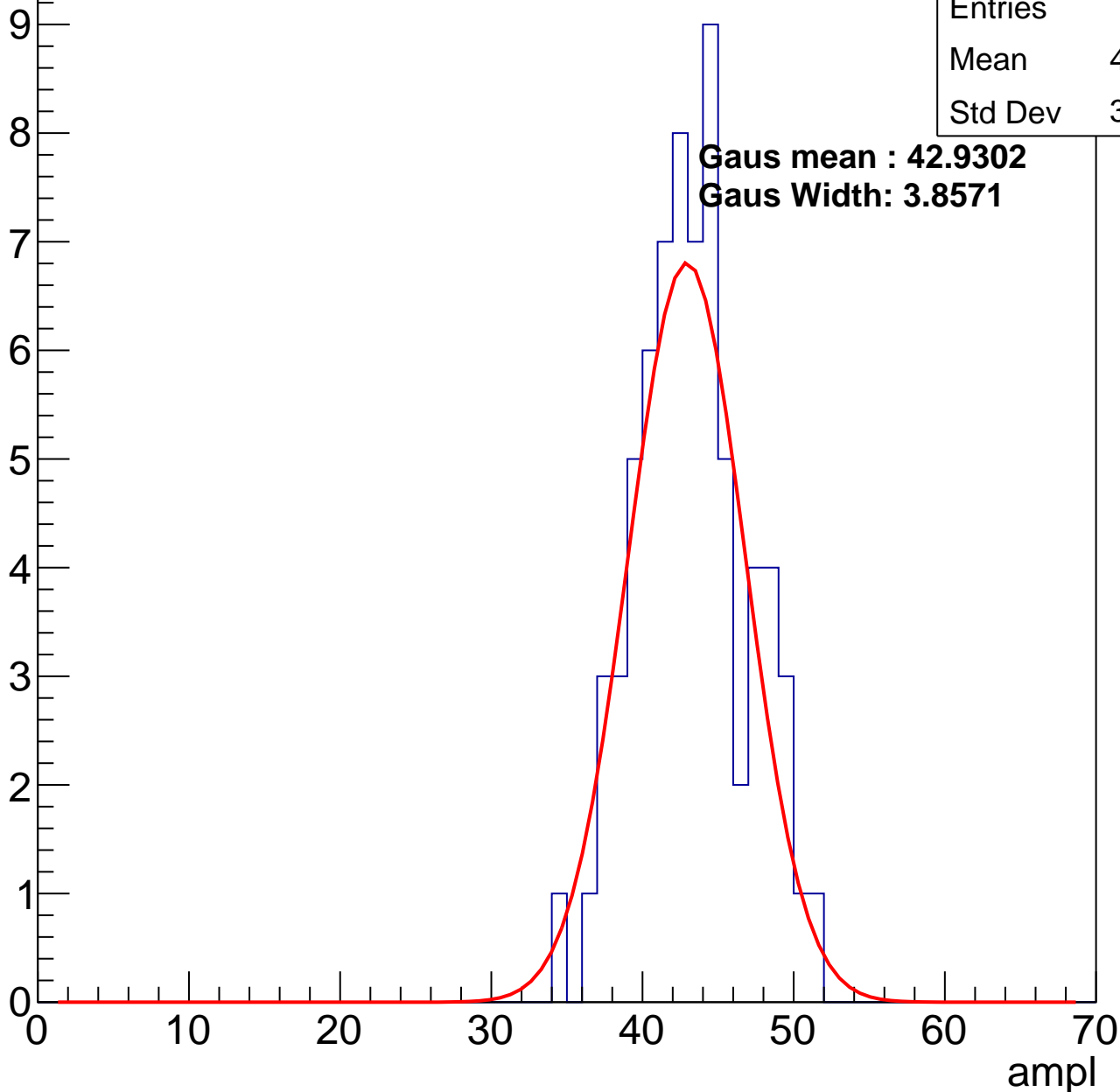
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	42.79
Std Dev	3.609

Gaus mean : 42.9302

Gaus Width: 3.8571

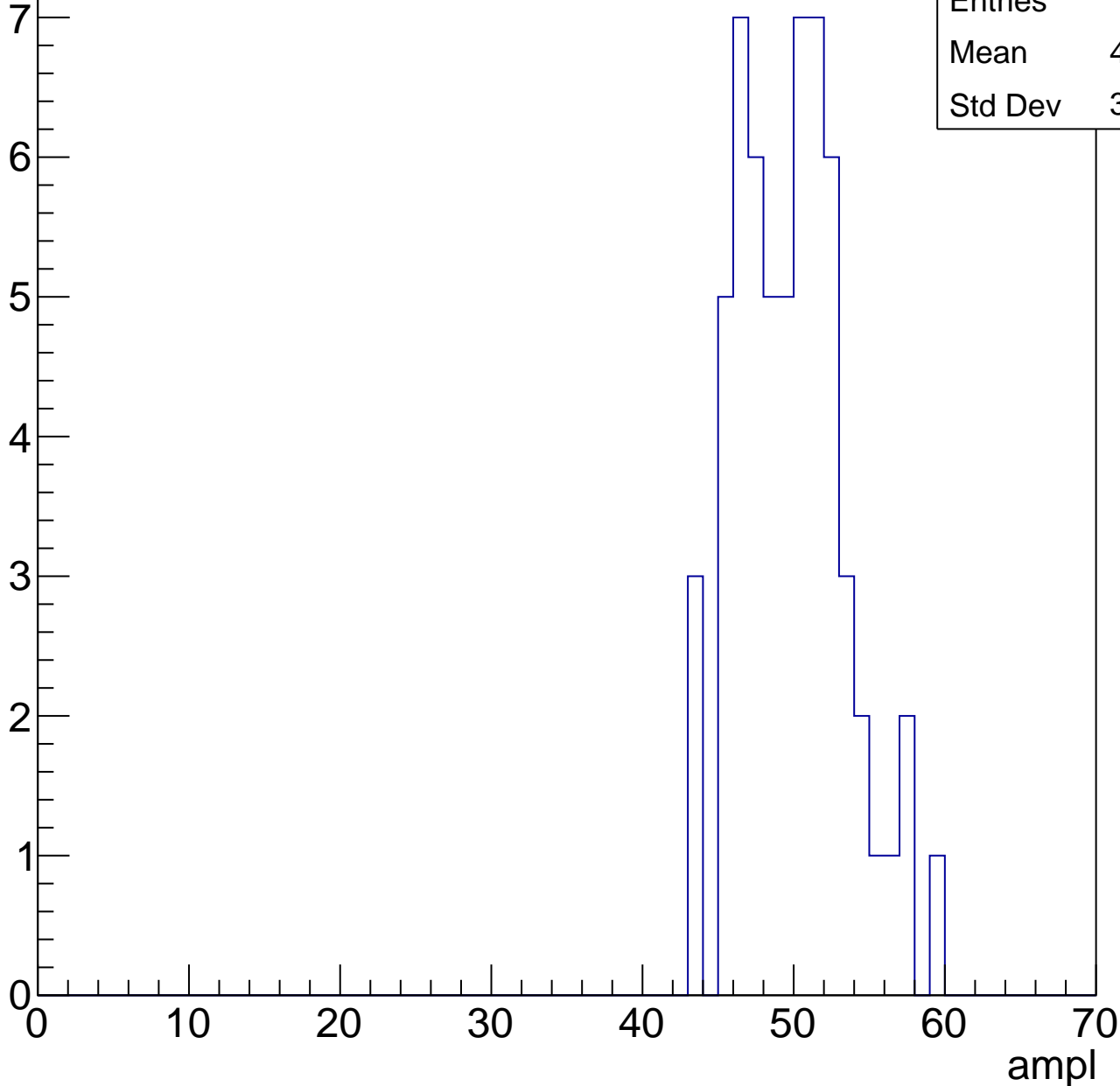


B0L001S, U19-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	49.39
Std Dev	3.554

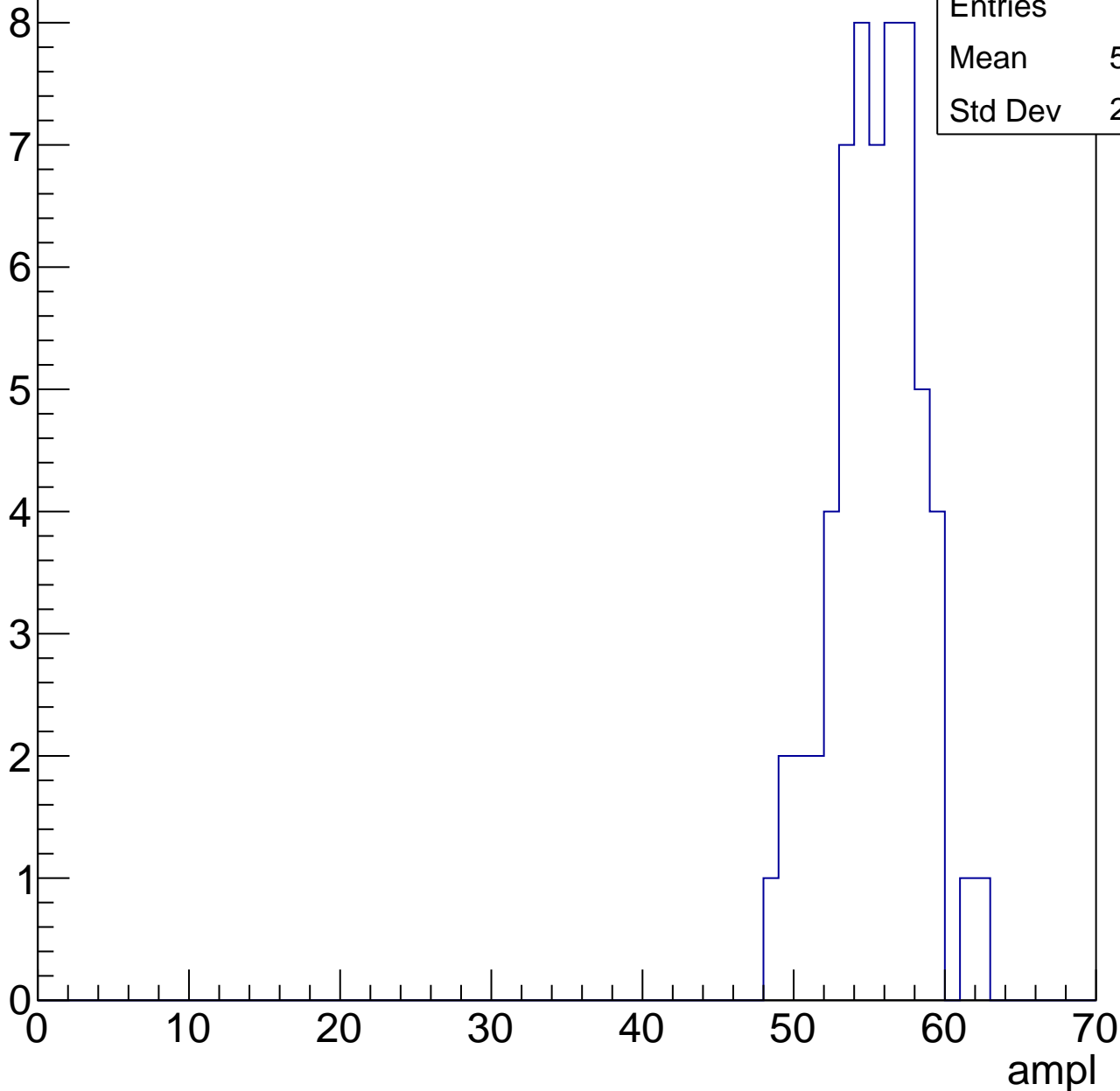


B0L001S, U19-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	54.95
Std Dev	2.912

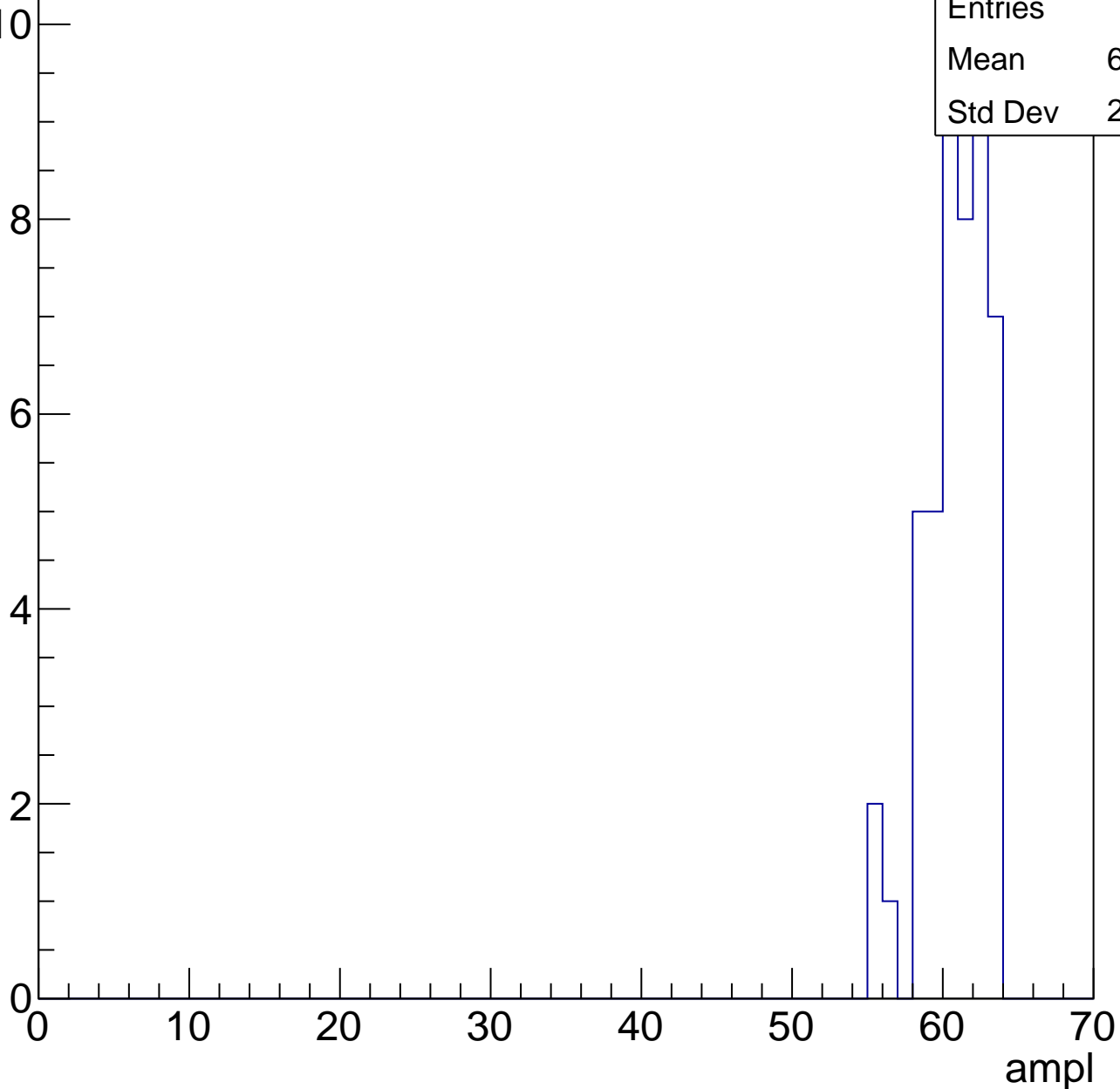


B0L001S, U19-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

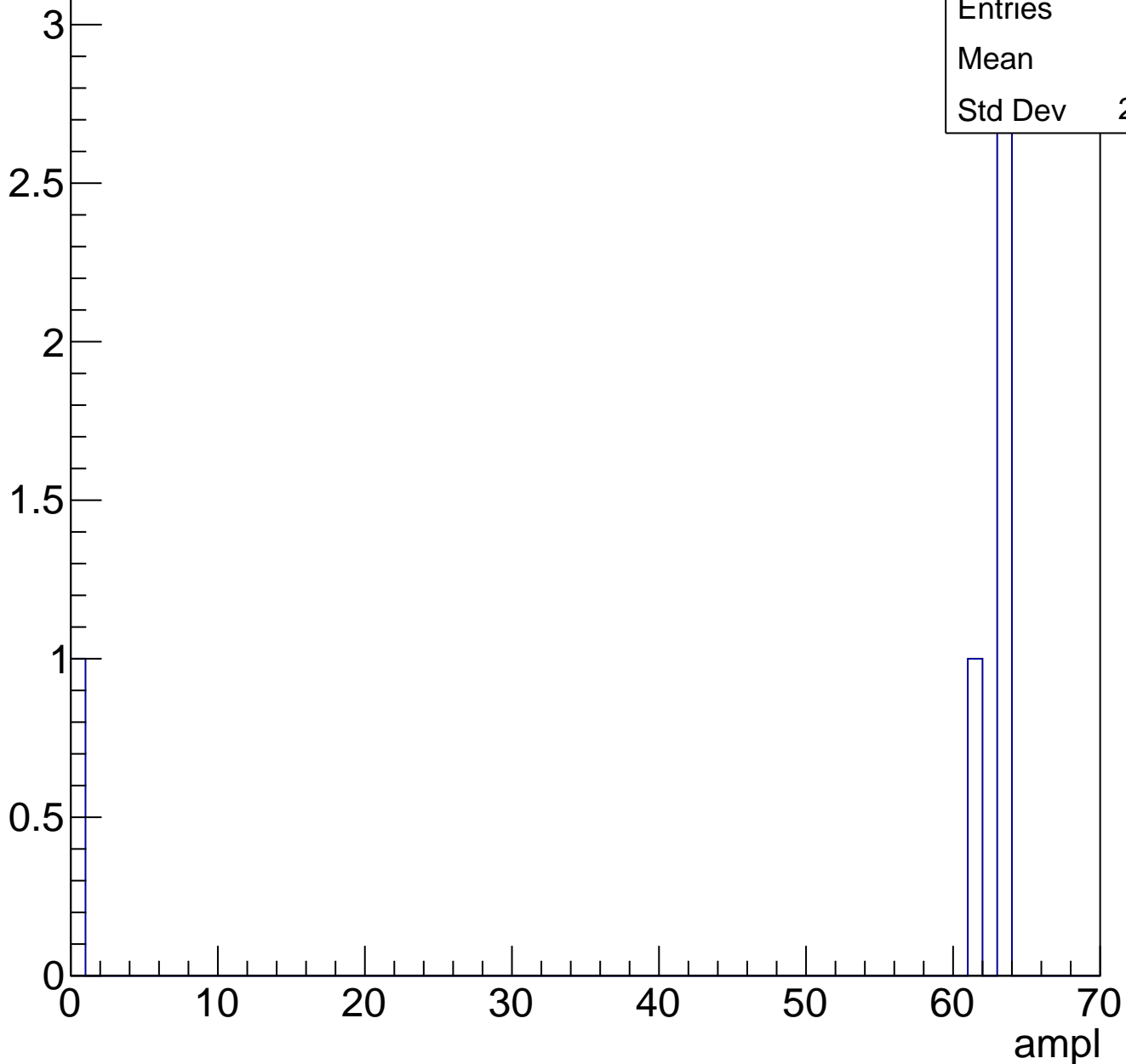
Entries	47
Mean	60.38
Std Dev	2.016



B0L001S, U19-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U19-ch107, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	29.85
Std Dev	5.014

Gaus mean : 31.4438

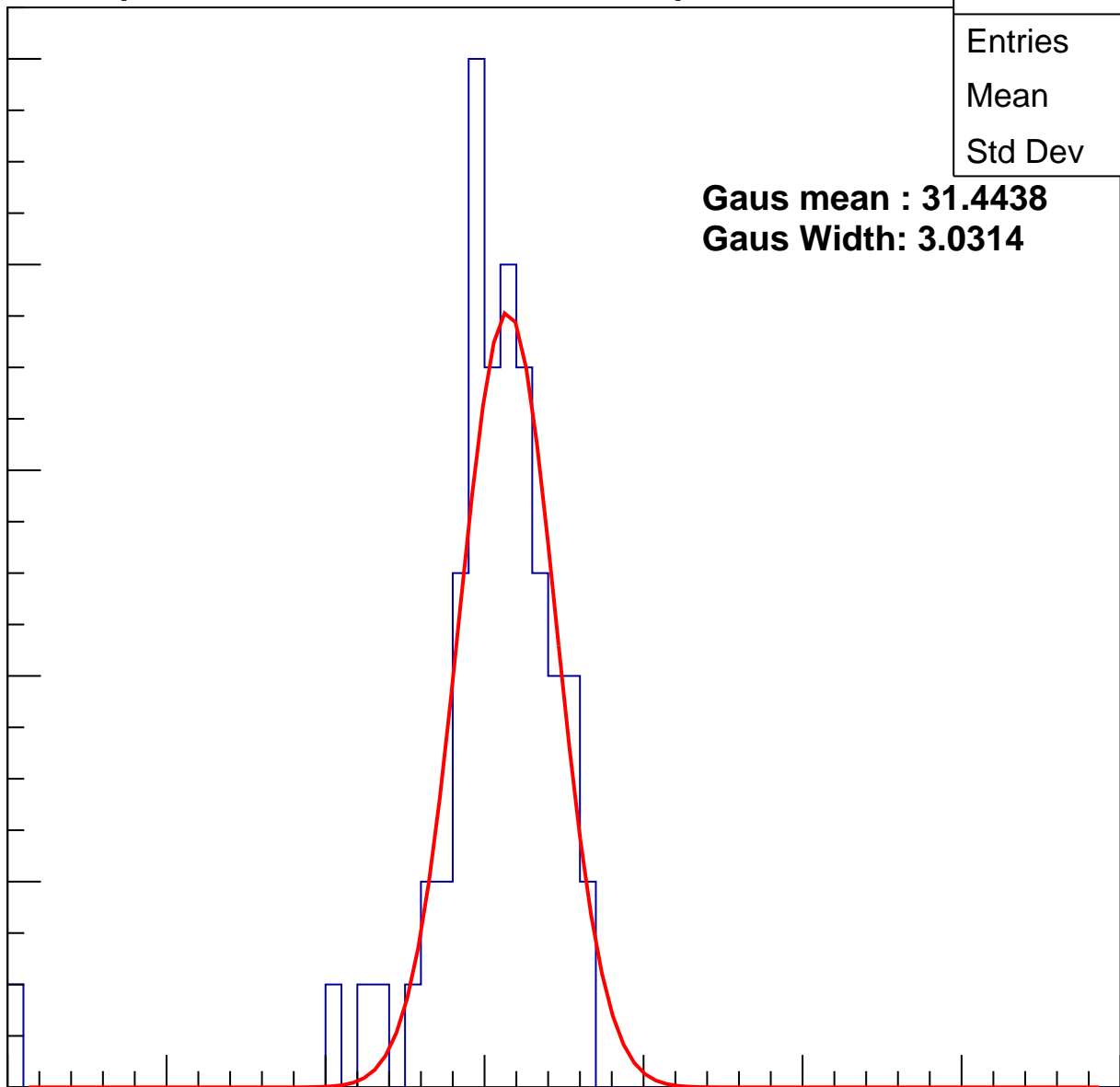
Gaus Width: 3.0314

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch107, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	36.75
Std Dev	3.299

Gaus mean : 37.3240

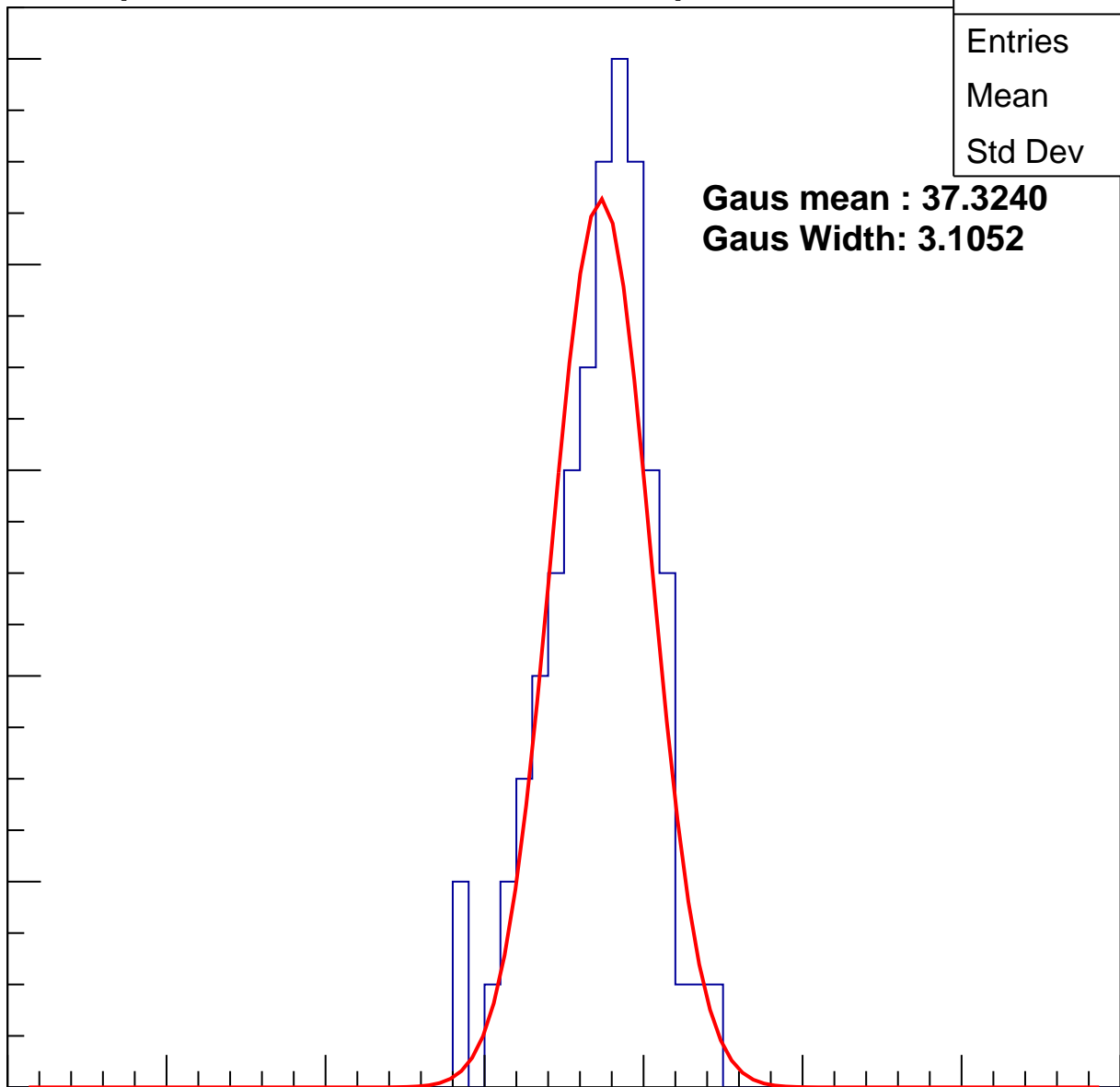
Gaus Width: 3.1052

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch107, adc2

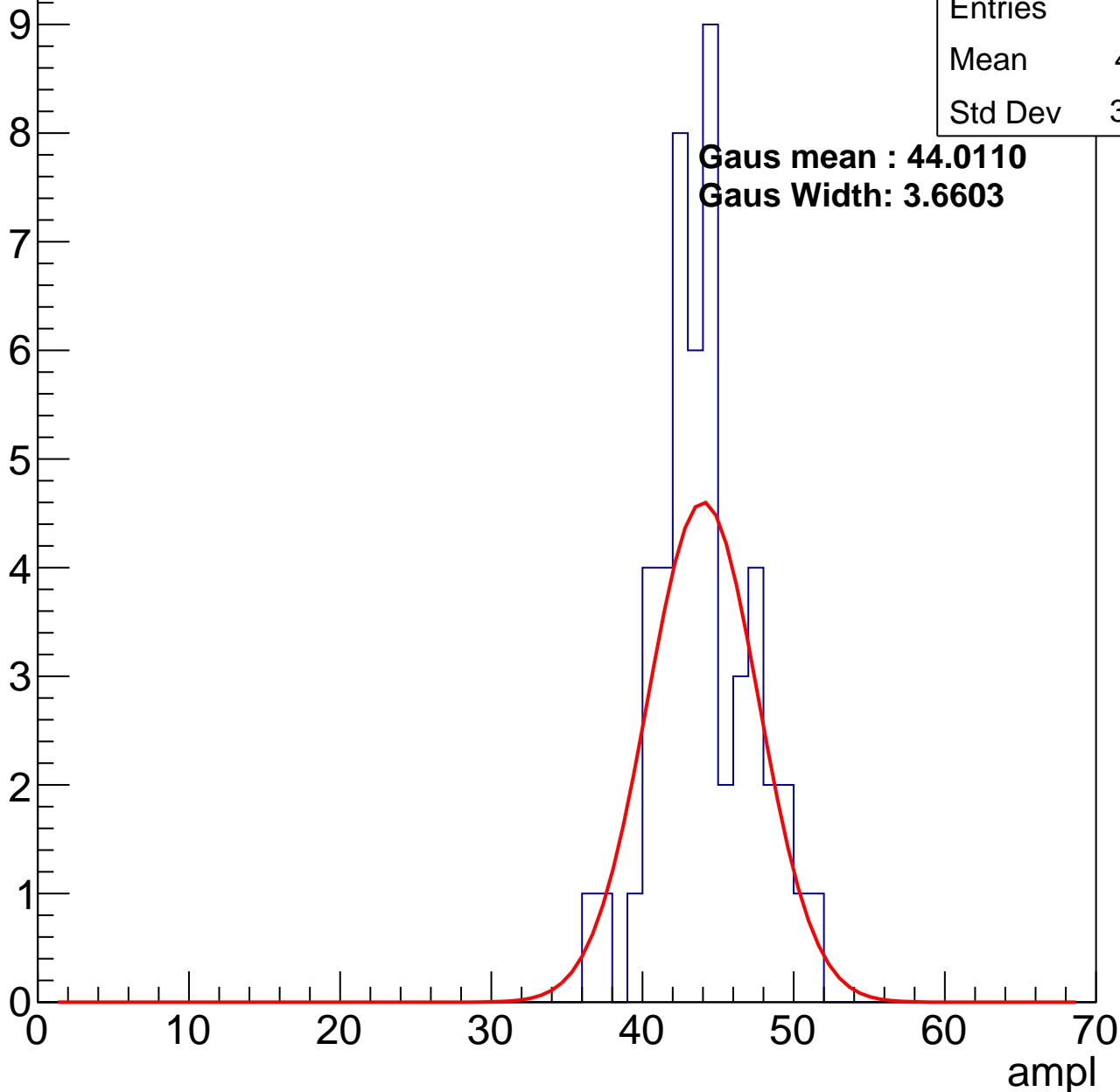
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	43.61
Std Dev	3.148

Gaus mean : 44.0110

Gaus Width: 3.6603

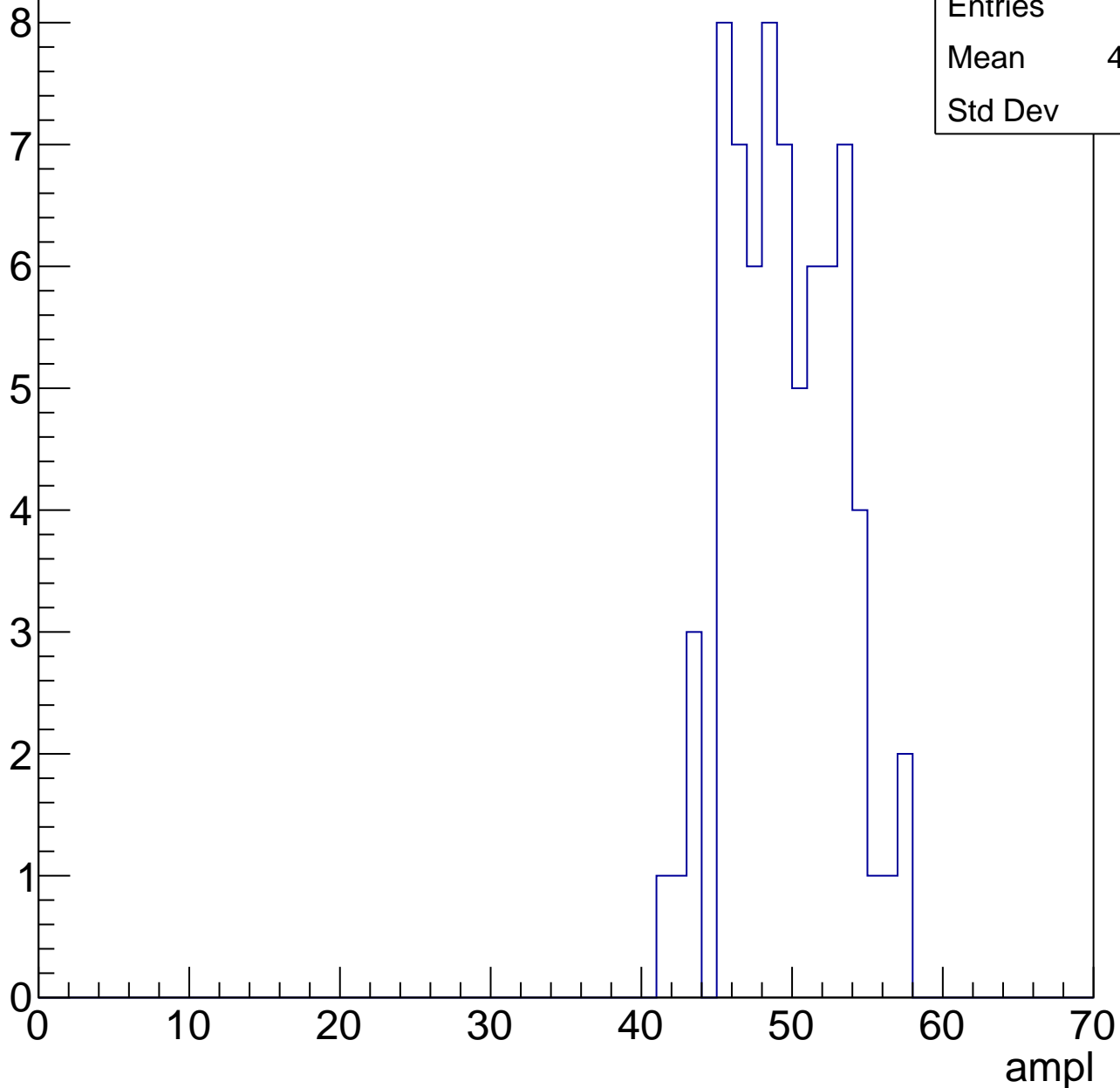


B0L001S, U19-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	49.08
Std Dev	3.61

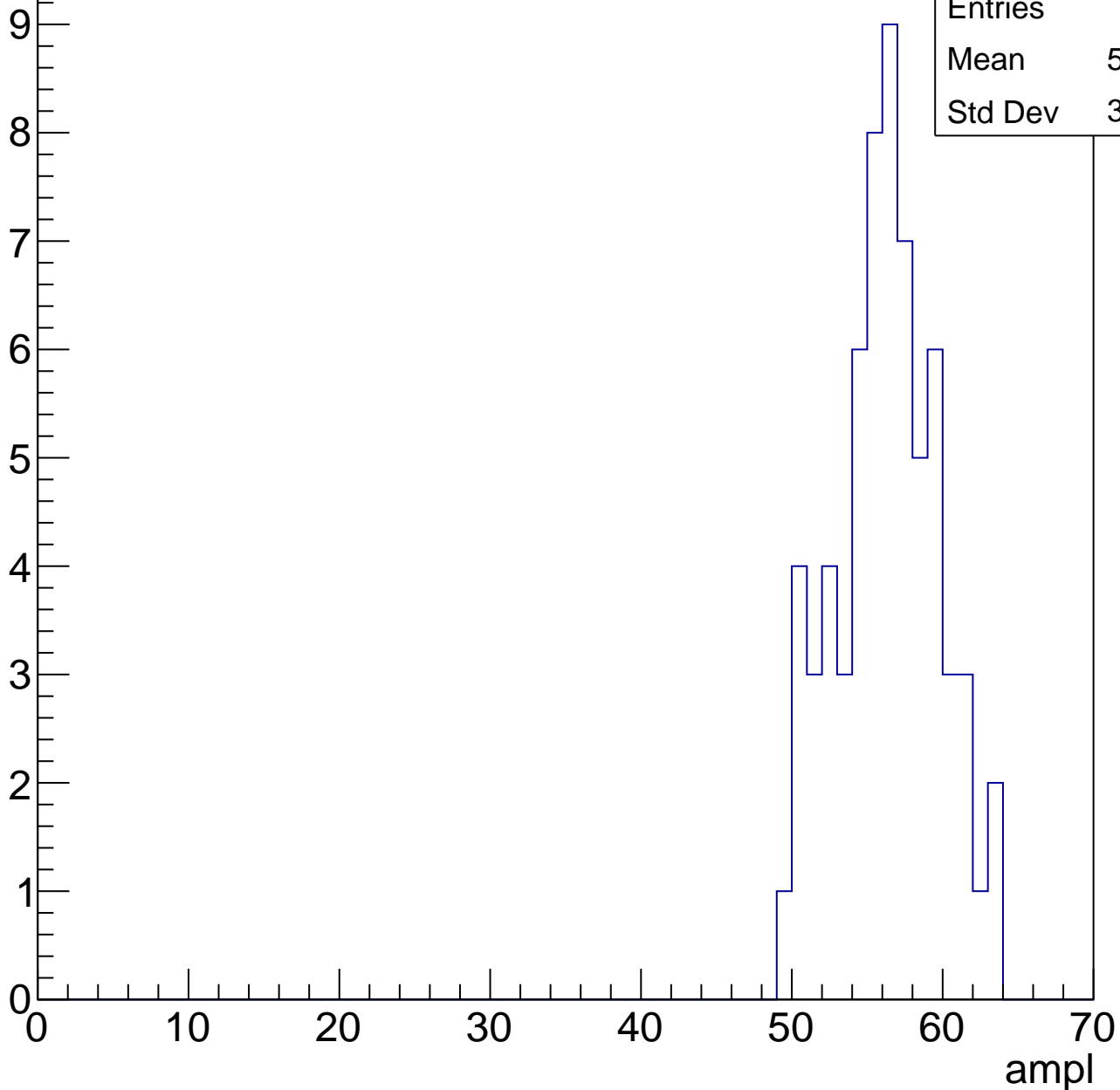


B0L001S, U19-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	55.86
Std Dev	3.346

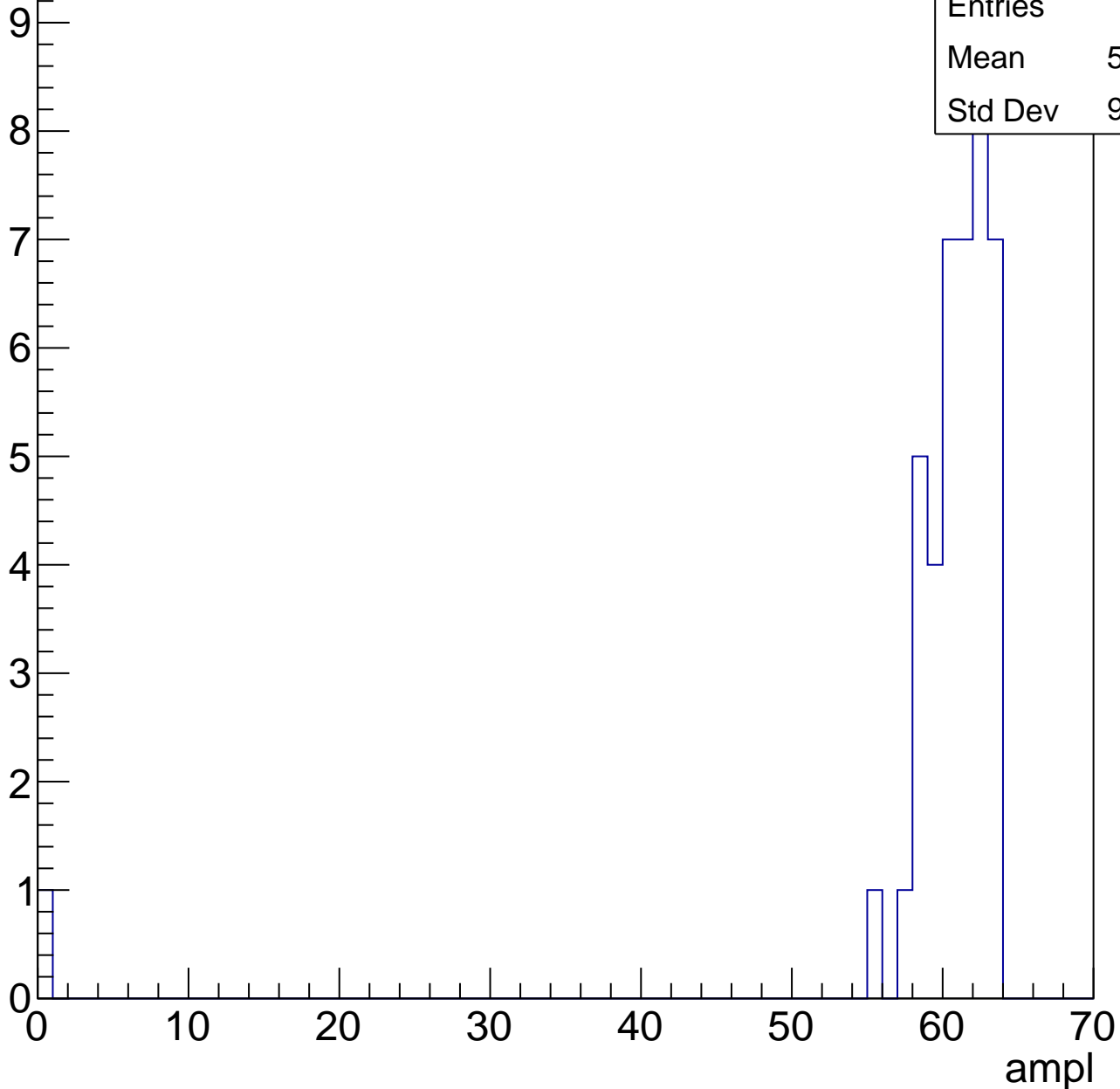


B0L001S, U19-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59.14
Std Dev	9.428



B0L001S, U19-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	63
Std Dev	0

B0L001S, U19-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch108, adc0

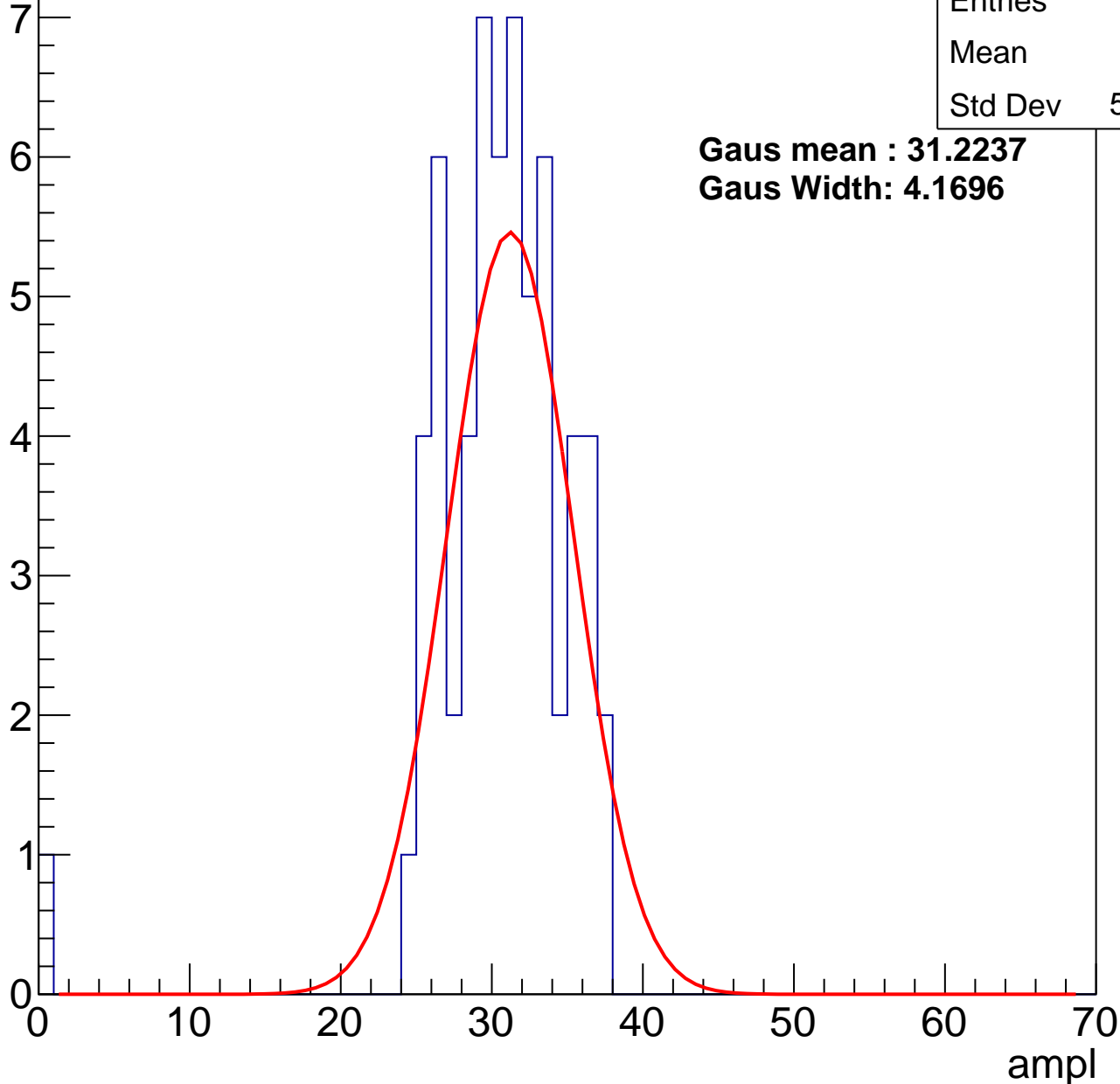
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30
Std Dev	5.166

Gaus mean : 31.2237

Gaus Width: 4.1696



B0L001S, U19-ch108, adc1

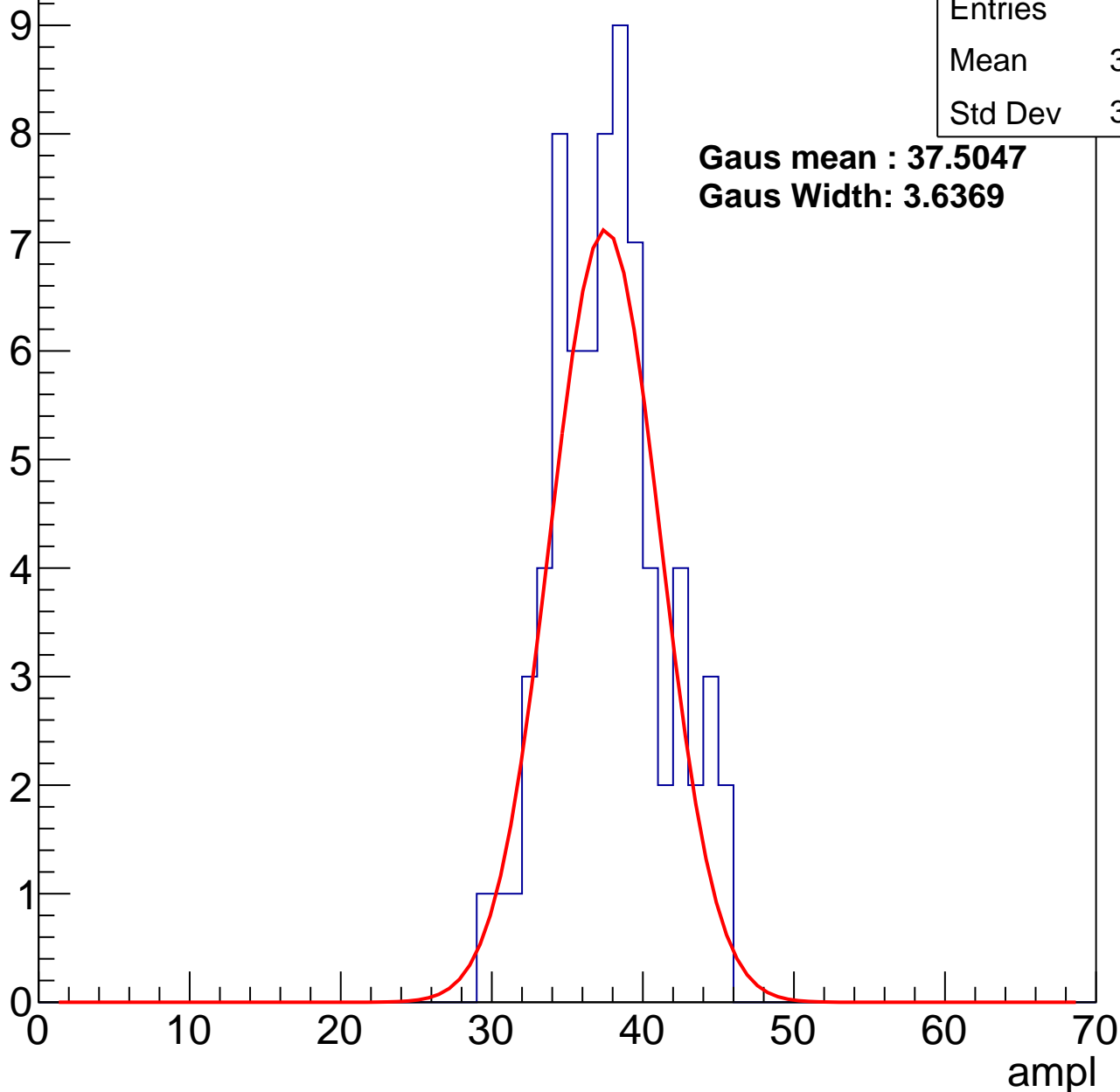
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.25
Std Dev	3.634

Gaus mean : 37.5047

Gaus Width: 3.6369



B0L001S, U19-ch108, adc2

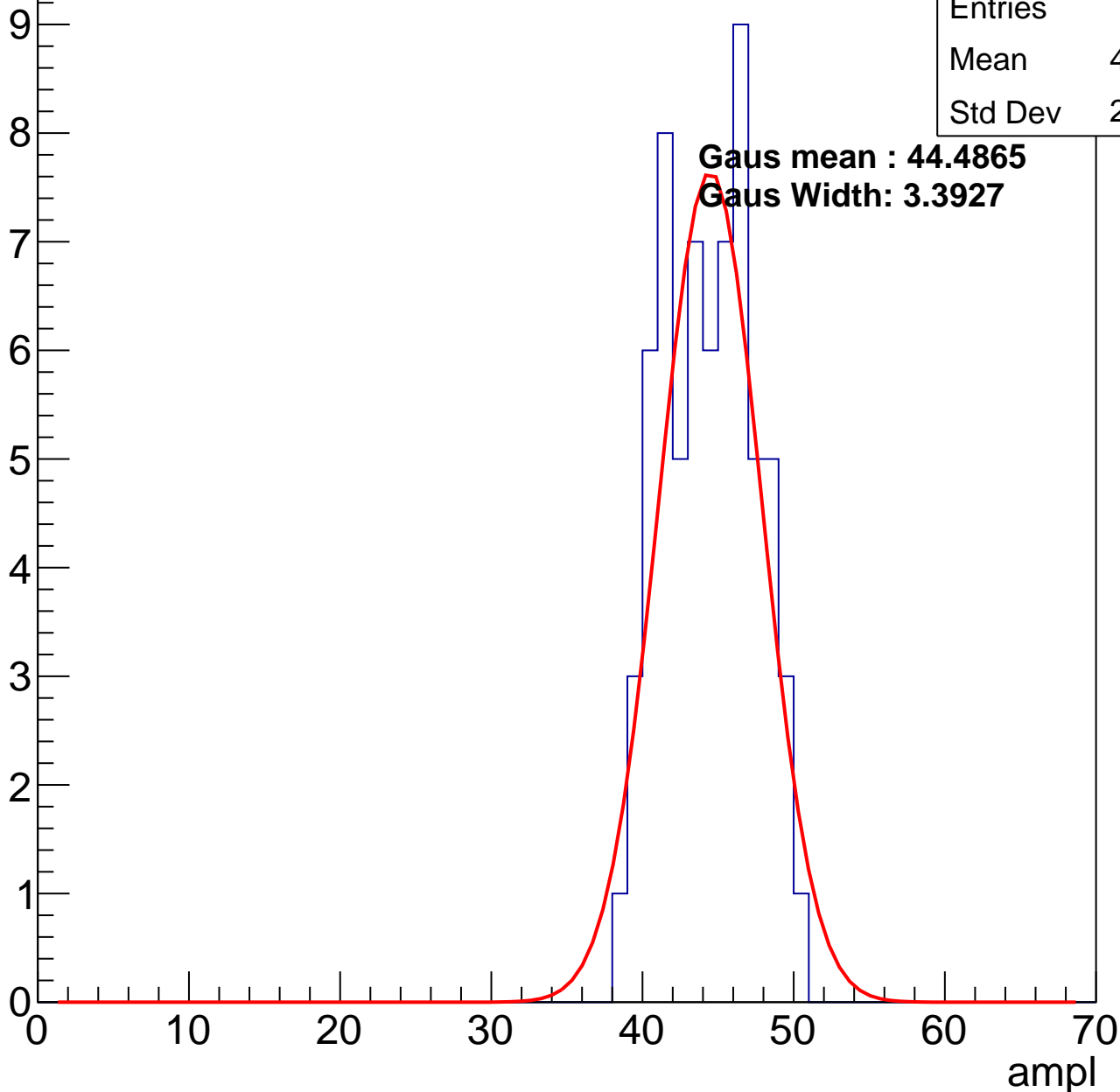
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.92
Std Dev	2.976

Gaus mean : 44.4865

Gaus Width: 3.3927

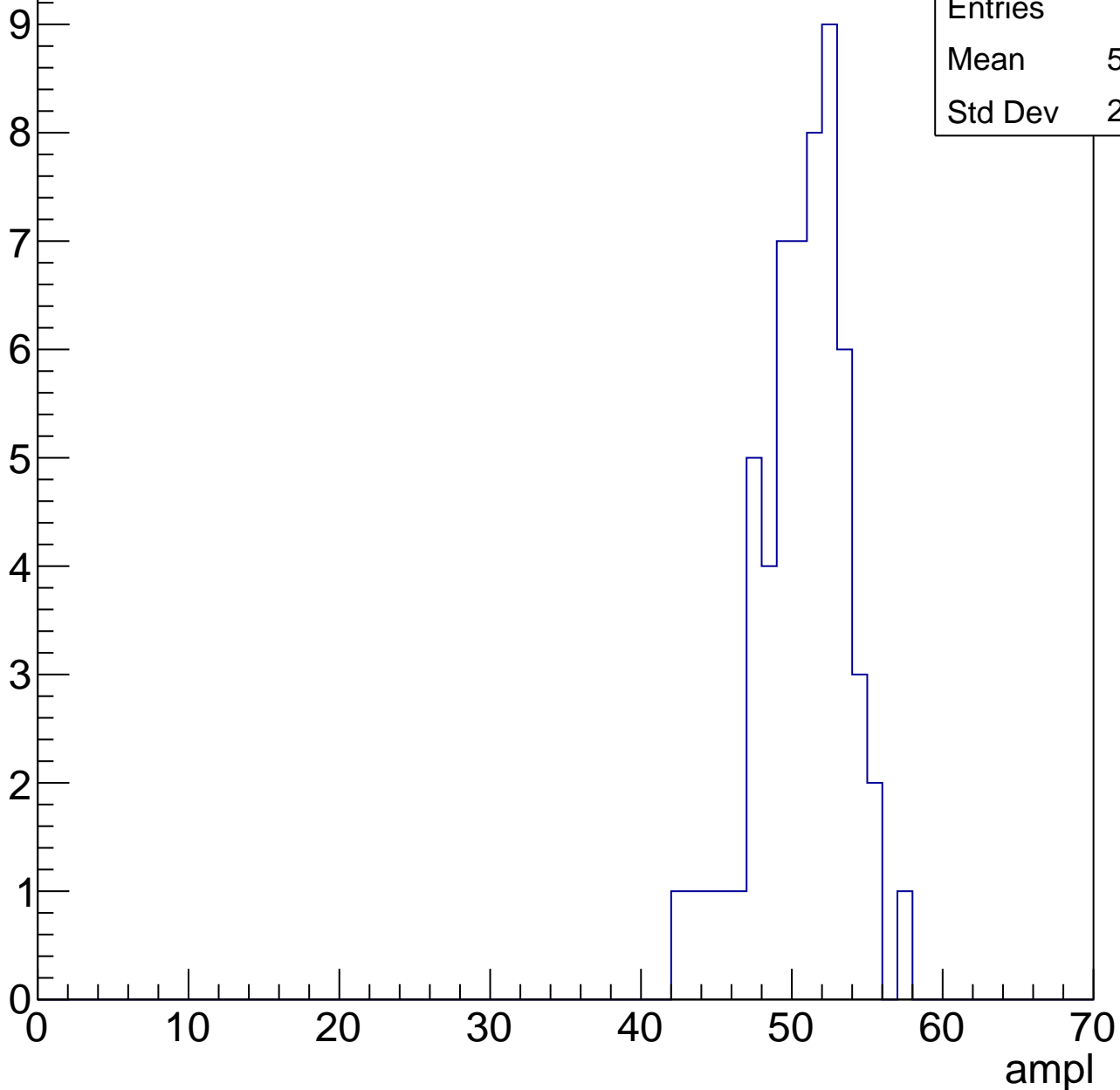


B0L001S, U19-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	50.23
Std Dev	2.962

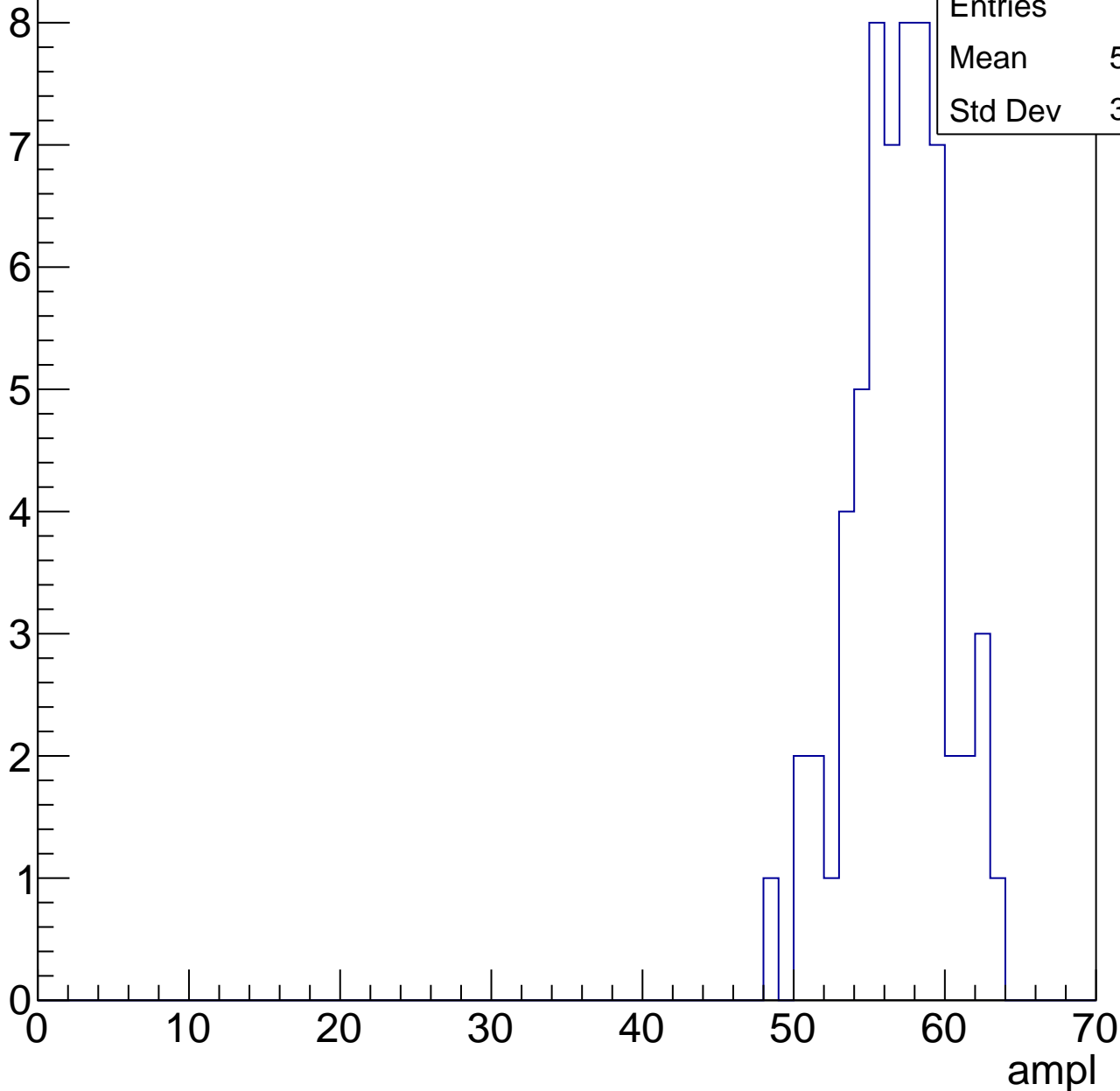


B0L001S, U19-ch108, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

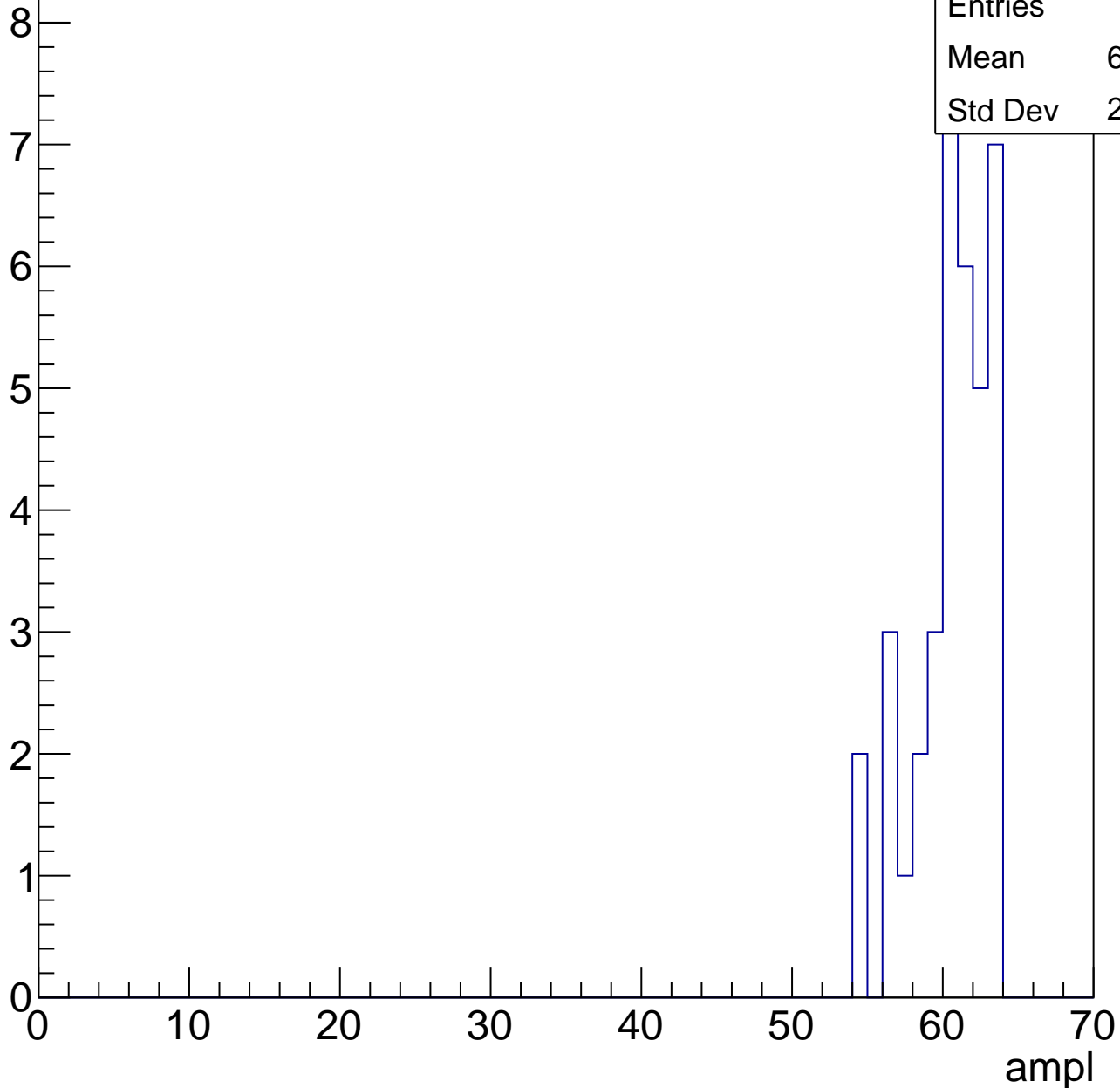
Entries	61
Mean	56.39
Std Dev	3.132



B0L001S, U19-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

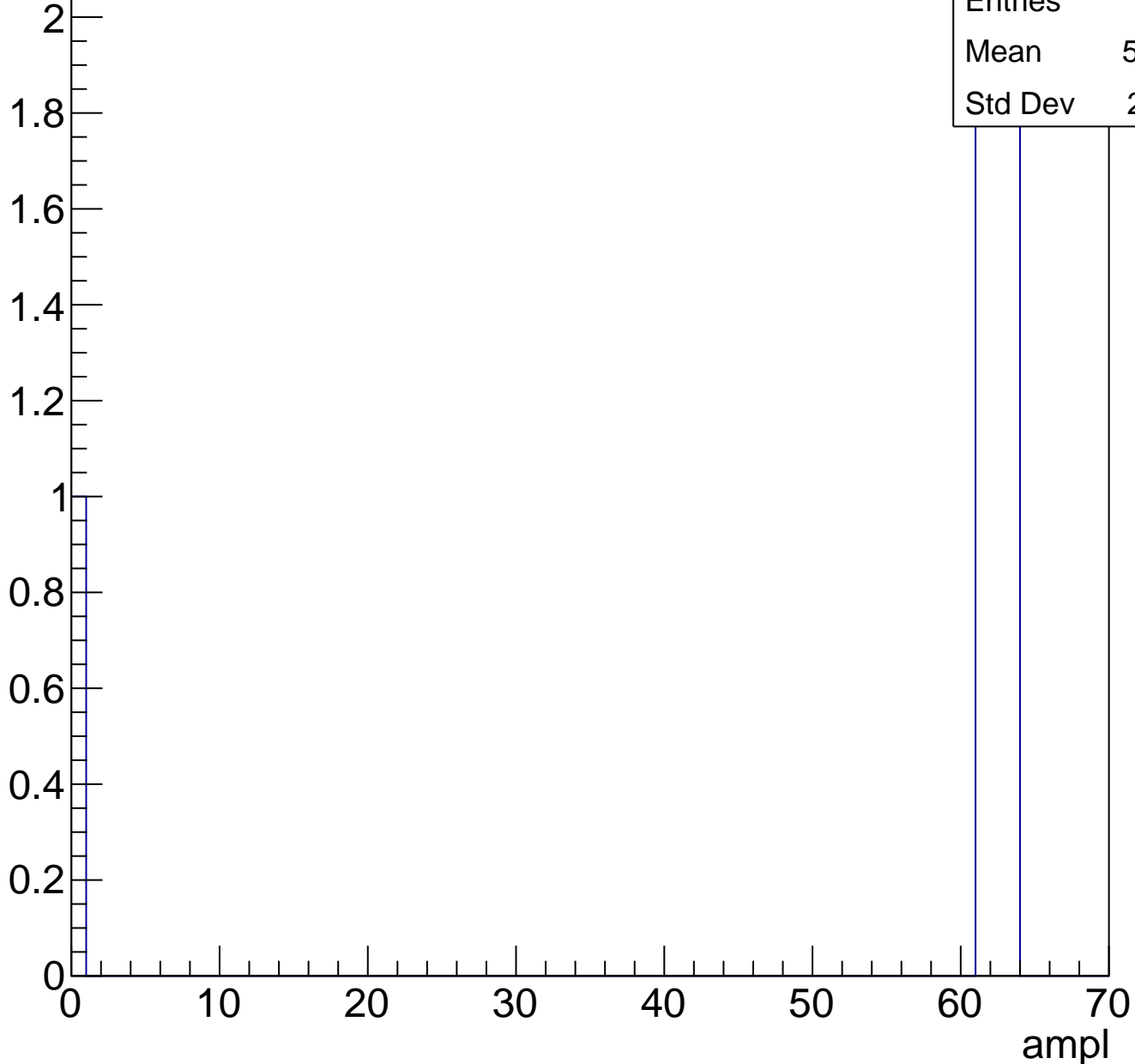
Entry



B0L001S, U19-ch108, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch109, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	28.71
Std Dev	5.827

Gaus mean : 29.7754

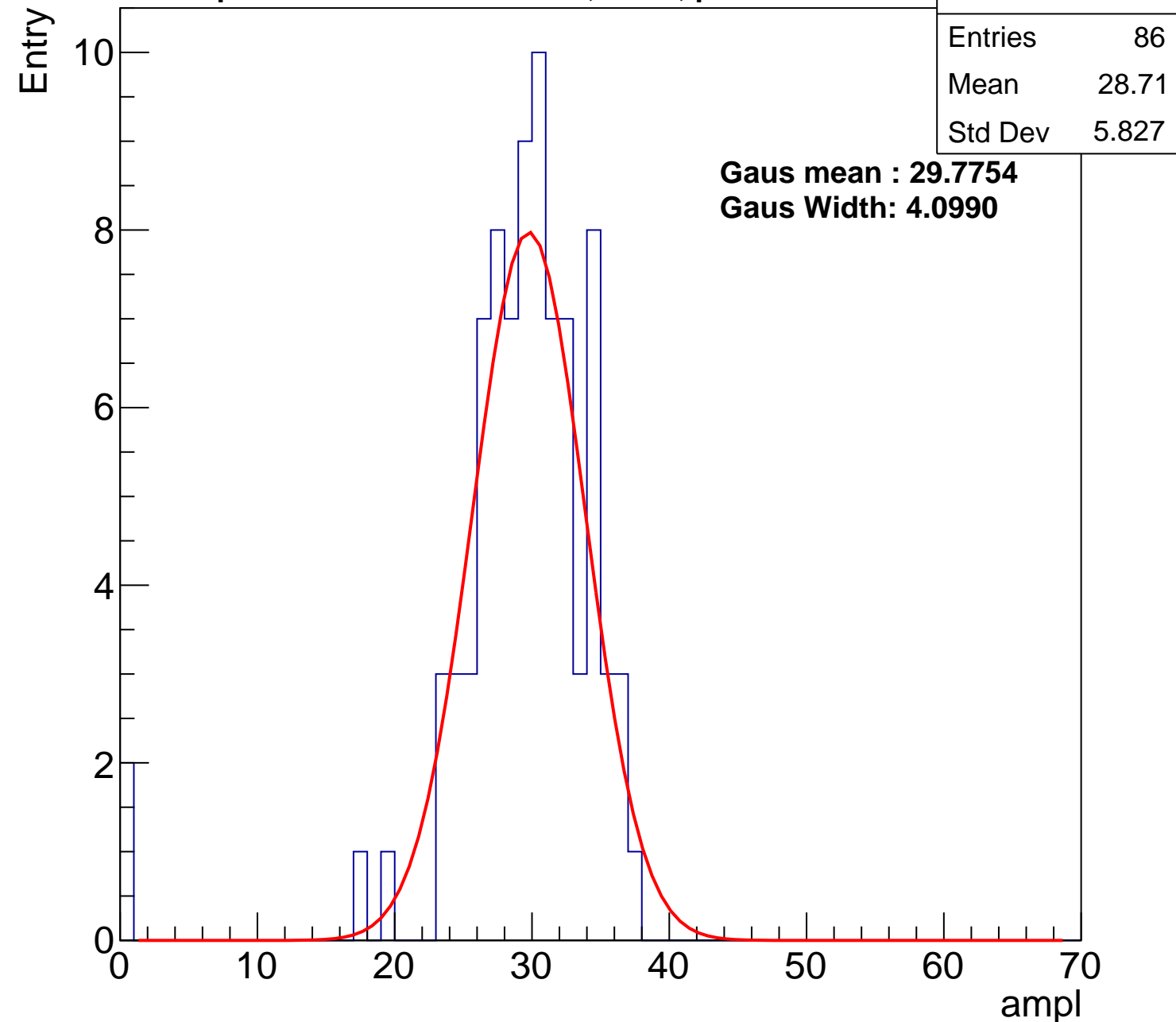
Gaus Width: 4.0990

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch109, adc1

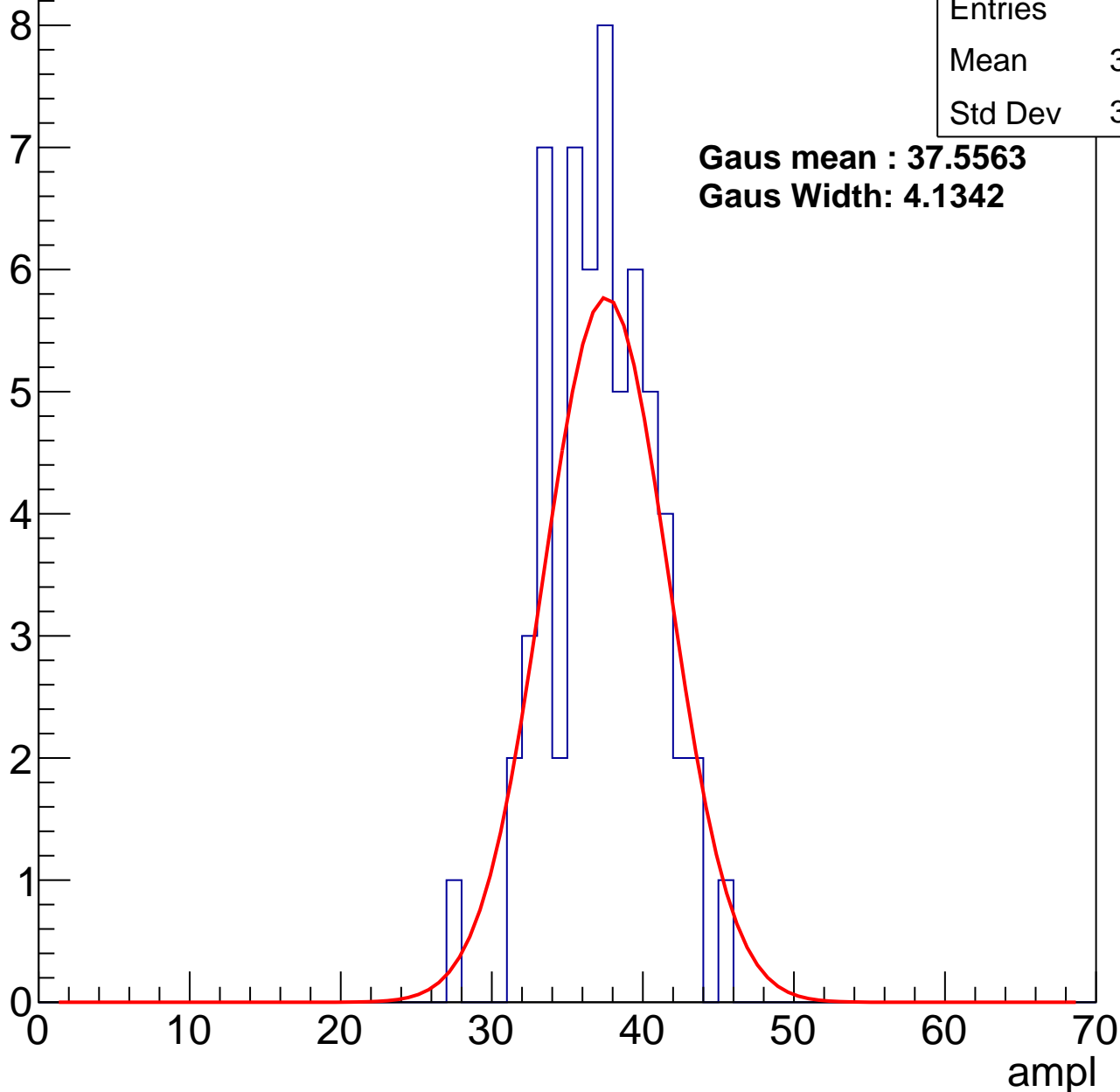
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	36.79
Std Dev	3.465

Gaus mean : 37.5563

Gaus Width: 4.1342



B0L001S, U19-ch109, adc2

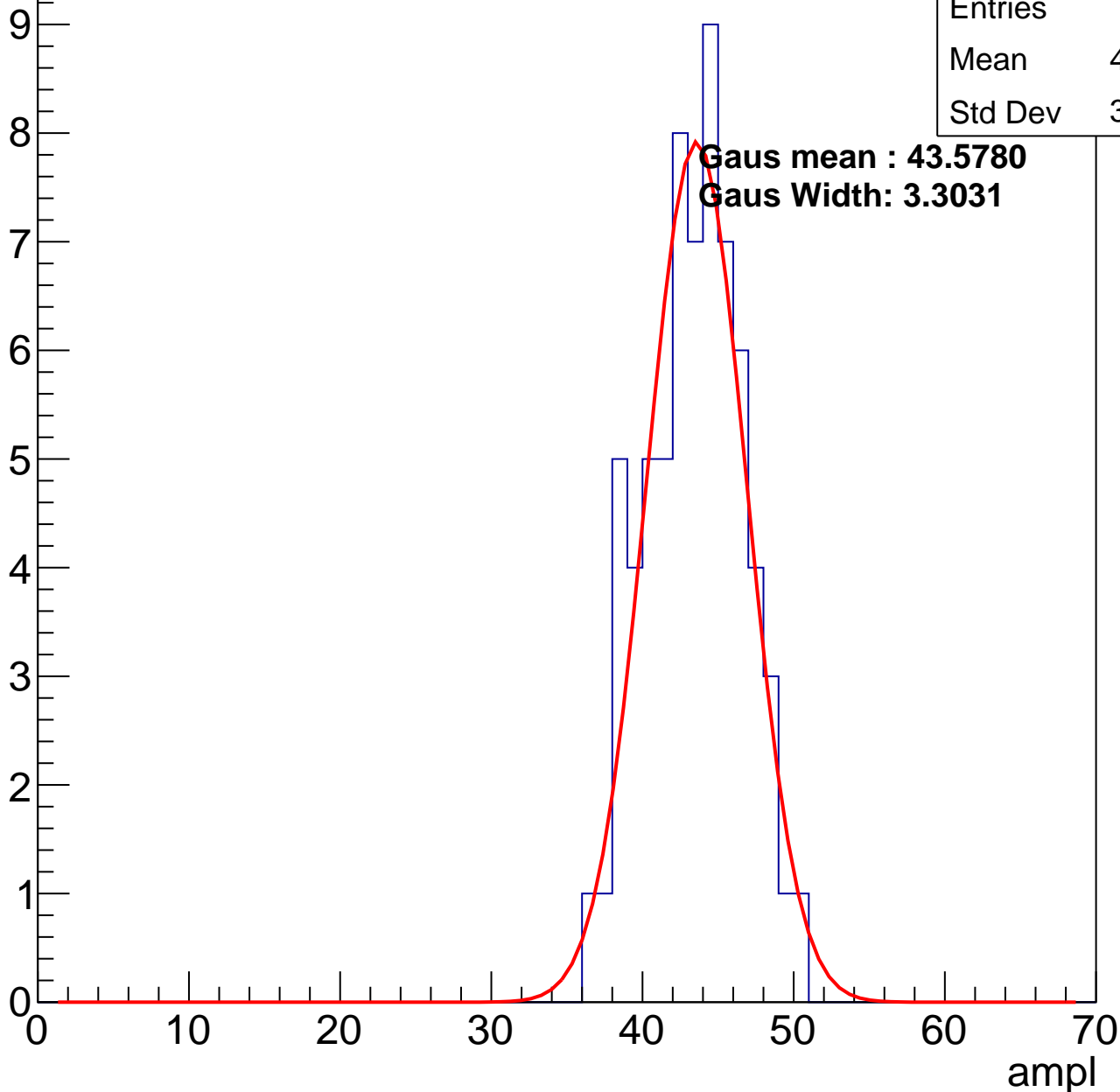
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	42.97
Std Dev	3.143

Gaus mean : 43.5780

Gaus Width: 3.3031

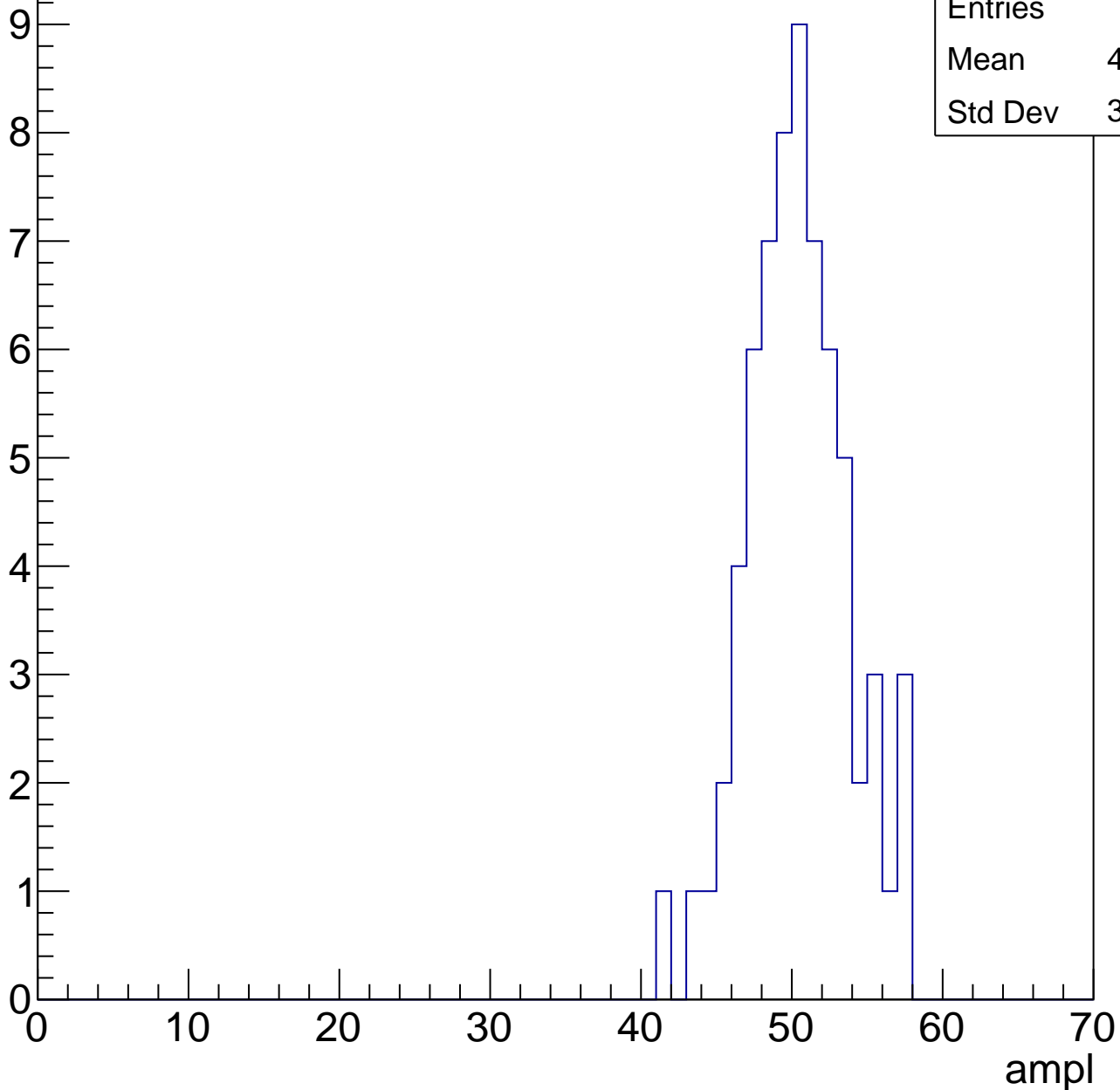


B0L001S, U19-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49.94
Std Dev	3.339

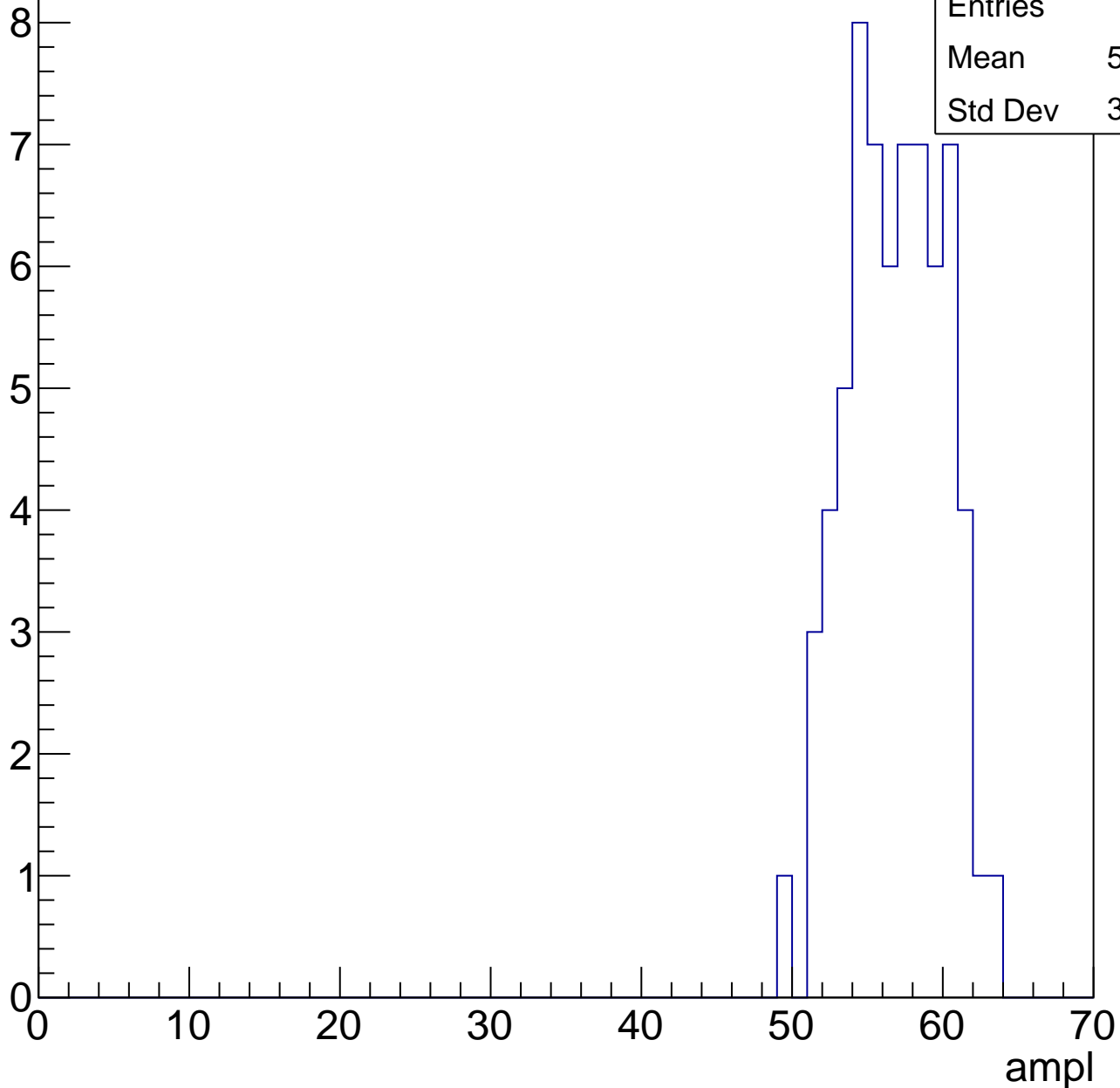


B0L001S, U19-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	56.36
Std Dev	3.113

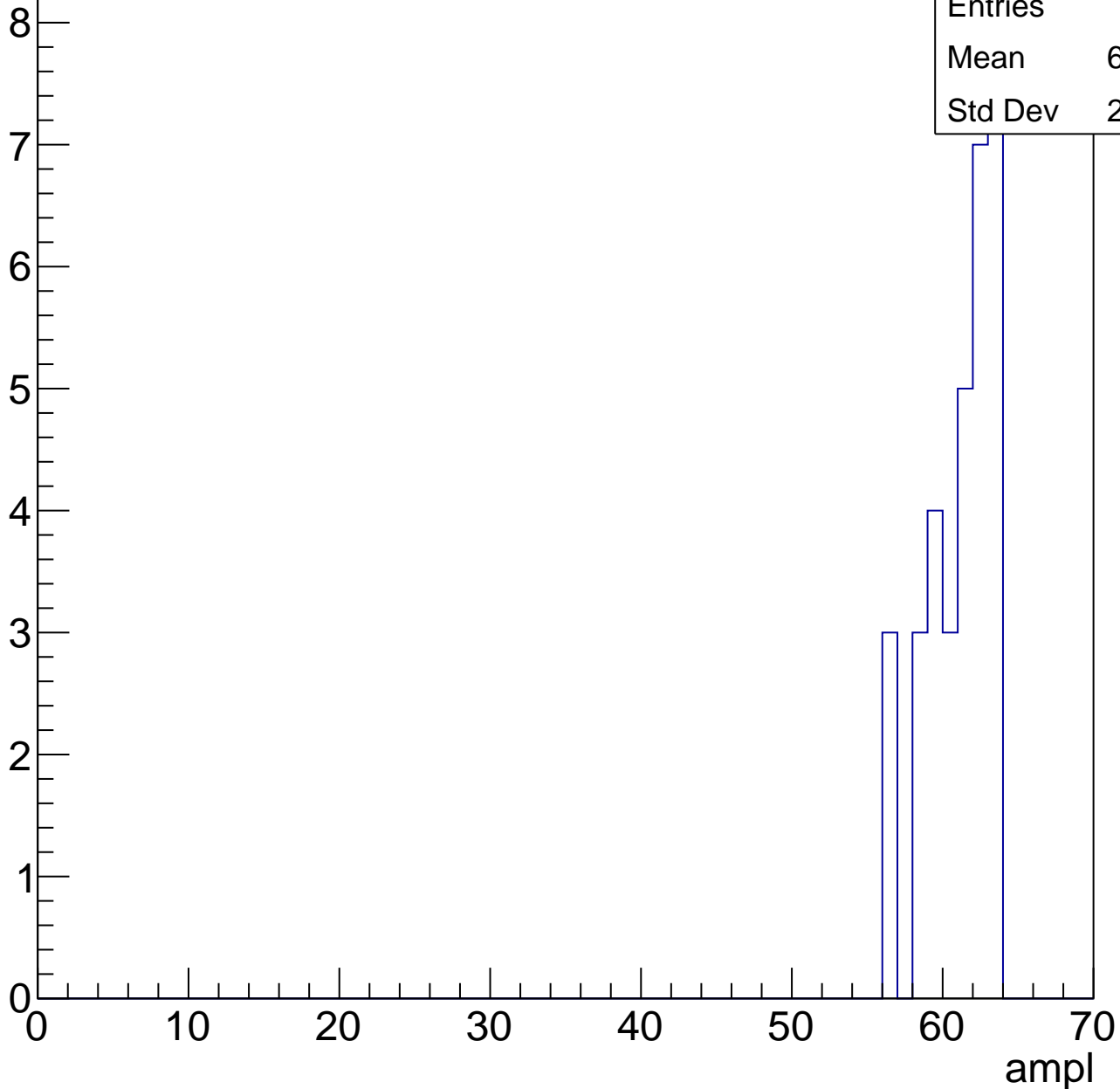


B0L001S, U19-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	60.64
Std Dev	2.172



B0L001S, U19-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	5
Mean	49.6
Std Dev	24.81

B0L001S, U19-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch110, adc0

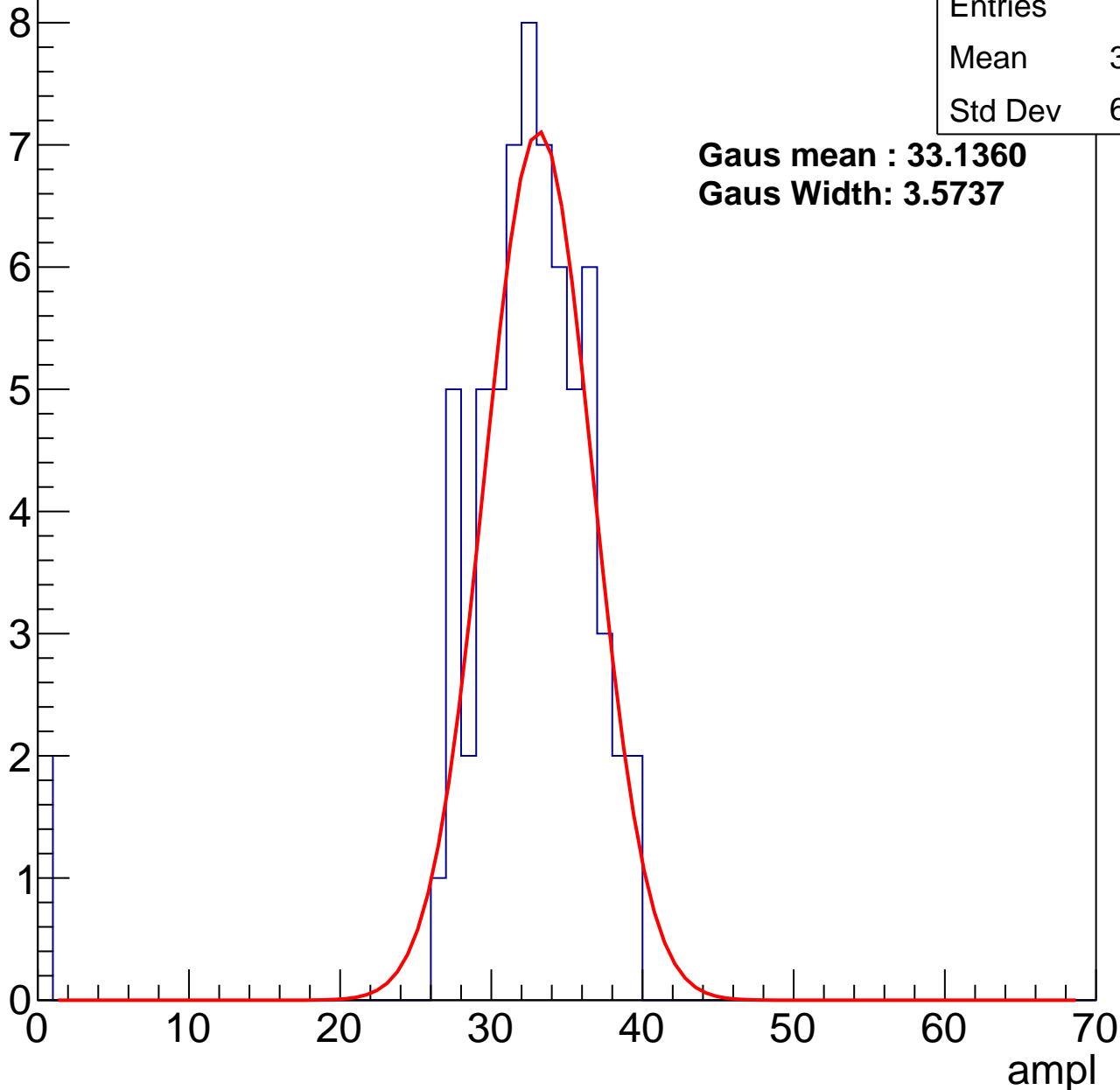
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	31.45
Std Dev	6.408

Gaus mean : 33.1360

Gaus Width: 3.5737



B0L001S, U19-ch110, adc1

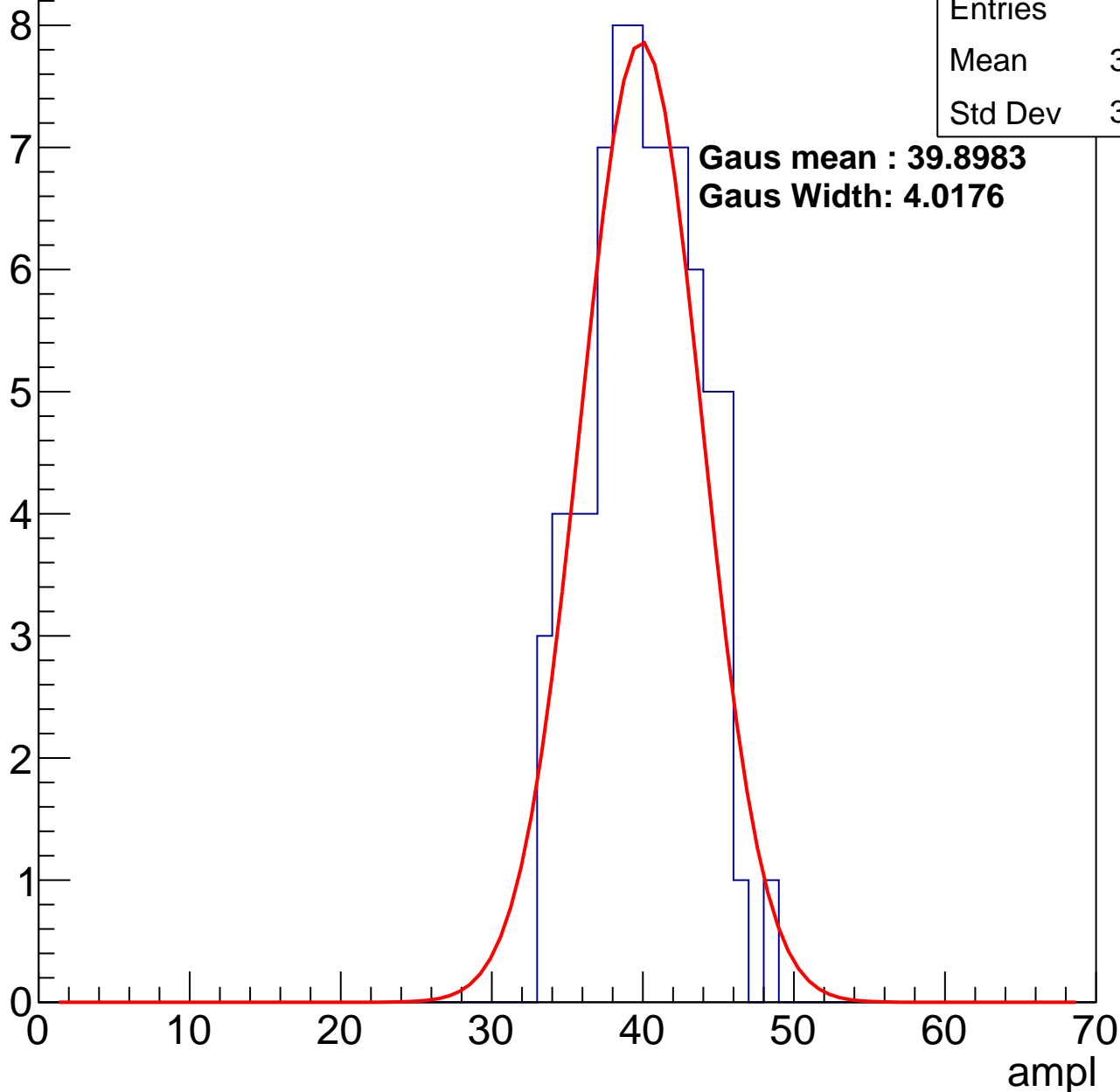
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	39.64
Std Dev	3.497

Gaus mean : 39.8983

Gaus Width: 4.0176



B0L001S, U19-ch110, adc2

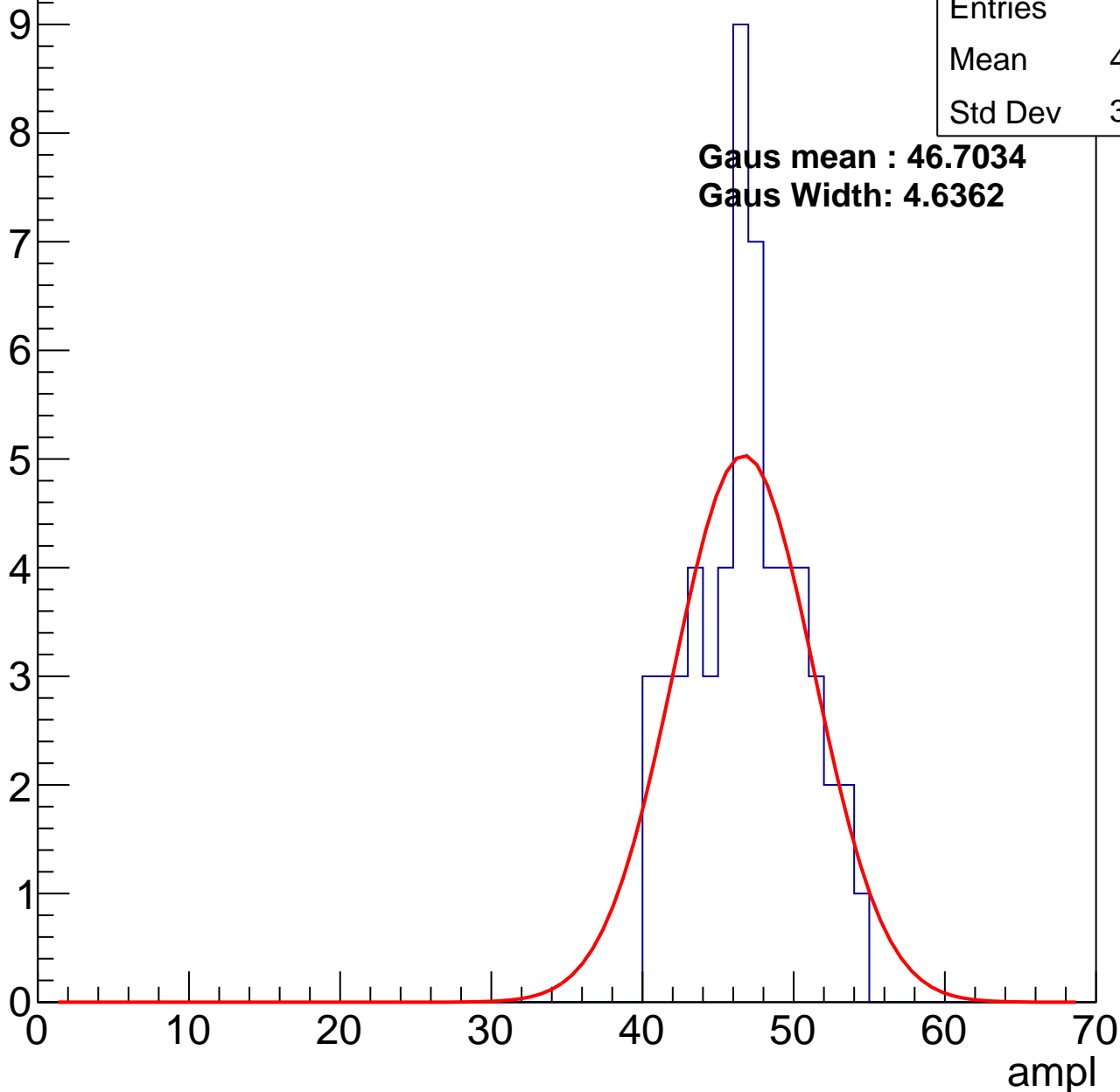
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	46.45
Std Dev	3.545

Gaus mean : 46.7034

Gaus Width: 4.6362

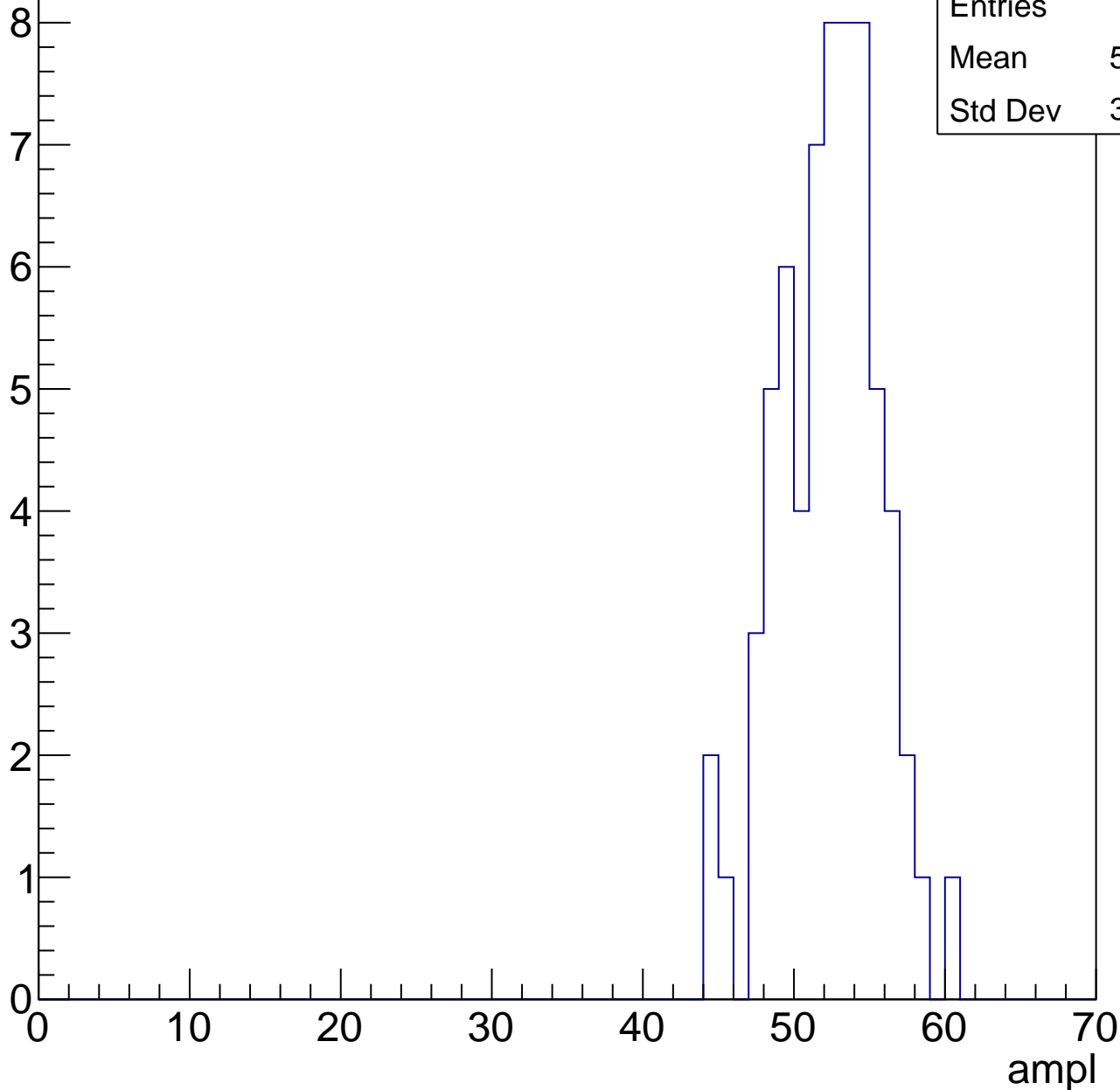


B0L001S, U19-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.82
Std Dev	3.295

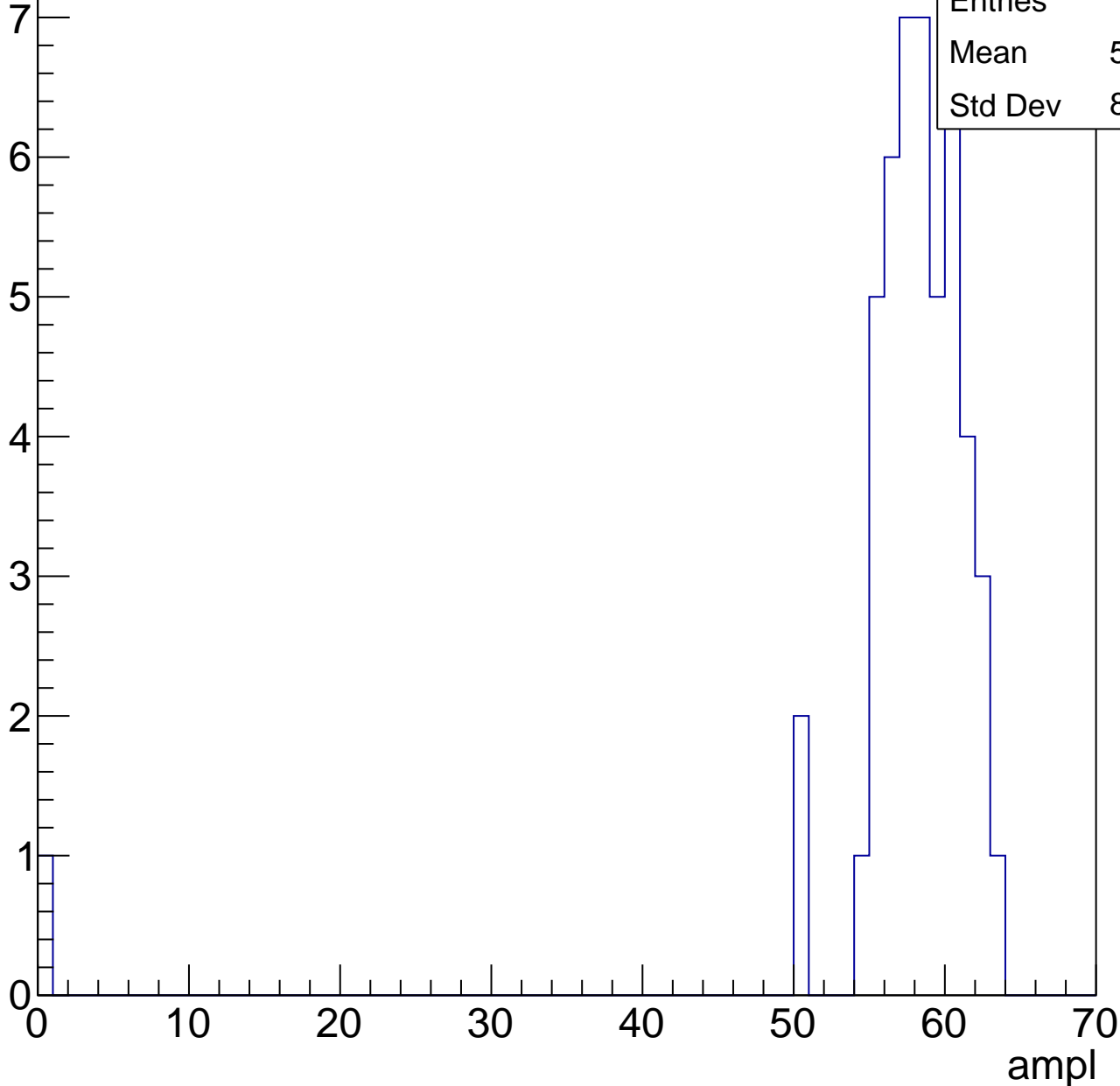


B0L001S, U19-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	56.69
Std Dev	8.622

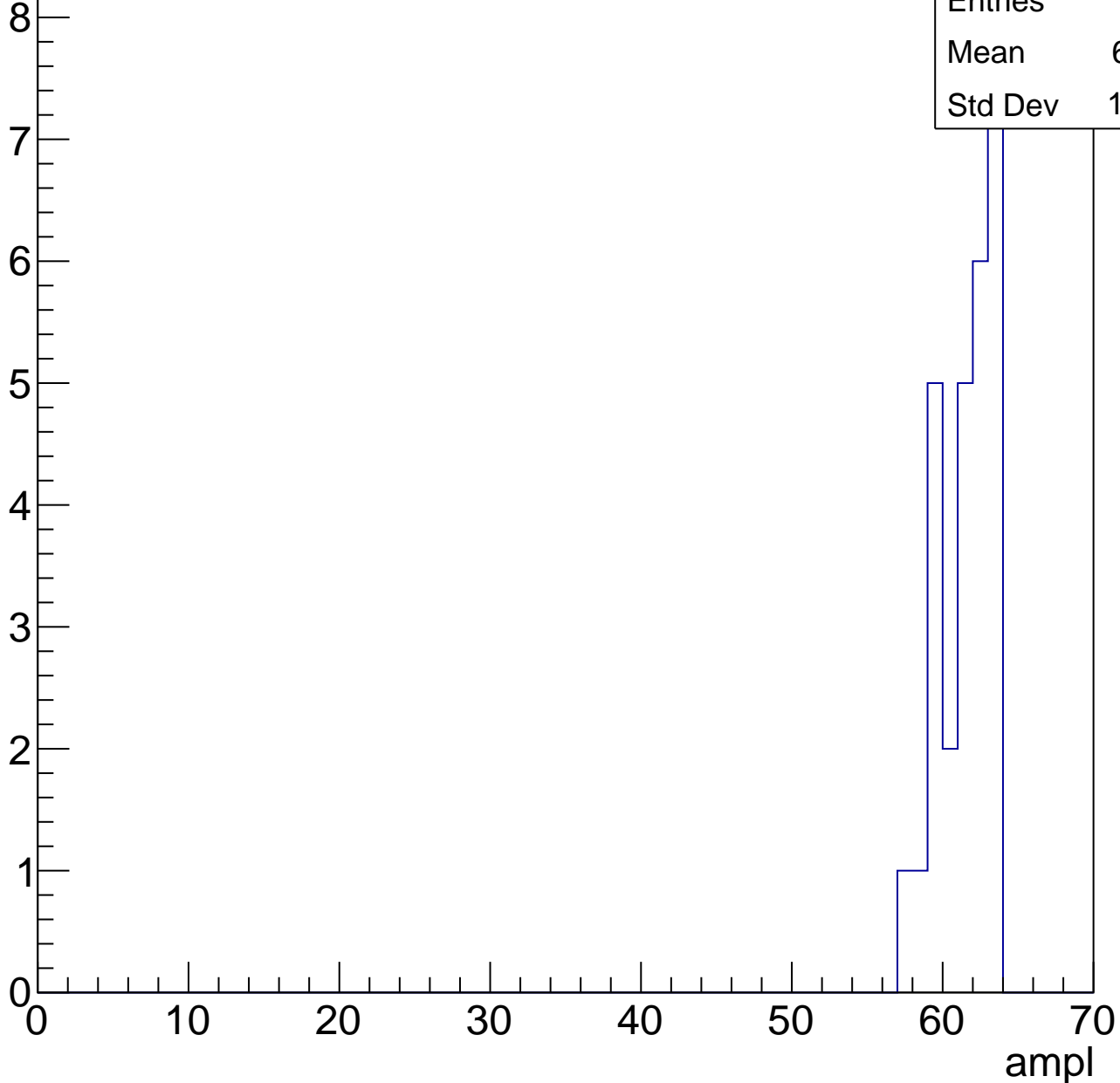


B0L001S, U19-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

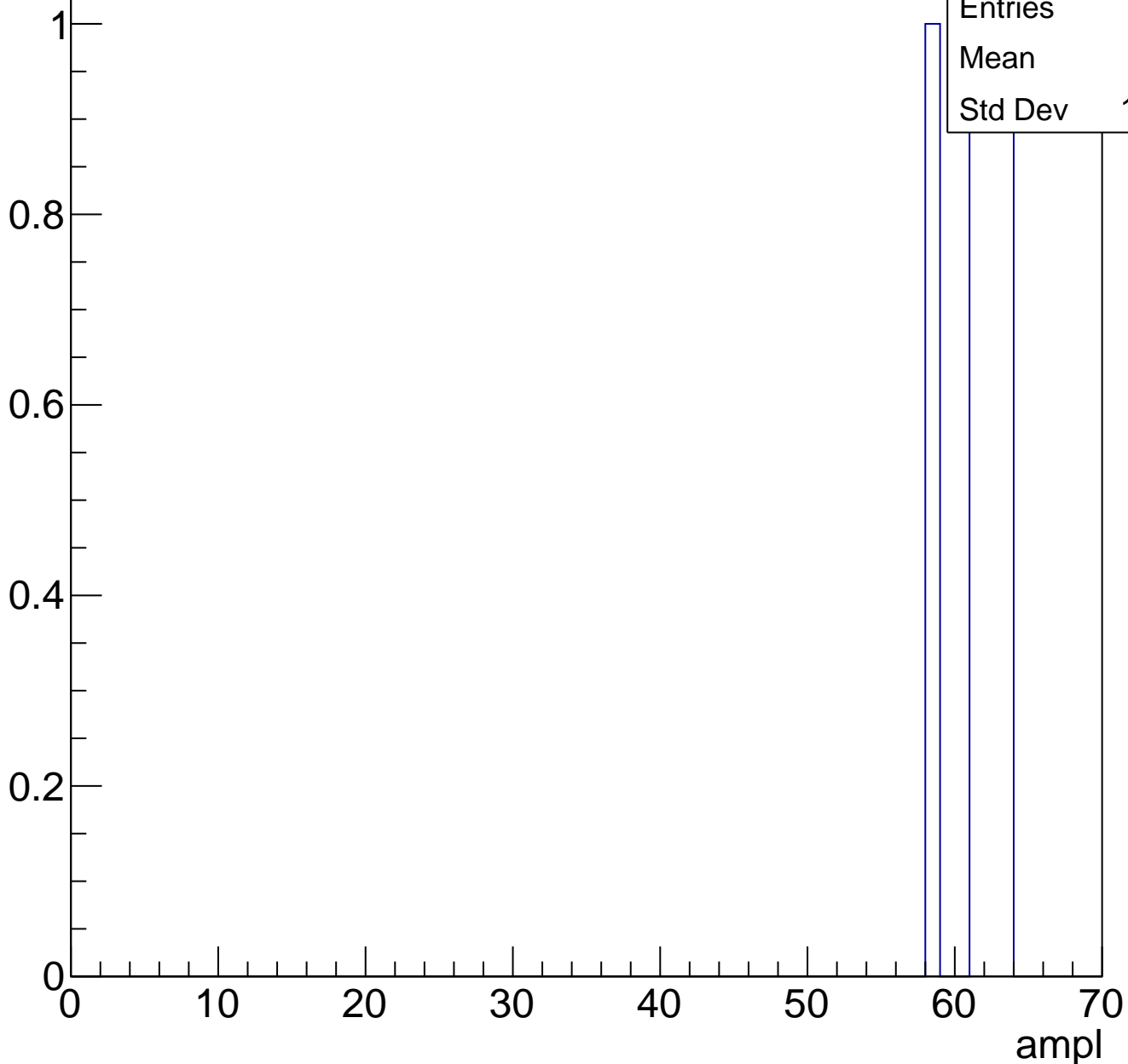
Entries	28
Mean	61.11
Std Dev	1.739



B0L001S, U19-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	61
Std Dev	1.871

B0L001S, U19-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch111, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	29.35
Std Dev	4.897

Gaus mean : 29.6408

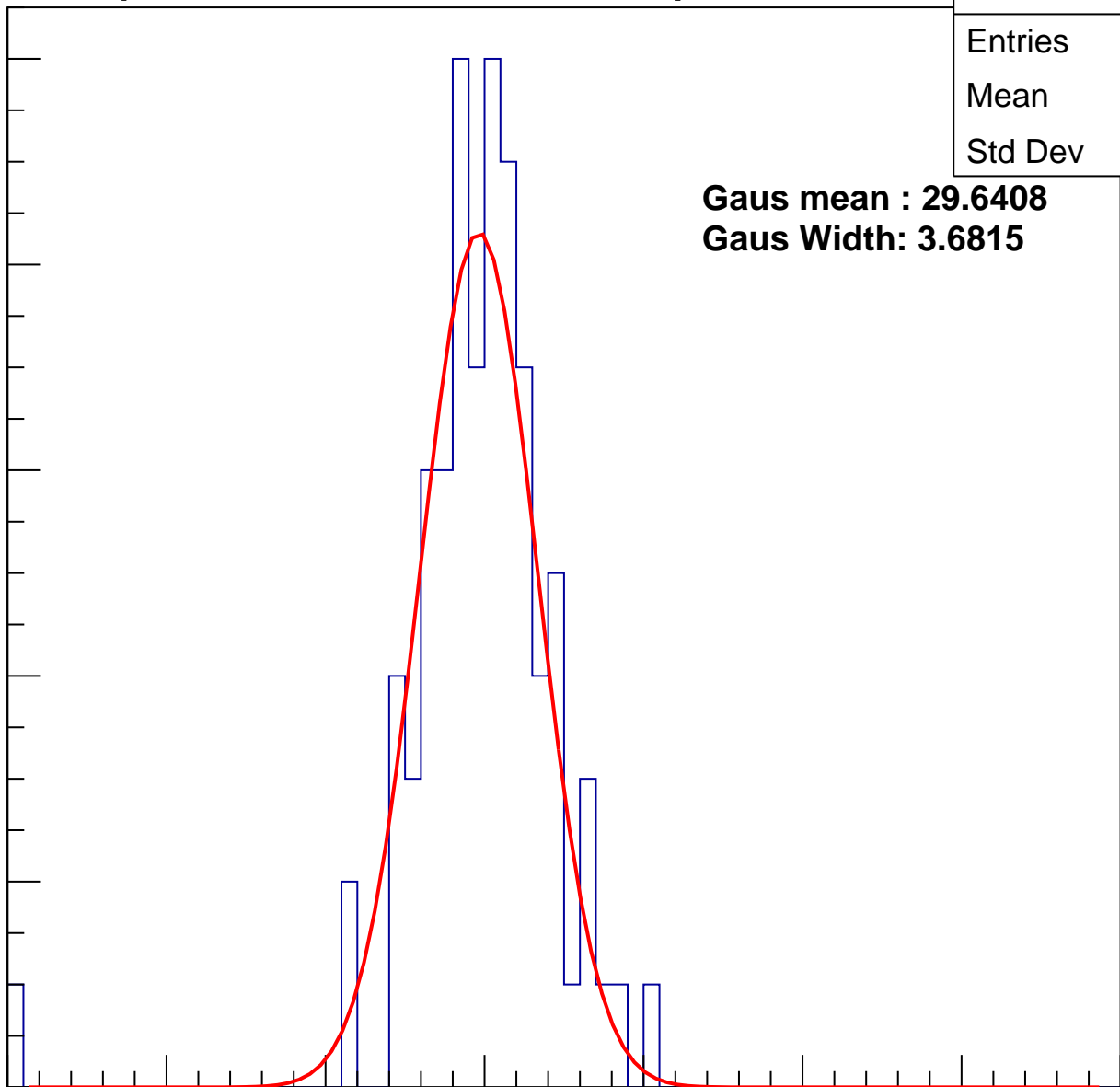
Gaus Width: 3.6815

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch111, adc1

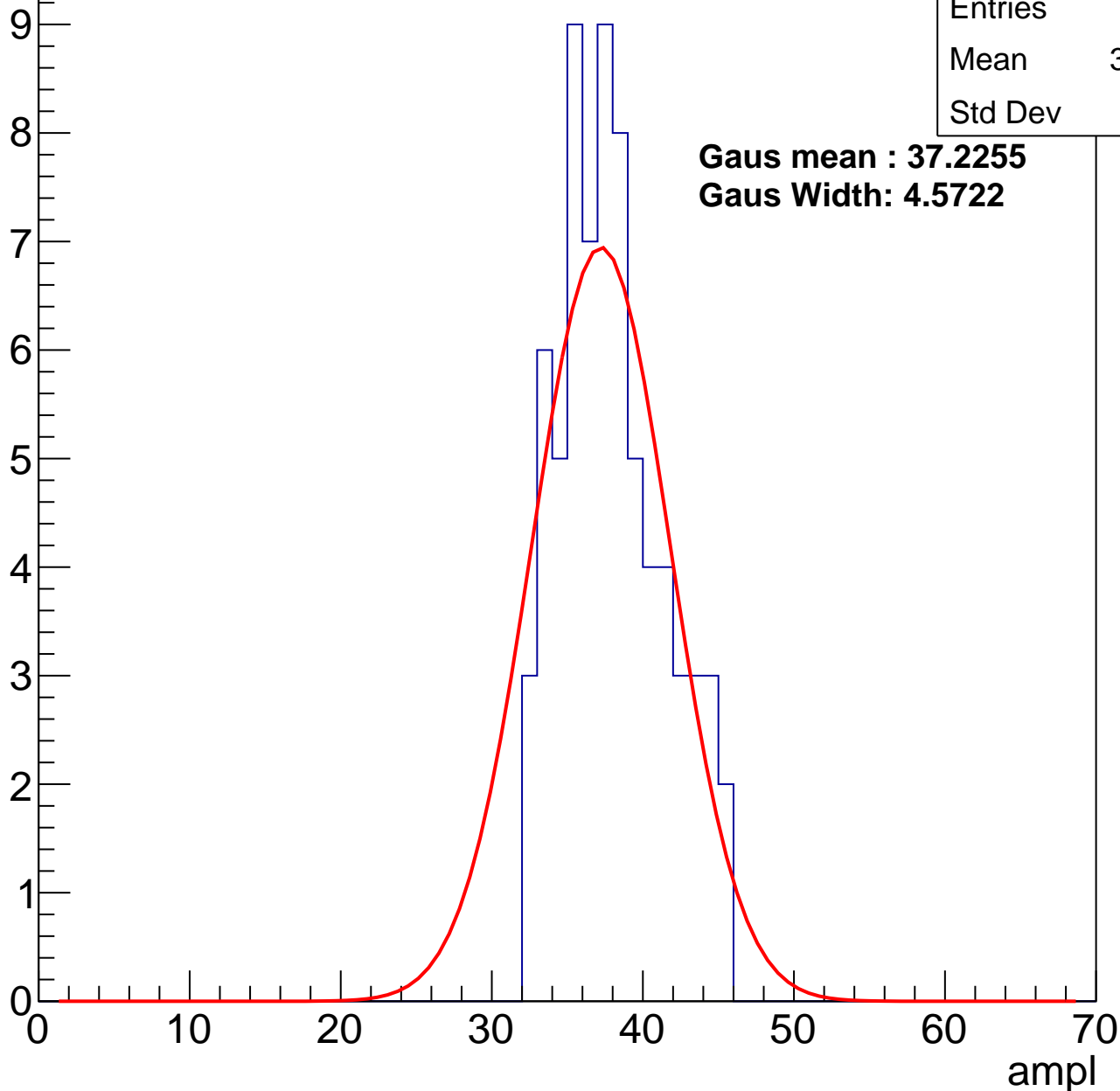
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.52
Std Dev	3.41

Gaus mean : 37.2255

Gaus Width: 4.5722



B0L001S, U19-ch111, adc2

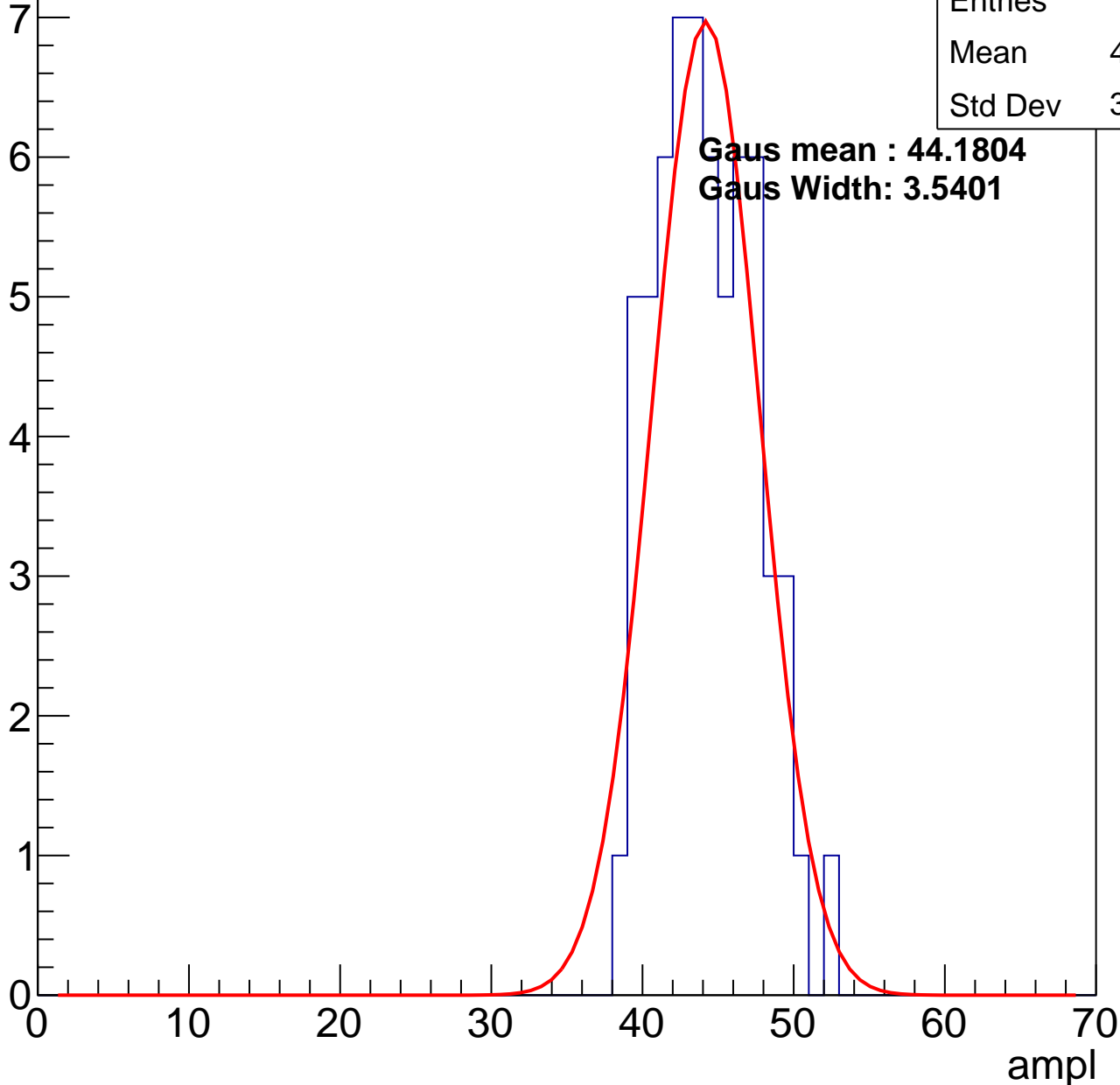
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.77
Std Dev	3.195

Gaus mean : 44.1804

Gaus Width: 3.5401

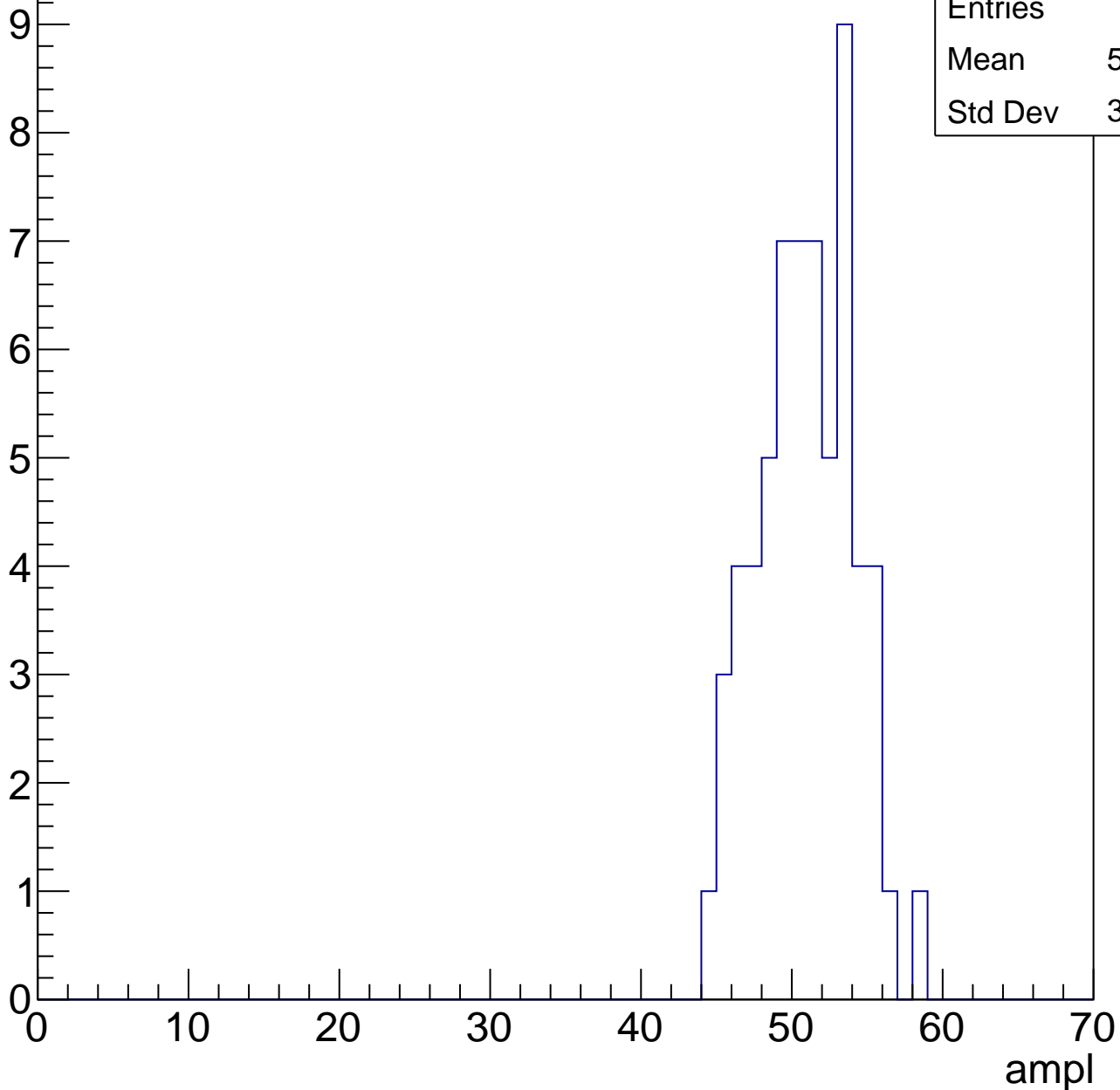


B0L001S, U19-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.45
Std Dev	3.104

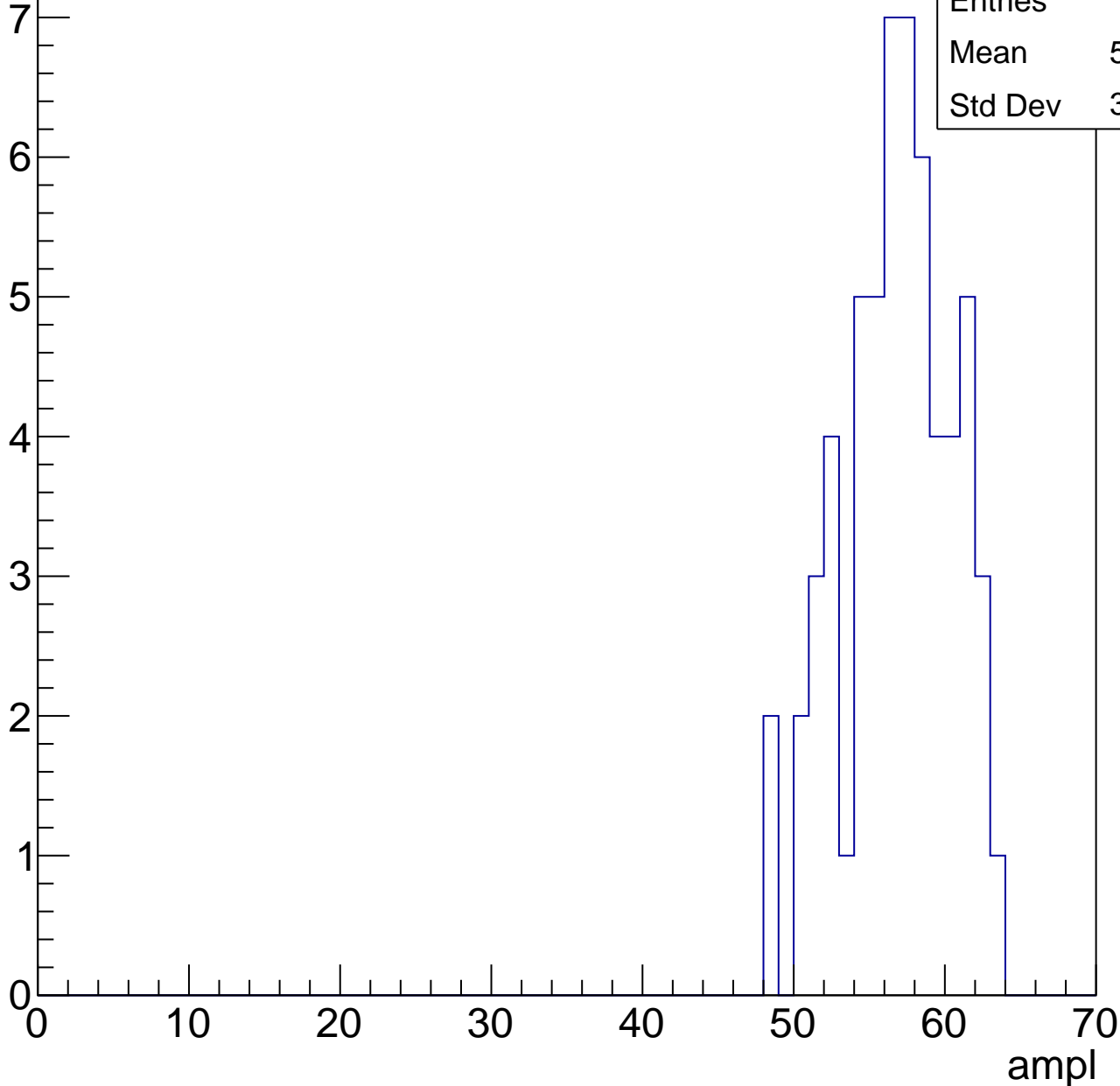


B0L001S, U19-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.34
Std Dev	3.634

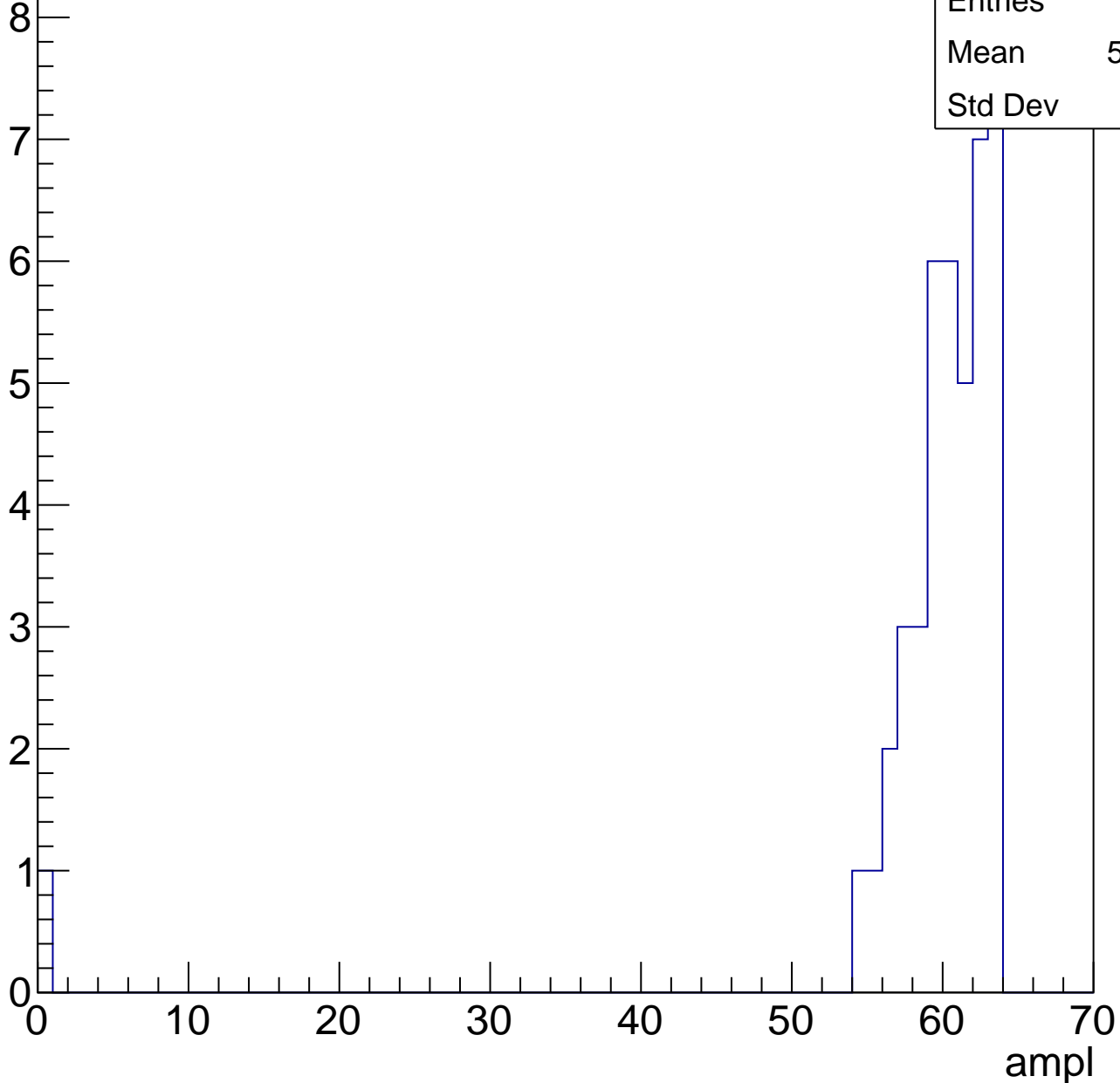


B0L001S, U19-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.67
Std Dev	9.36



B0L001S, U19-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch112, adc0

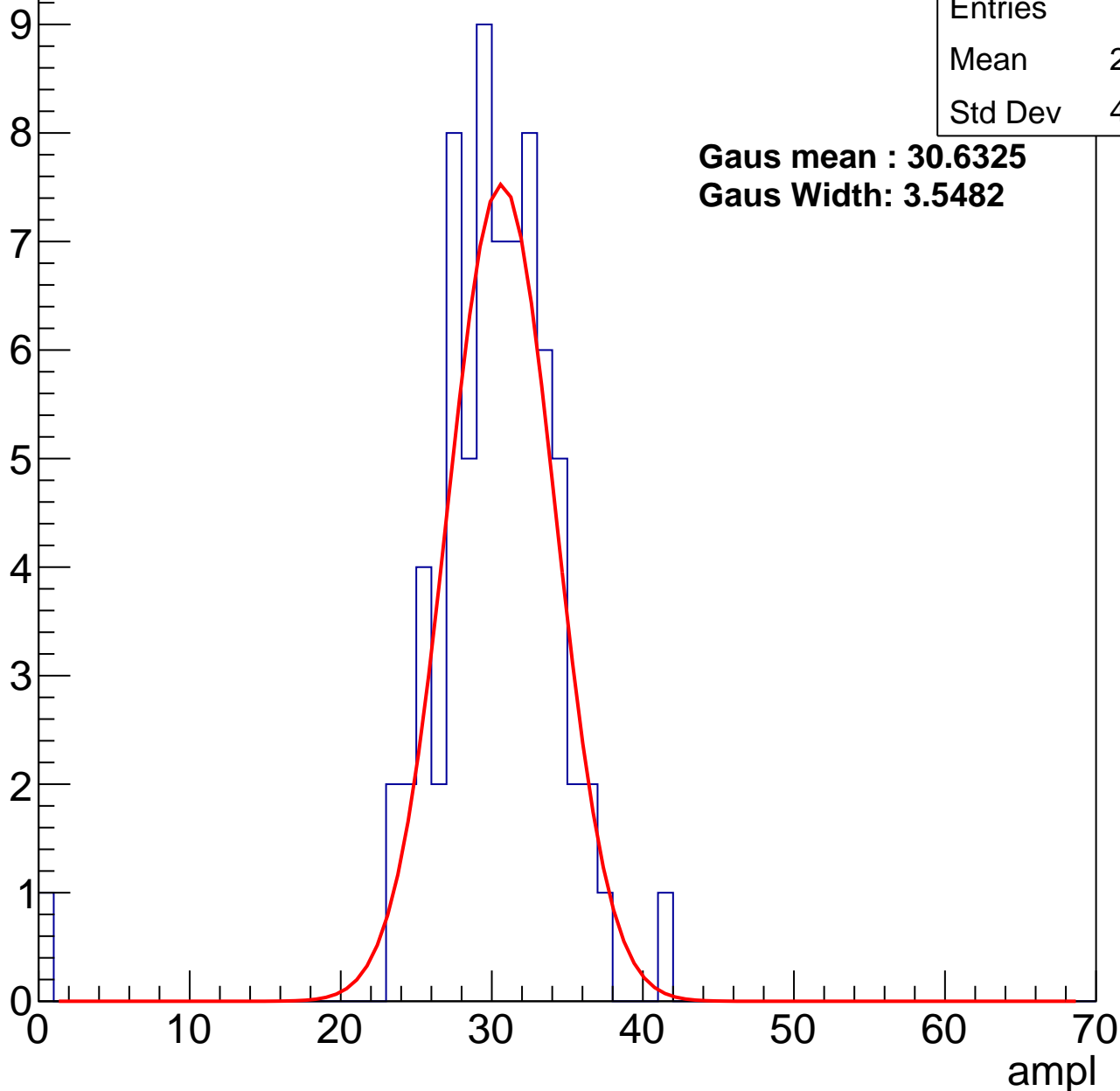
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.64
Std Dev	4.942

Gaus mean : 30.6325

Gaus Width: 3.5482



B0L001S, U19-ch112, adc1

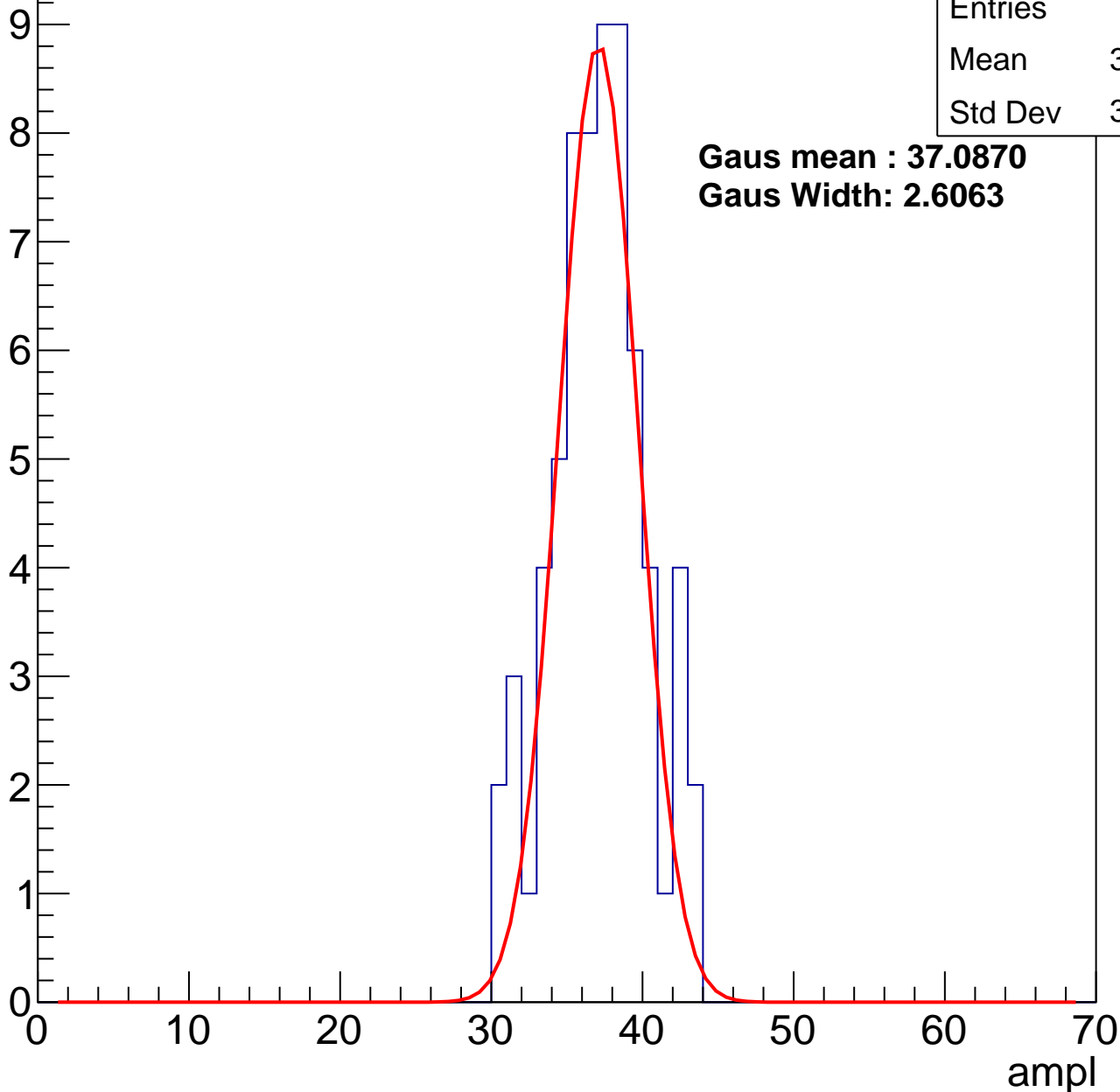
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	36.65
Std Dev	3.087

Gaus mean : 37.0870

Gaus Width: 2.6063



B0L001S, U19-ch112, adc2

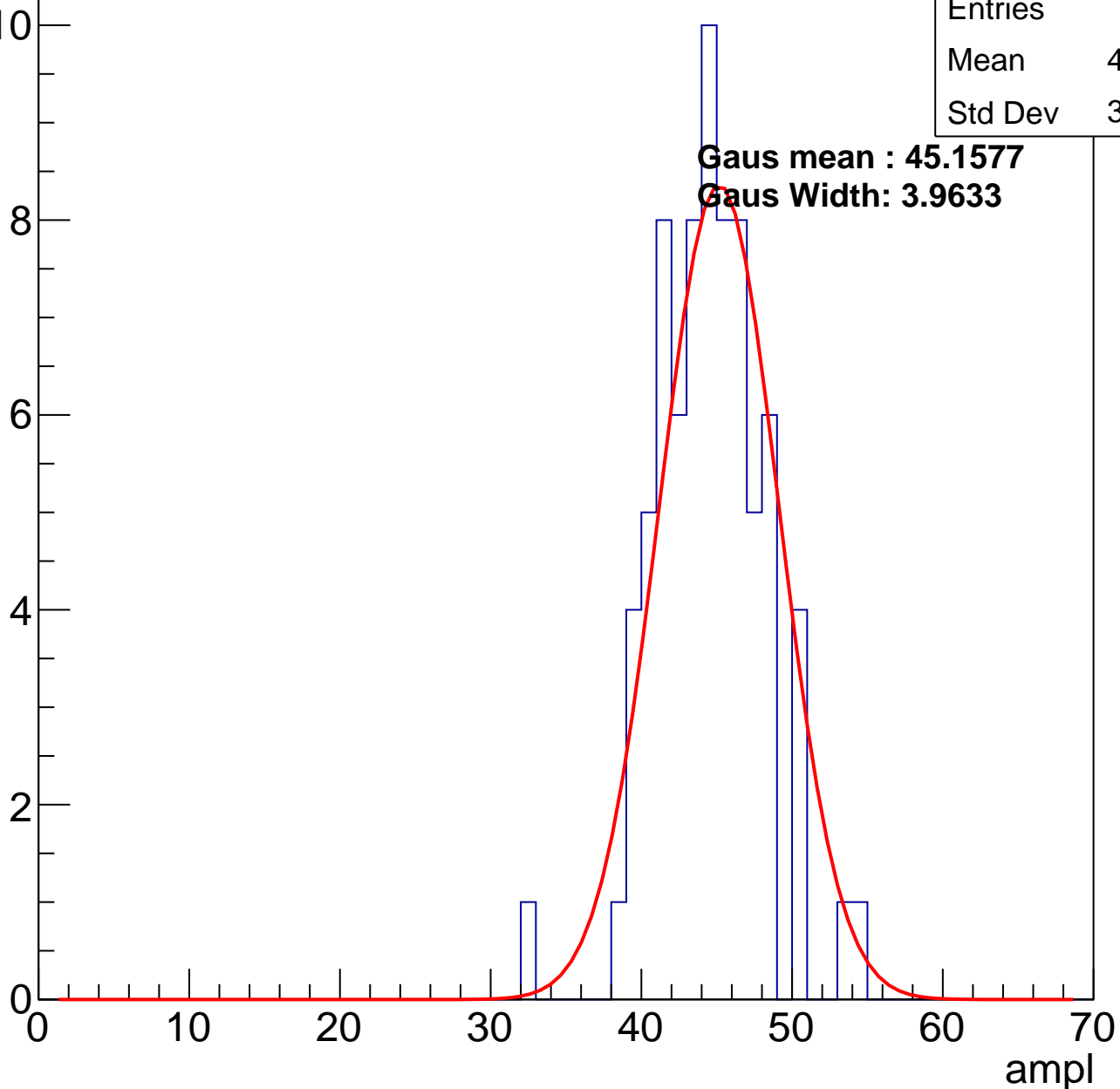
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	44.05
Std Dev	3.572

Gaus mean : 45.1577

Gaus Width: 3.9633

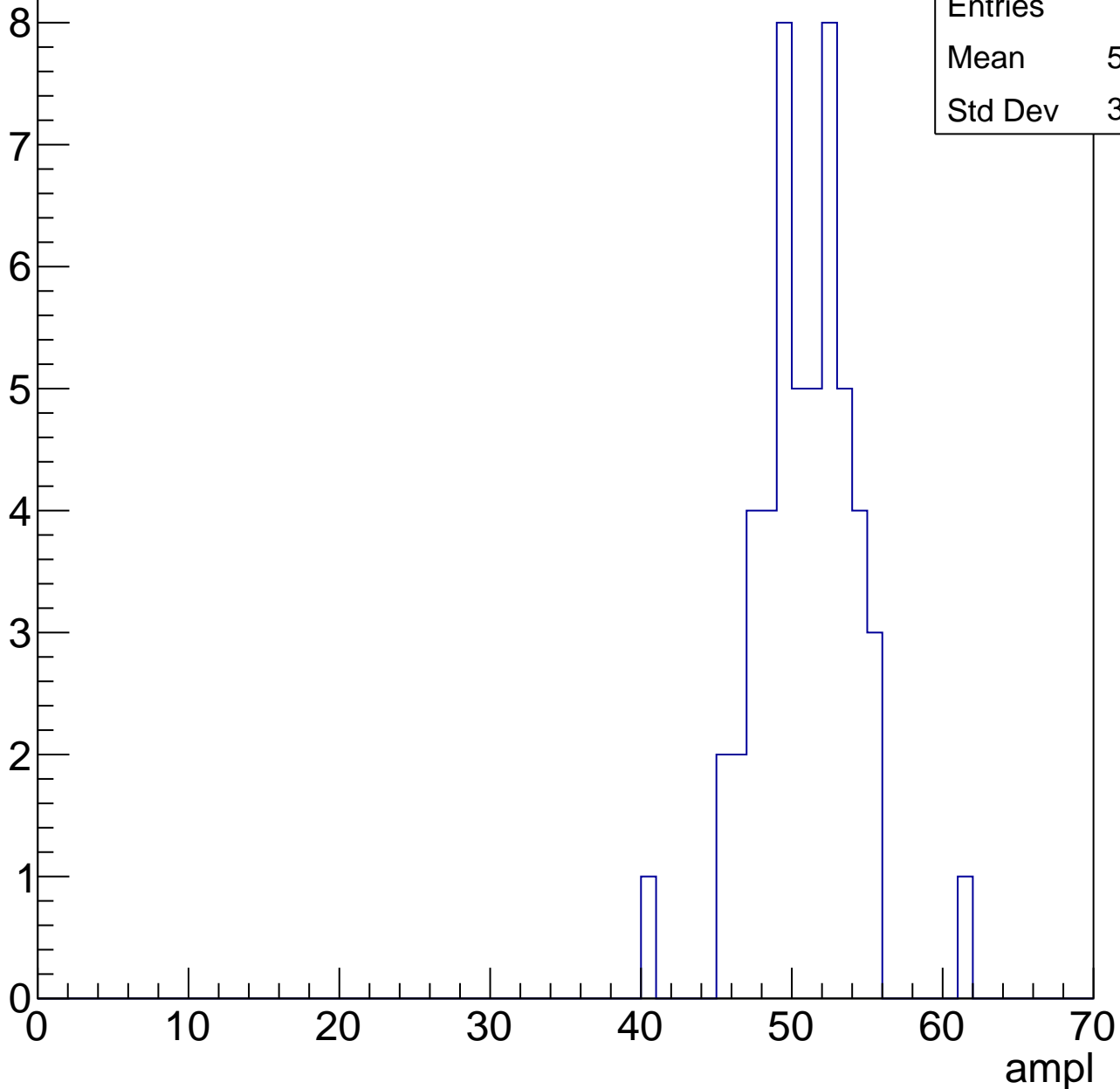


B0L001S, U19-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	50.42
Std Dev	3.324

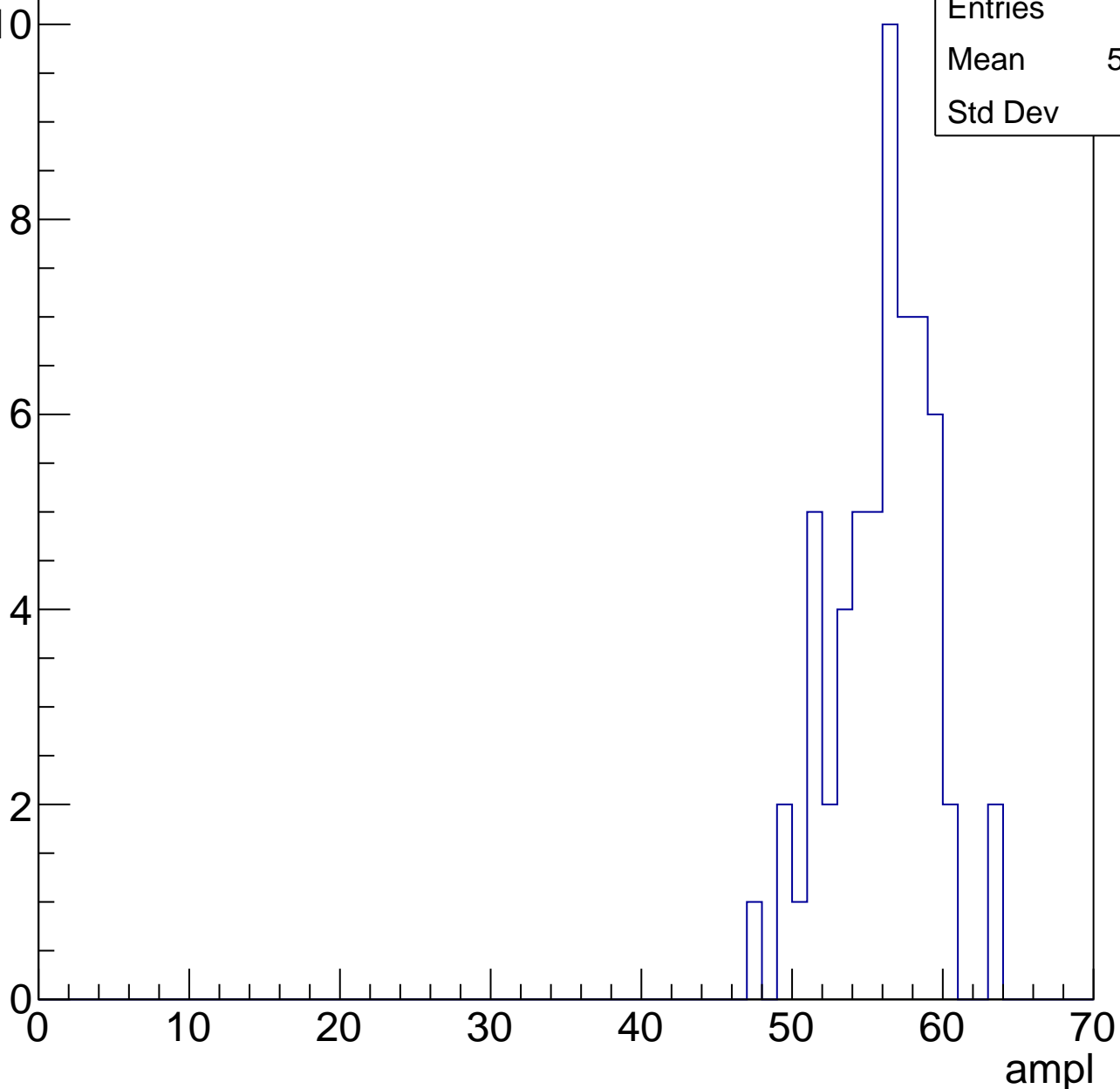


B0L001S, U19-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.53
Std Dev	3.29

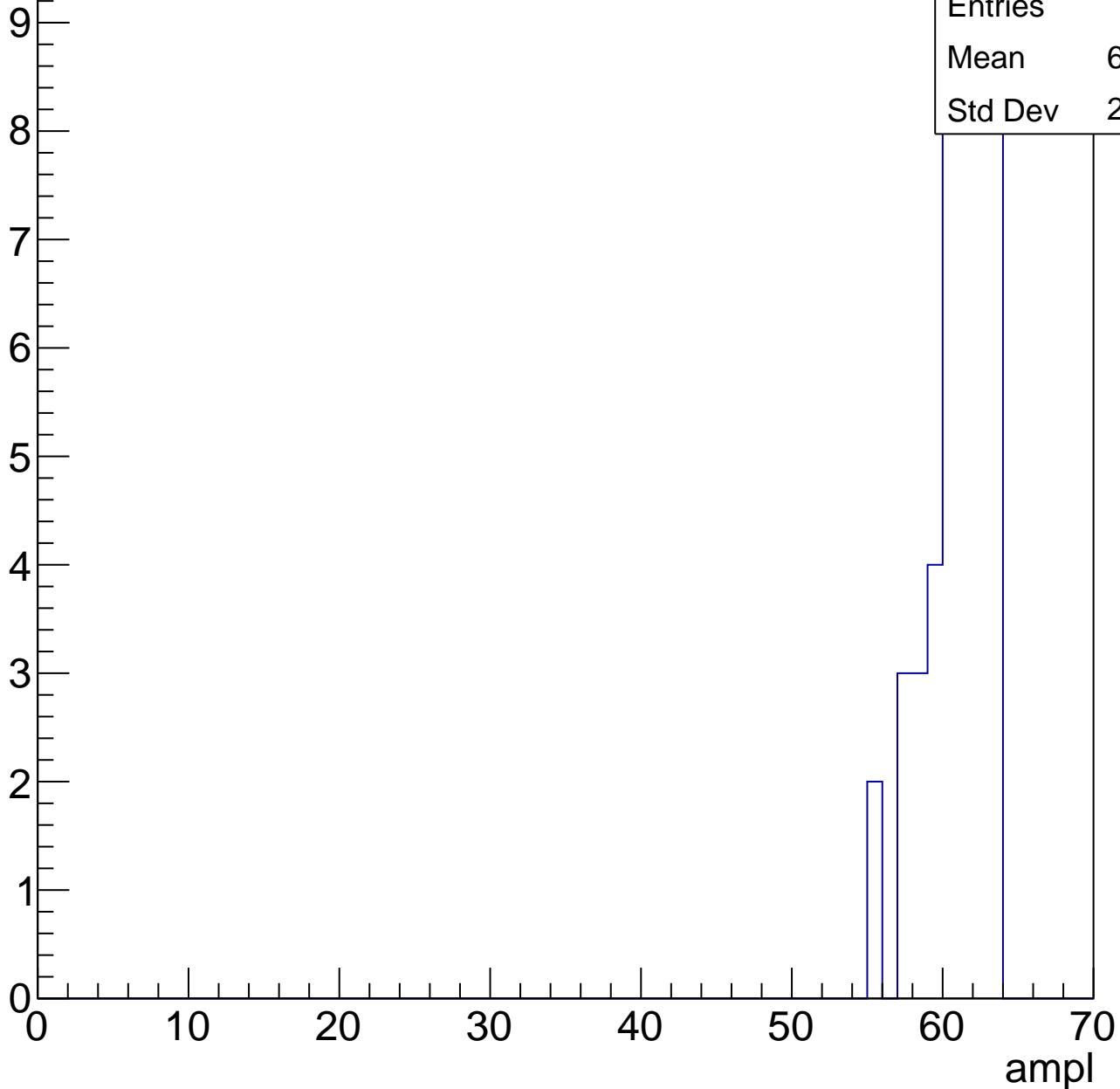


B0L001S, U19-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	60.44
Std Dev	2.093



B0L001S, U19-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

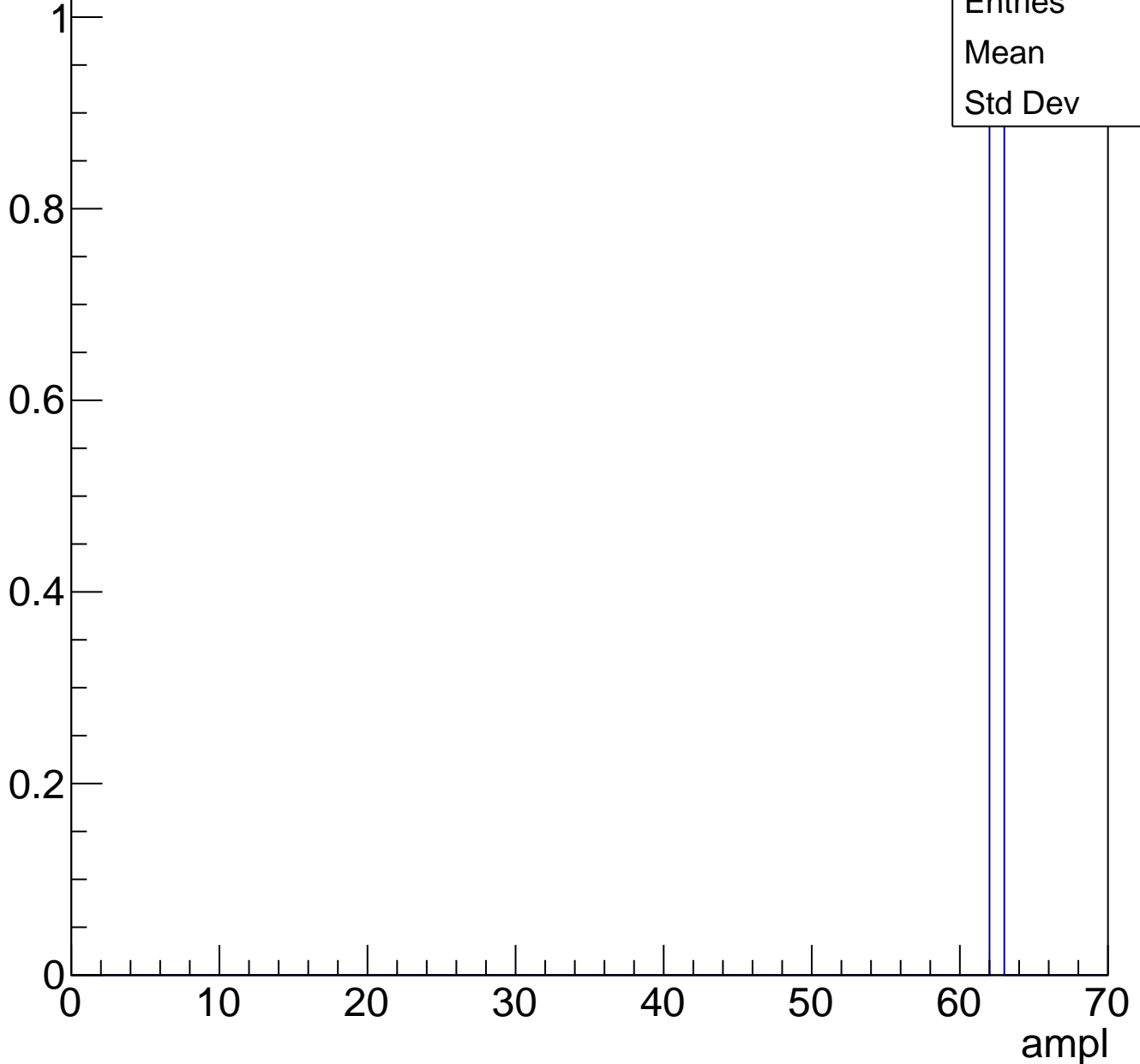
Entry



B0L001S, U19-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch113, adc0

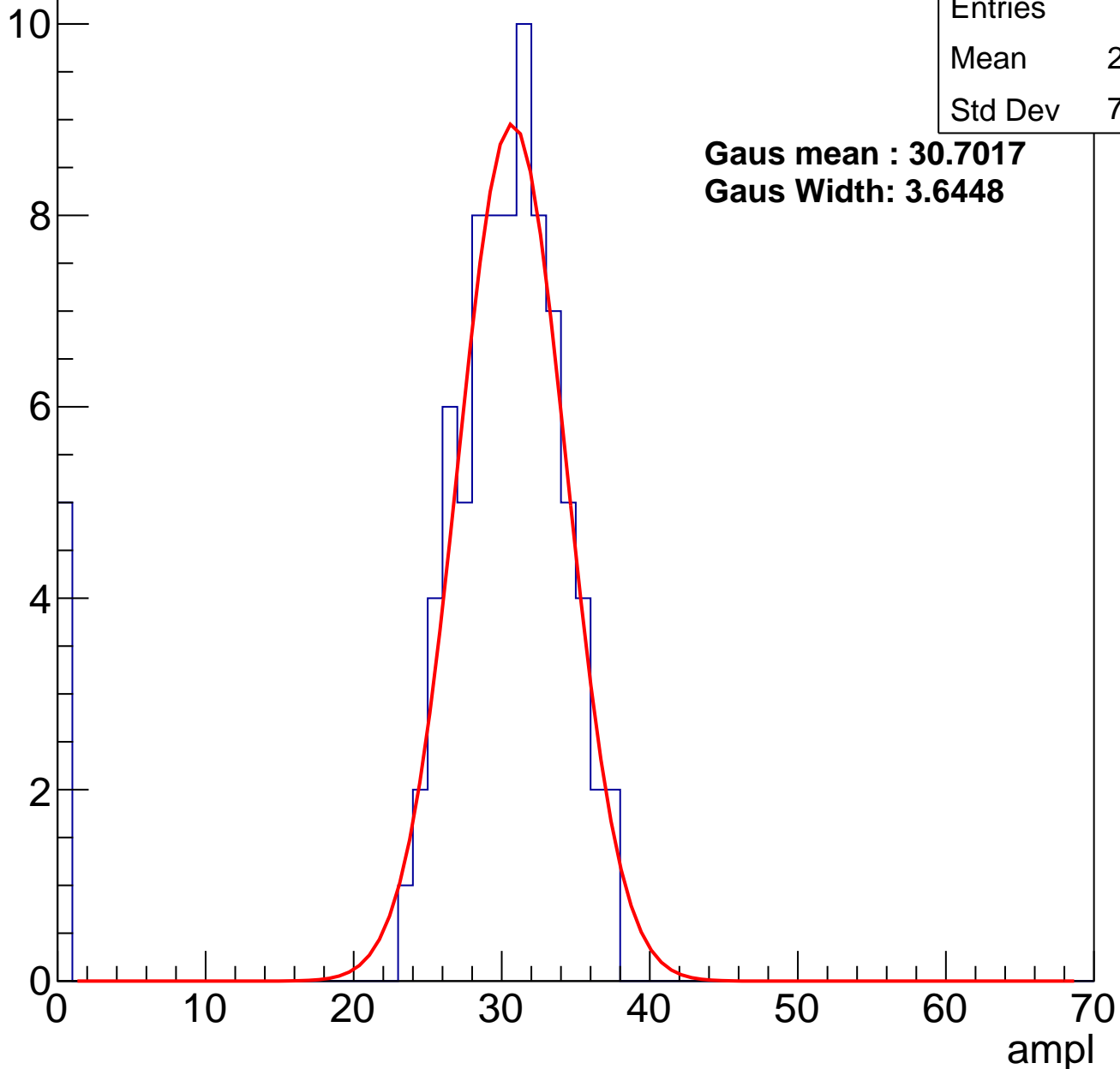
calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	28.36
Std Dev	7.768

Gaus mean : 30.7017

Gaus Width: 3.6448

Entry



B0L001S, U19-ch113, adc1

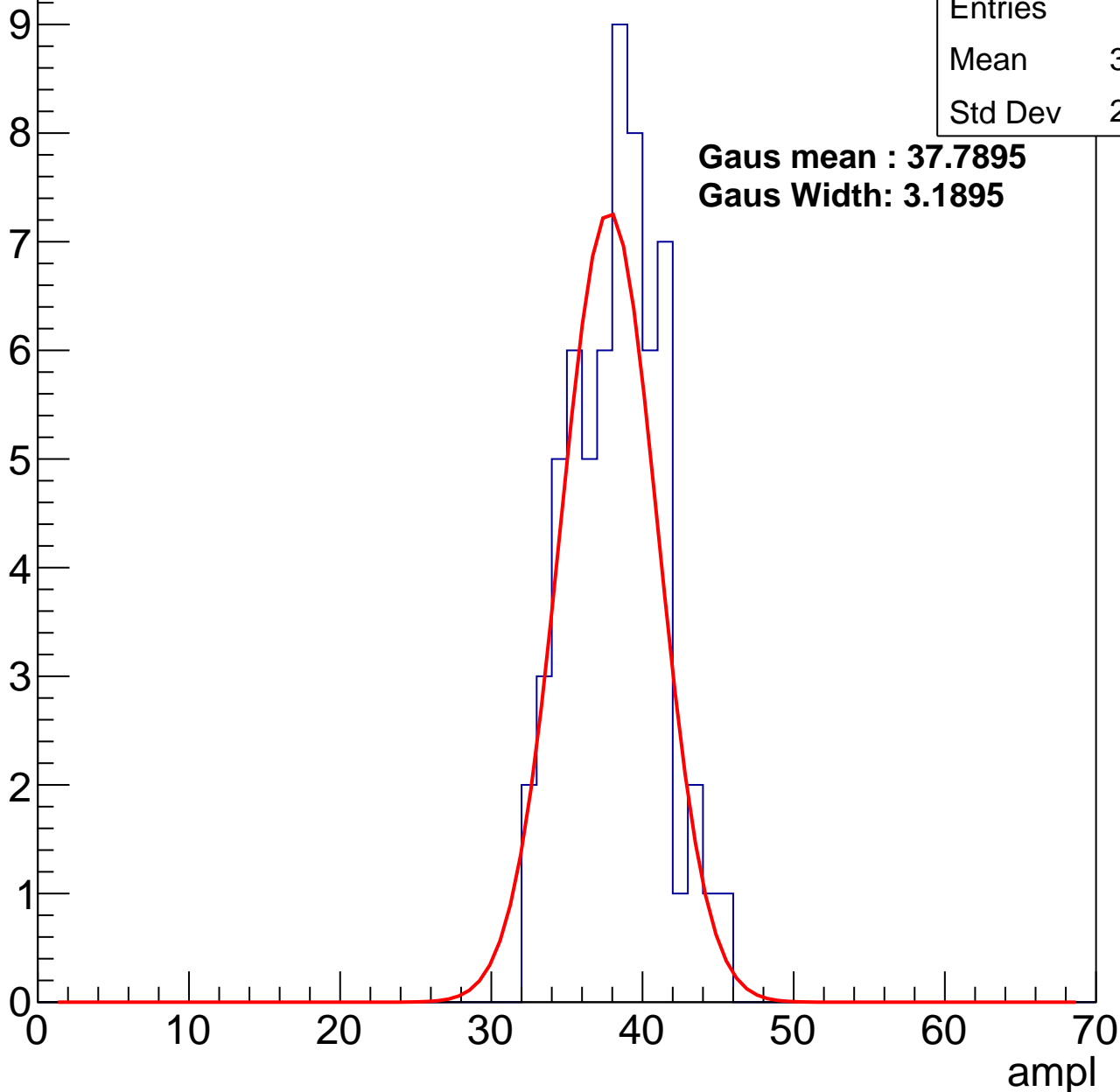
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.79
Std Dev	2.979

Gaus mean : 37.7895

Gaus Width: 3.1895



B0L001S, U19-ch113, adc2

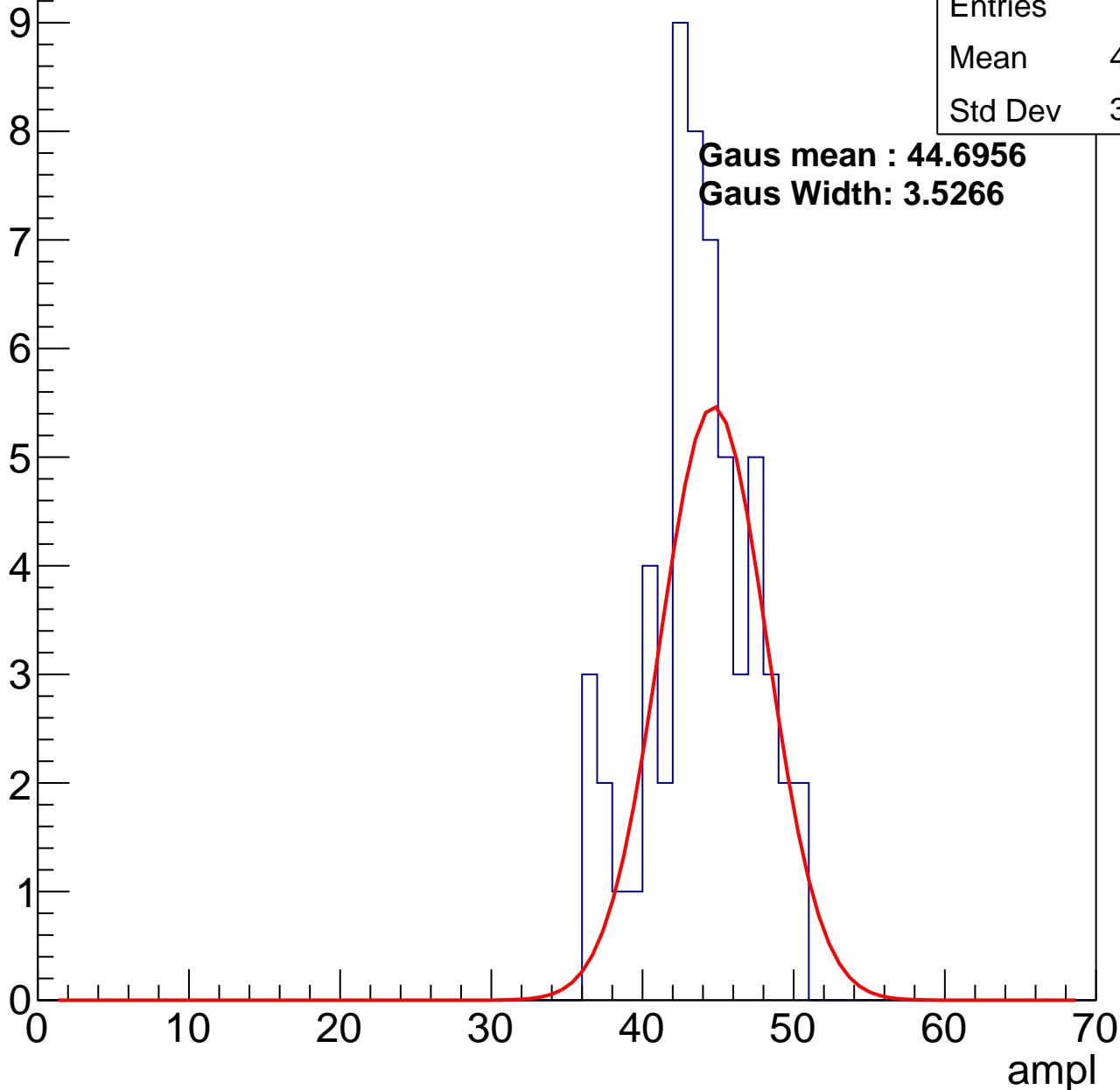
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.35
Std Dev	3.467

Gaus mean : 44.6956

Gaus Width: 3.5266

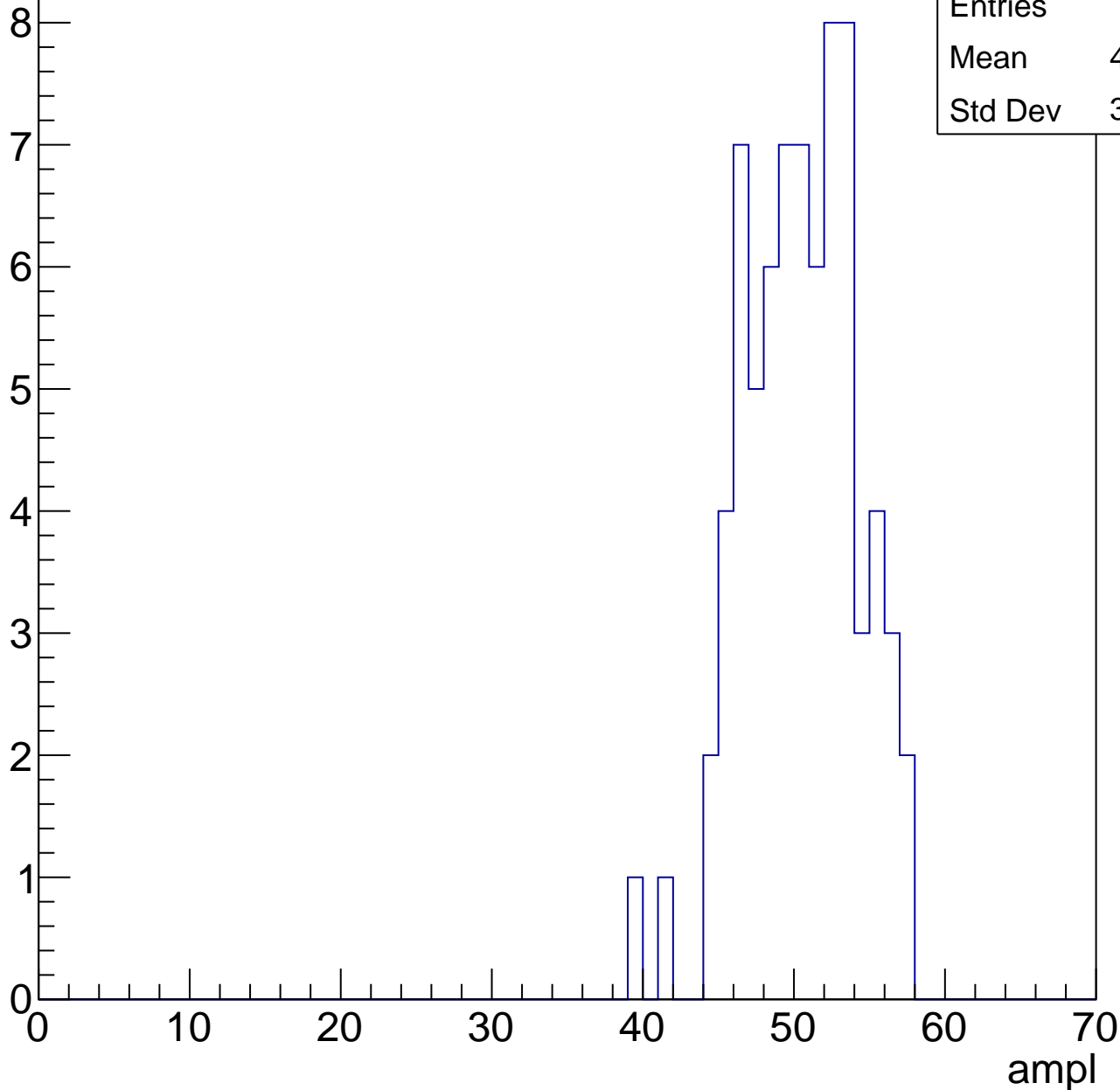


B0L001S, U19-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	49.95
Std Dev	3.723



B0L001S, U19-ch113, adc4

calib_packv5_042523_0143.root, FC#9, port A1

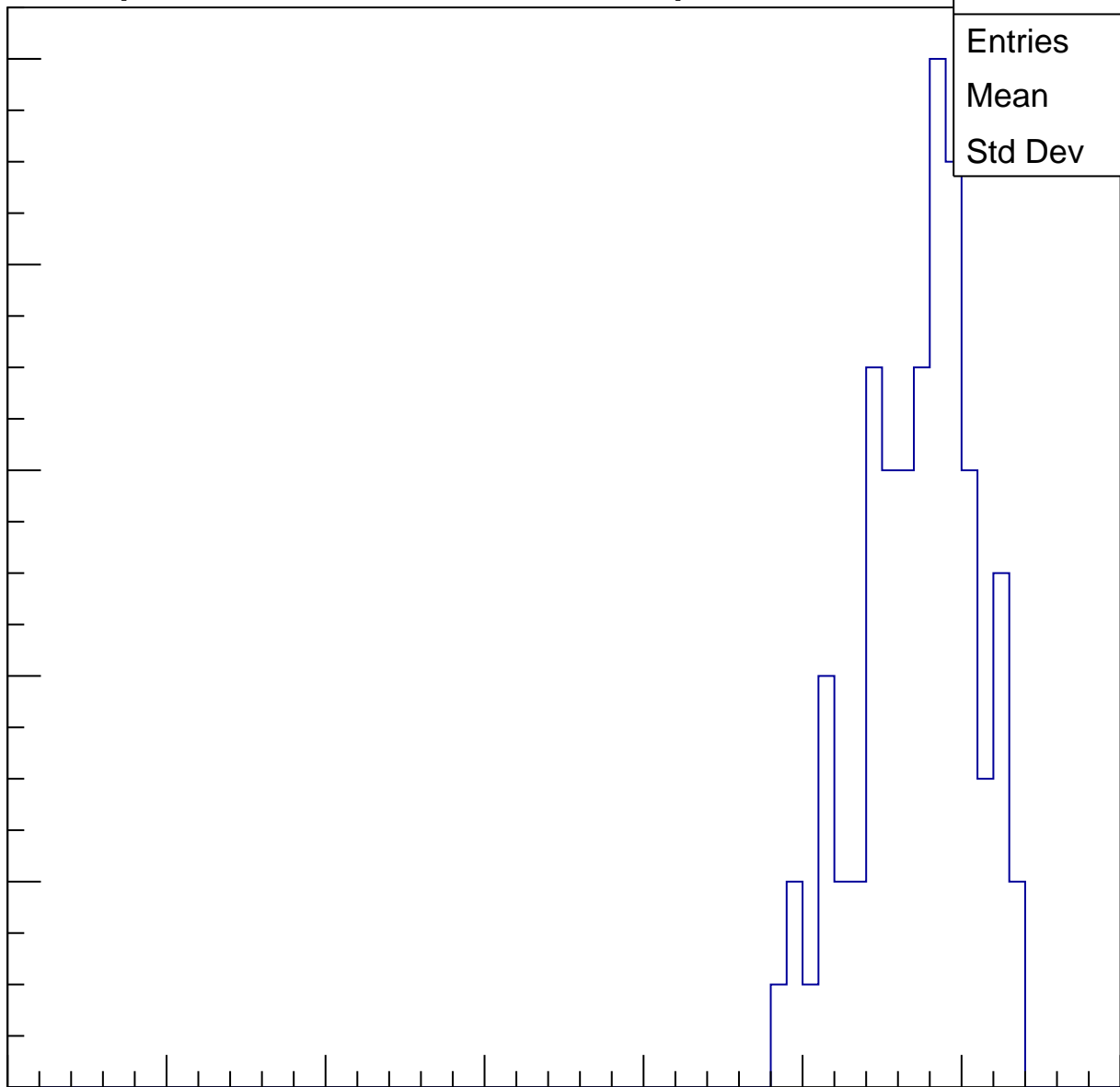
Entries	73
Mean	56.75
Std Dev	3.545

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

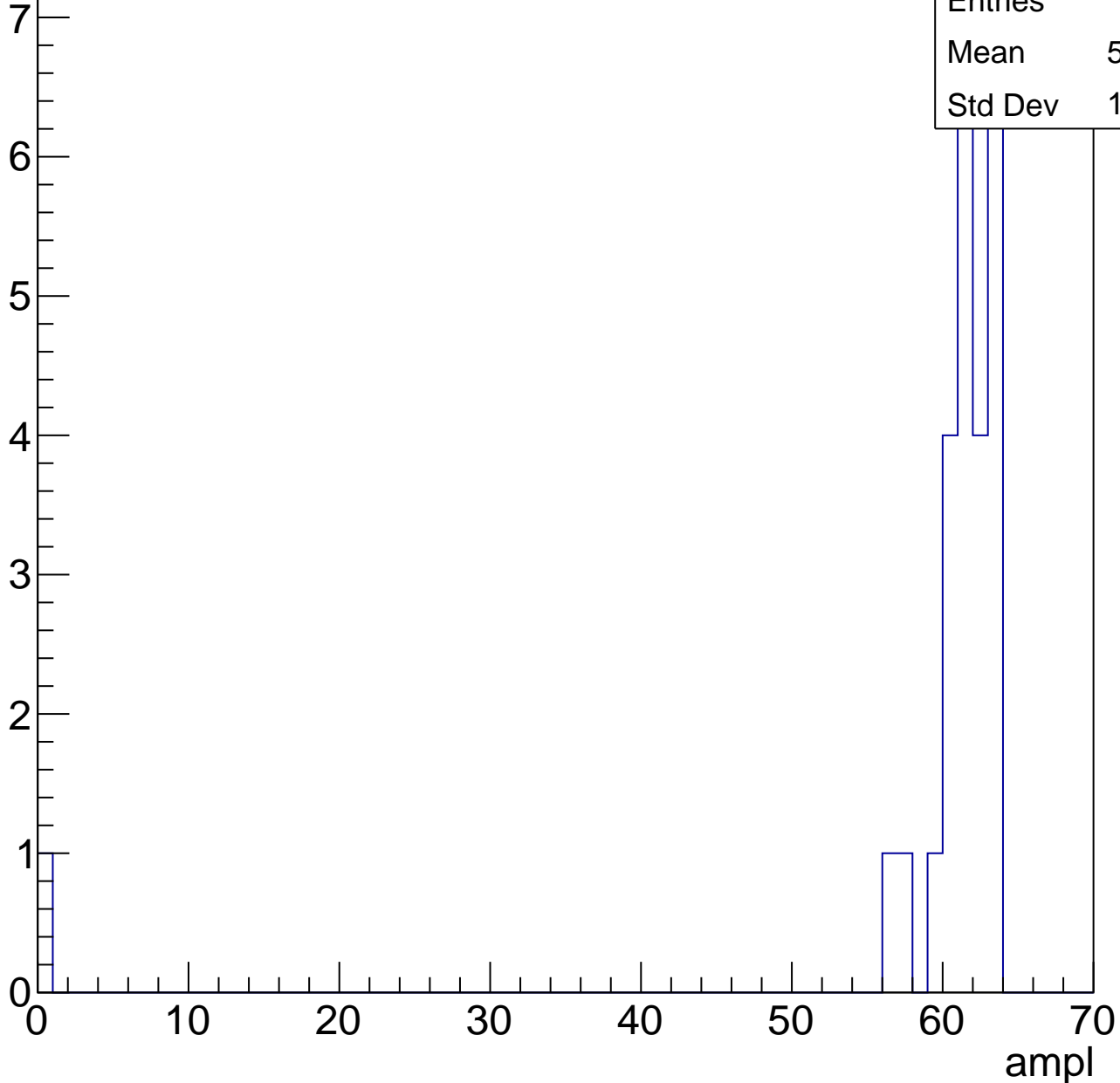


B0L001S, U19-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

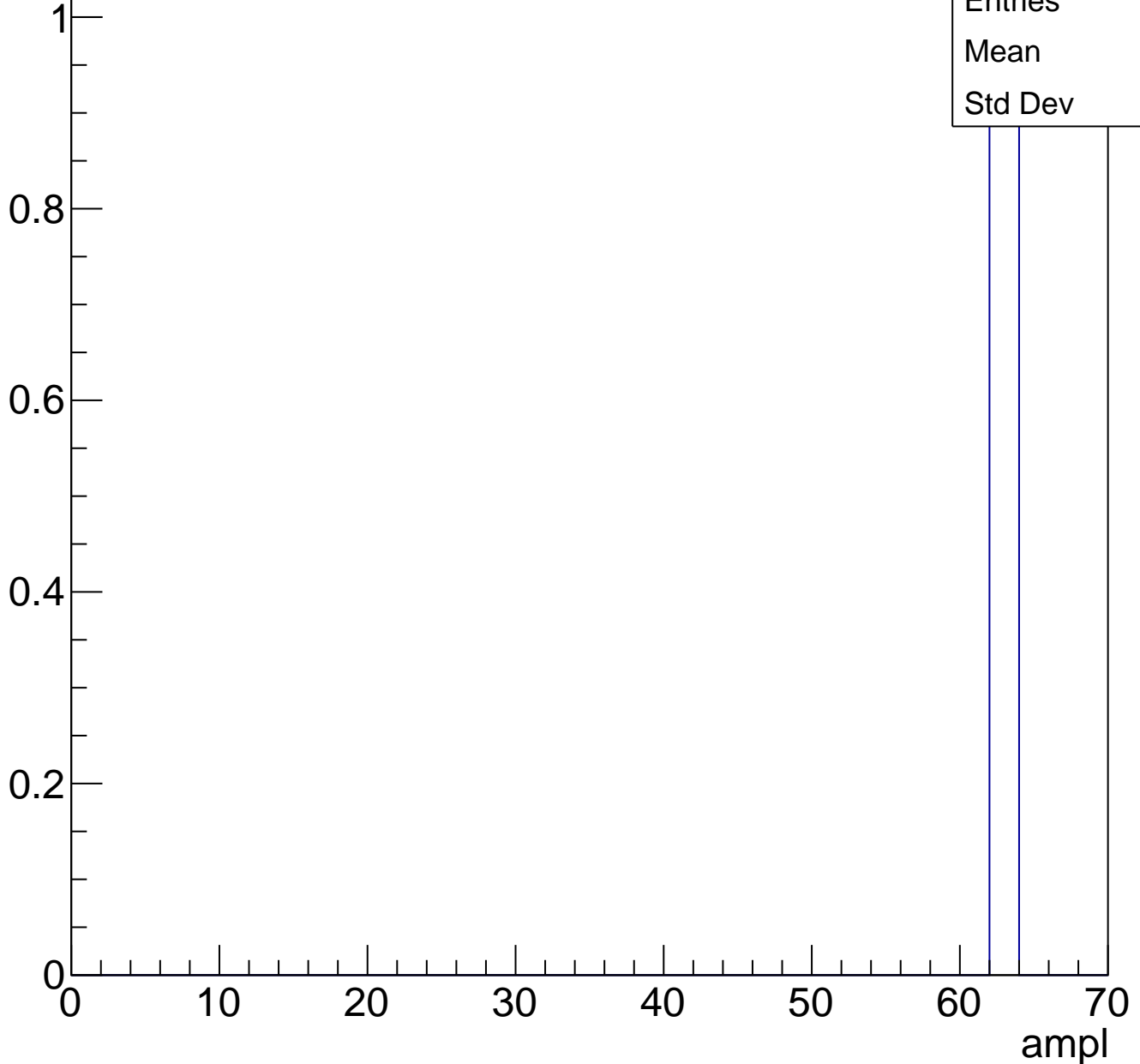
Entries	26
Mean	58.77
Std Dev	11.89



B0L001S, U19-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch114, adc0

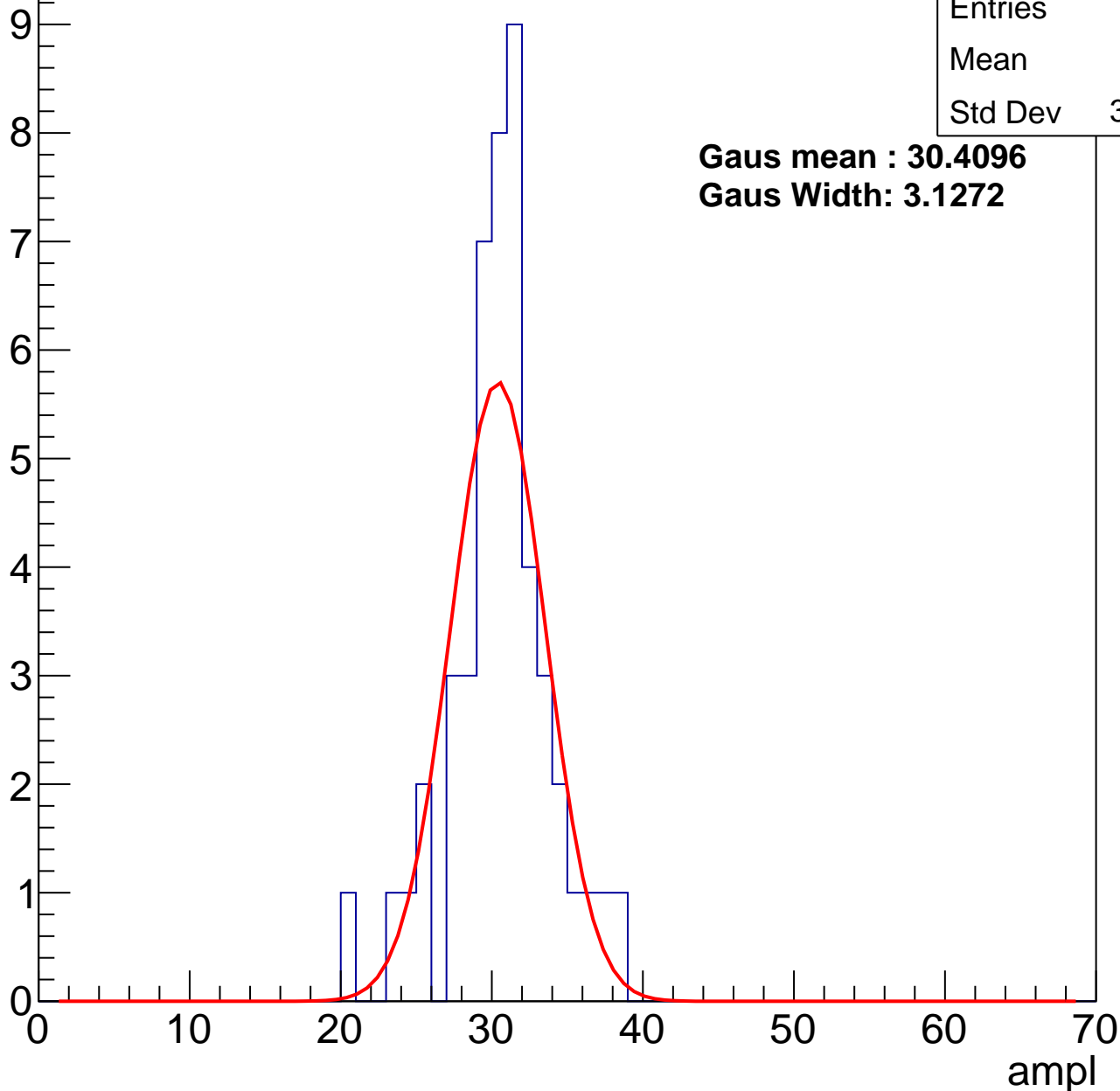
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	30.1
Std Dev	3.349

Gaus mean : 30.4096

Gaus Width: 3.1272



B0L001S, U19-ch114, adc1

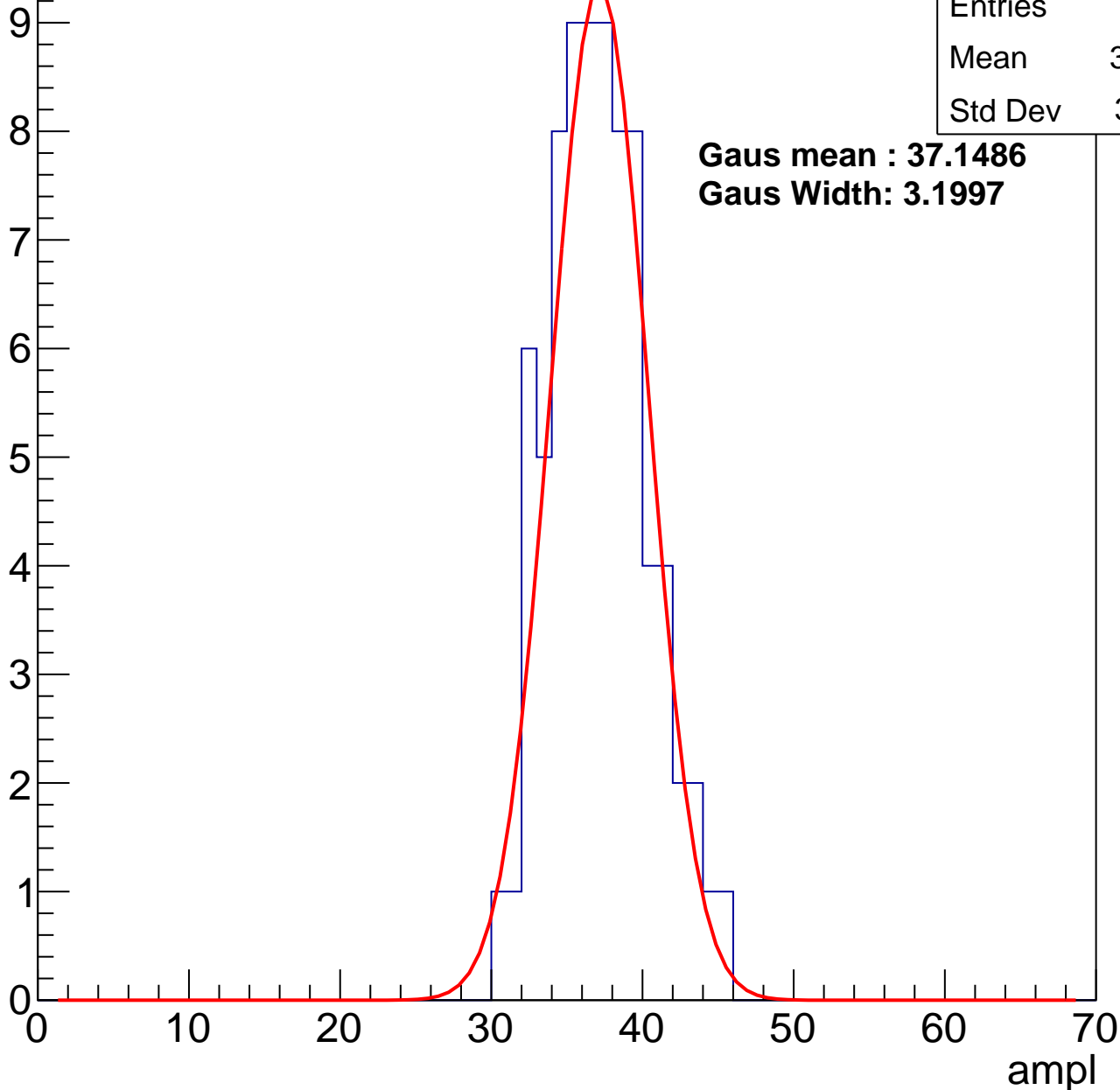
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	36.68
Std Dev	3.201

Gaus mean : 37.1486

Gaus Width: 3.1997



B0L001S, U19-ch114, adc2

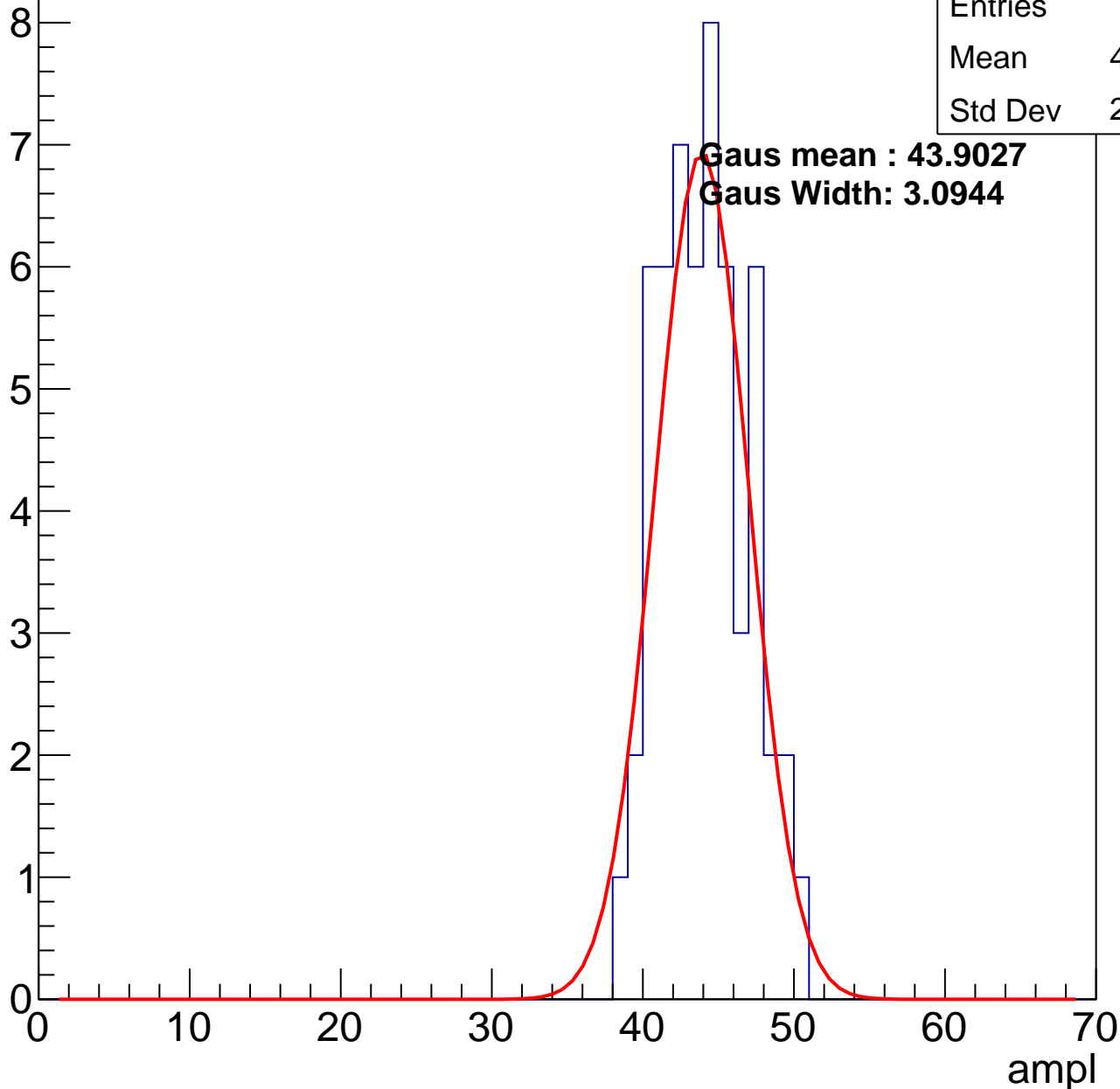
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.57
Std Dev	2.834

Gaus mean : 43.9027

Gaus Width: 3.0944

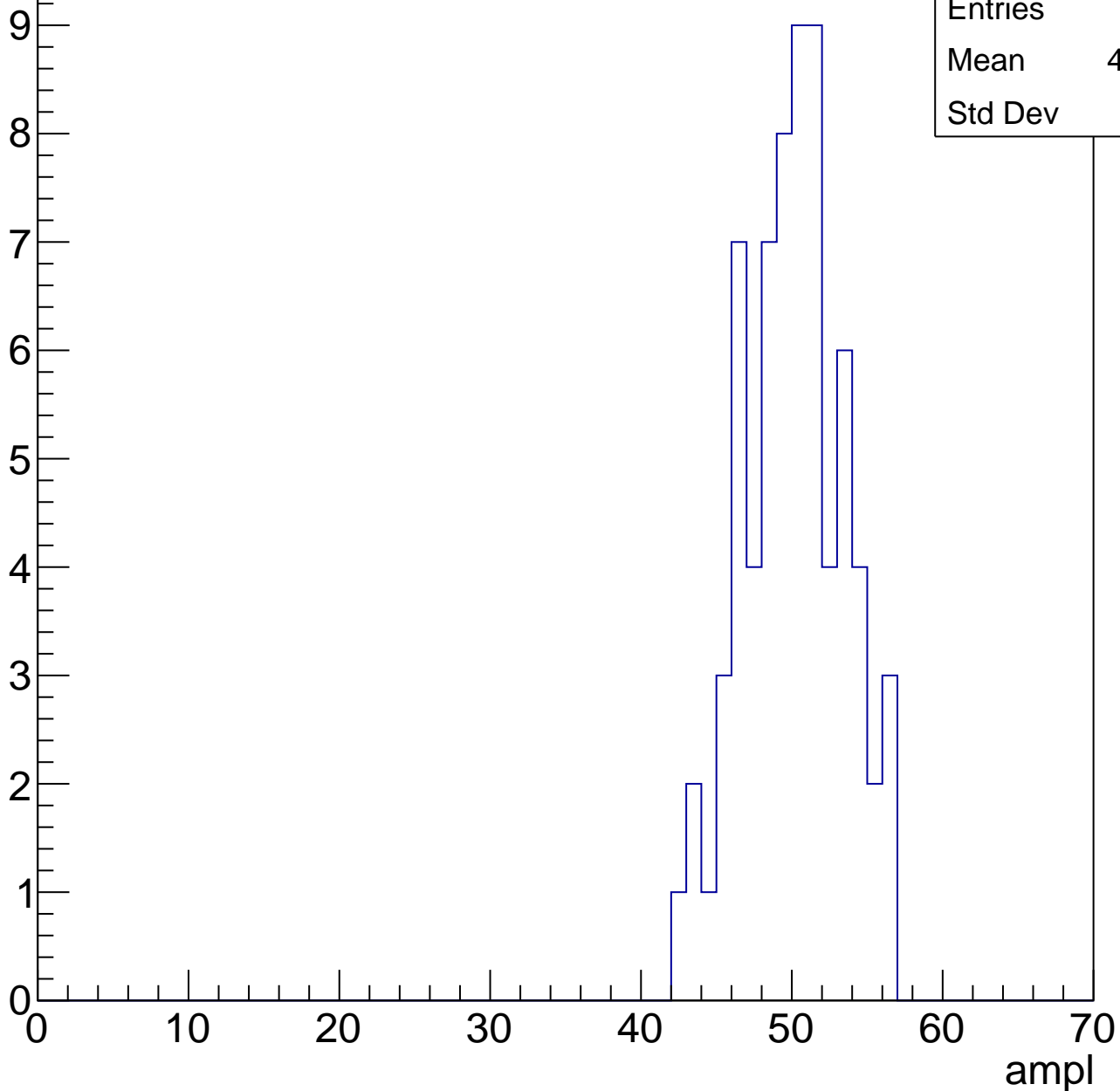


B0L001S, U19-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

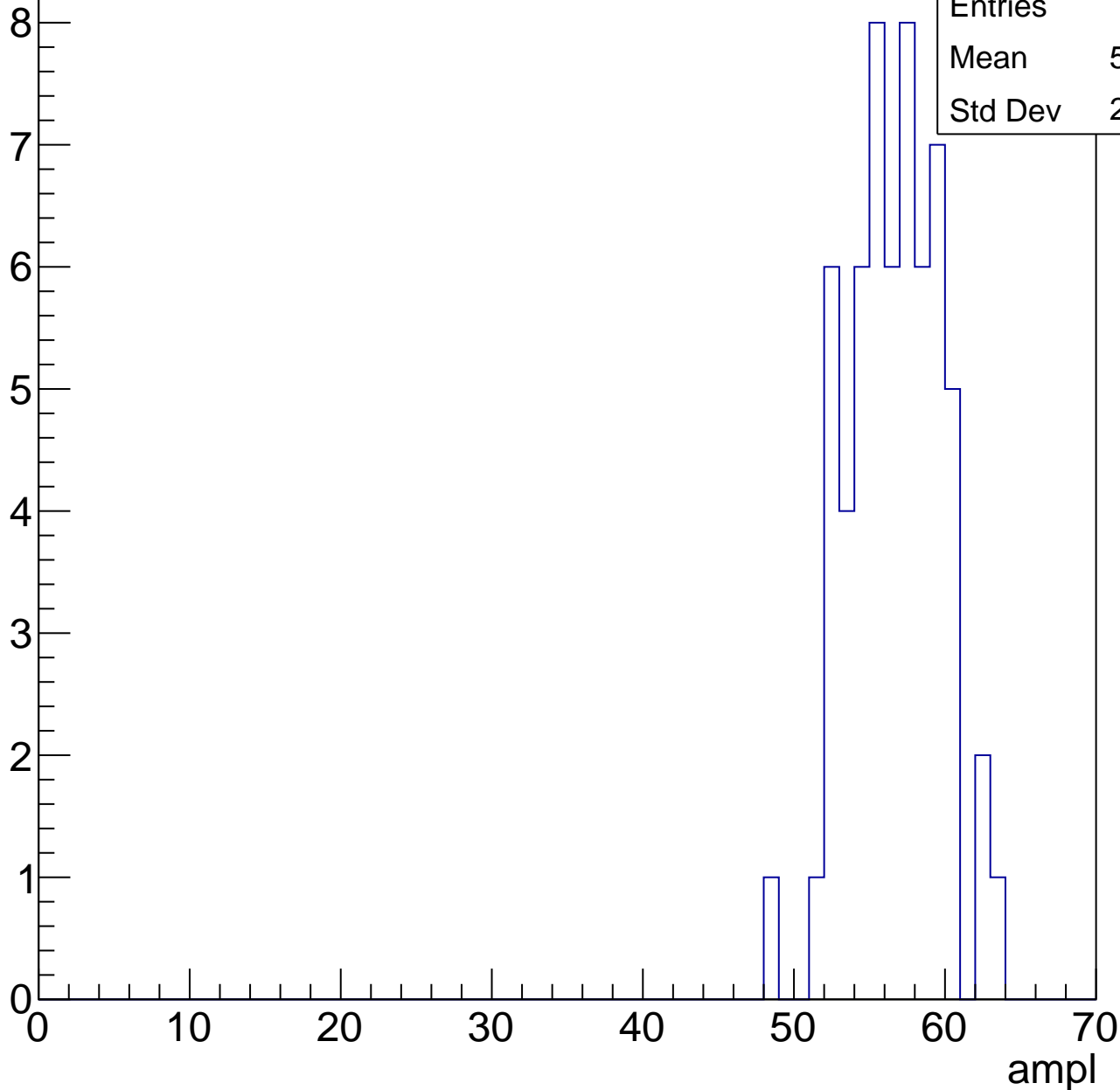
Entries	70
Mean	49.63
Std Dev	3.27



B0L001S, U19-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

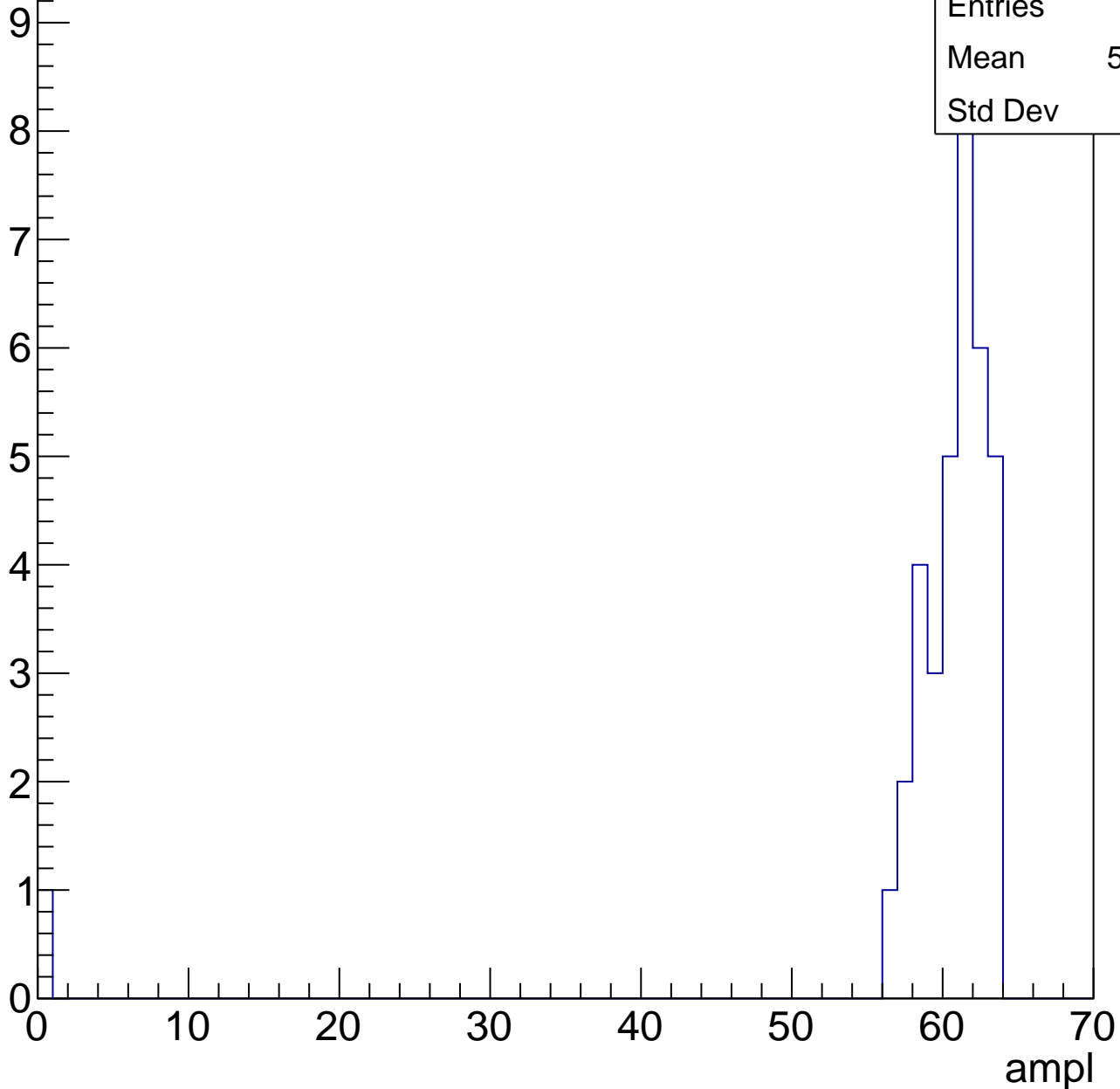


B0L001S, U19-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.75
Std Dev	10.1



B0L001S, U19-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch115, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	29.55
Std Dev	4.68

Gaus mean : 30.6096

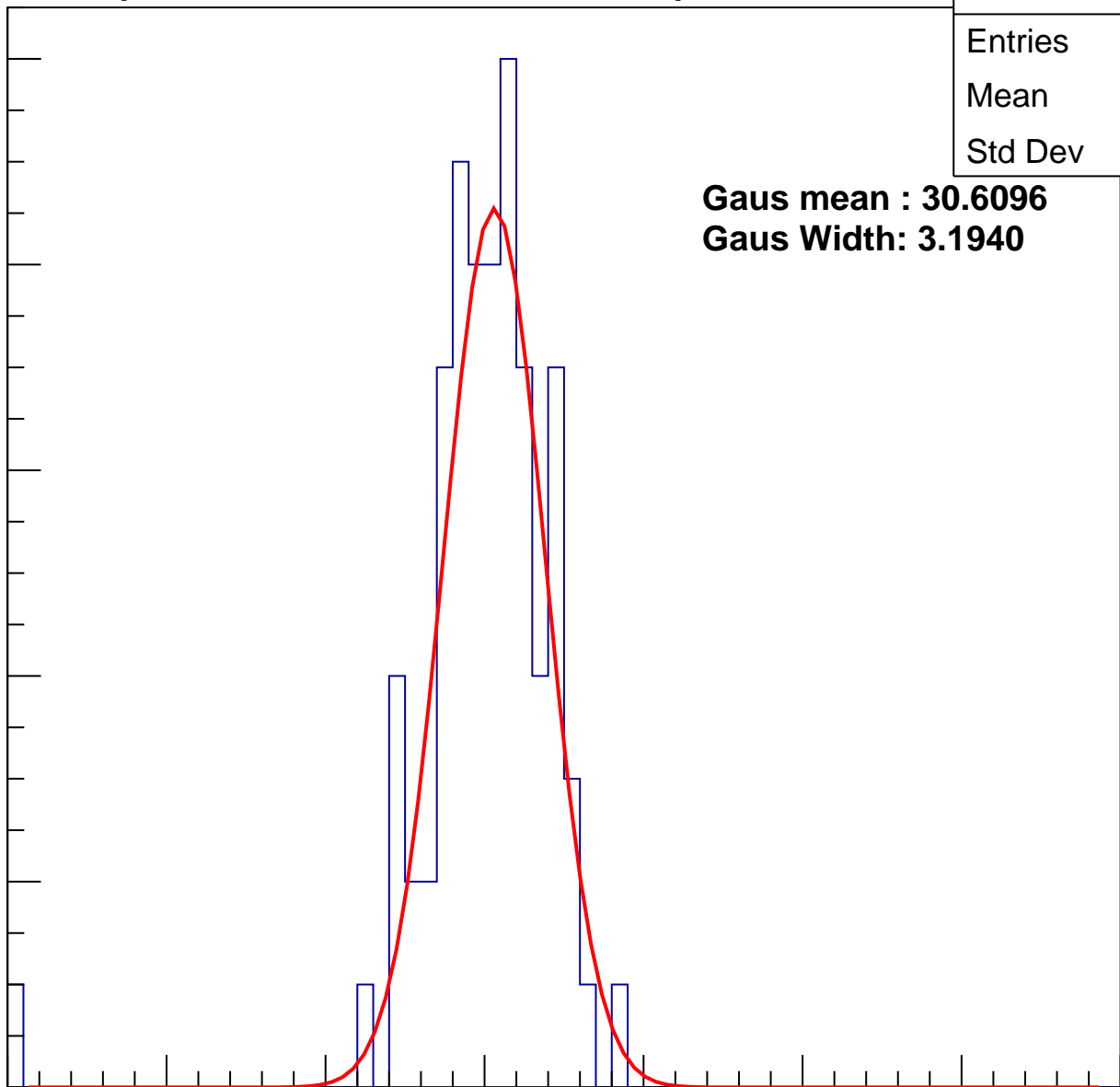
Gaus Width: 3.1940

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch115, adc1

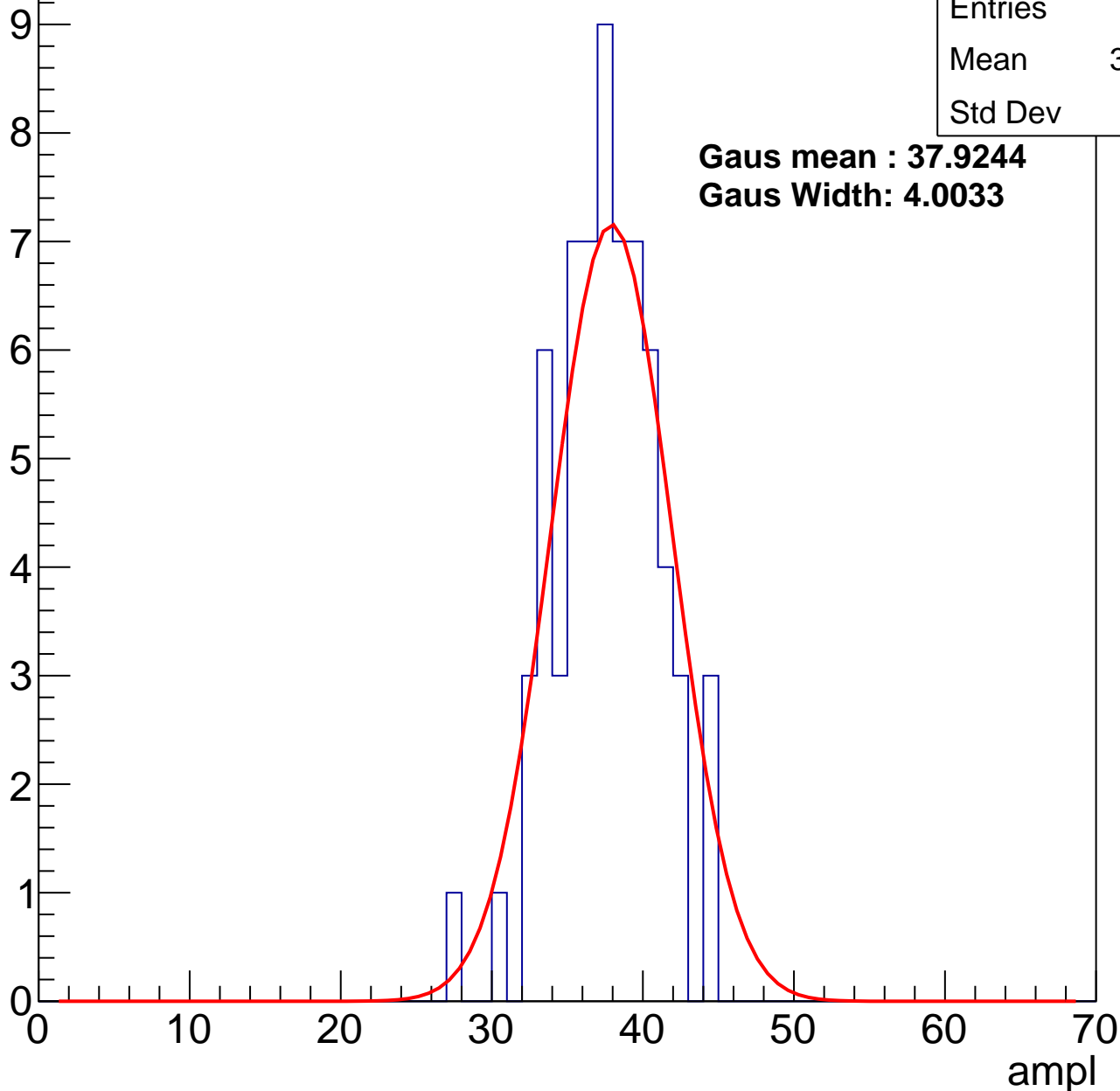
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.07
Std Dev	3.36

Gaus mean : 37.9244

Gaus Width: 4.0033



B0L001S, U19-ch115, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	43.27
Std Dev	3.313

Gaus mean : 43.9110

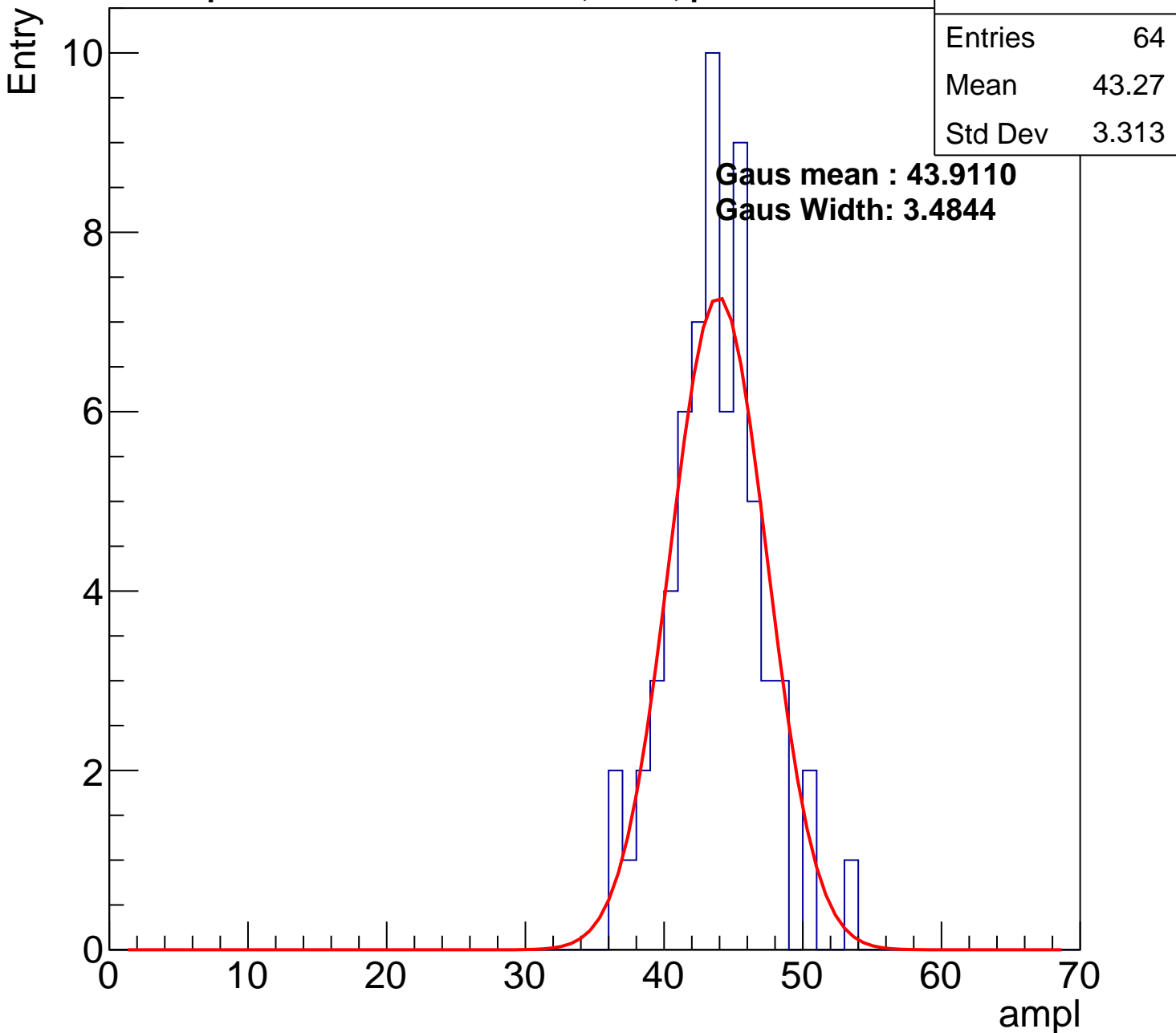
Gaus Width: 3.4844

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

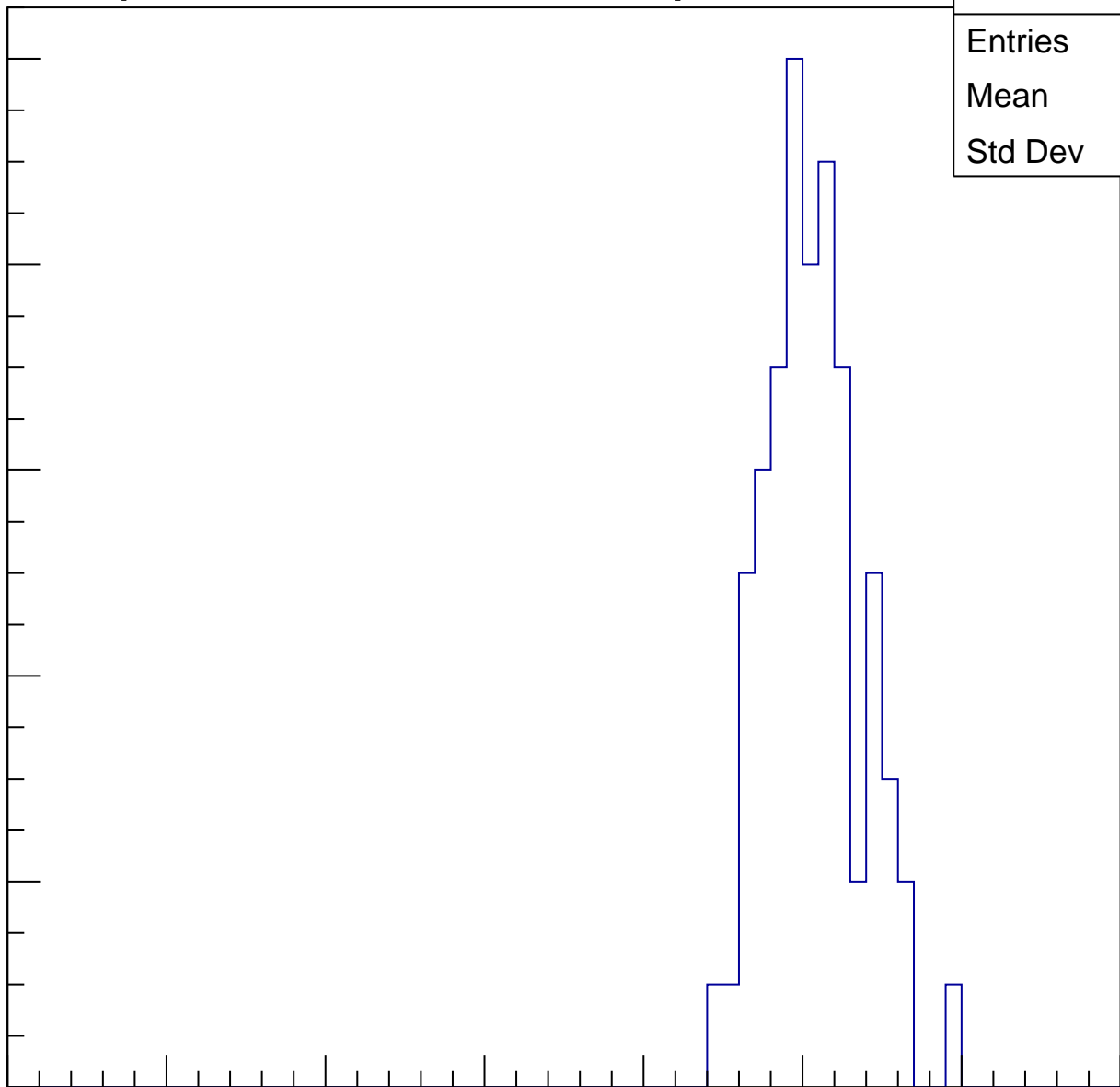
Entries	67
Mean	50.18
Std Dev	2.977

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

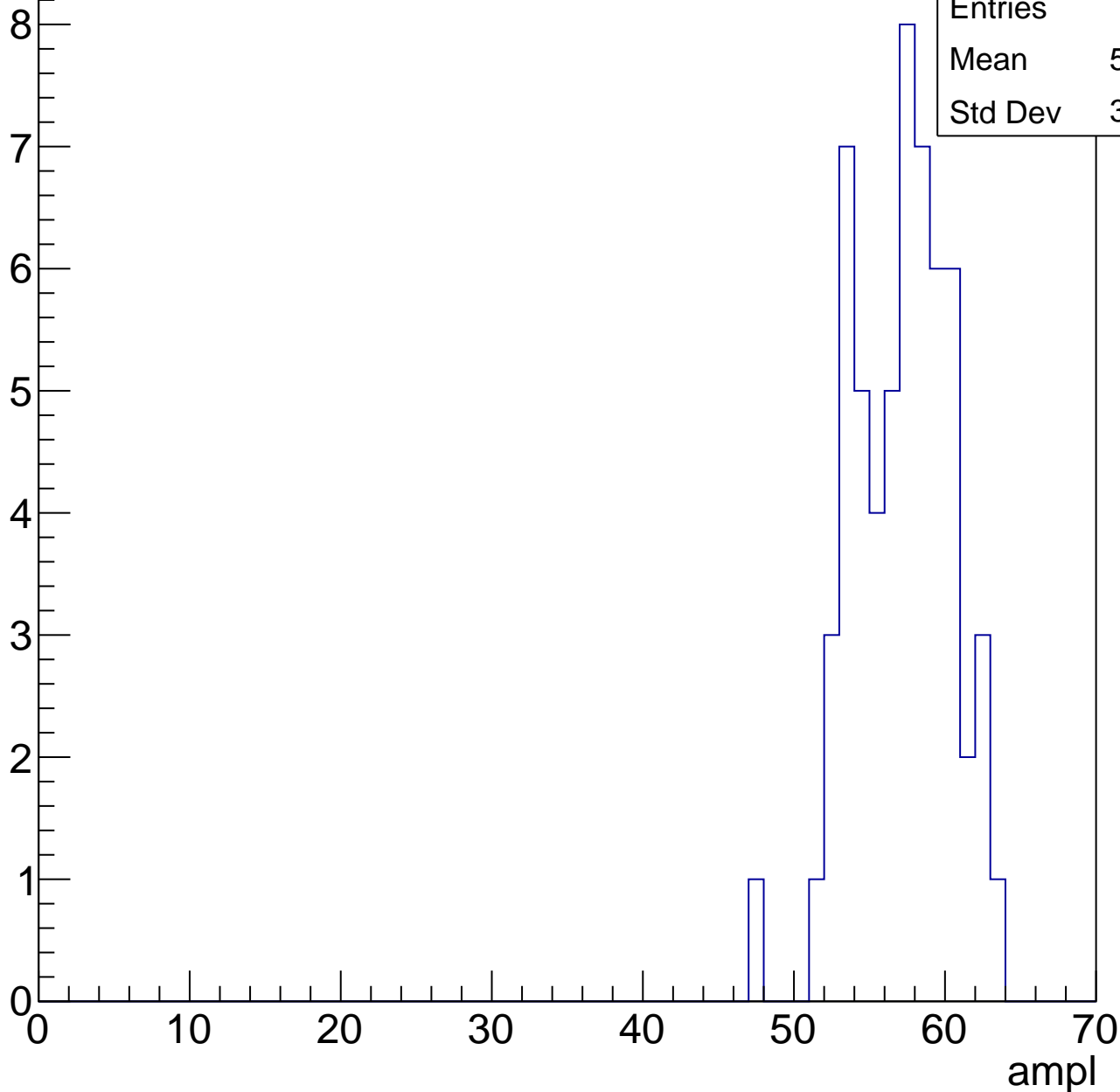


B0L001S, U19-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.64
Std Dev	3.214

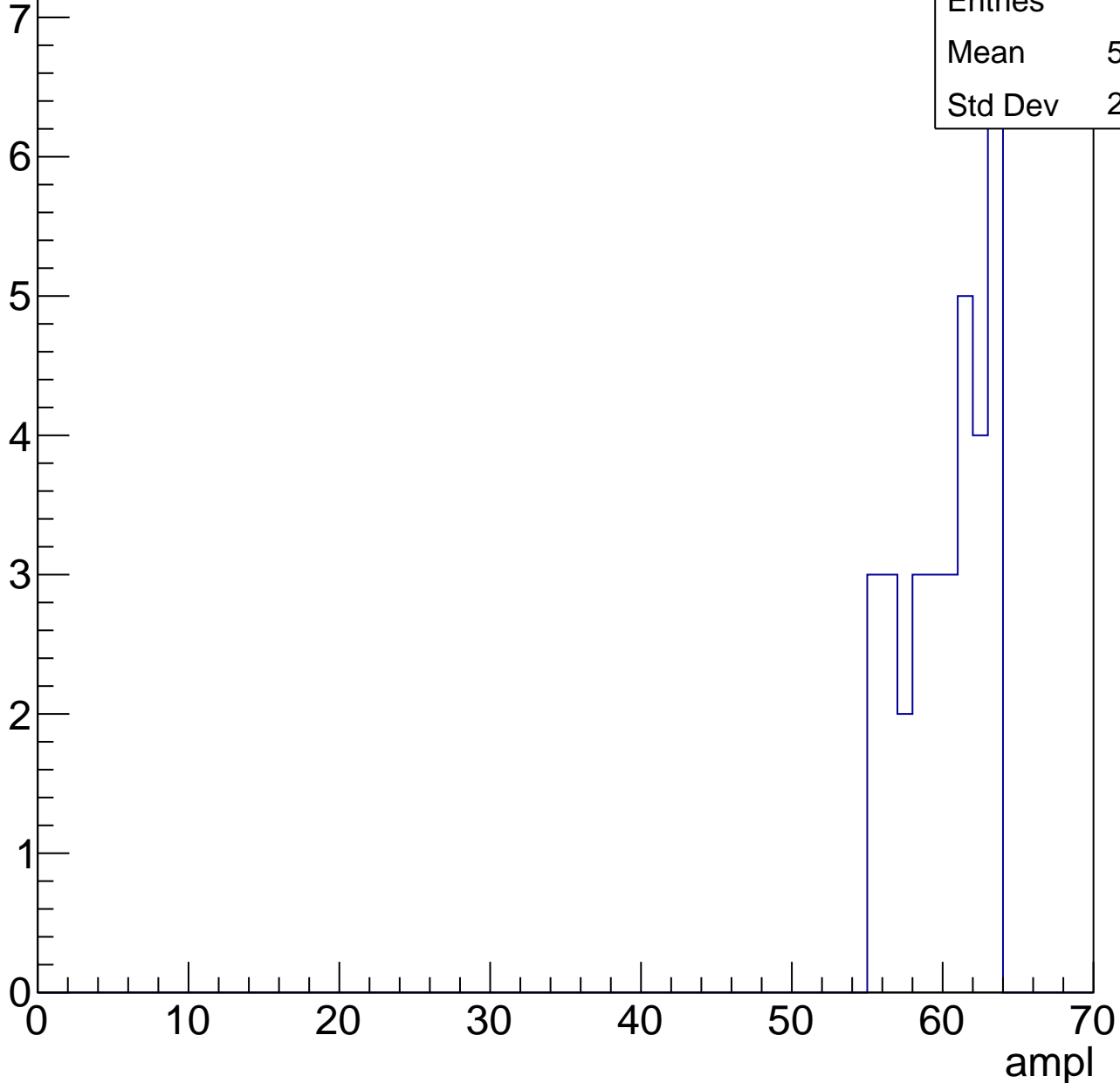


B0L001S, U19-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

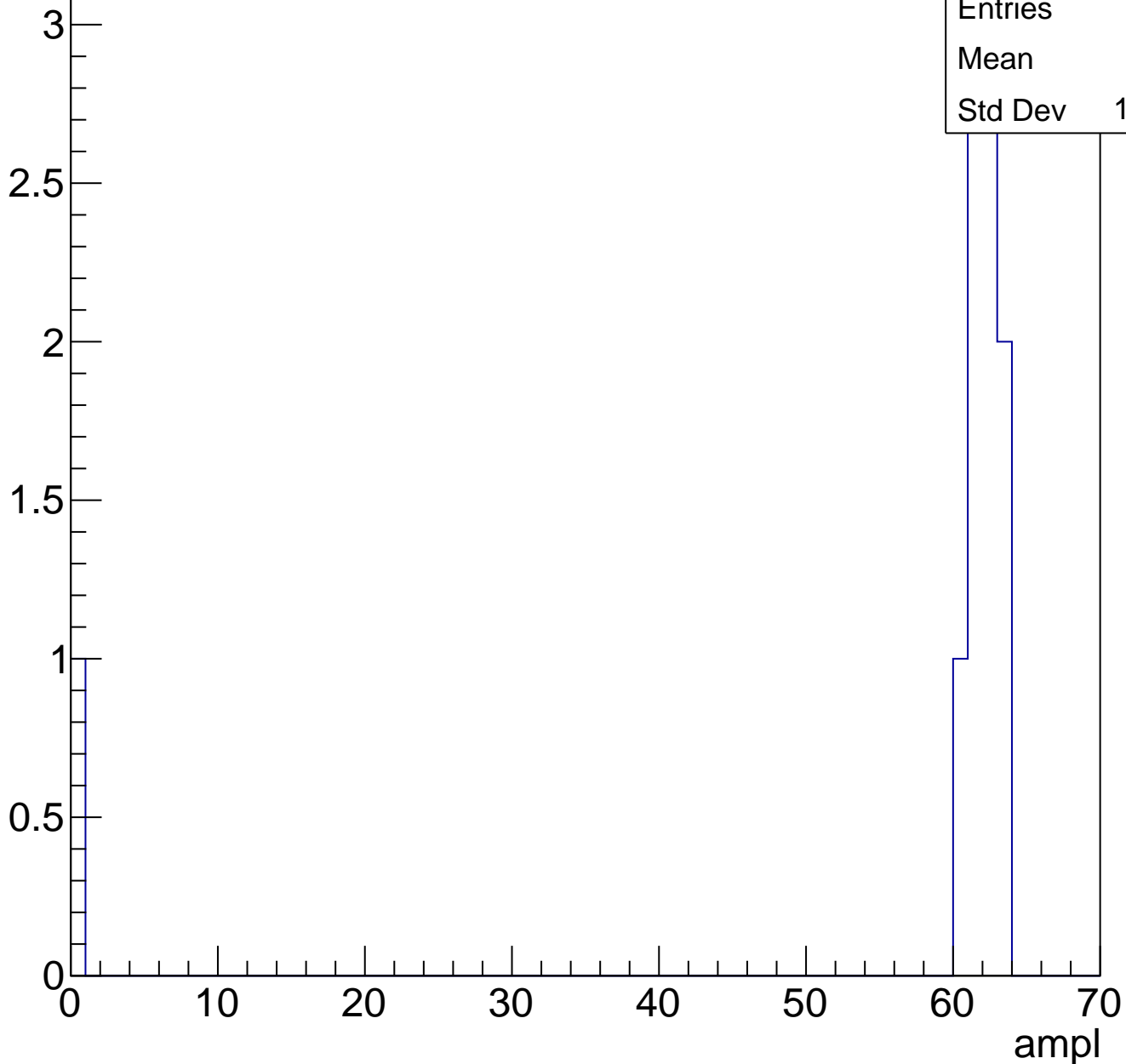
Entries	33
Mean	59.76
Std Dev	2.686



B0L001S, U19-ch115, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U19-ch116, adc0

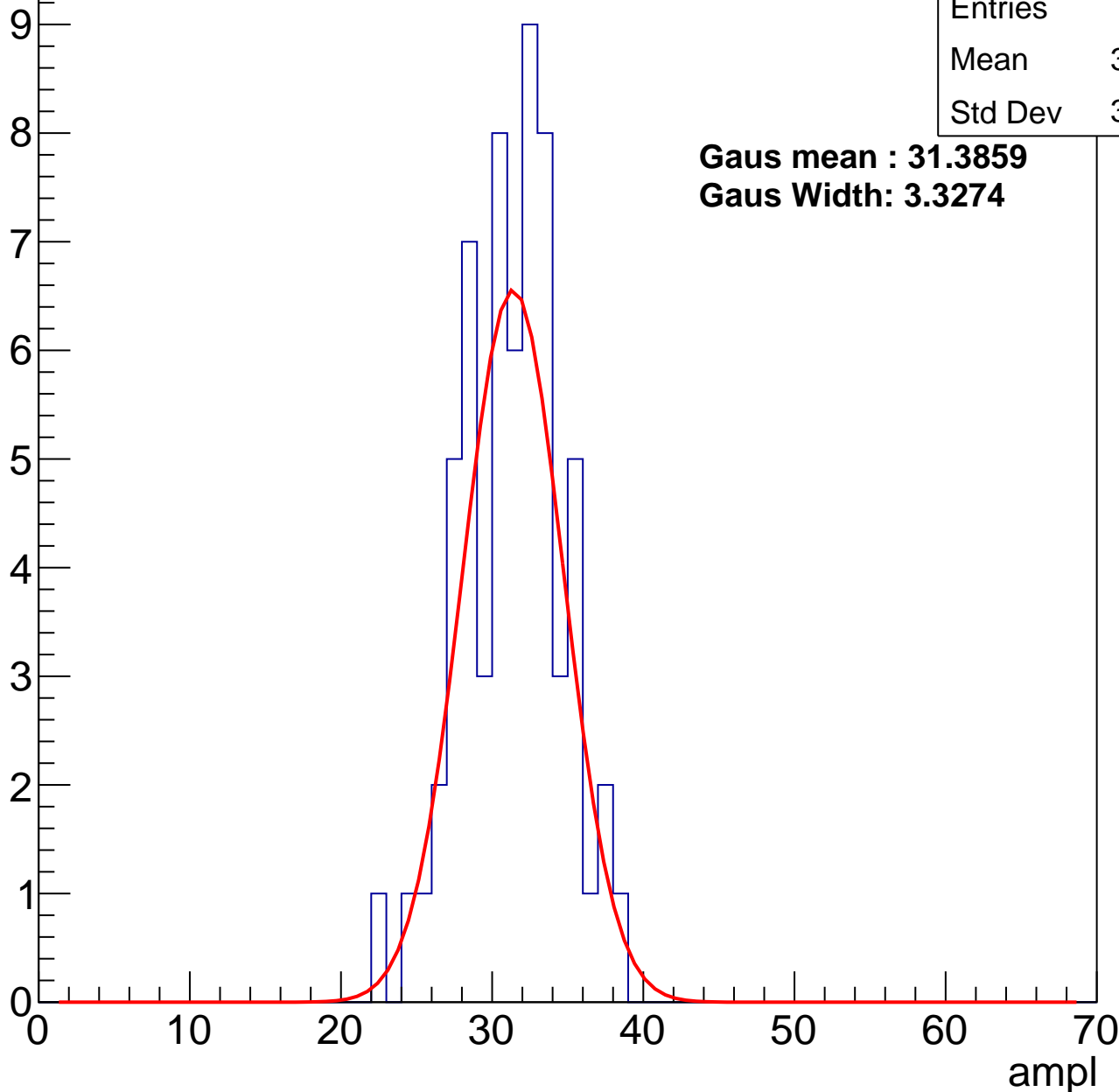
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	30.86
Std Dev	3.265

Gaus mean : 31.3859

Gaus Width: 3.3274



B0L001S, U19-ch116, adc1

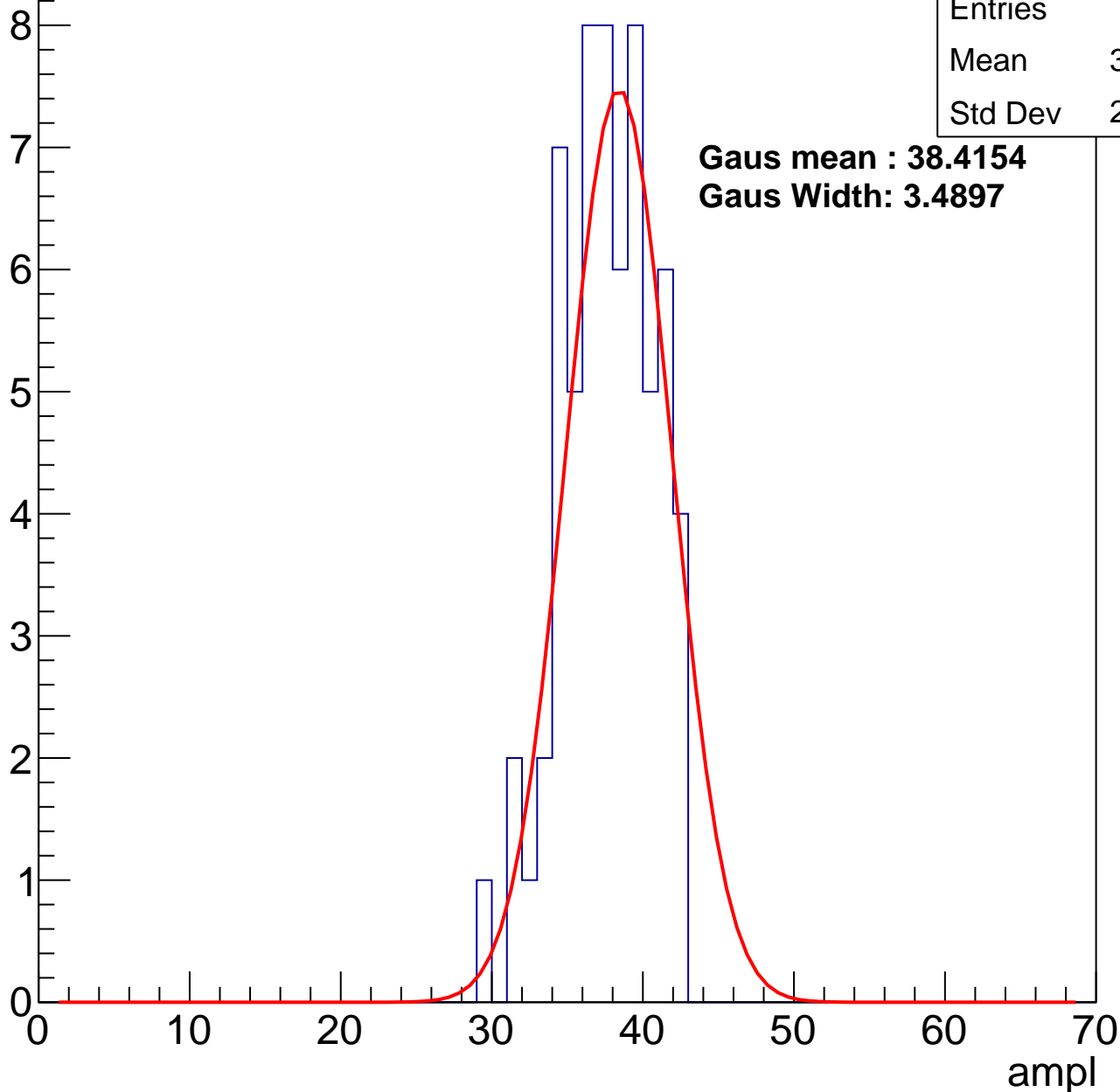
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.14
Std Dev	2.986

Gaus mean : 38.4154

Gaus Width: 3.4897



B0L001S, U19-ch116, adc2

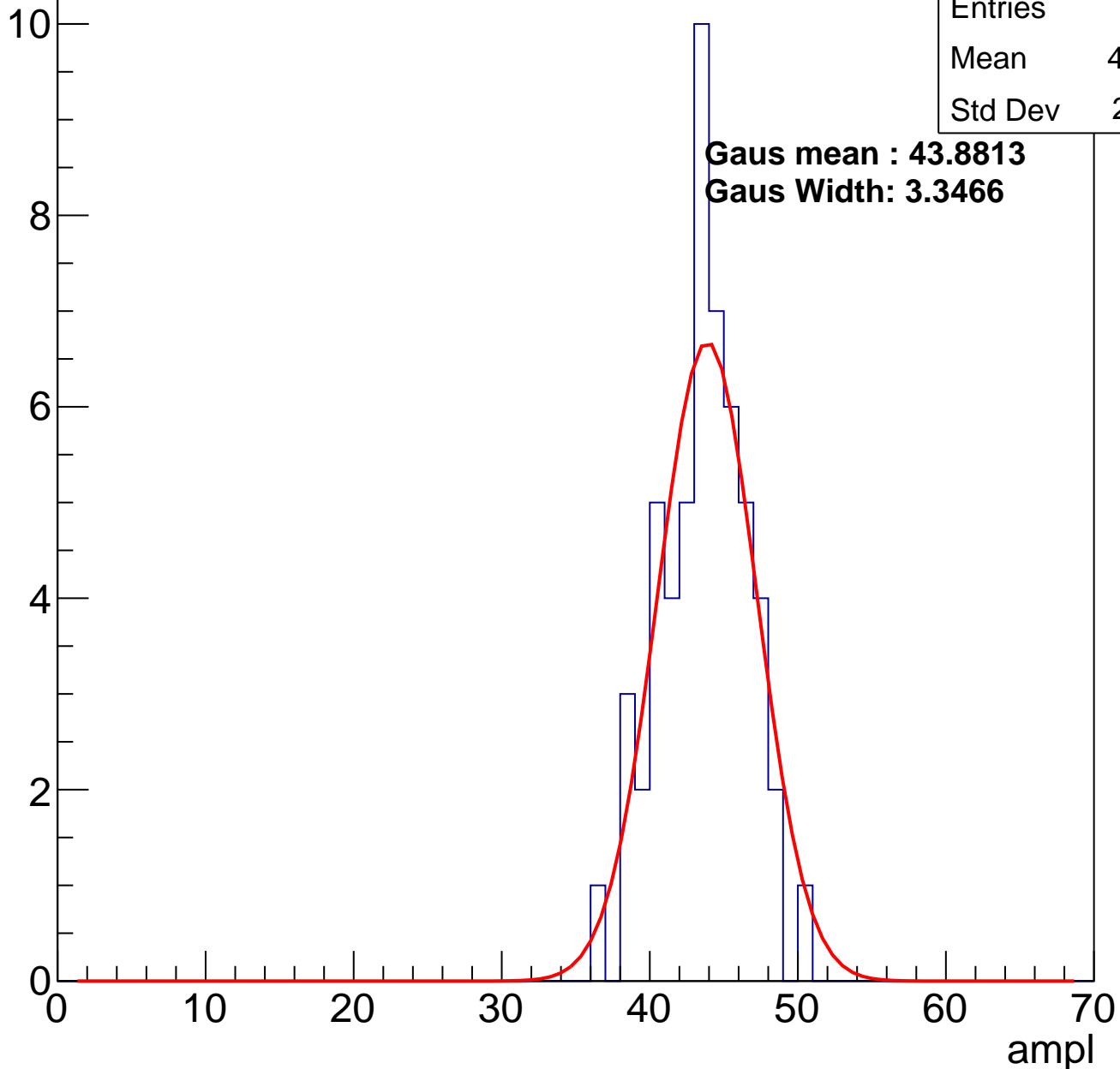
calib_packv5_042523_0143.root, FC#9, port A1

Entries	55
Mean	43.16
Std Dev	2.891

Gaus mean : 43.8813

Gaus Width: 3.3466

Entry



B0L001S, U19-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

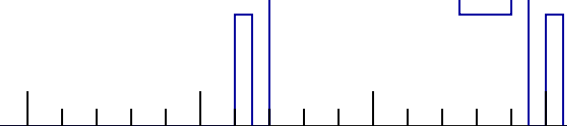
Entries	81
Mean	50.12
Std Dev	3.588

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

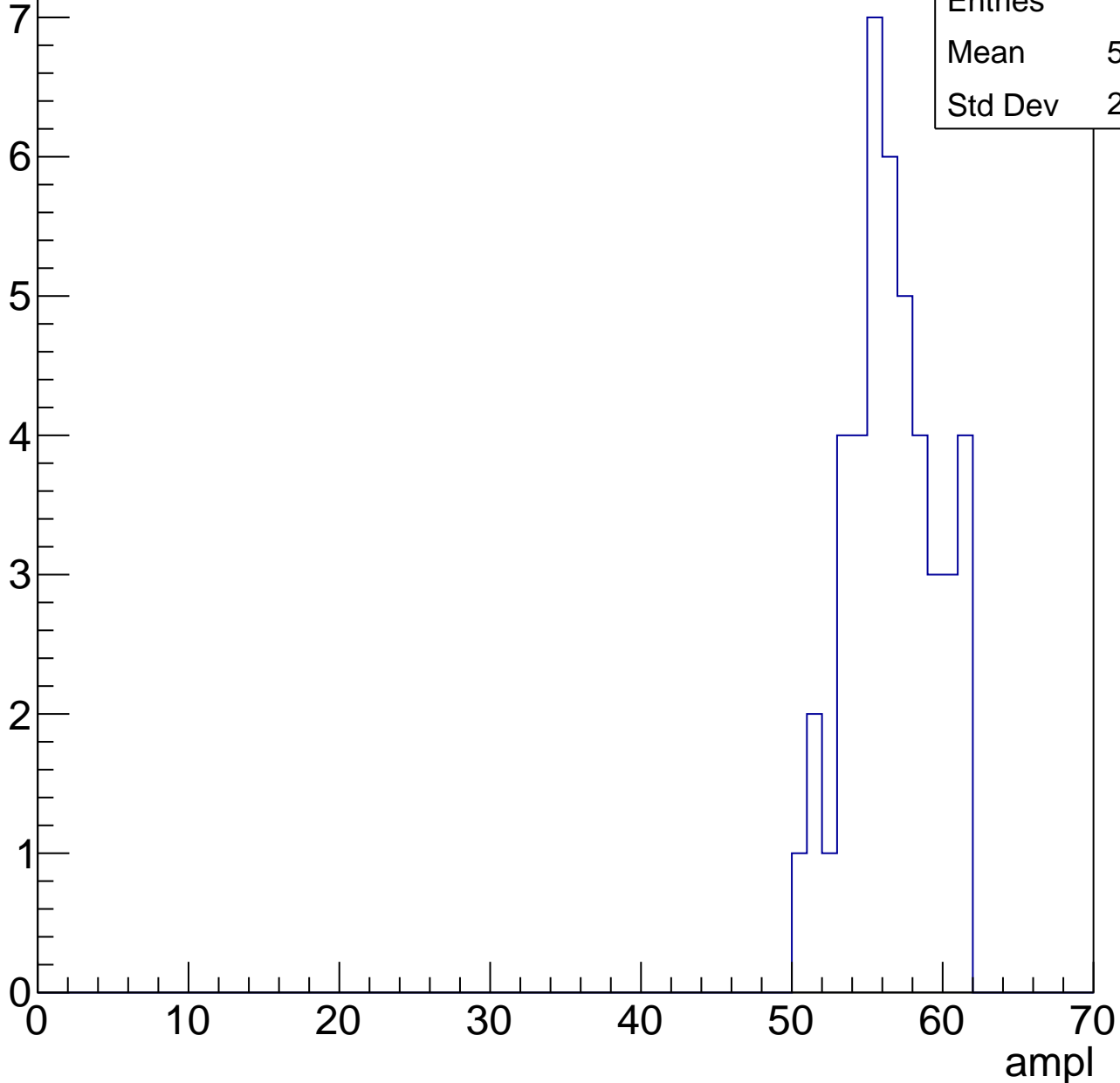


B0L001S, U19-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	56.16
Std Dev	2.844



B0L001S, U19-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	49
Mean	58.88
Std Dev	8.826

Entry

10

8

6

4

2

0

0

10

20

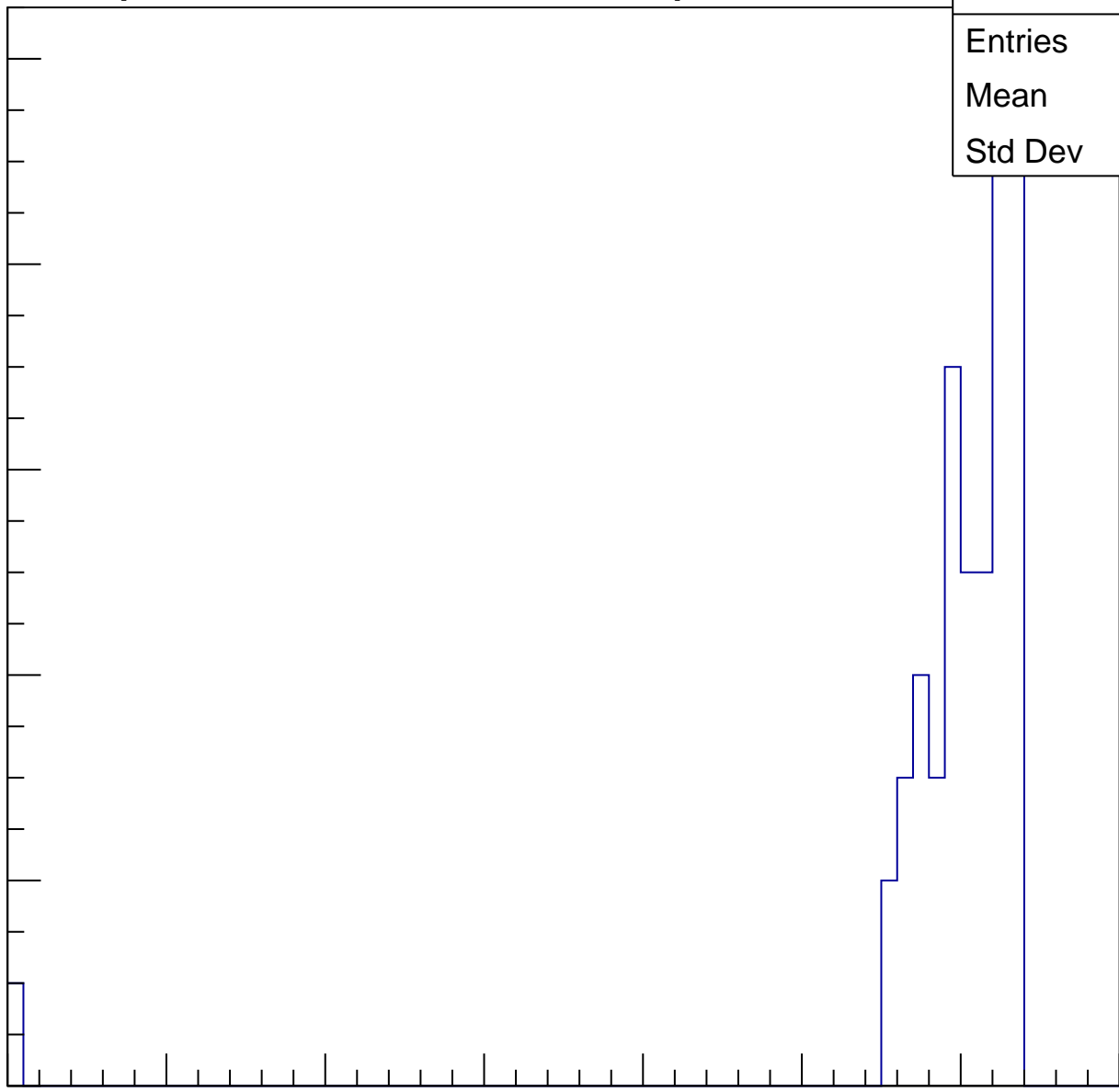
30

40

50

60

ampl



B0L001S, U19-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch117, adc0

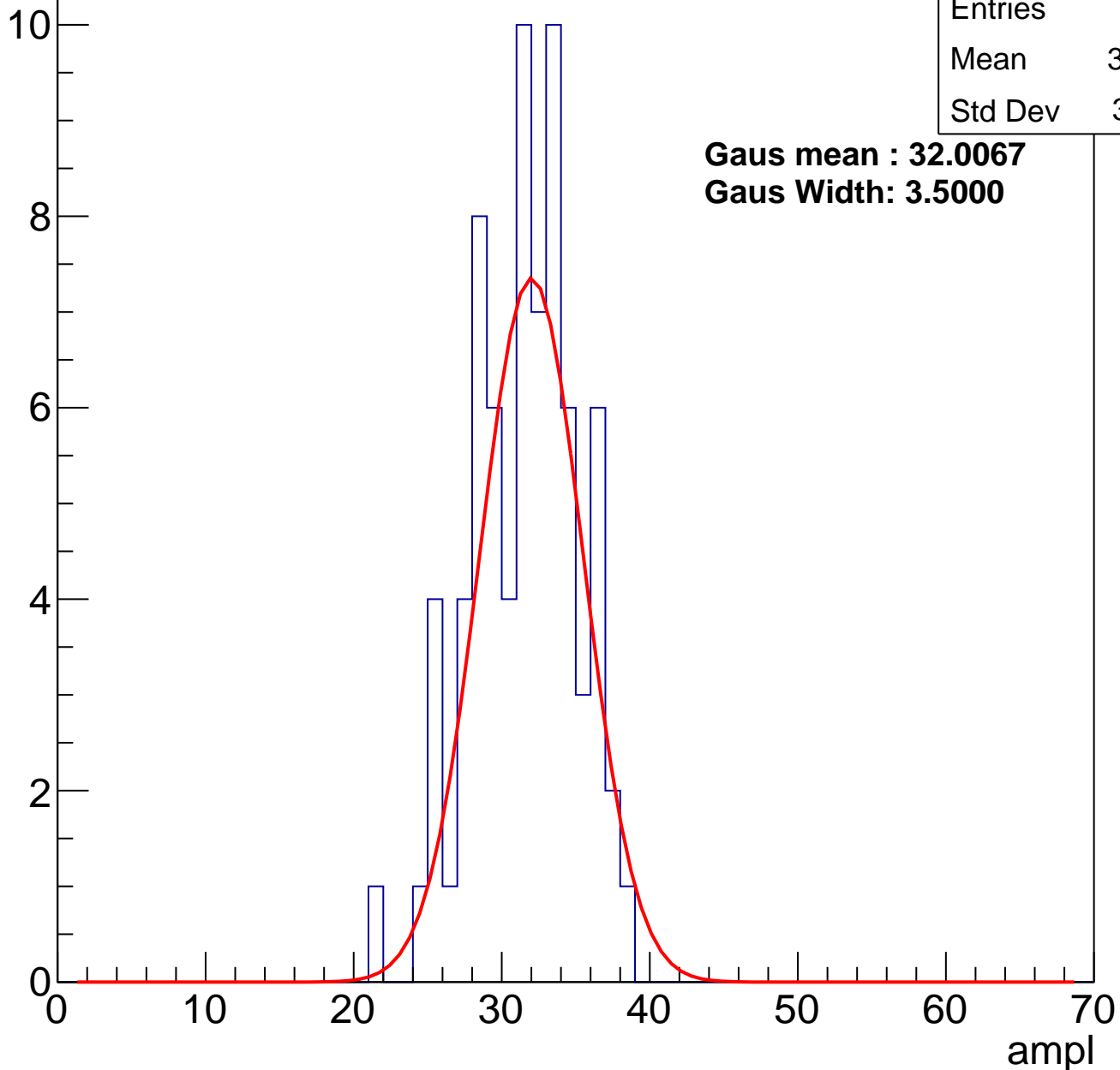
calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	31.05
Std Dev	3.491

Gaus mean : 32.0067

Gaus Width: 3.5000

Entry



B0L001S, U19-ch117, adc1

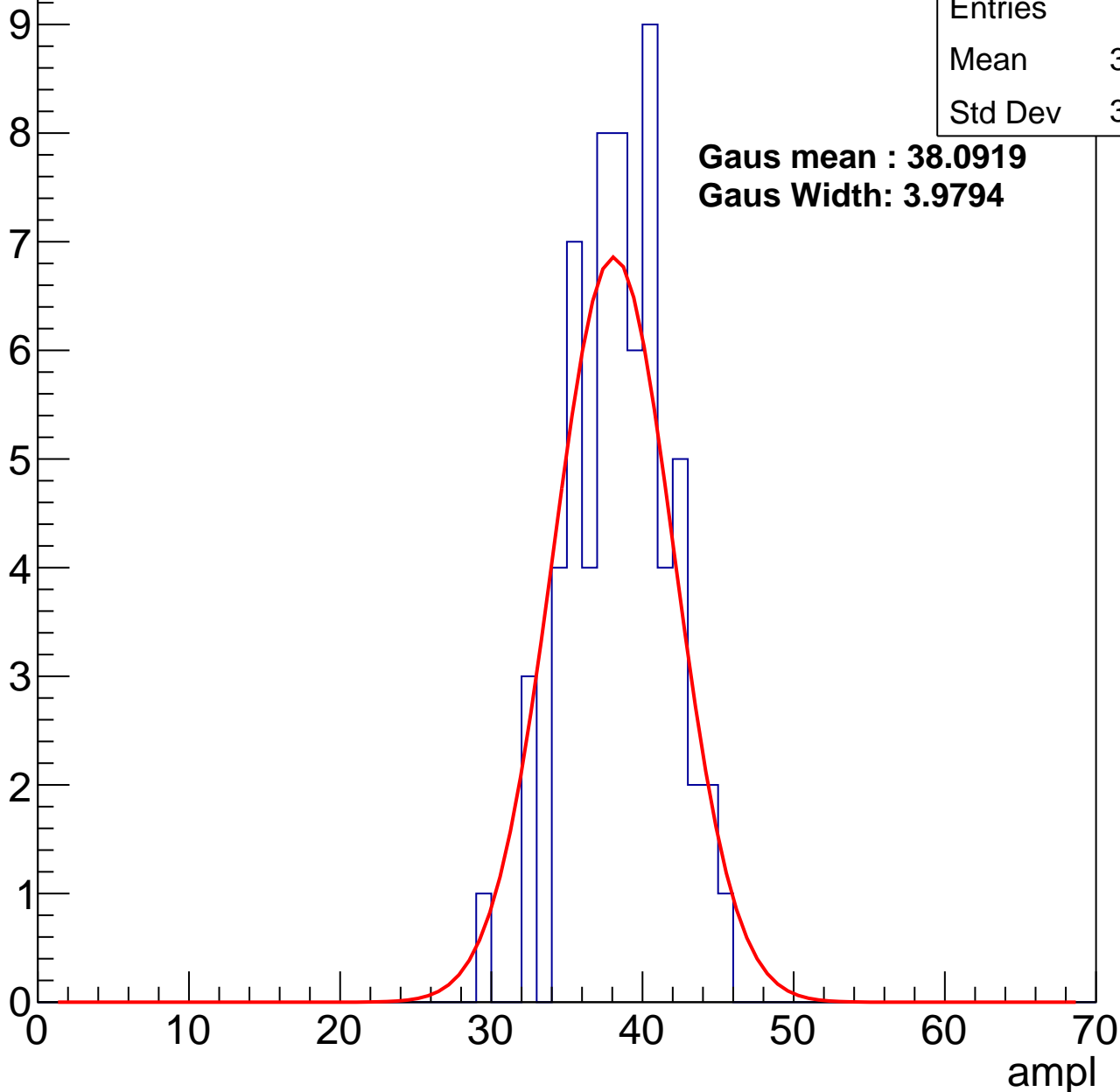
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	38.08
Std Dev	3.232

Gaus mean : 38.0919

Gaus Width: 3.9794



B0L001S, U19-ch117, adc2

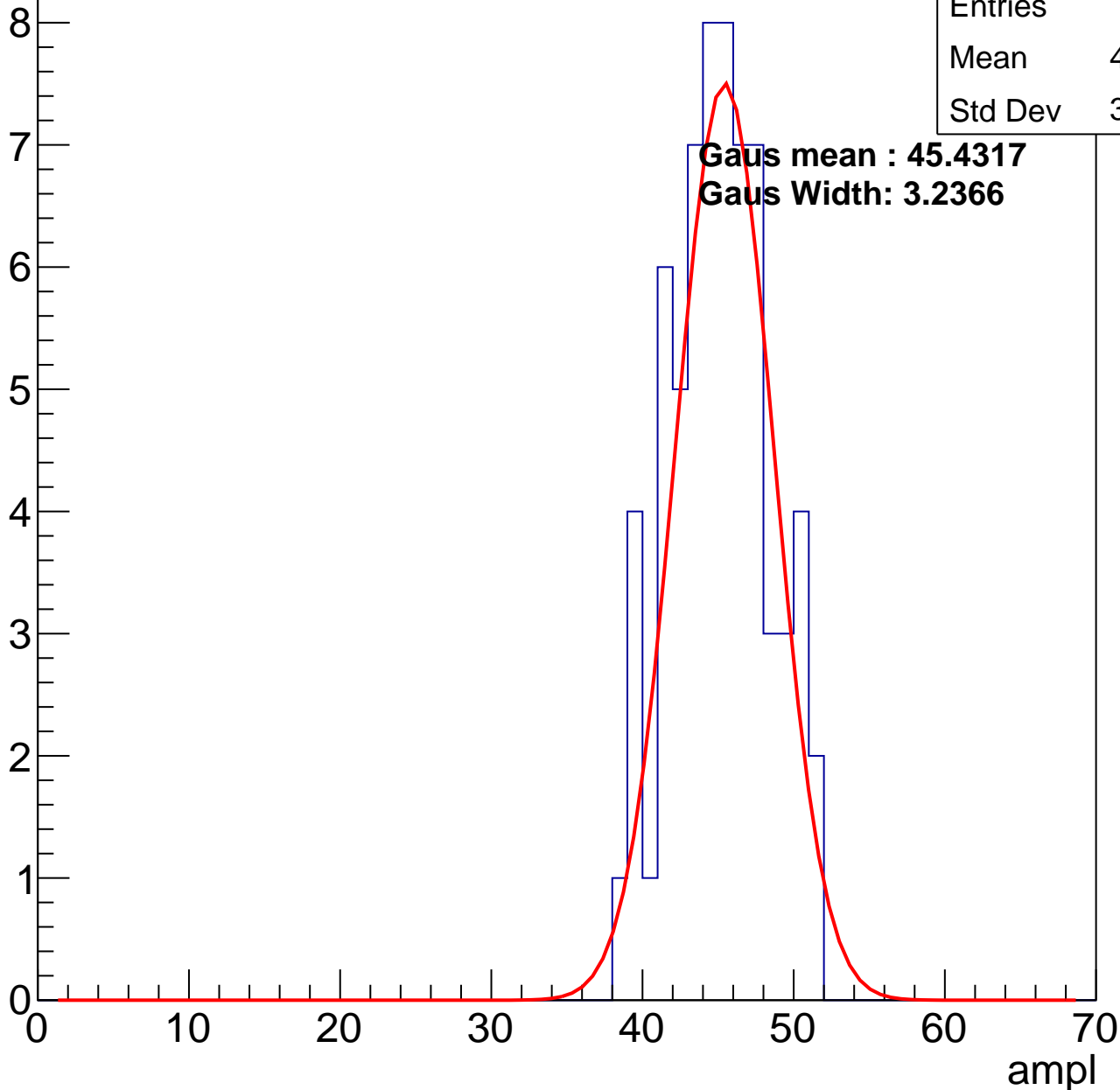
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.65
Std Dev	3.184

Gaus mean : 45.4317

Gaus Width: 3.2366

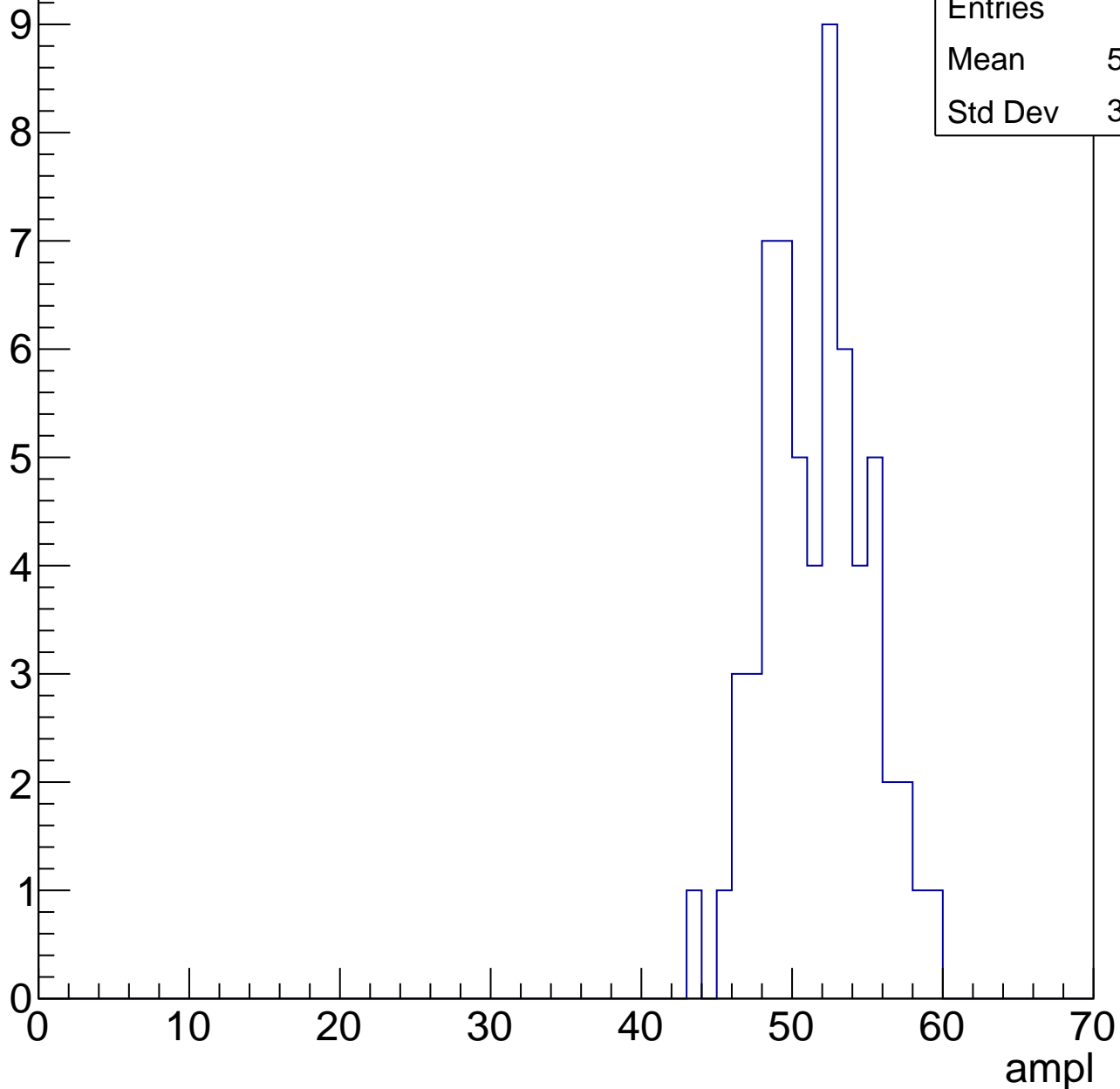


B0L001S, U19-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	51.15
Std Dev	3.392

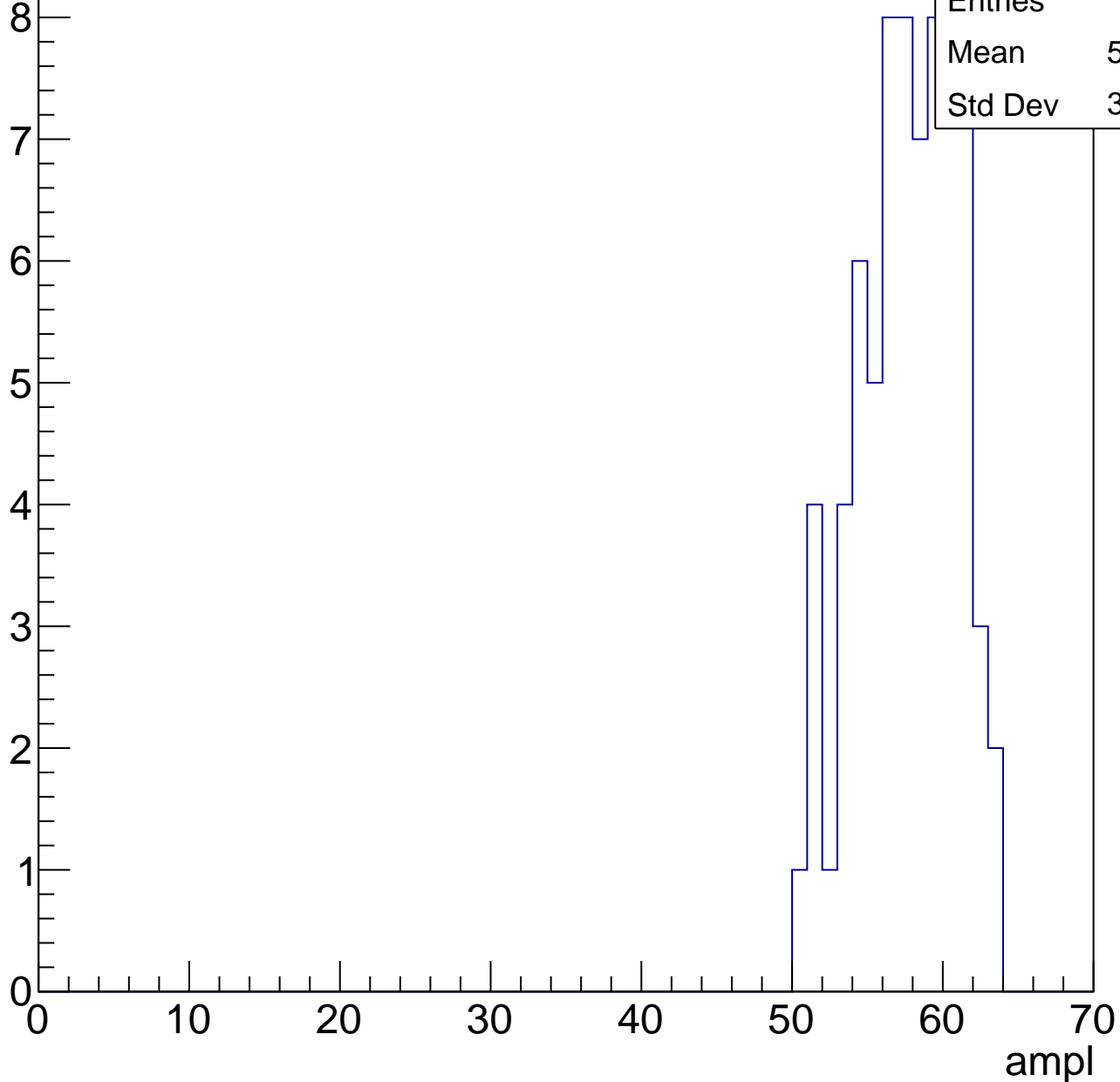


B0L001S, U19-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	57.25
Std Dev	3.196

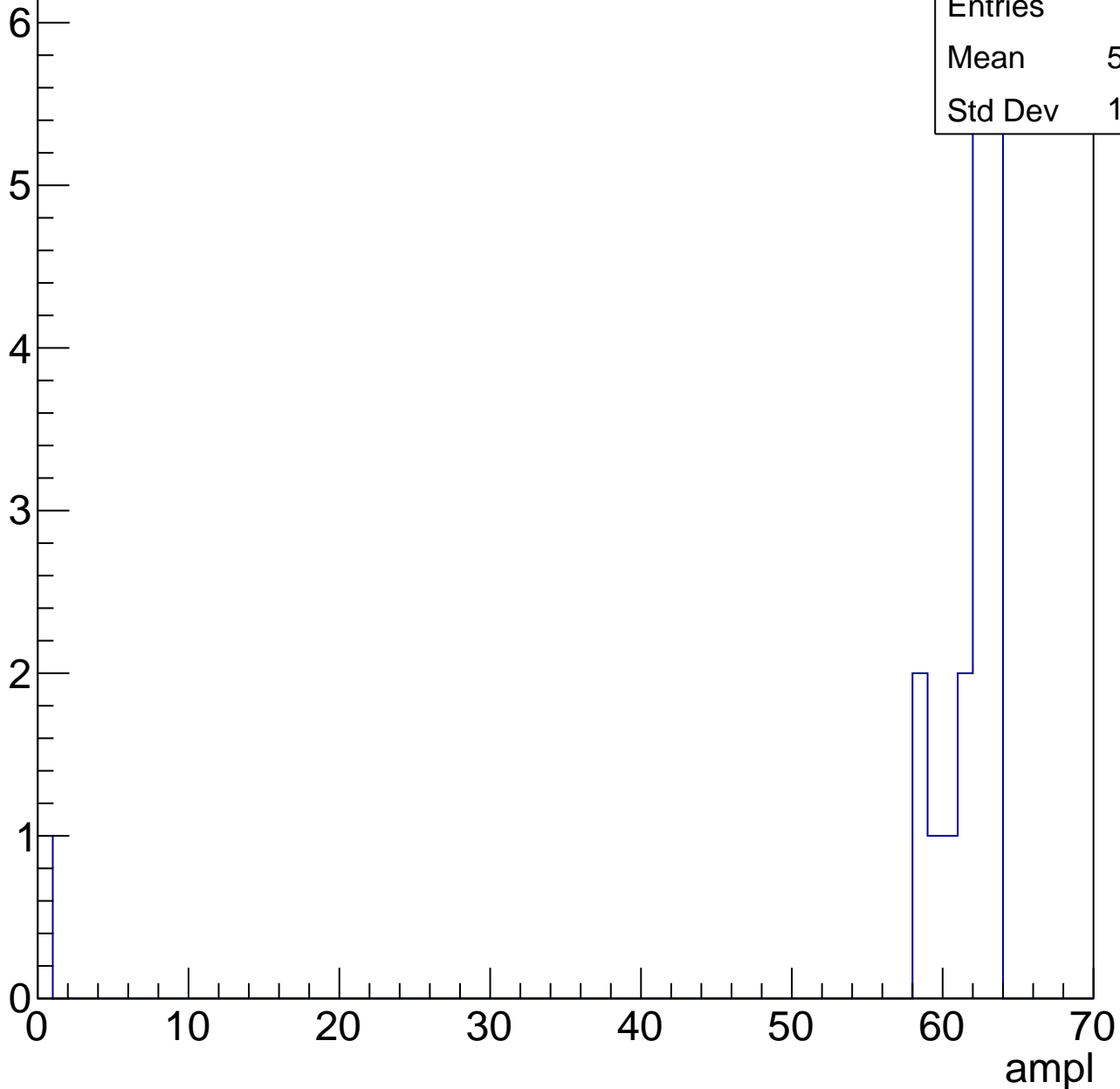


B0L001S, U19-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	19
Mean	58.26
Std Dev	13.83



B0L001S, U19-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	62
Std Dev	1.225

B0L001S, U19-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch118, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	29.62
Std Dev	5.775

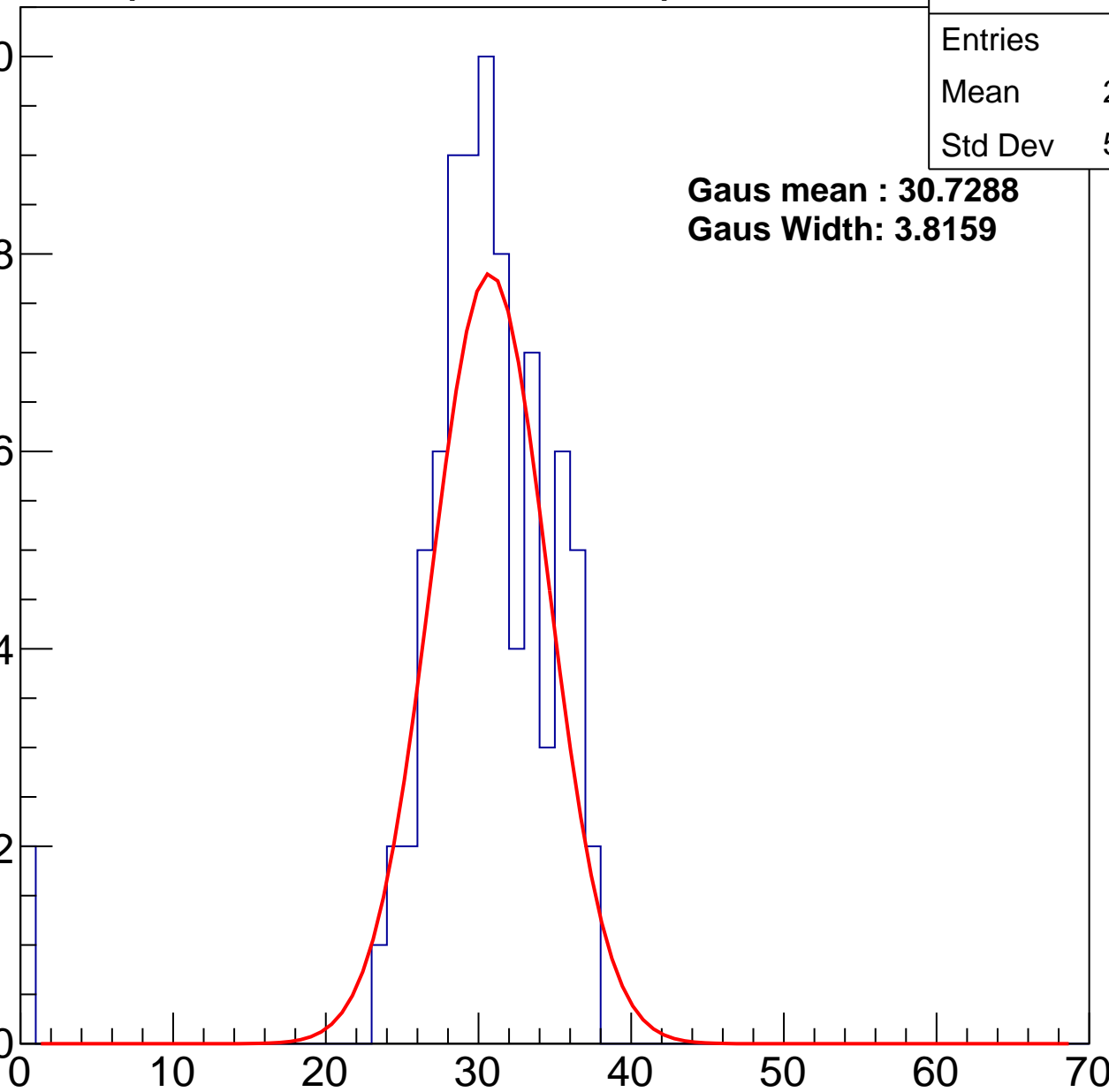
Gaus mean : 30.7288

Gaus Width: 3.8159

Entry

10
8
6
4
2
0

ampl



B0L001S, U19-ch118, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	37.87
Std Dev	3.522

Gaus mean : 37.9992

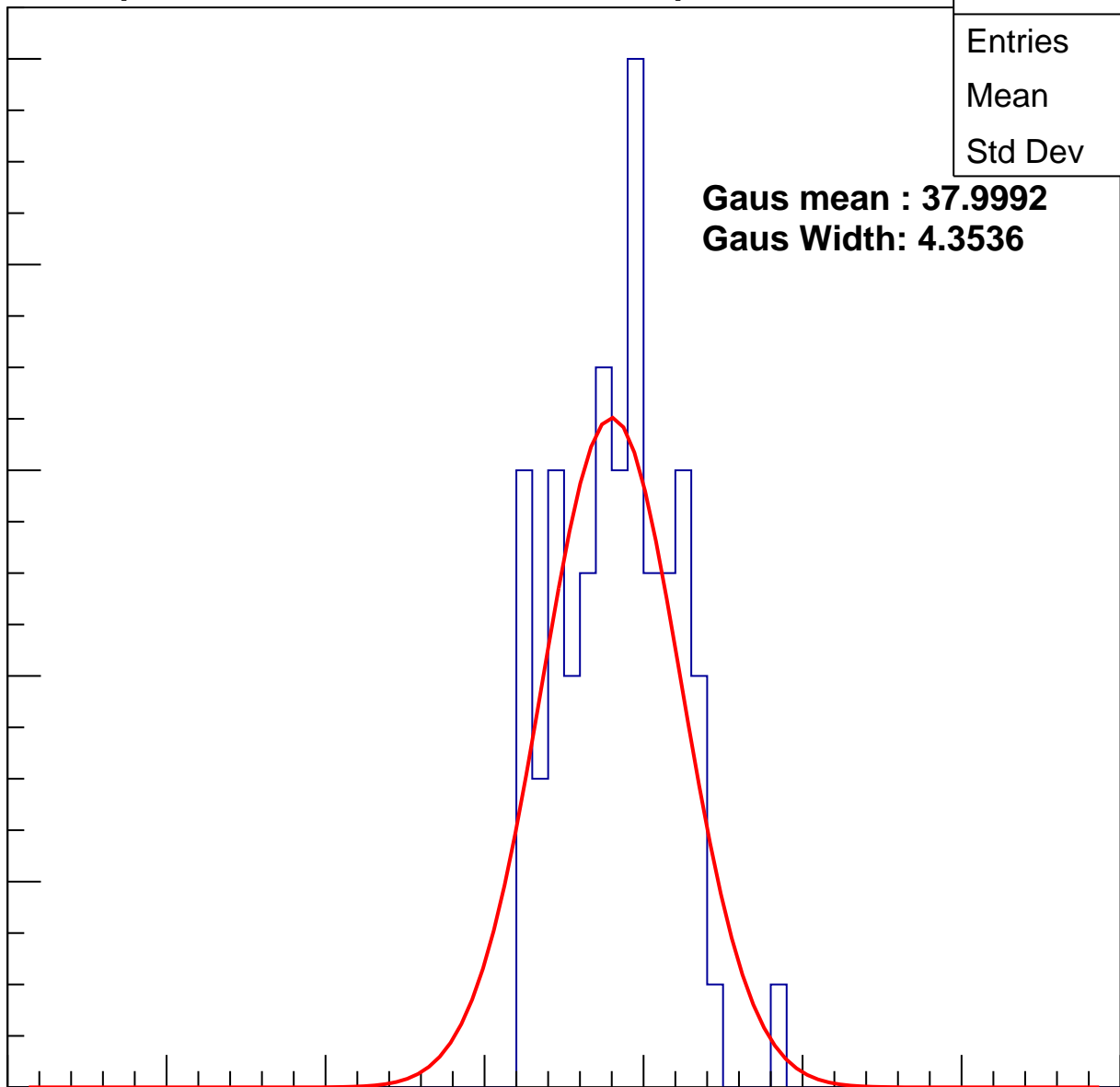
Gaus Width: 4.3536

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U19-ch118, adc2

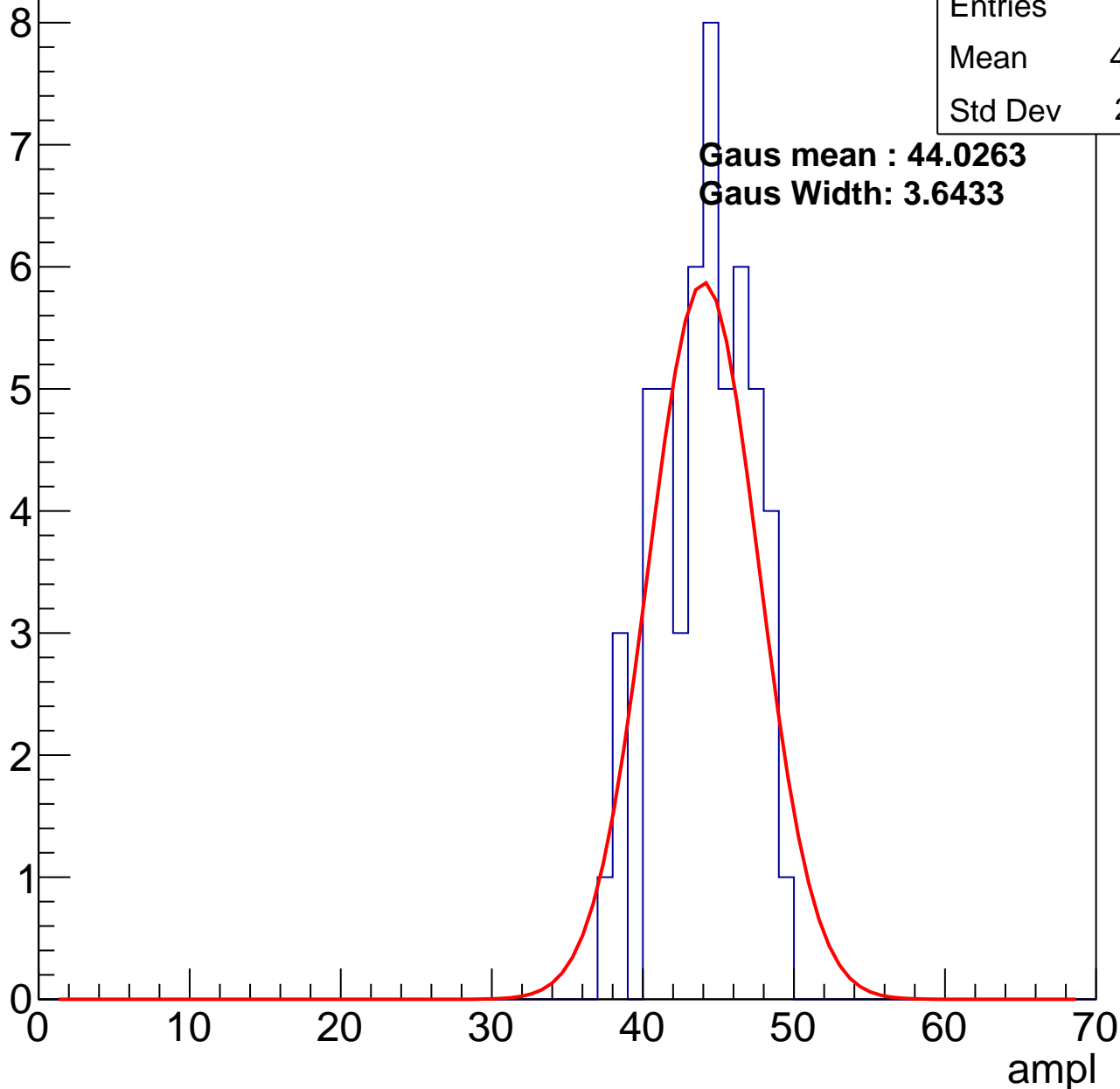
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	43.63
Std Dev	2.961

Gaus mean : 44.0263

Gaus Width: 3.6433



B0L001S, U19-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

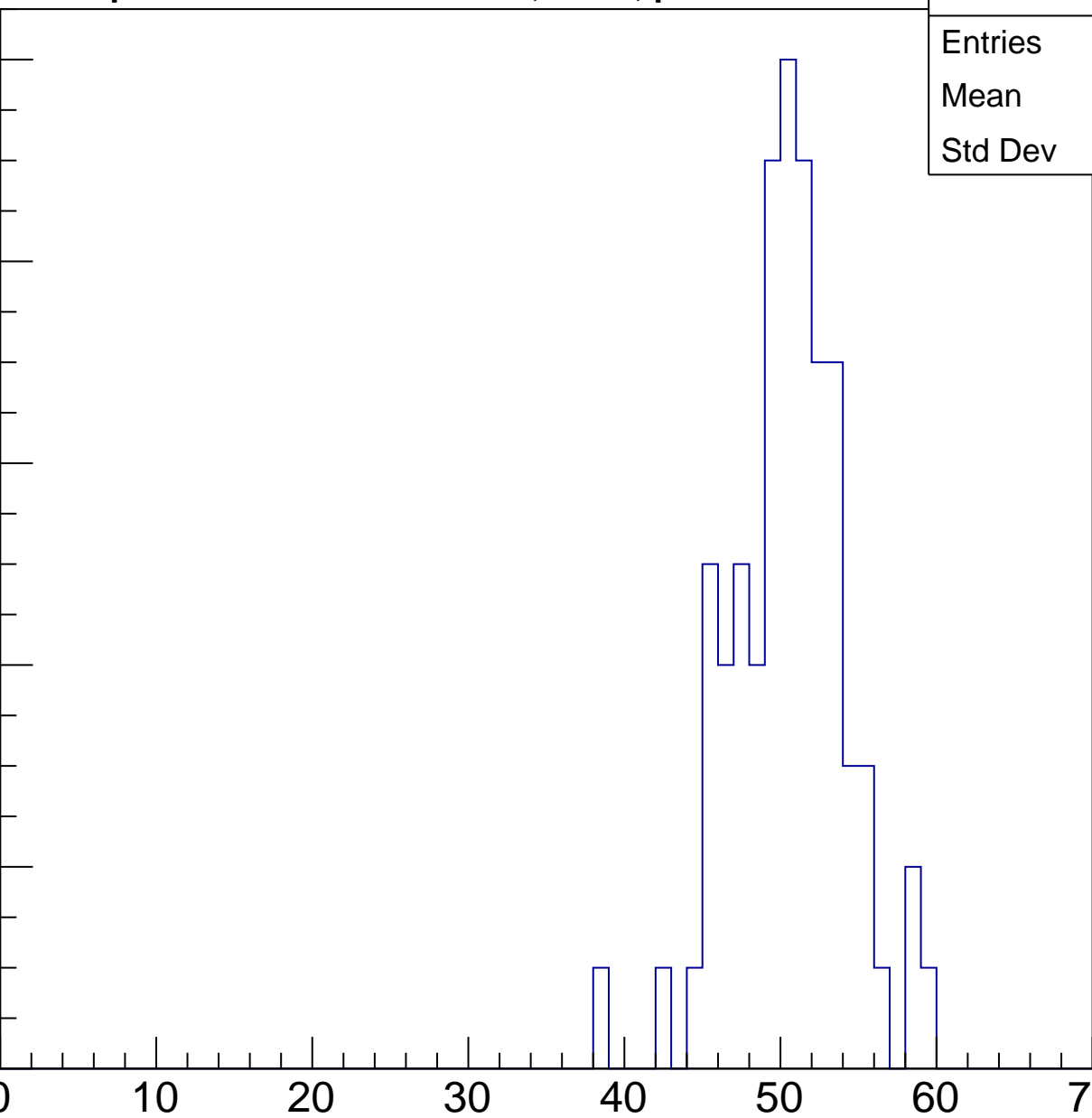
Entries	73
Mean	50.04
Std Dev	3.647

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

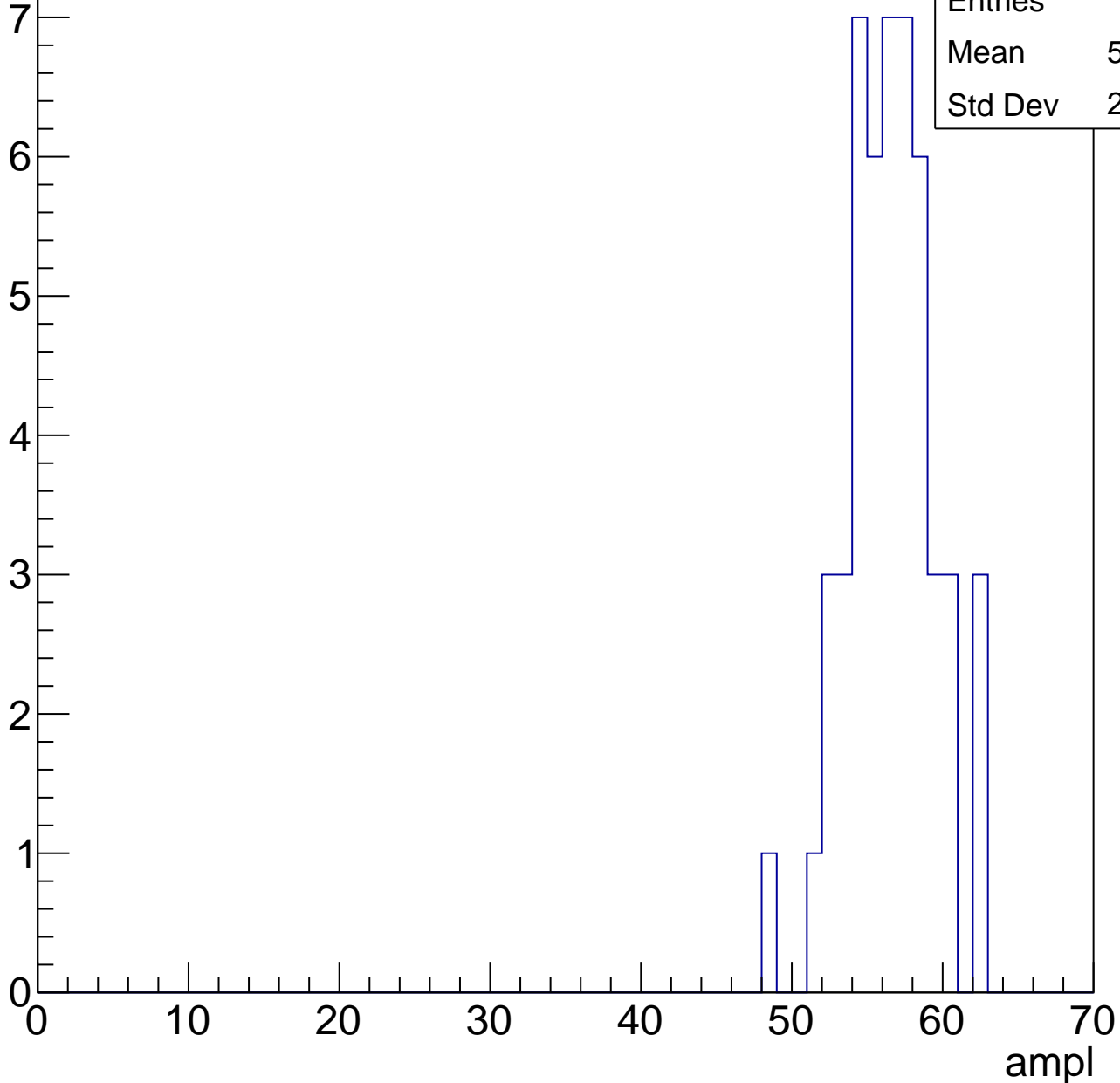


B0L001S, U19-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	56.08
Std Dev	2.869



B0L001S, U19-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	47
Mean	59.06
Std Dev	8.952

Entry

10

8

6

4

2

0

0

10

20

30

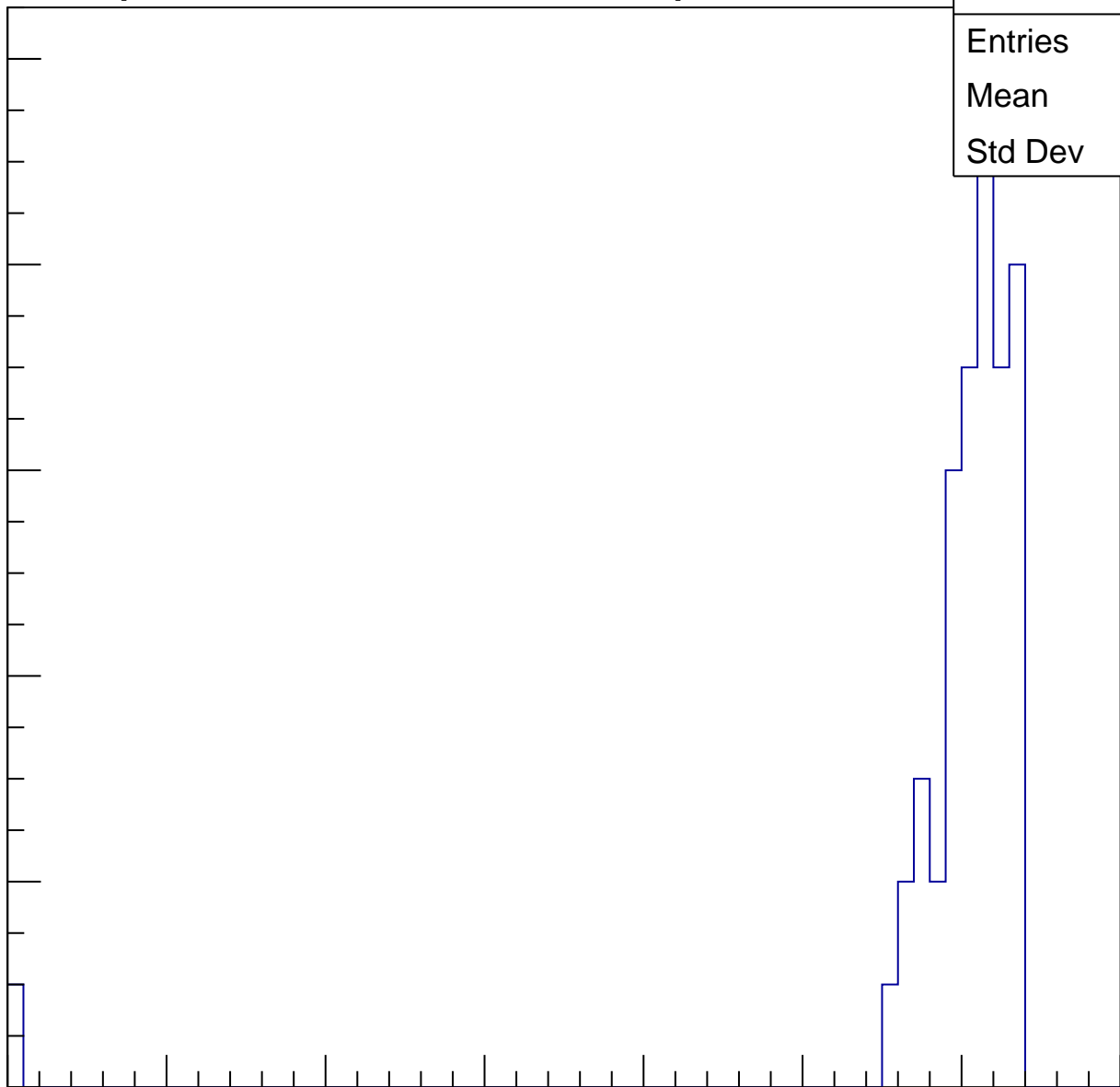
40

50

60

70

ampl



B0L001S, U19-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

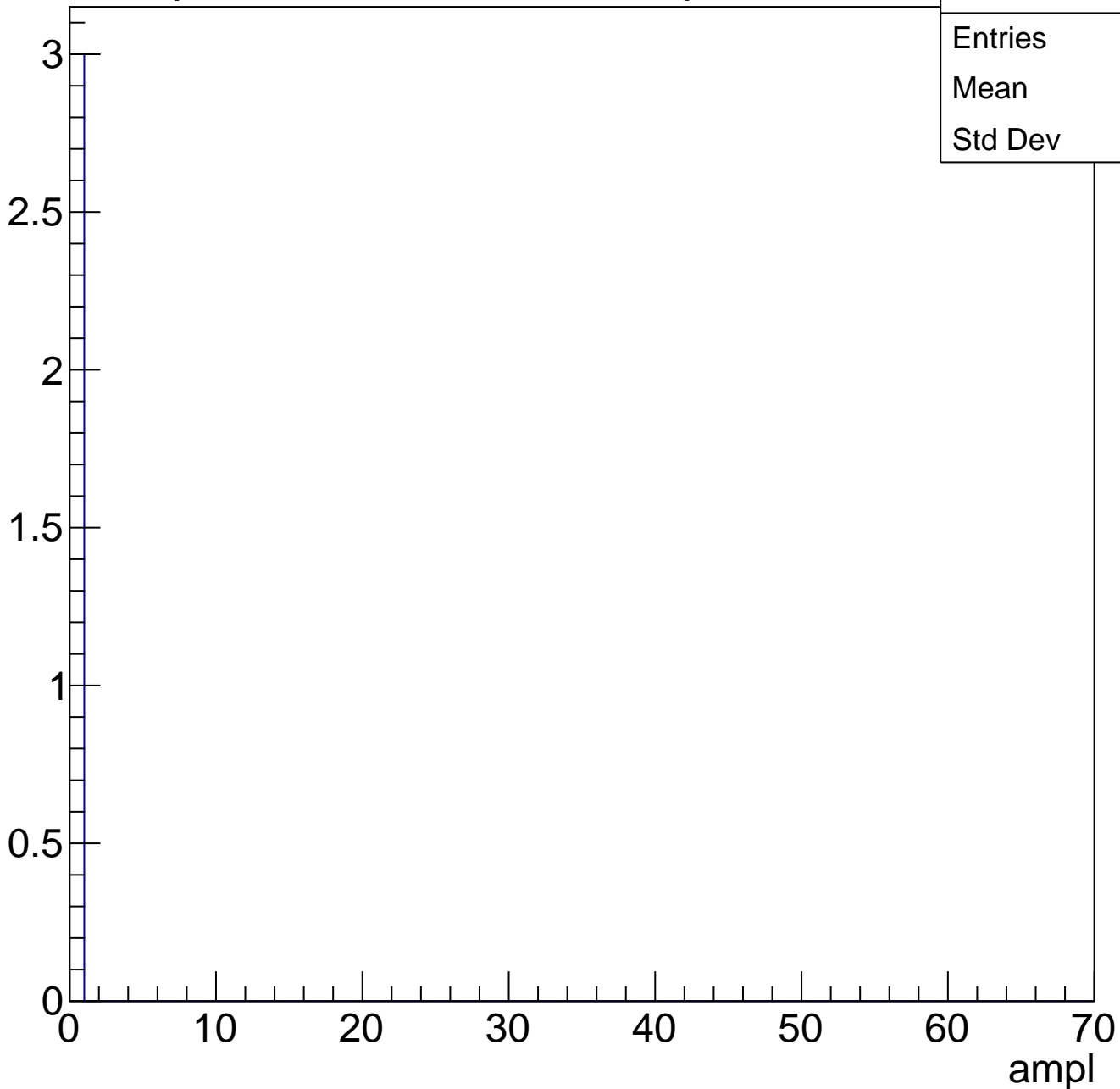
Entry



B0L001S, U19-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U19-ch119, adc0

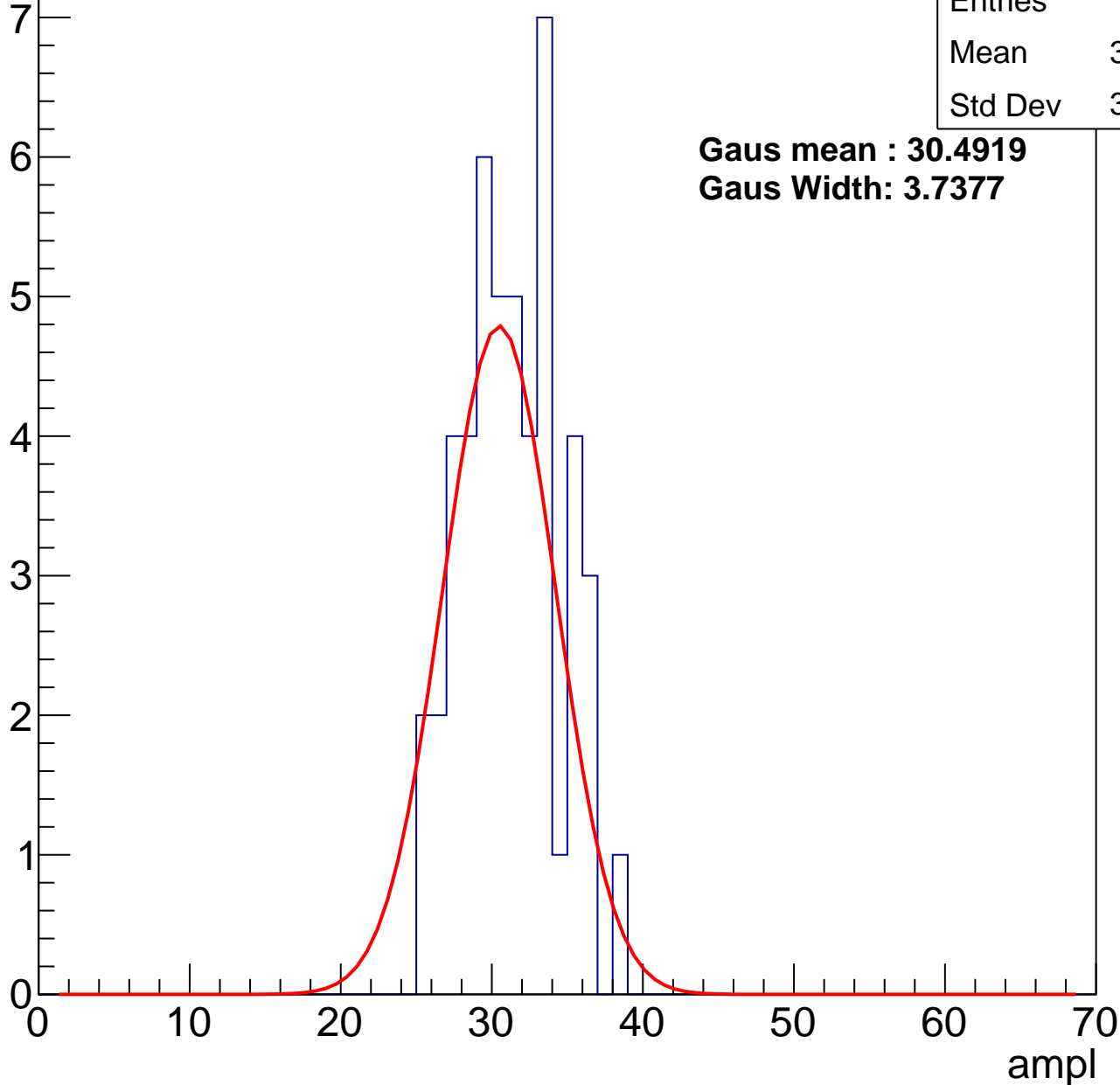
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	30.83
Std Dev	3.158

Gaus mean : 30.4919

Gaus Width: 3.7377



B0L001S, U19-ch119, adc1

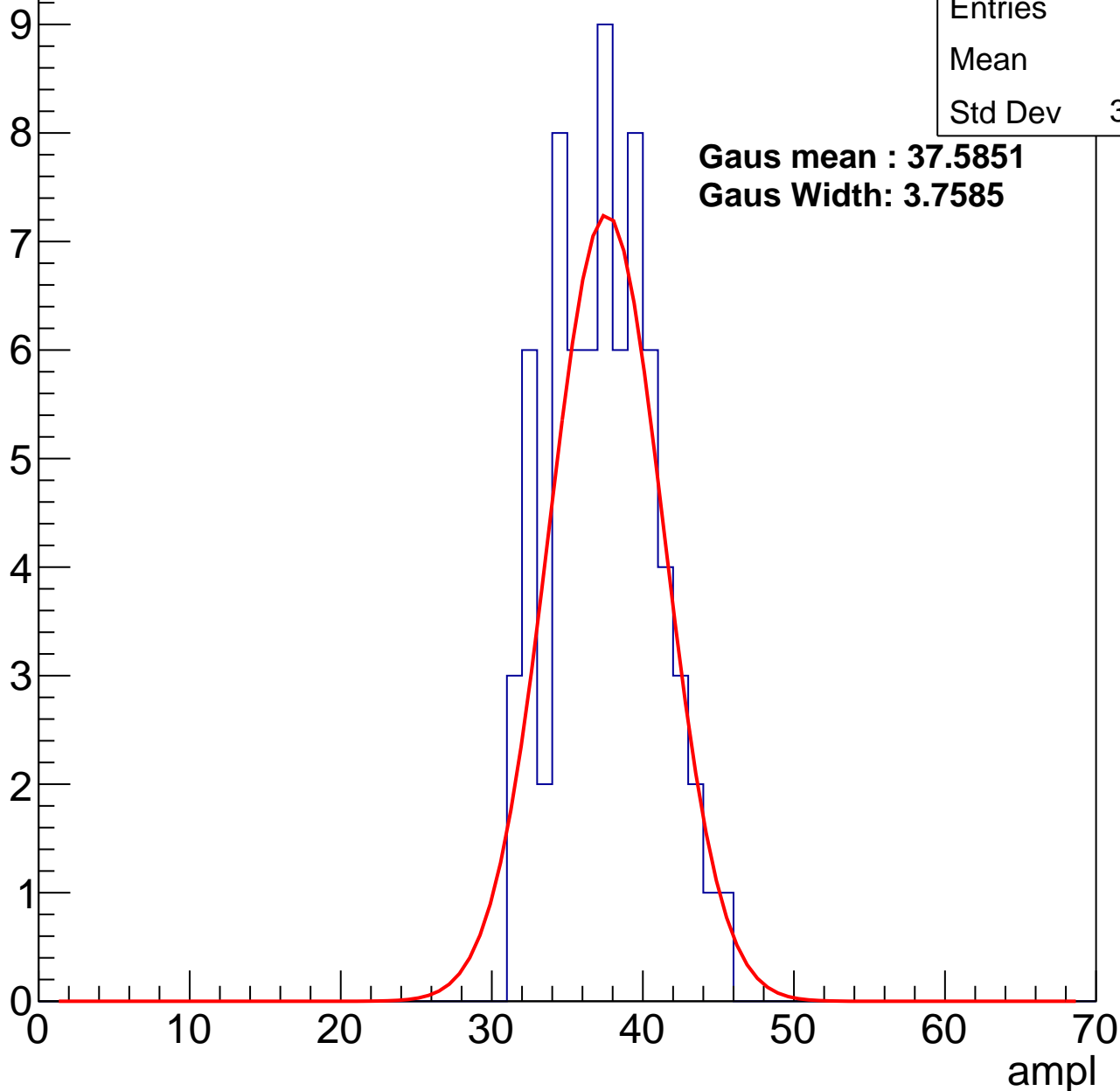
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37
Std Dev	3.373

Gaus mean : 37.5851

Gaus Width: 3.7585



B0L001S, U19-ch119, adc2

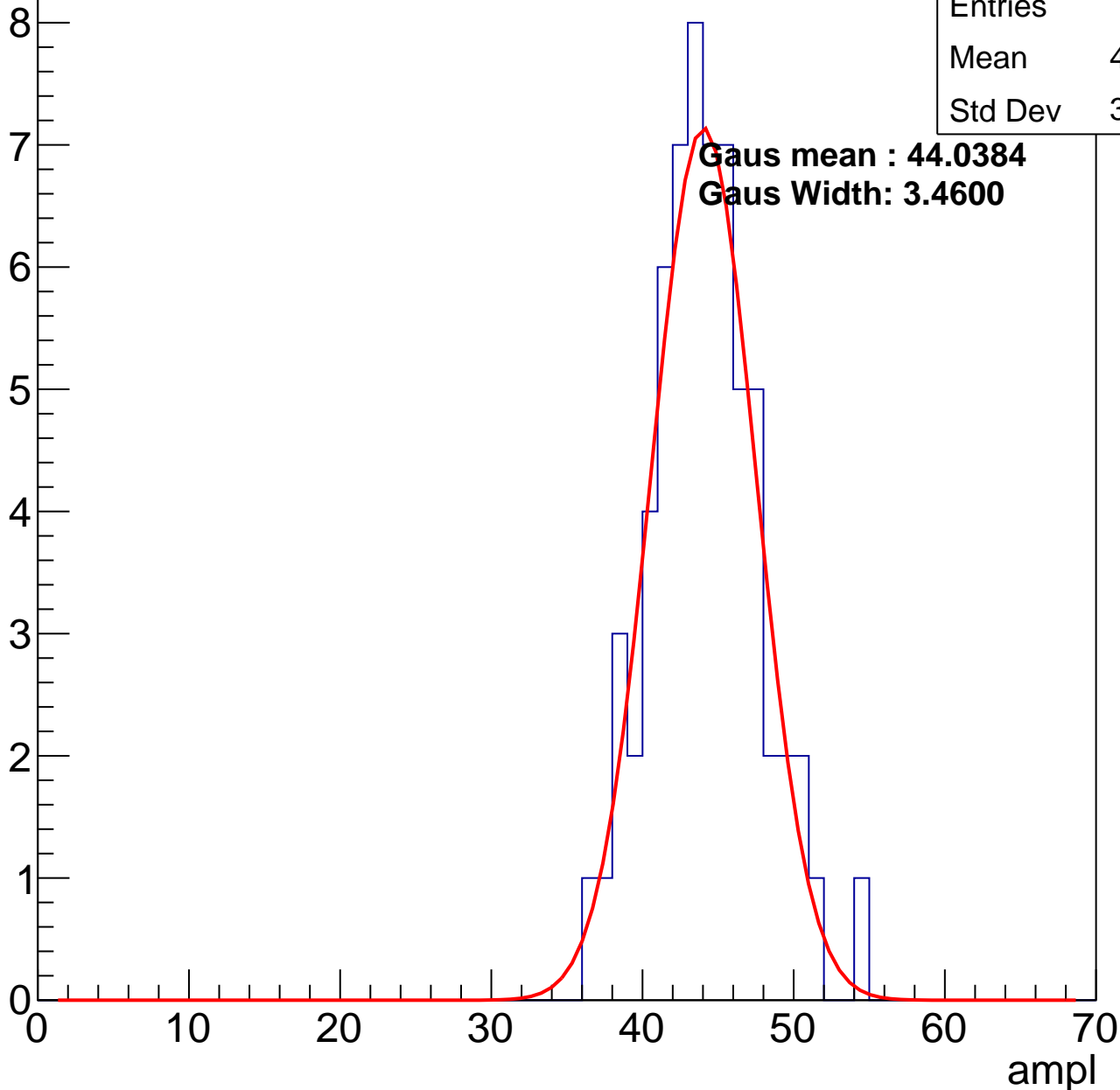
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	43.69
Std Dev	3.522

Gaus mean : 44.0384

Gaus Width: 3.4600

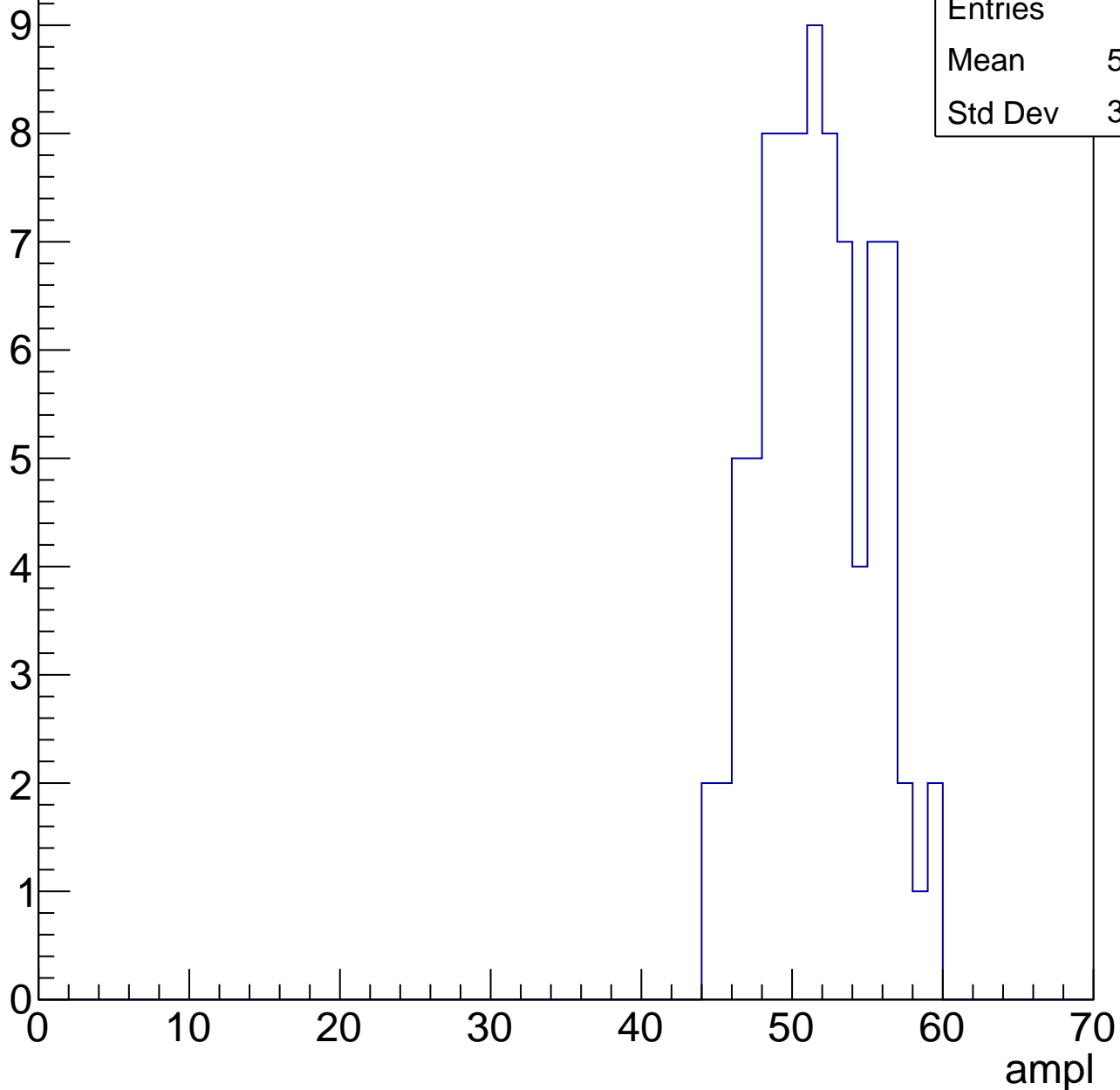


B0L001S, U19-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	51.15
Std Dev	3.586

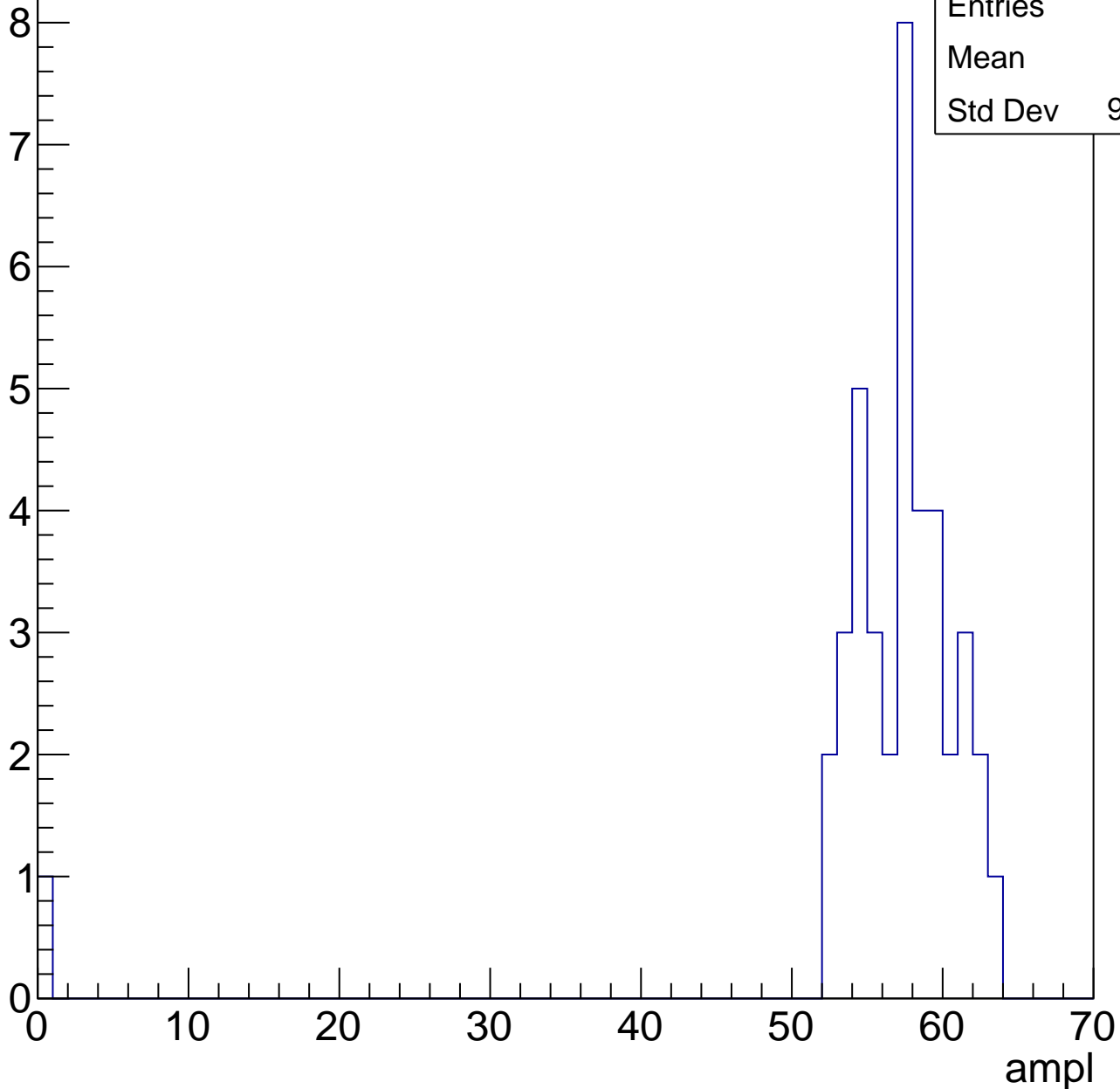


B0L001S, U19-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	55.6
Std Dev	9.354



B0L001S, U19-ch119, adc5

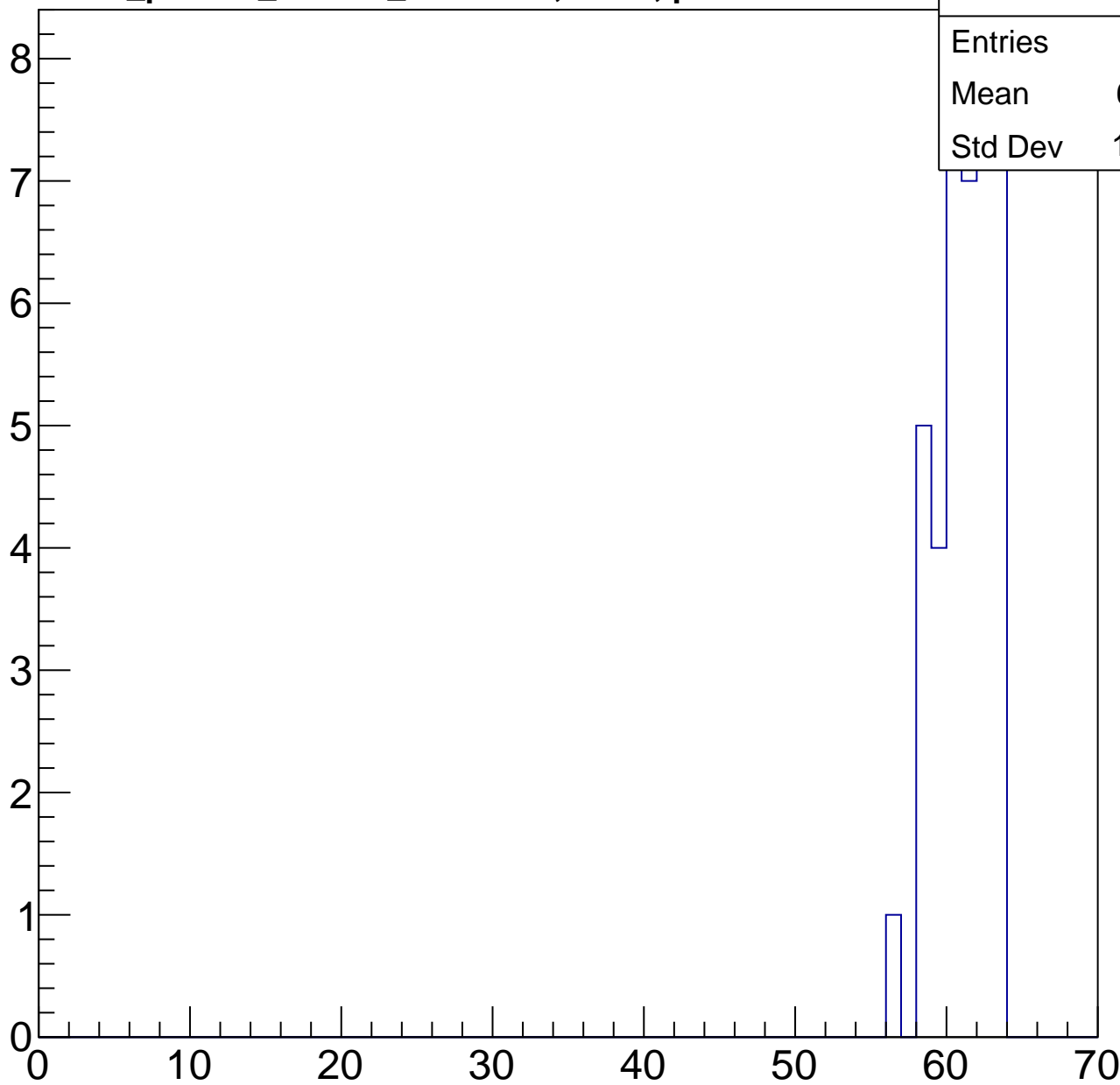
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.71
Std Dev	1.784

ampl



B0L001S, U19-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L001S, U19-ch120, adc0

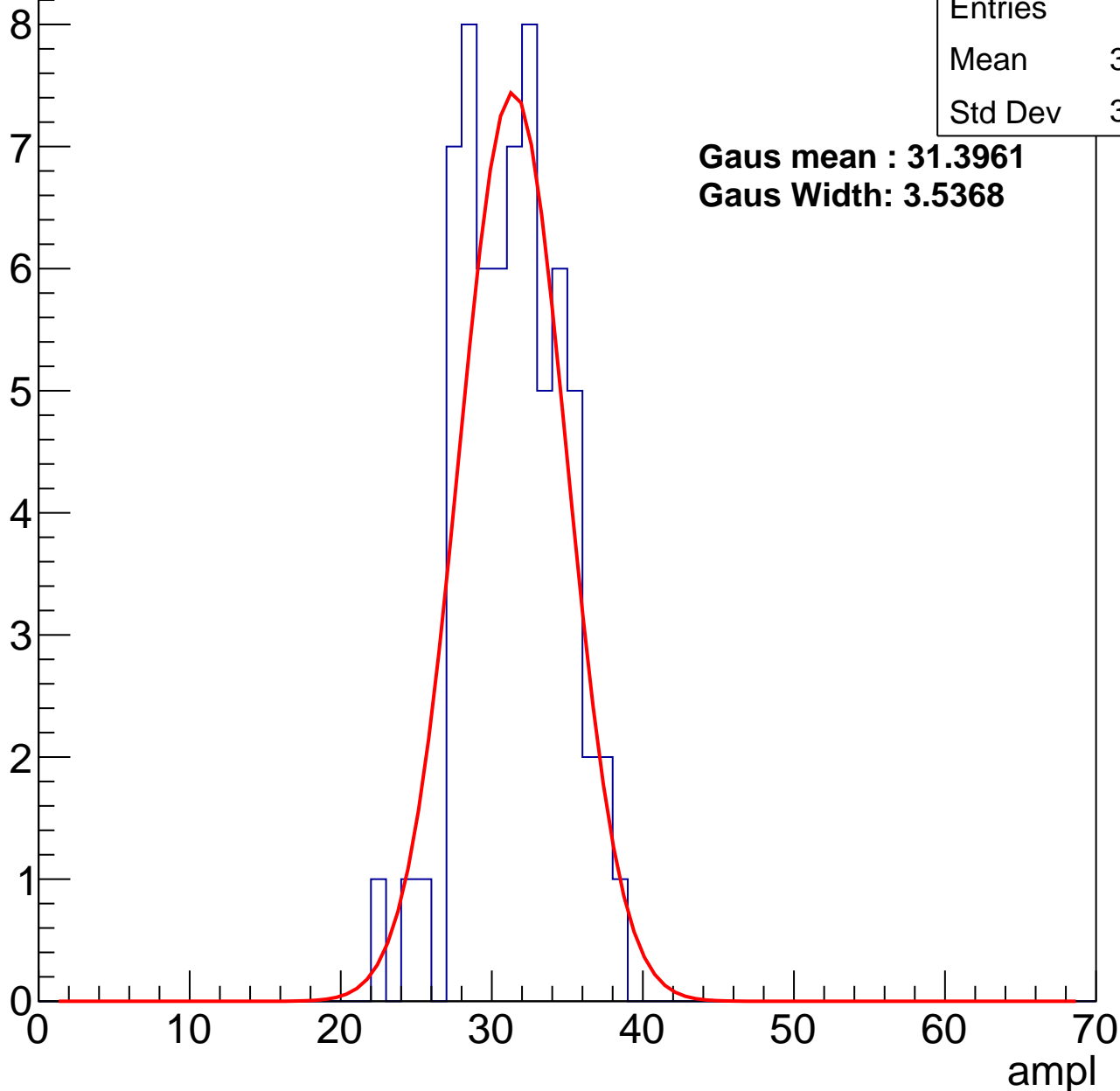
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.89
Std Dev	3.285

Gaus mean : 31.3961

Gaus Width: 3.5368



B0L001S, U19-ch120, adc1

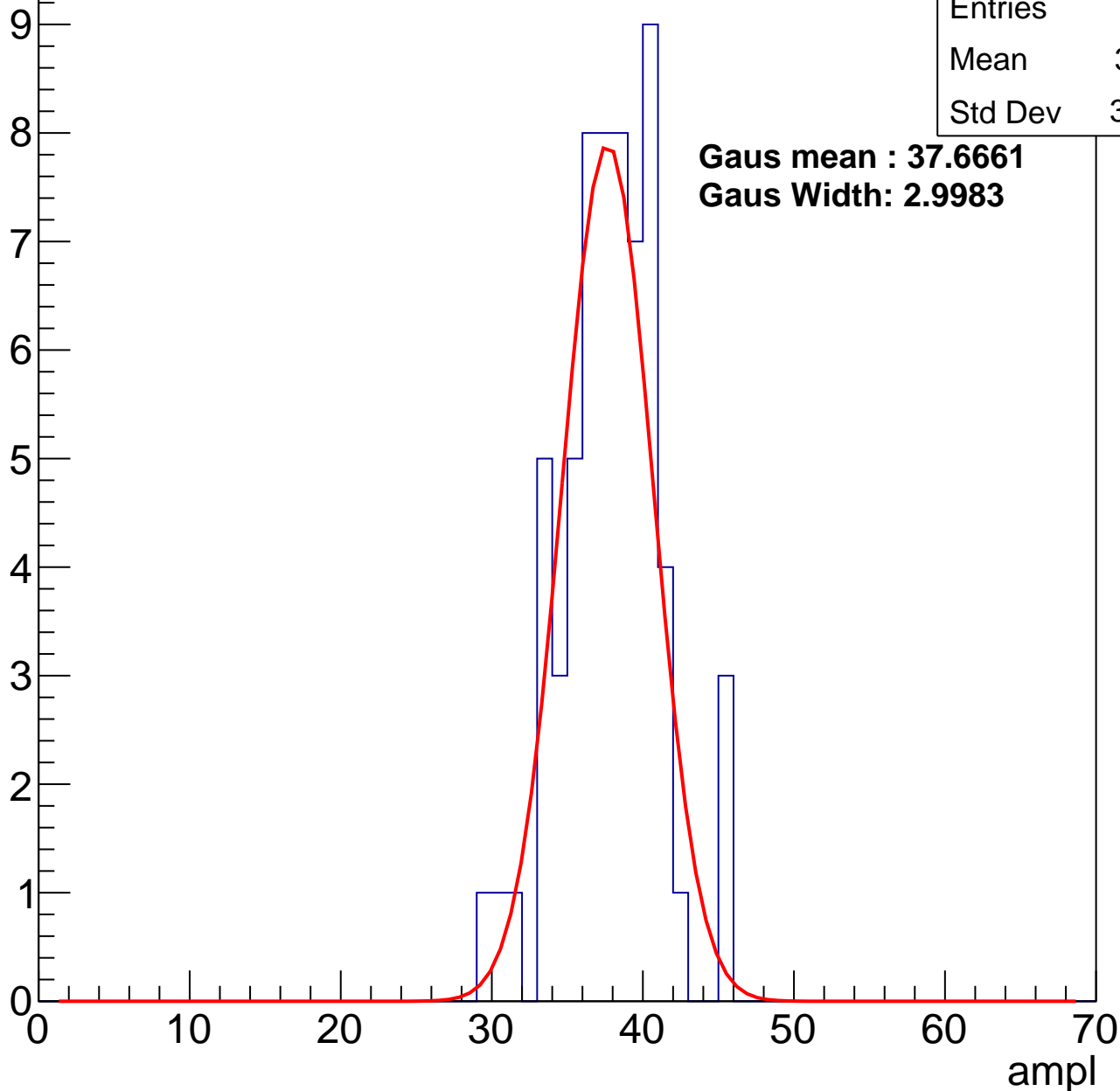
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.41
Std Dev	3.239

Gaus mean : 37.6661

Gaus Width: 2.9983



B0L001S, U19-ch120, adc2

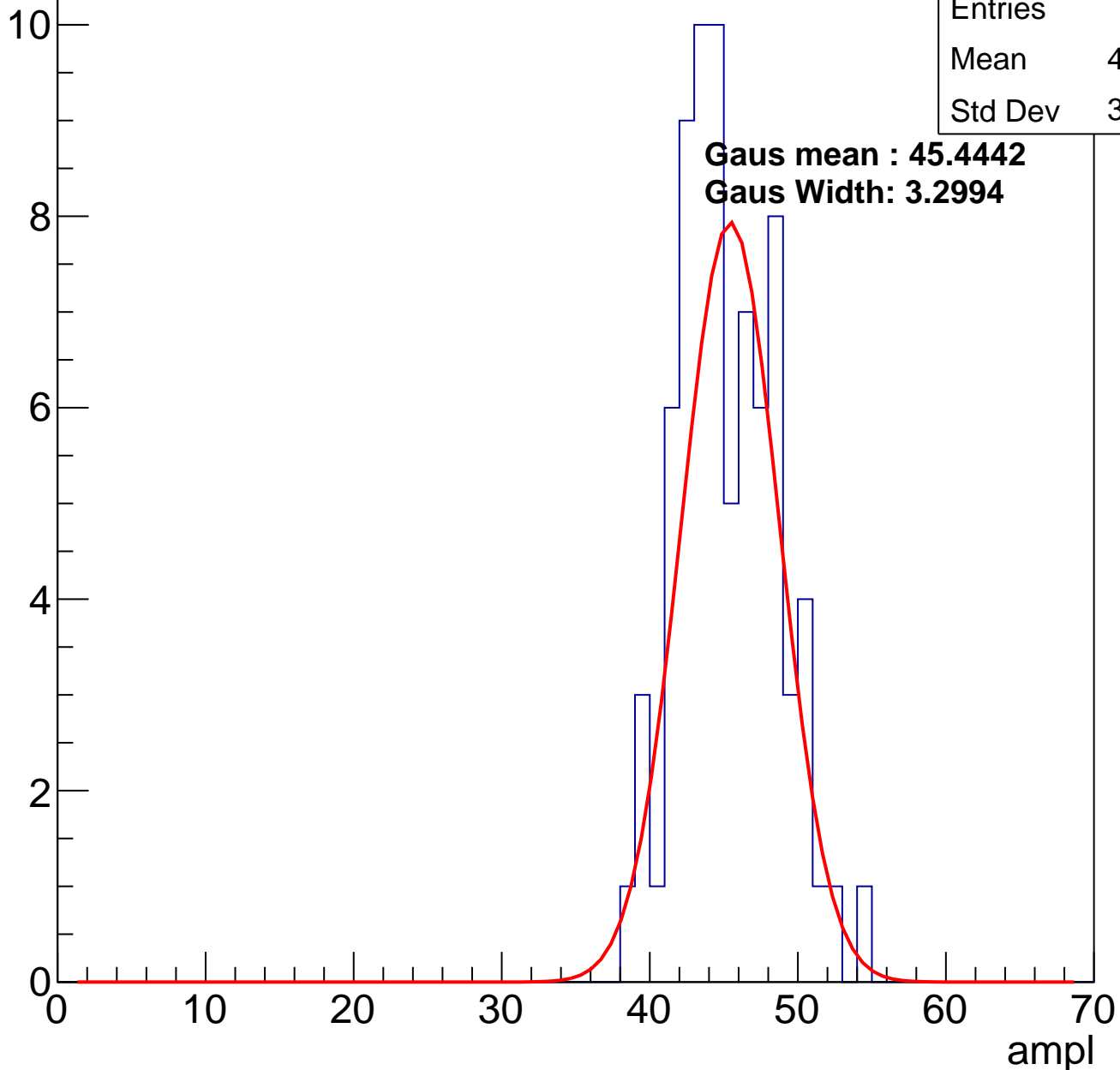
calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	44.82
Std Dev	3.292

Gaus mean : 45.4442

Gaus Width: 3.2994

Entry

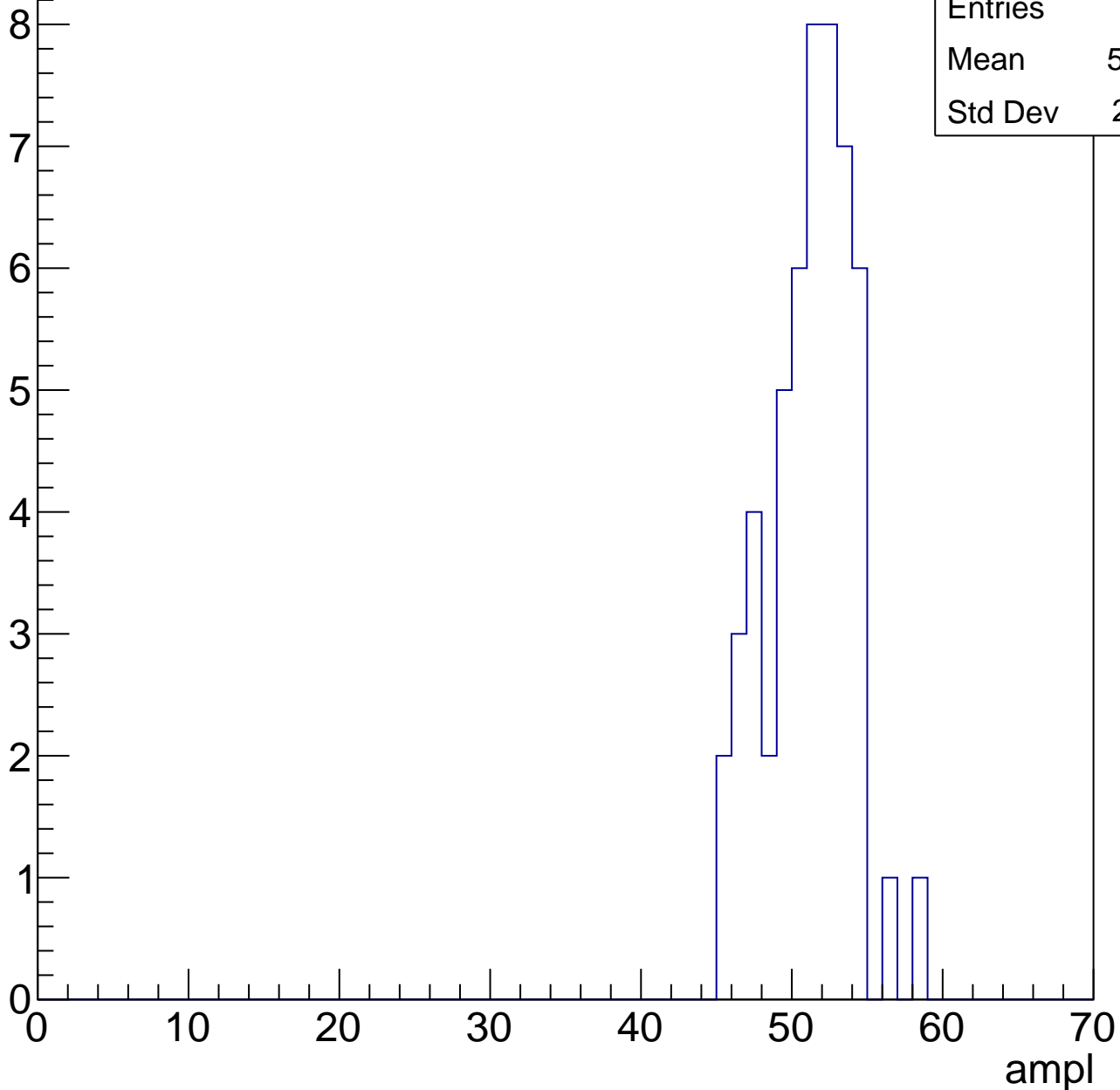


B0L001S, U19-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

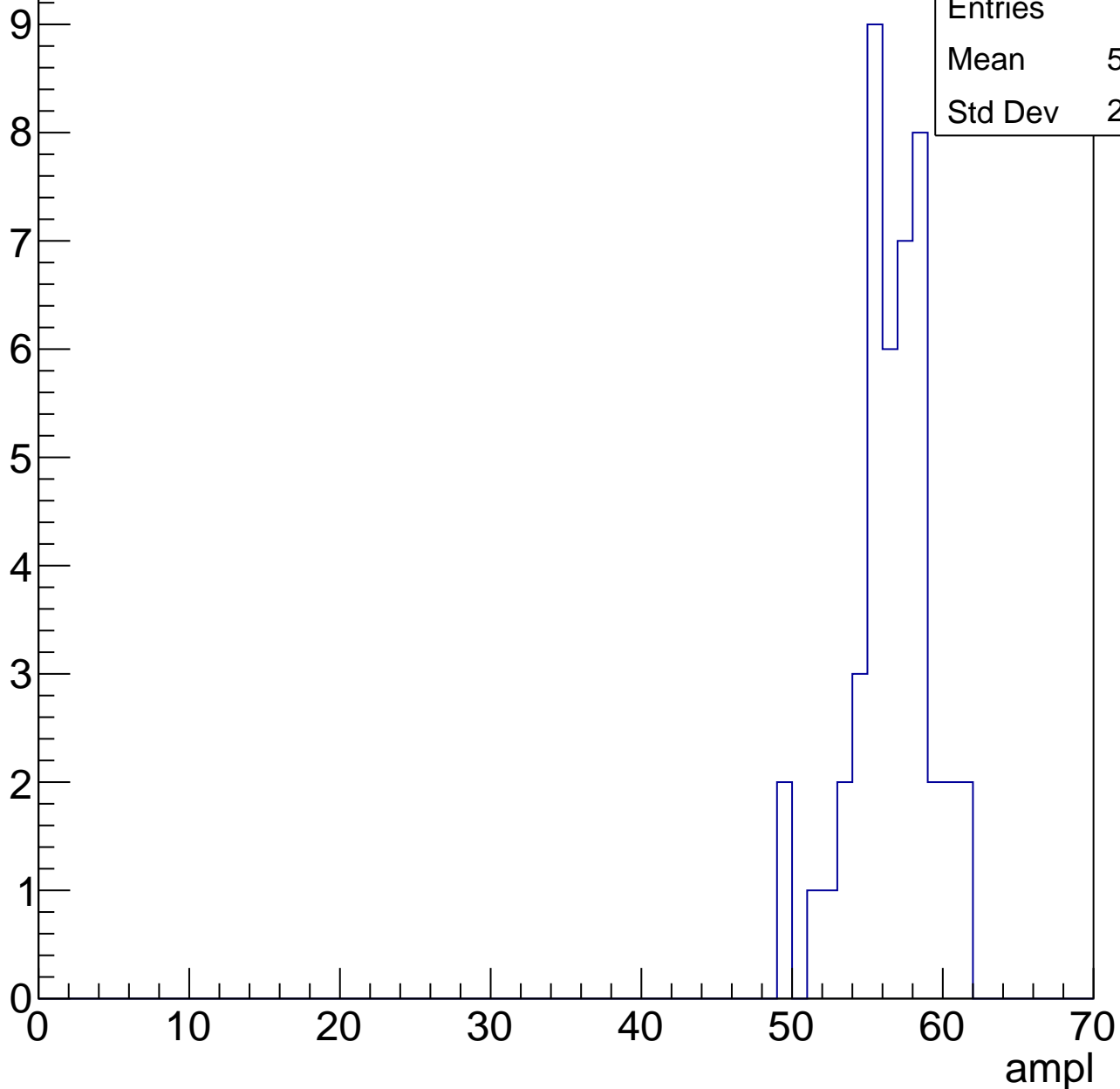
Entries	53
Mean	50.75
Std Dev	2.801



B0L001S, U19-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



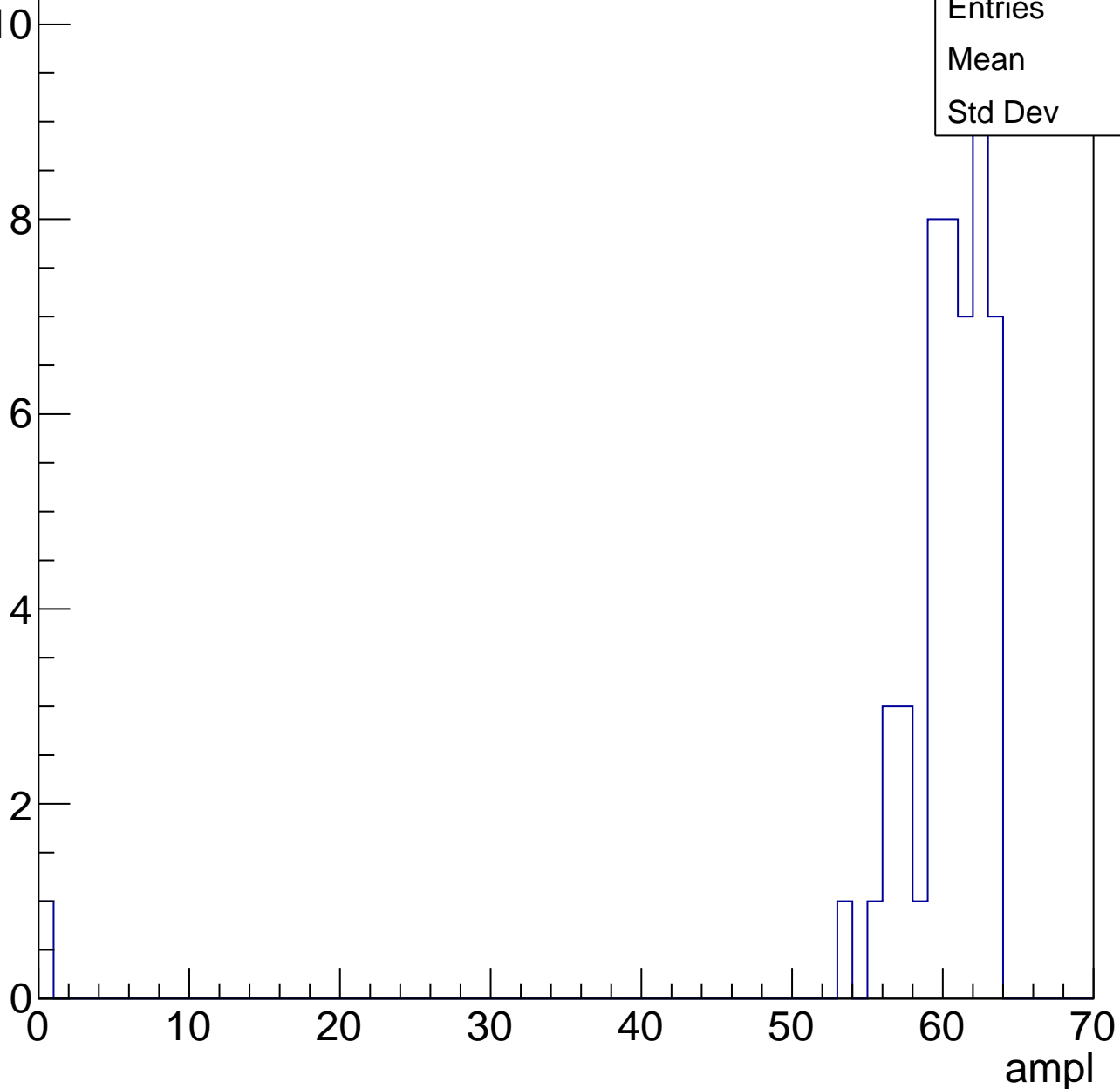
Entries	45
Mean	56.07
Std Dev	2.653

B0L001S, U19-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

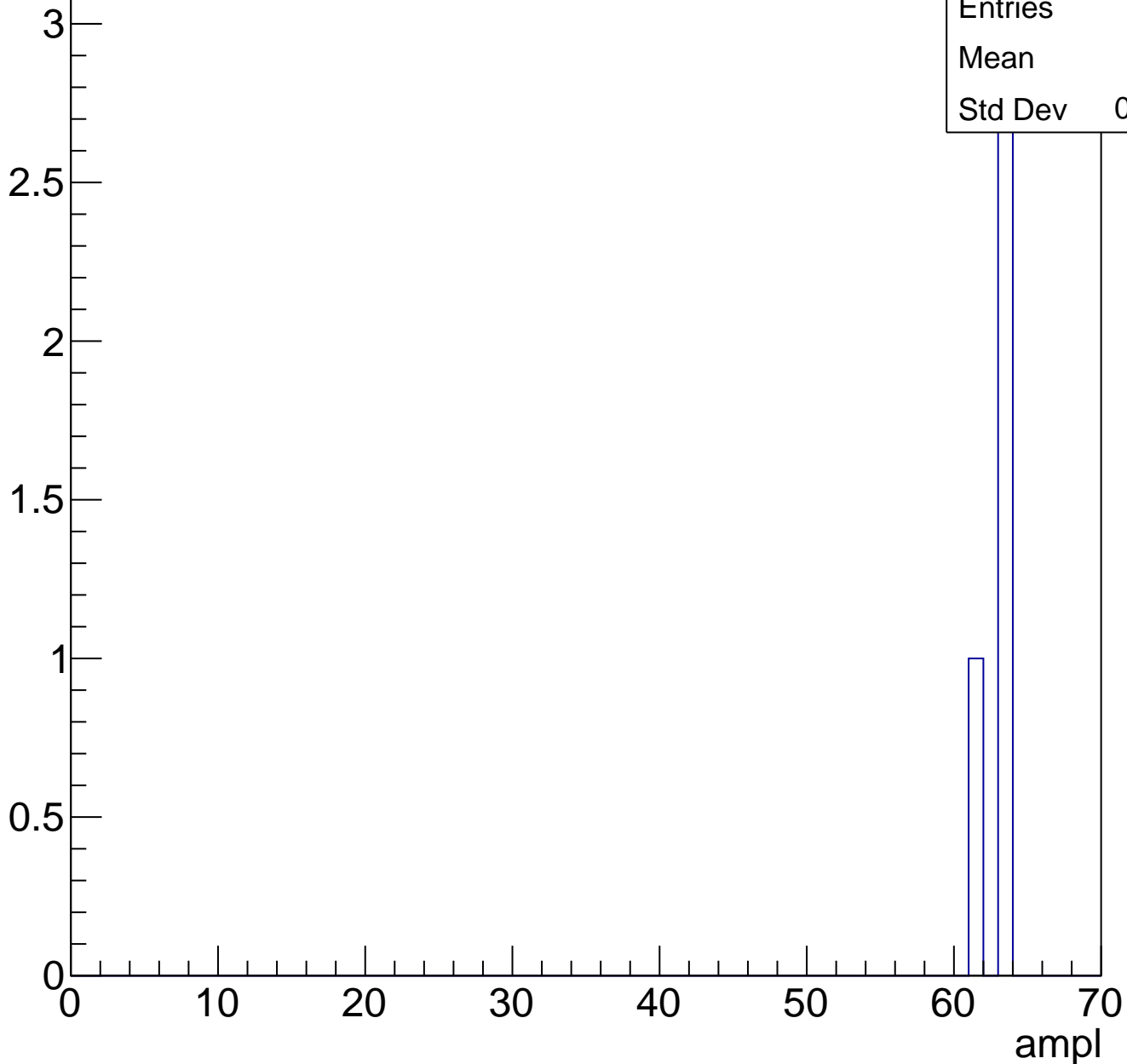
Entries	50
Mean	58.9
Std Dev	8.73



B0L001S, U19-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

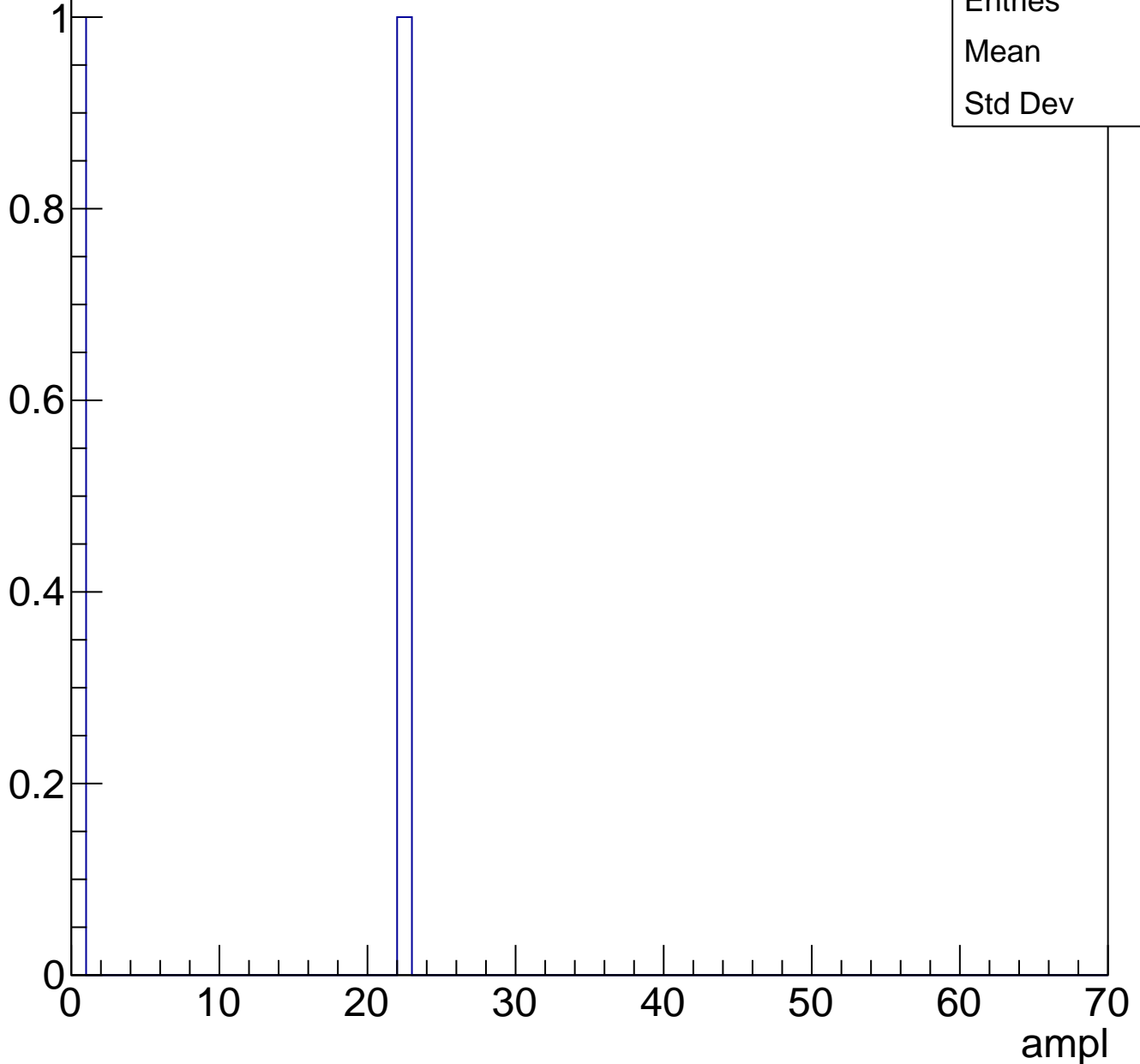
Entry



B0L001S, U19-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U19-ch121, adc0

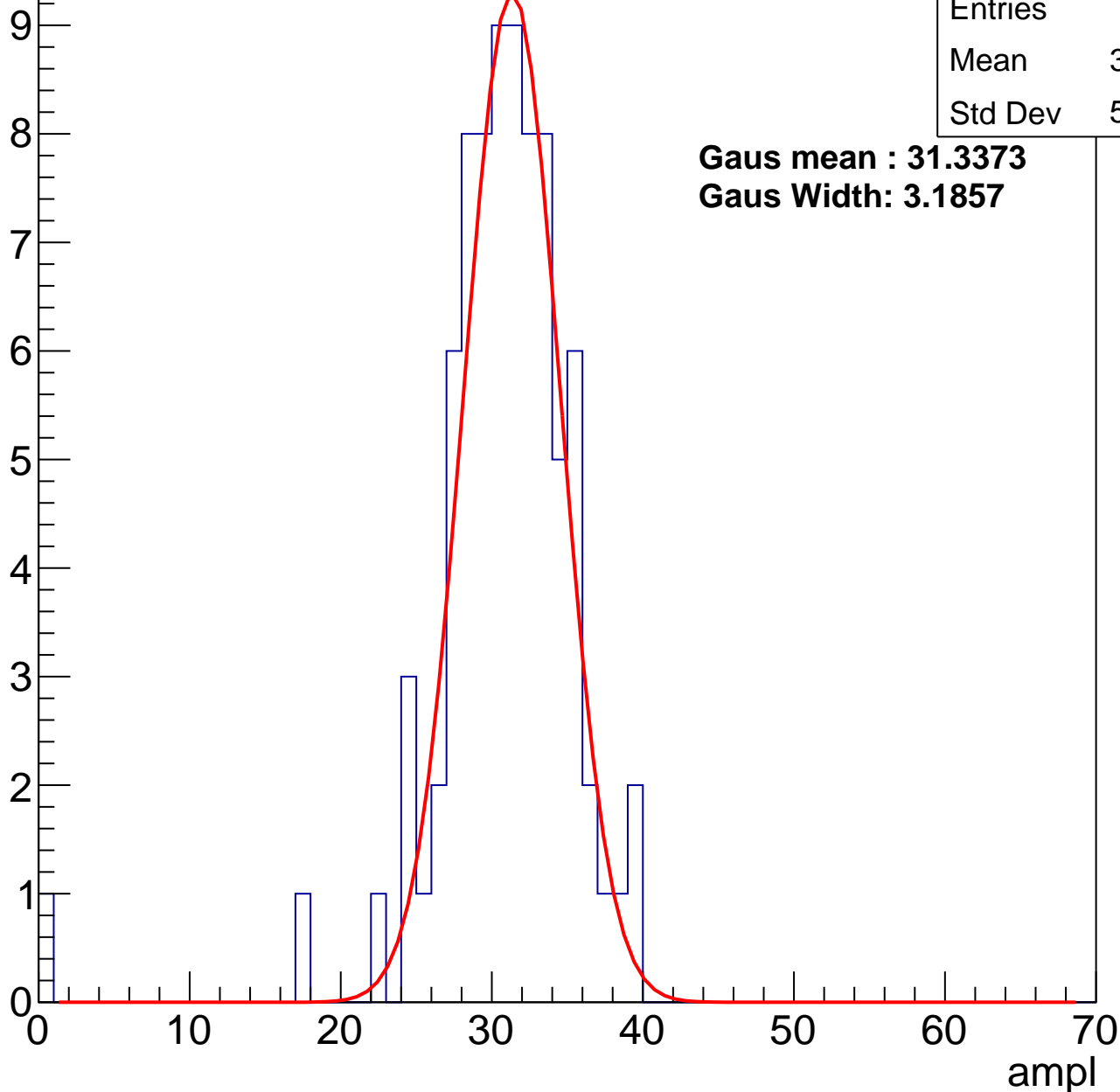
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	30.24
Std Dev	5.033

Gaus mean : 31.3373

Gaus Width: 3.1857



B0L001S, U19-ch121, adc1

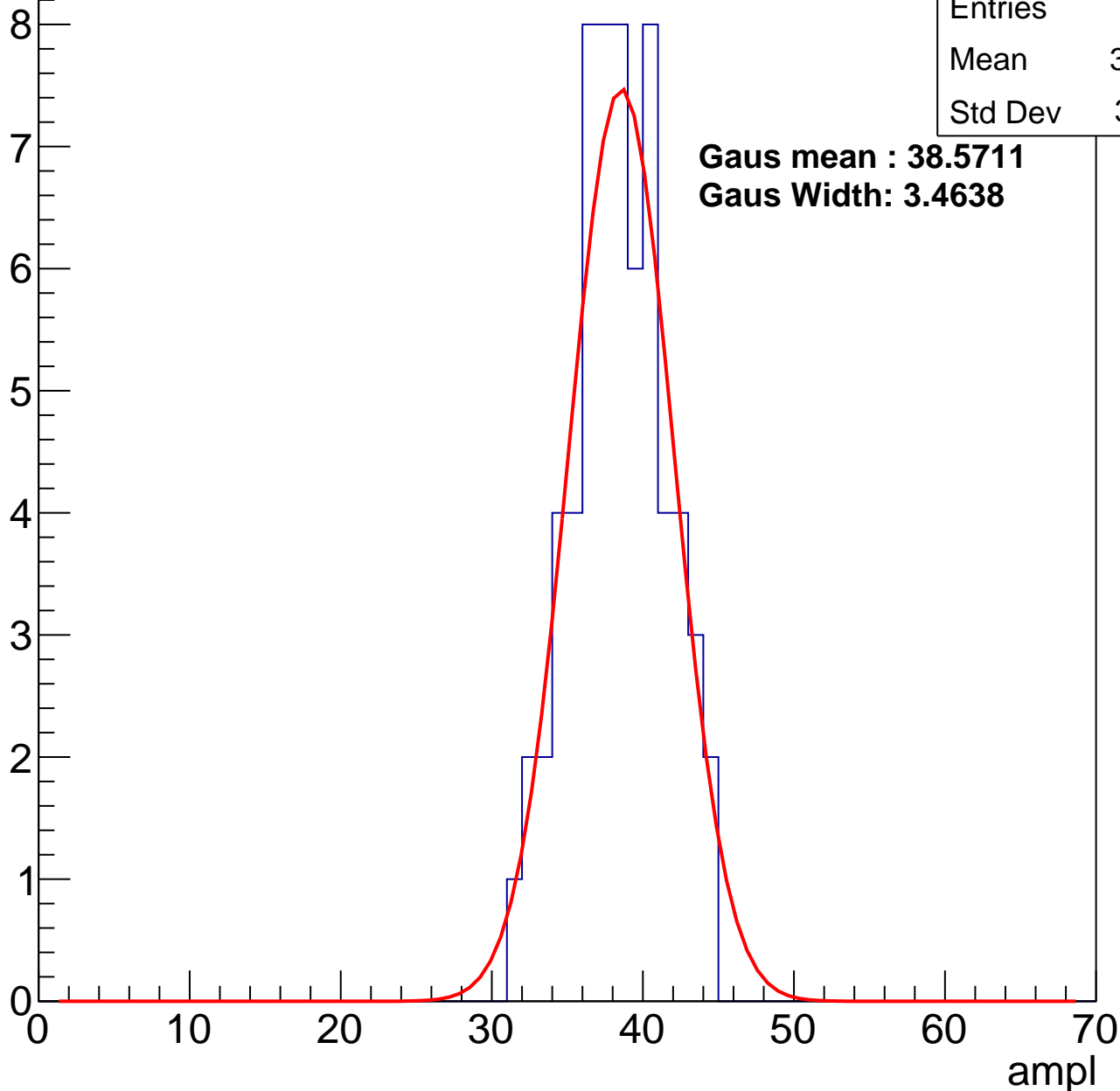
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.94
Std Dev	3.051

Gaus mean : 38.5711

Gaus Width: 3.4638



B0L001S, U19-ch121, adc2

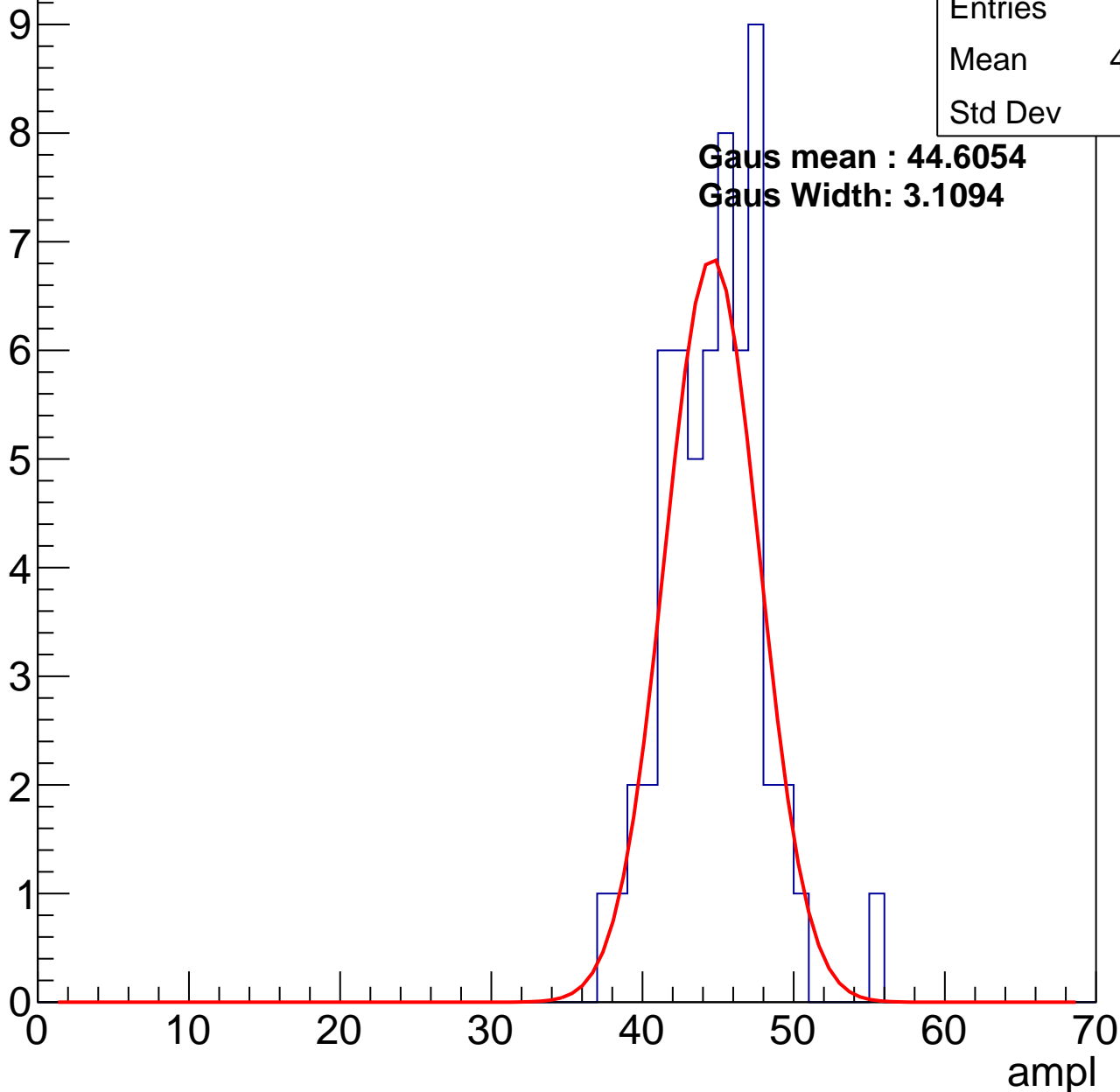
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.28
Std Dev	3.21

Gaus mean : 44.6054

Gaus Width: 3.1094

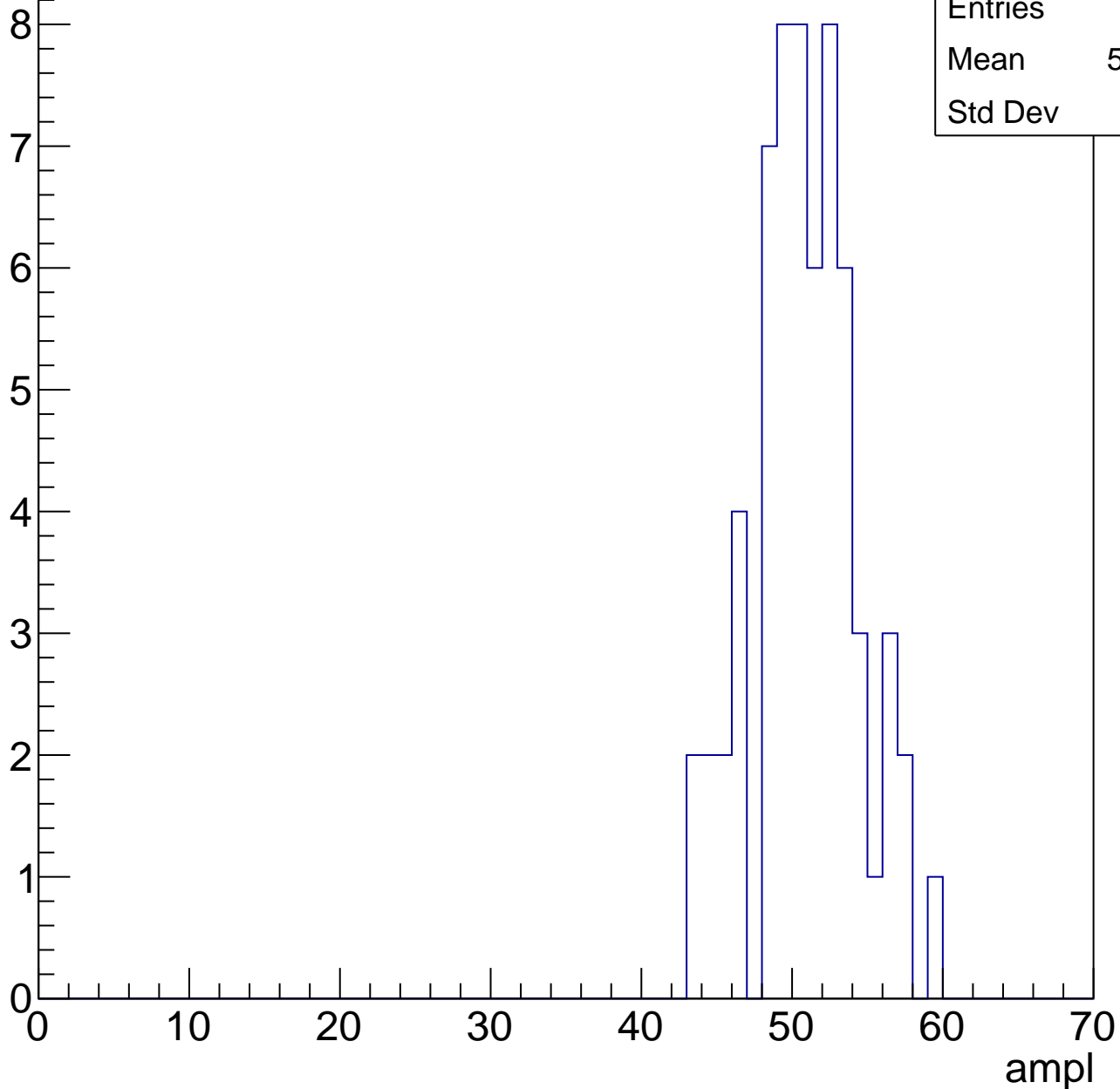


B0L001S, U19-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	50.38
Std Dev	3.48

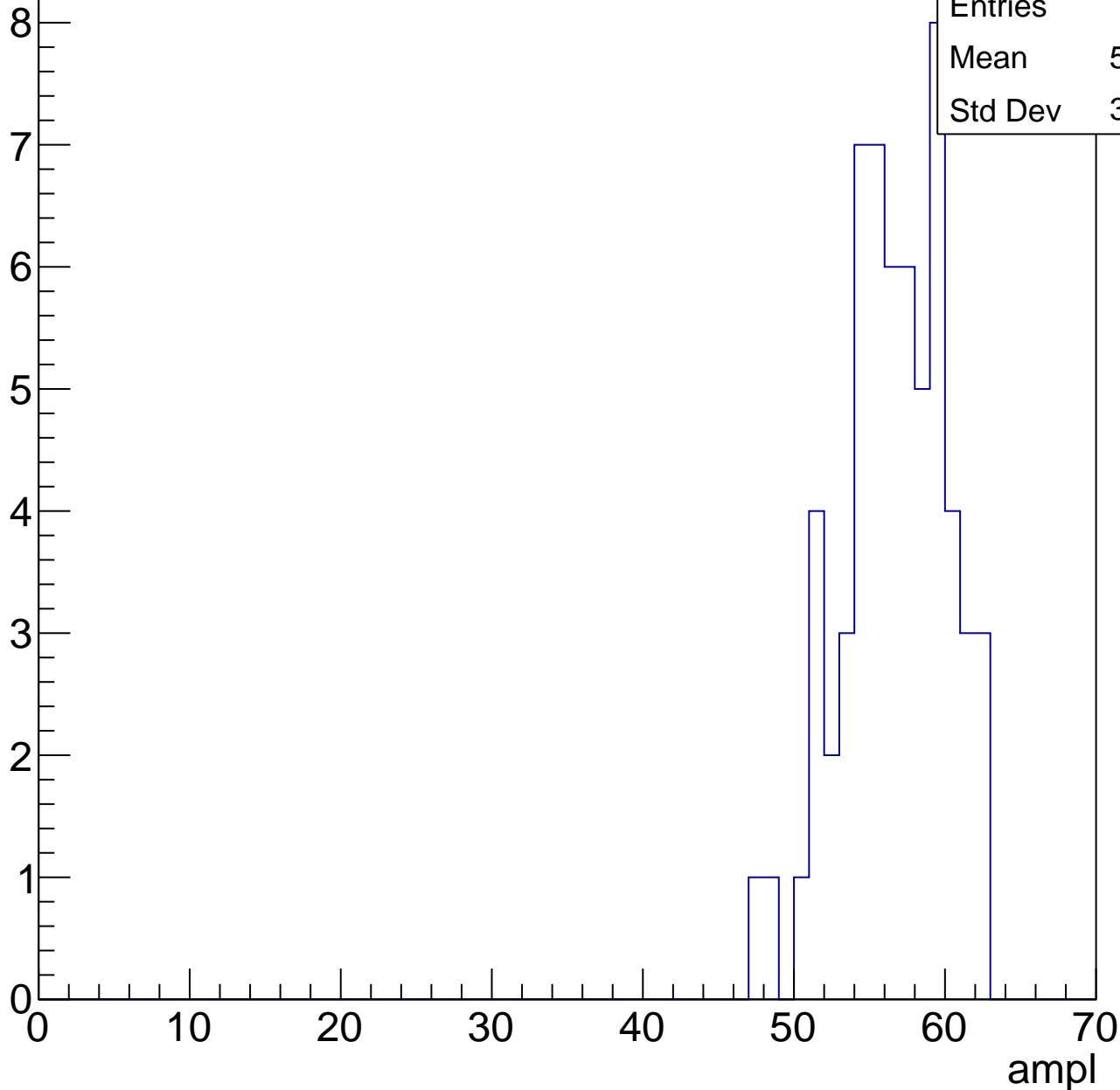


B0L001S, U19-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	56.13
Std Dev	3.433

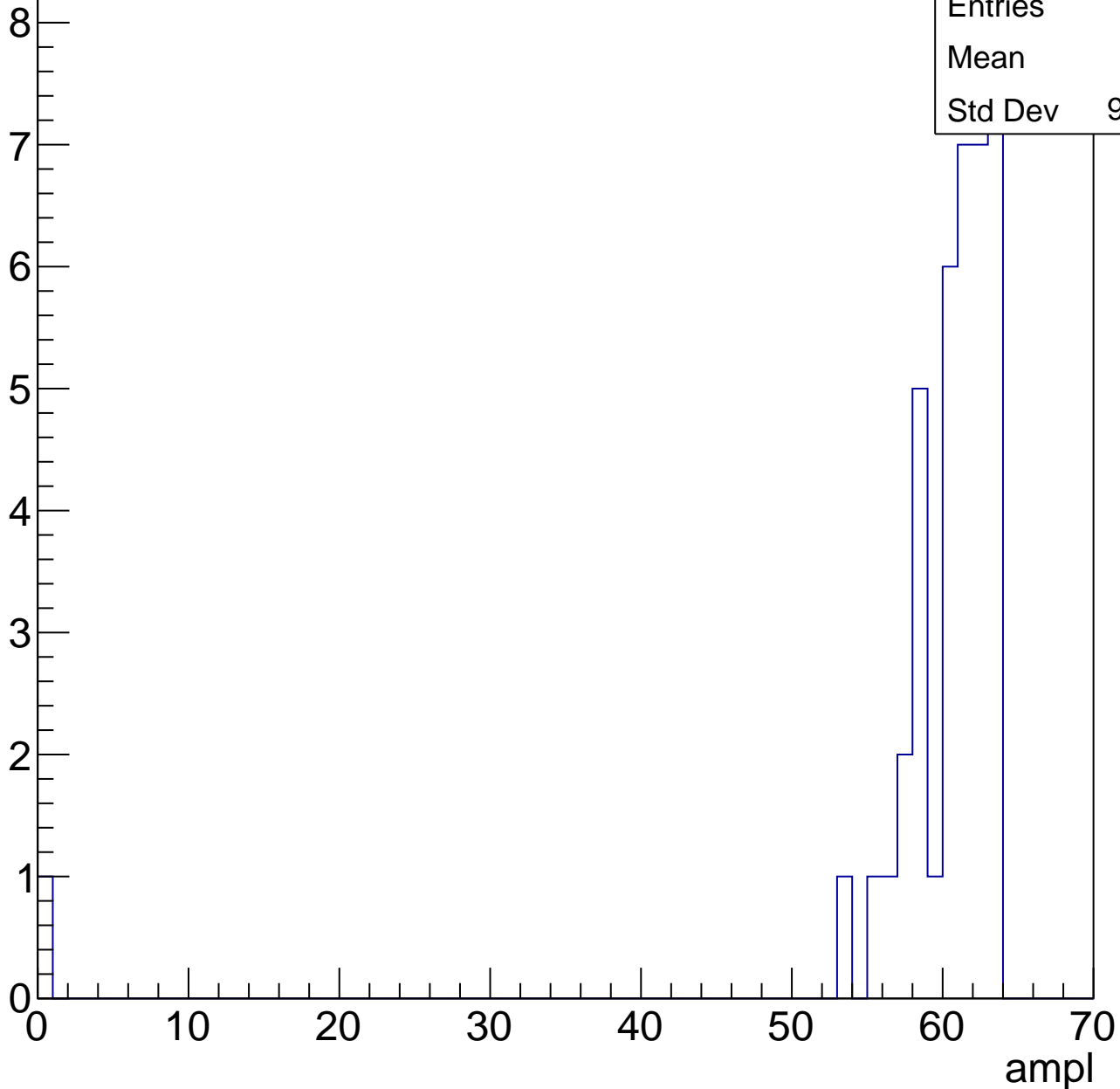


B0L001S, U19-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	58.8
Std Dev	9.719



B0L001S, U19-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L001S, U19-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch122, adc0

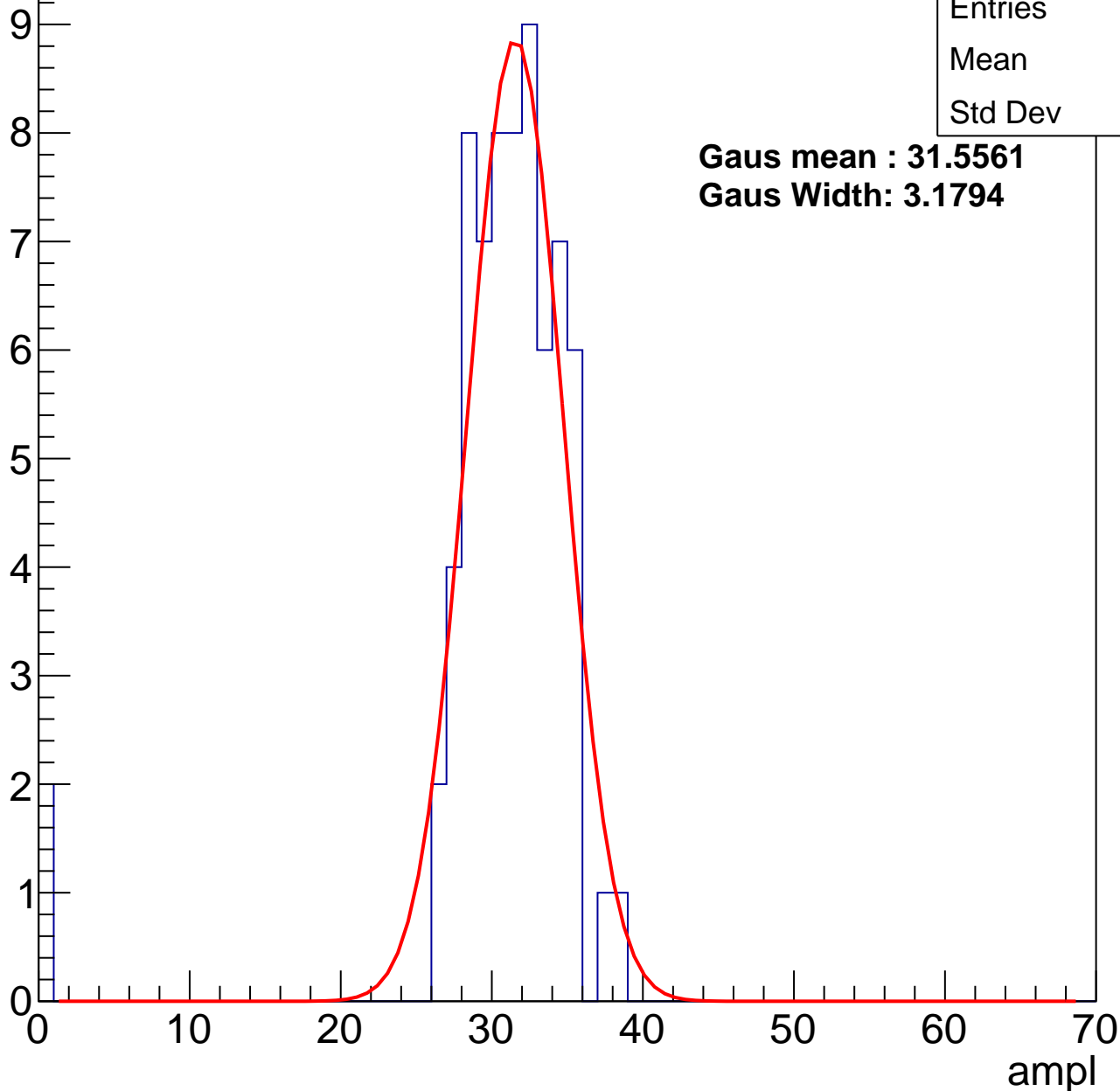
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.2
Std Dev	5.87

Gaus mean : 31.5561

Gaus Width: 3.1794



B0L001S, U19-ch122, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	38.26
Std Dev	3.197

Gaus mean : 38.9549

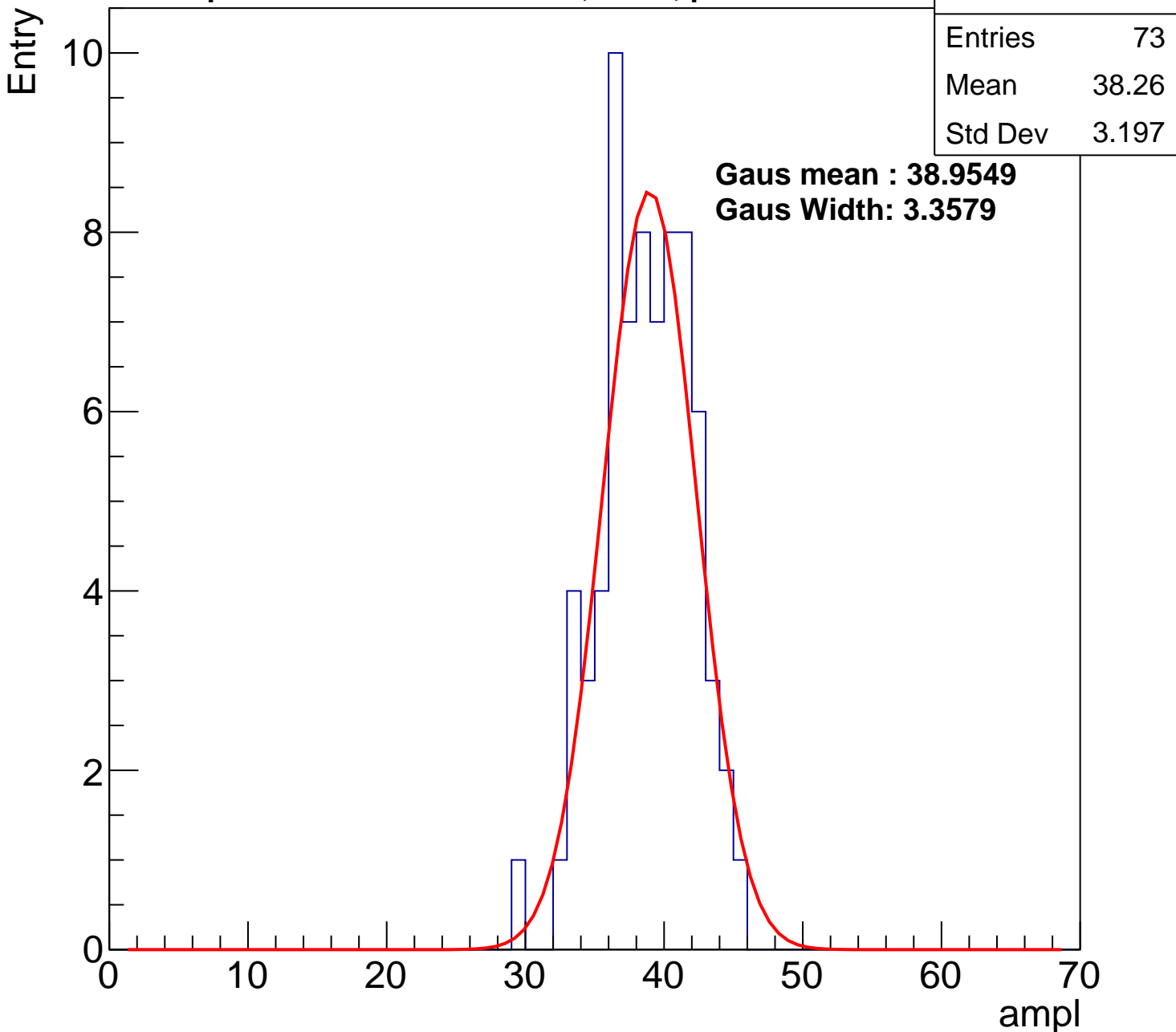
Gaus Width: 3.3579

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch122, adc2

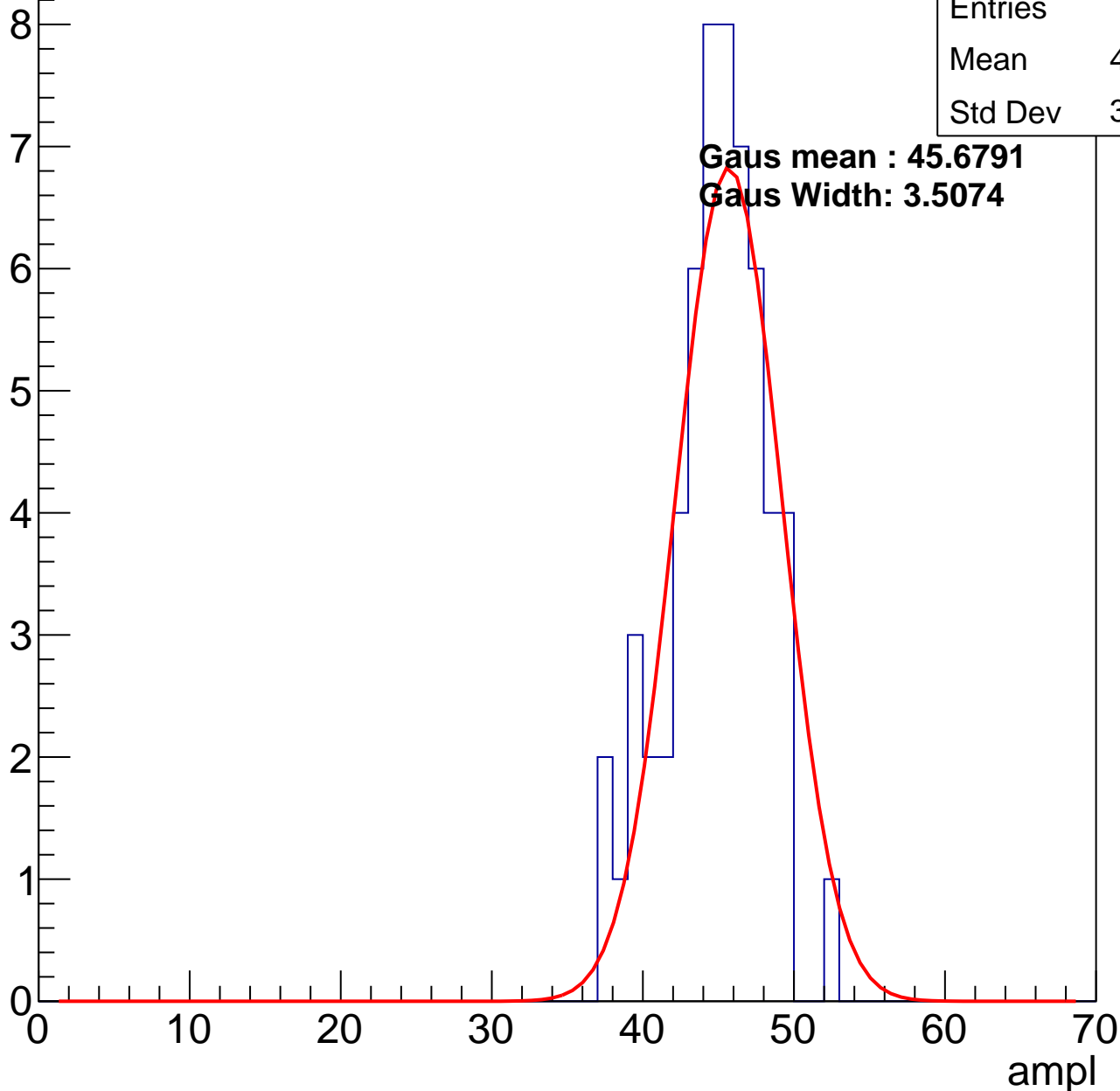
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.36
Std Dev	3.193

Gaus mean : 45.6791

Gaus Width: 3.5074

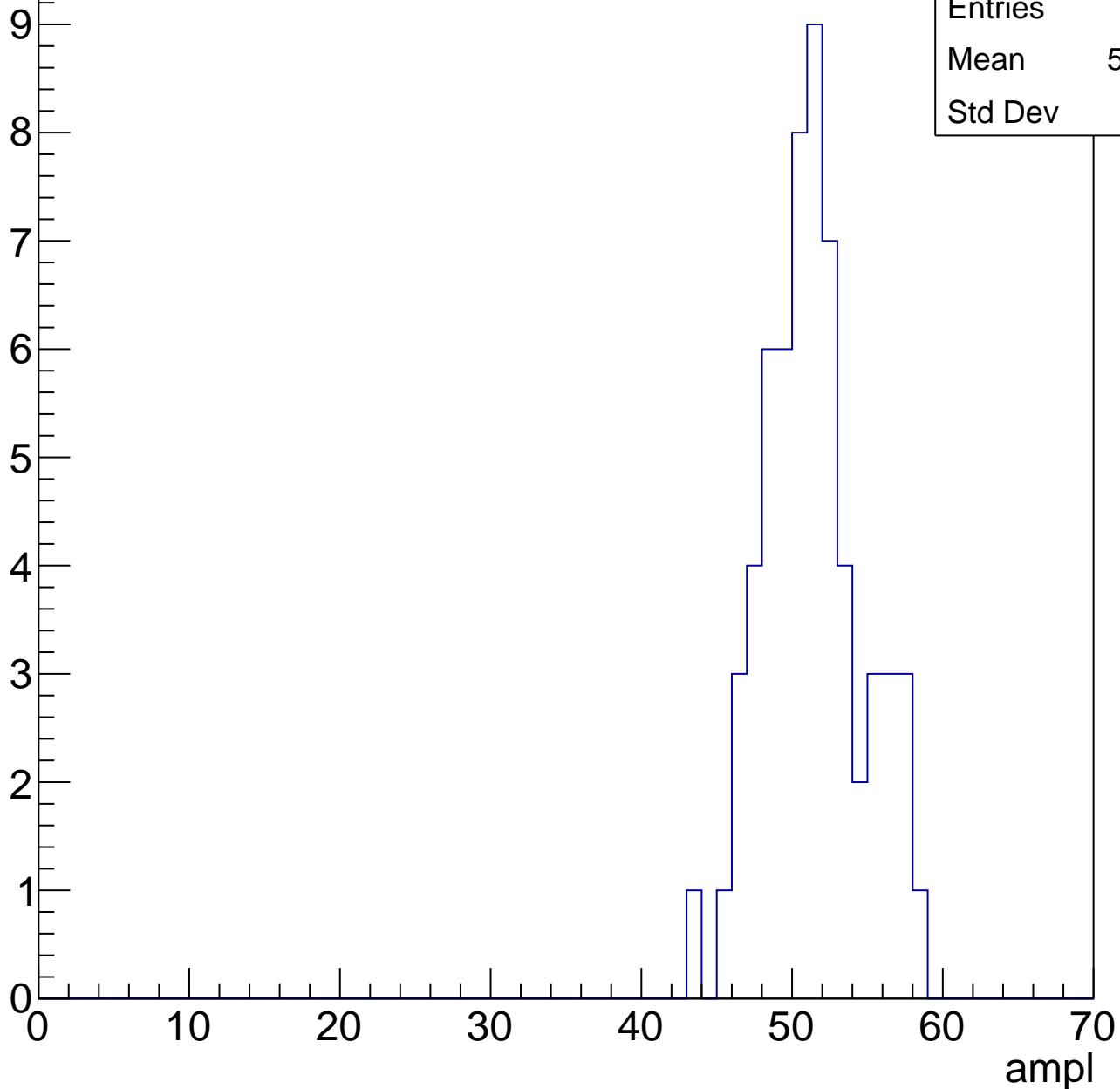


B0L001S, U19-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.84
Std Dev	3.25

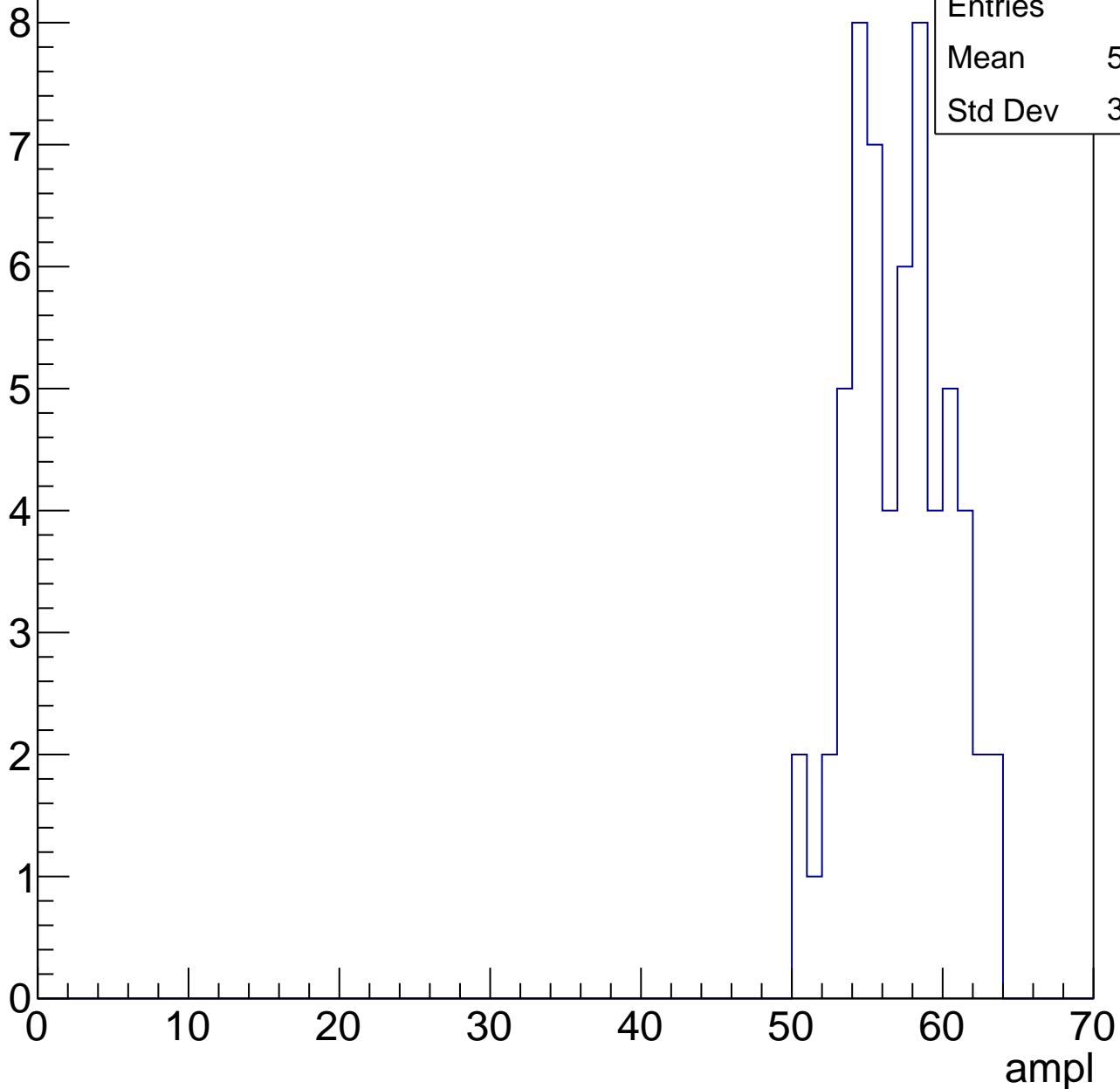


B0L001S, U19-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.62
Std Dev	3.199

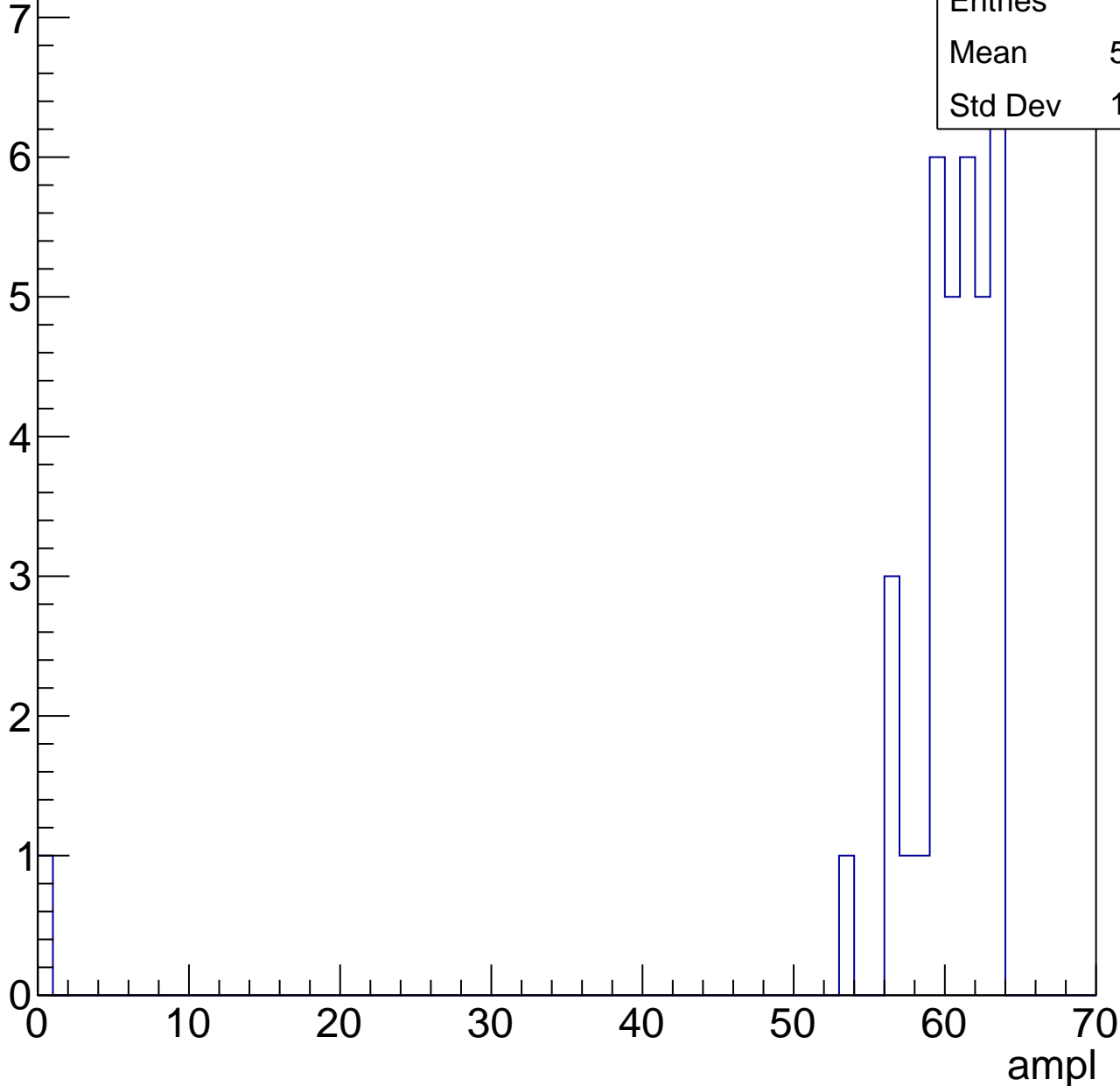


B0L001S, U19-ch122, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.53
Std Dev	10.17



B0L001S, U19-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	62.25
Std Dev	0.433

B0L001S, U19-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch123, adc0

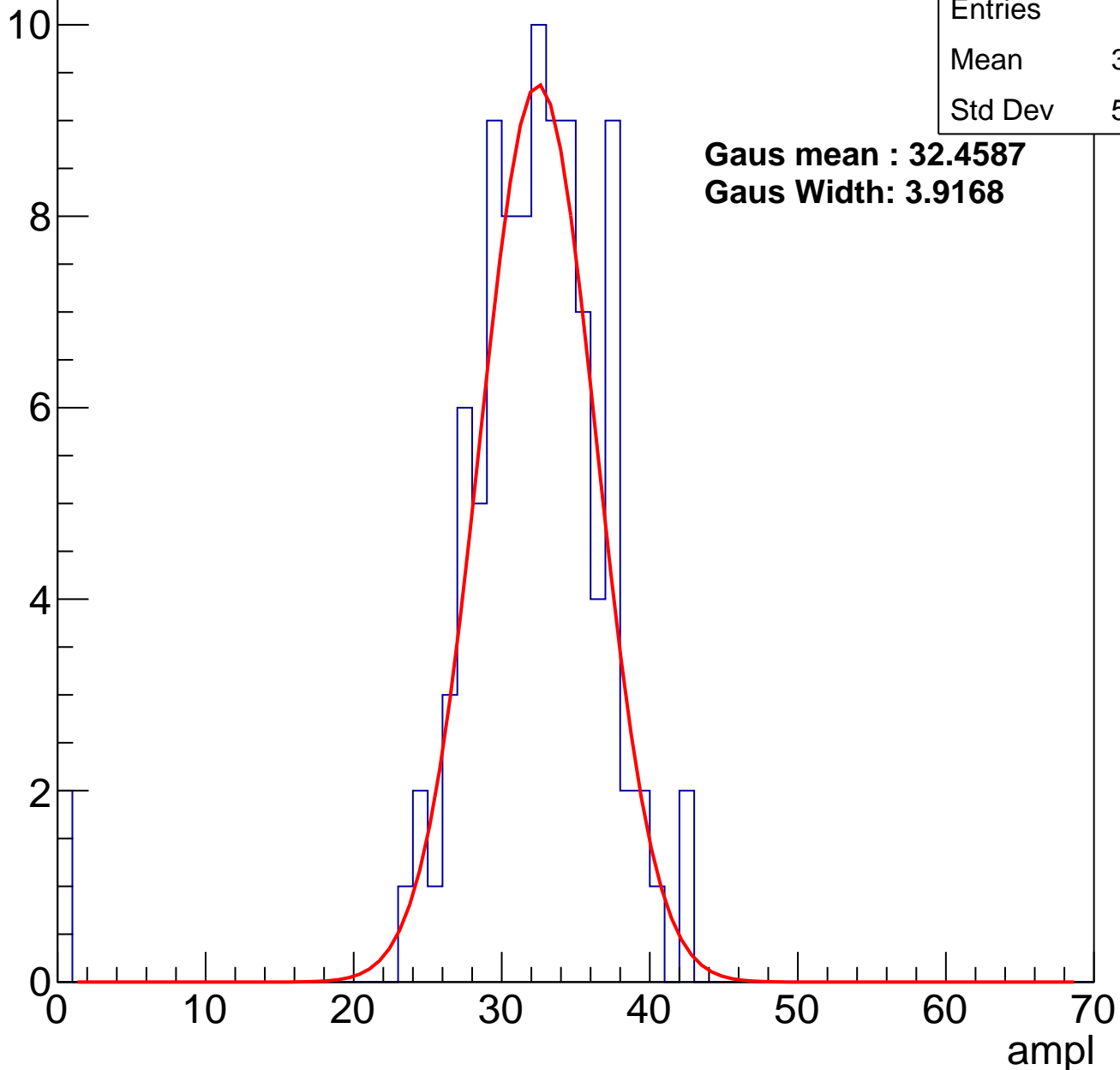
calib_packv5_042523_0143.root, FC#9, port A1

Entries	100
Mean	31.48
Std Dev	5.974

Gaus mean : 32.4587

Gaus Width: 3.9168

Entry



B0L001S, U19-ch123, adc1

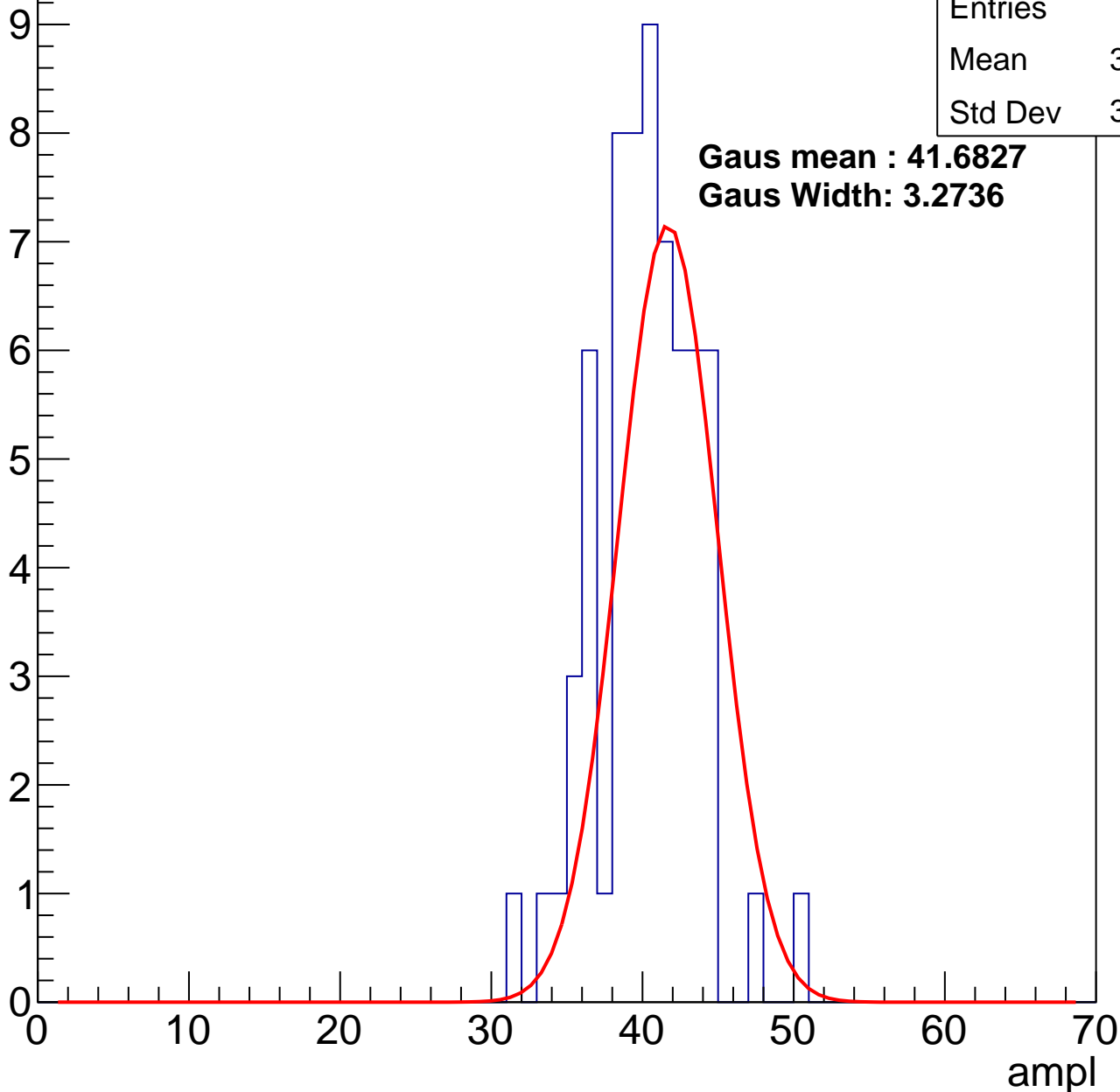
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	39.85
Std Dev	3.315

Gaus mean : 41.6827

Gaus Width: 3.2736



B0L001S, U19-ch123, adc2

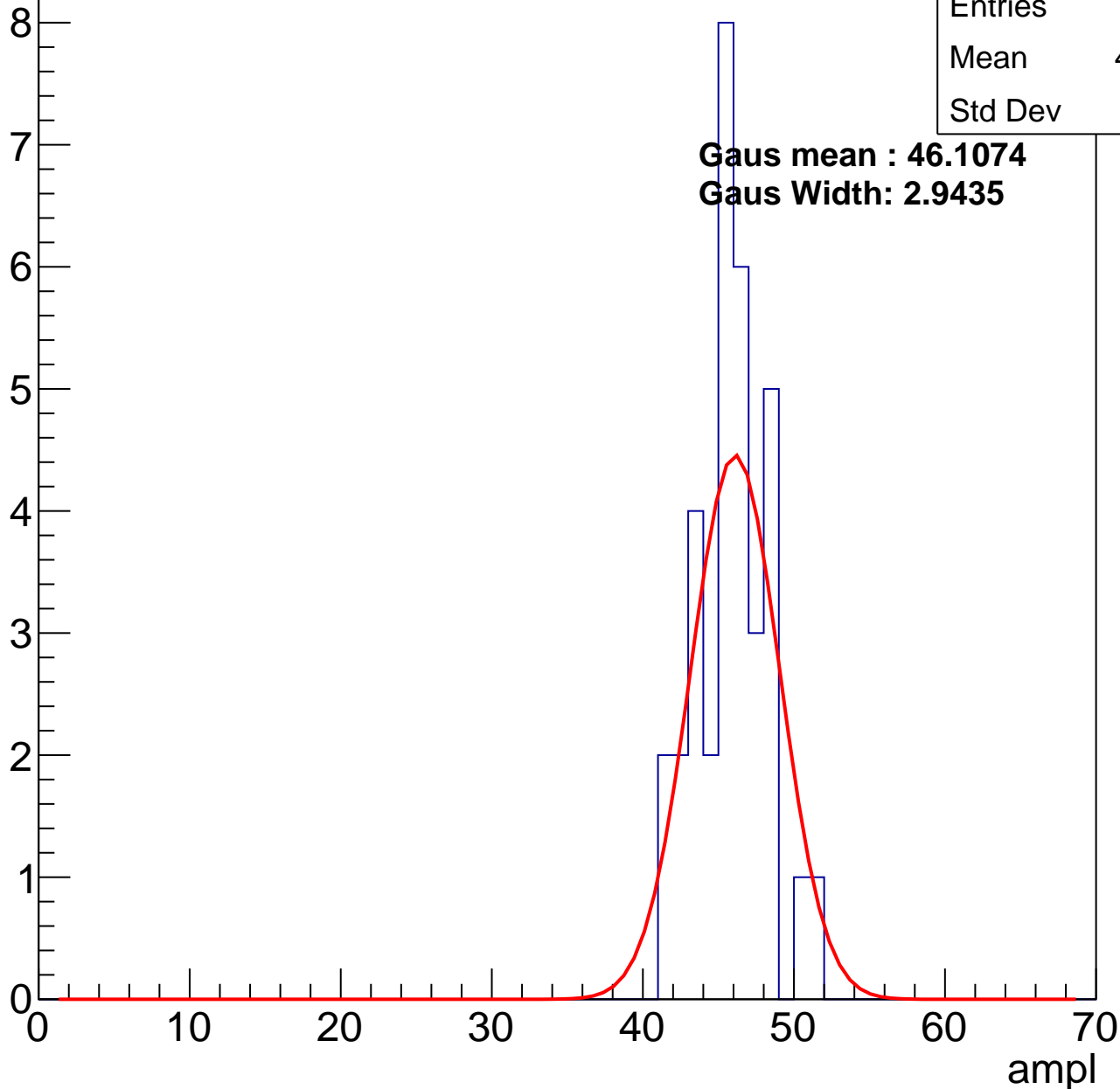
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	45.41
Std Dev	2.34

Gaus mean : 46.1074

Gaus Width: 2.9435



B0L001S, U19-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

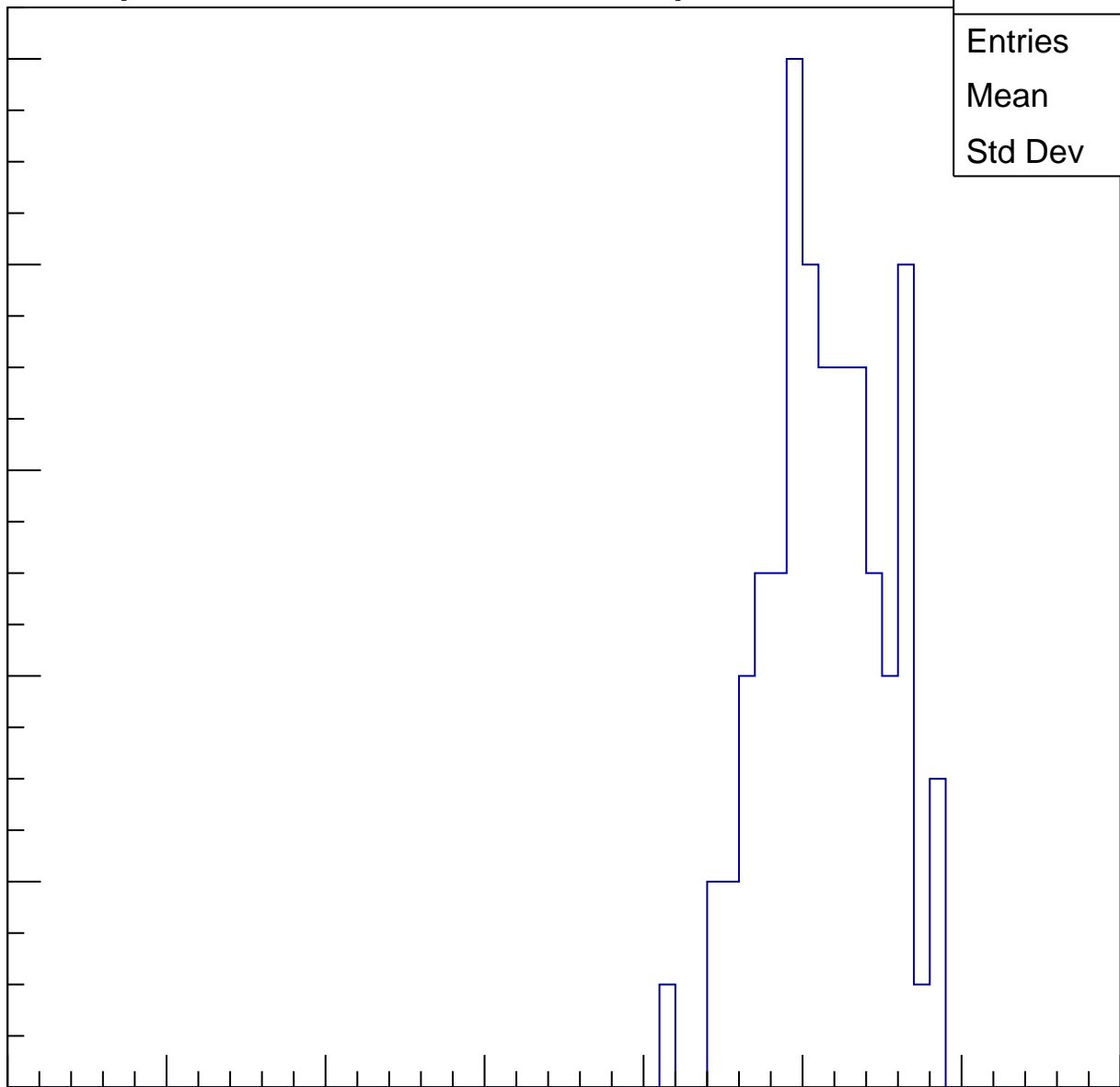
Entries	79
Mean	51
Std Dev	3.68

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

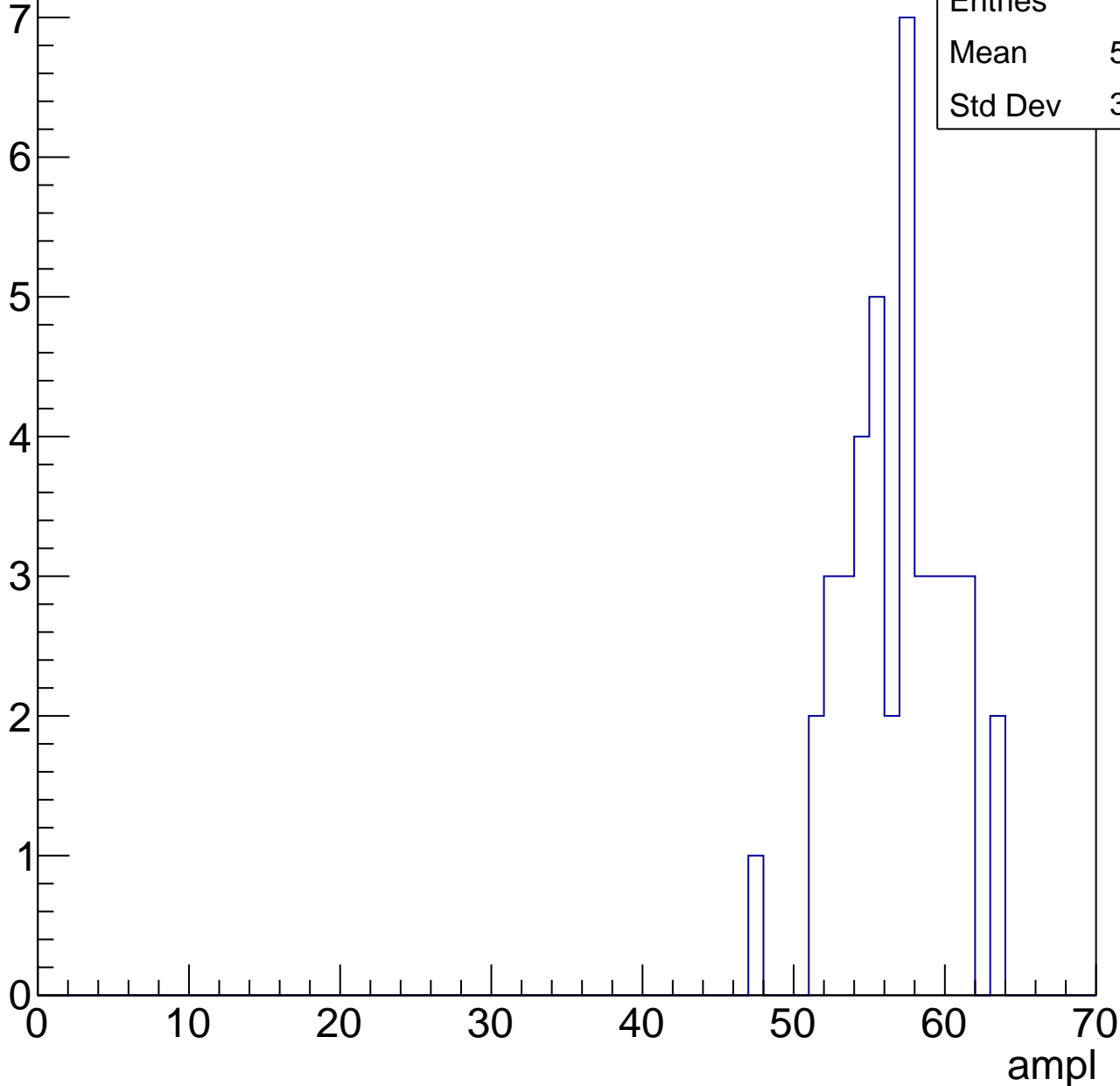


B0L001S, U19-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	56.24
Std Dev	3.463

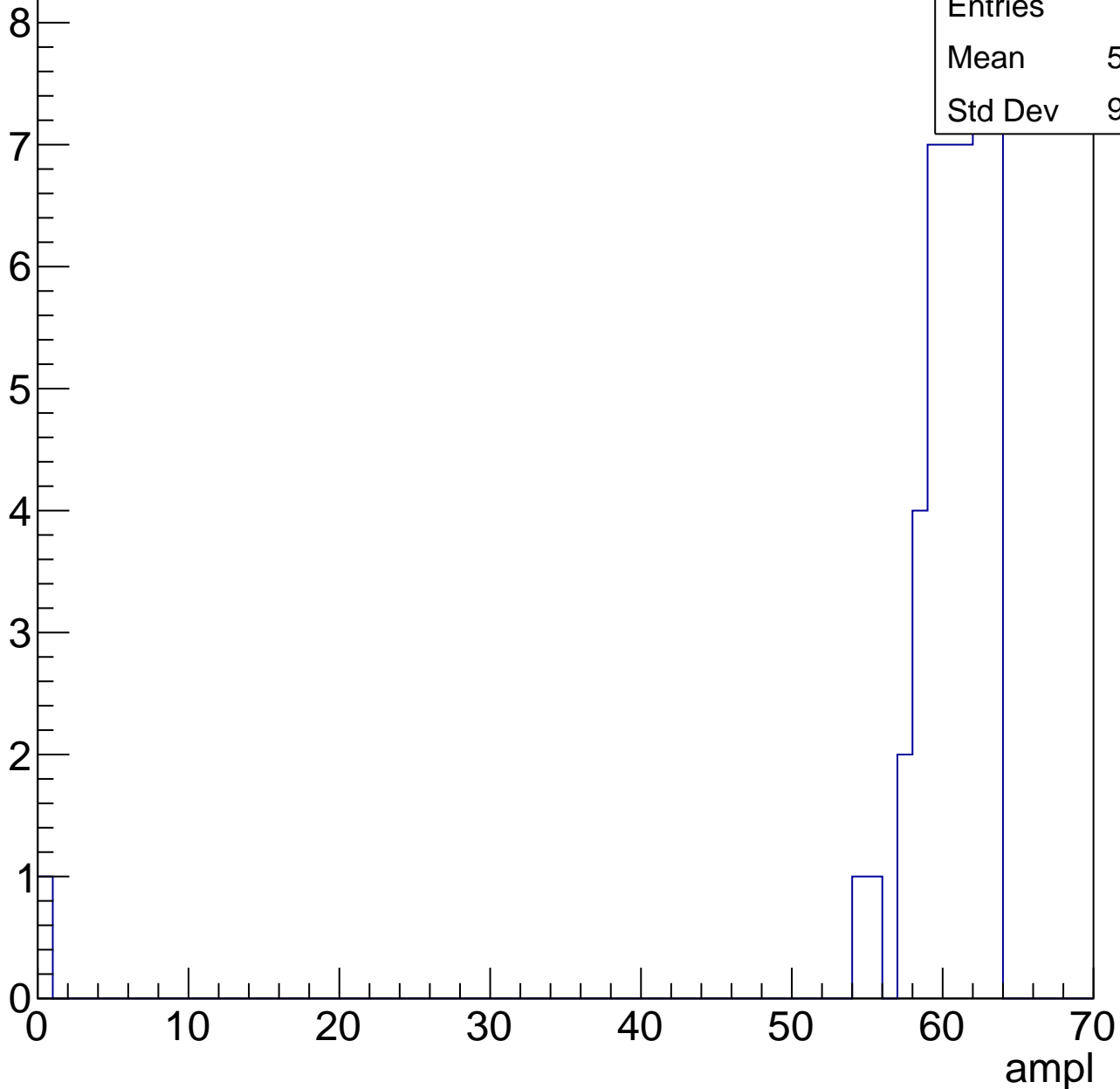


B0L001S, U19-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	59.02
Std Dev	9.052



B0L001S, U19-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

ampl

B0L001S, U19-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L001S, U19-ch124, adc0

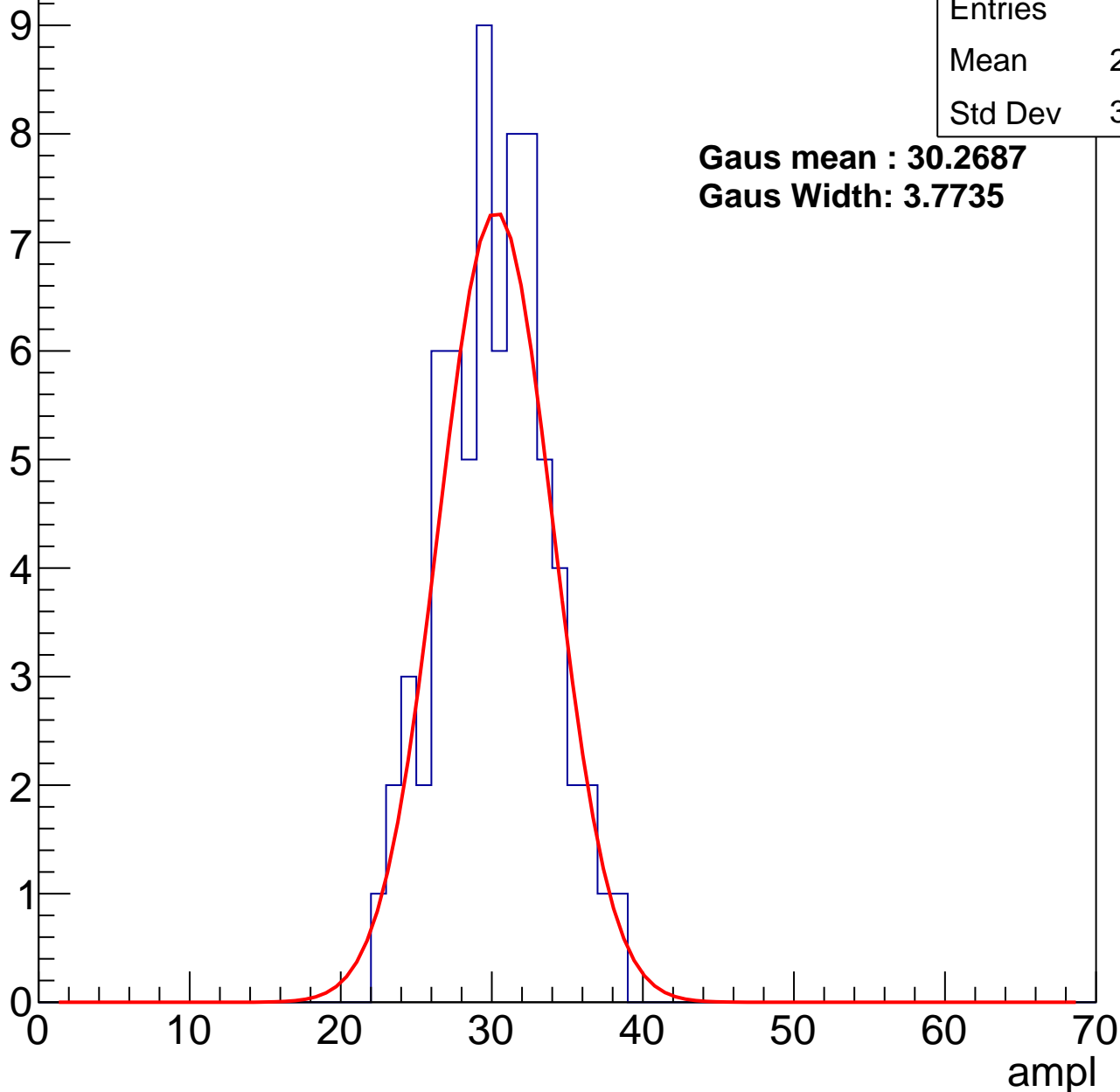
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	29.73
Std Dev	3.516

Gaus mean : 30.2687

Gaus Width: 3.7735



B0L001S, U19-ch124, adc1

calib_packv5_042523_0143.root, FC#9, port A1

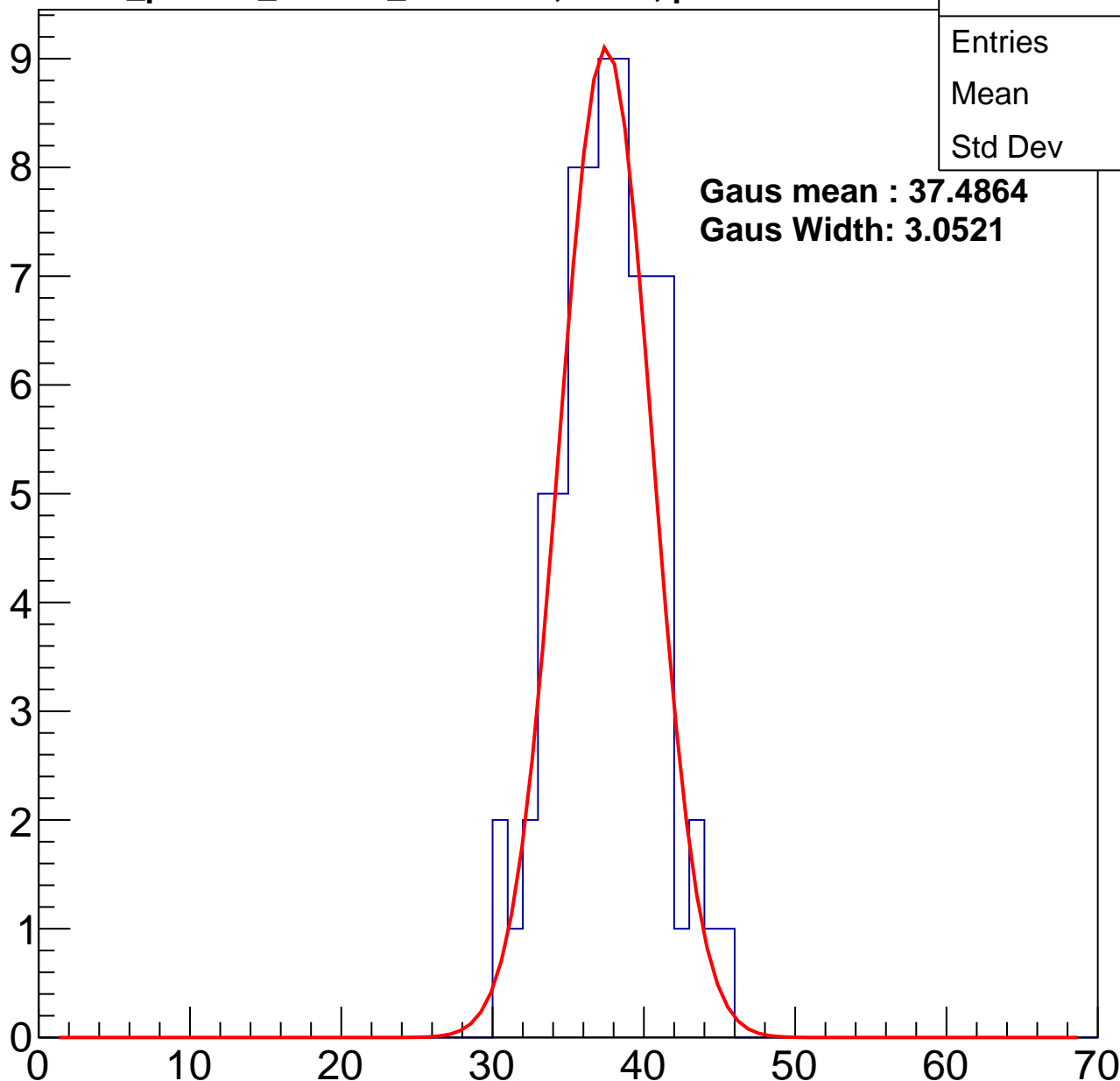
Entry

9
8
7
6
5
4
3
2
1
0

Entries	75
Mean	37.2
Std Dev	3.2

Gaus mean : 37.4864
Gaus Width: 3.0521

ampl



B0L001S, U19-ch124, adc2

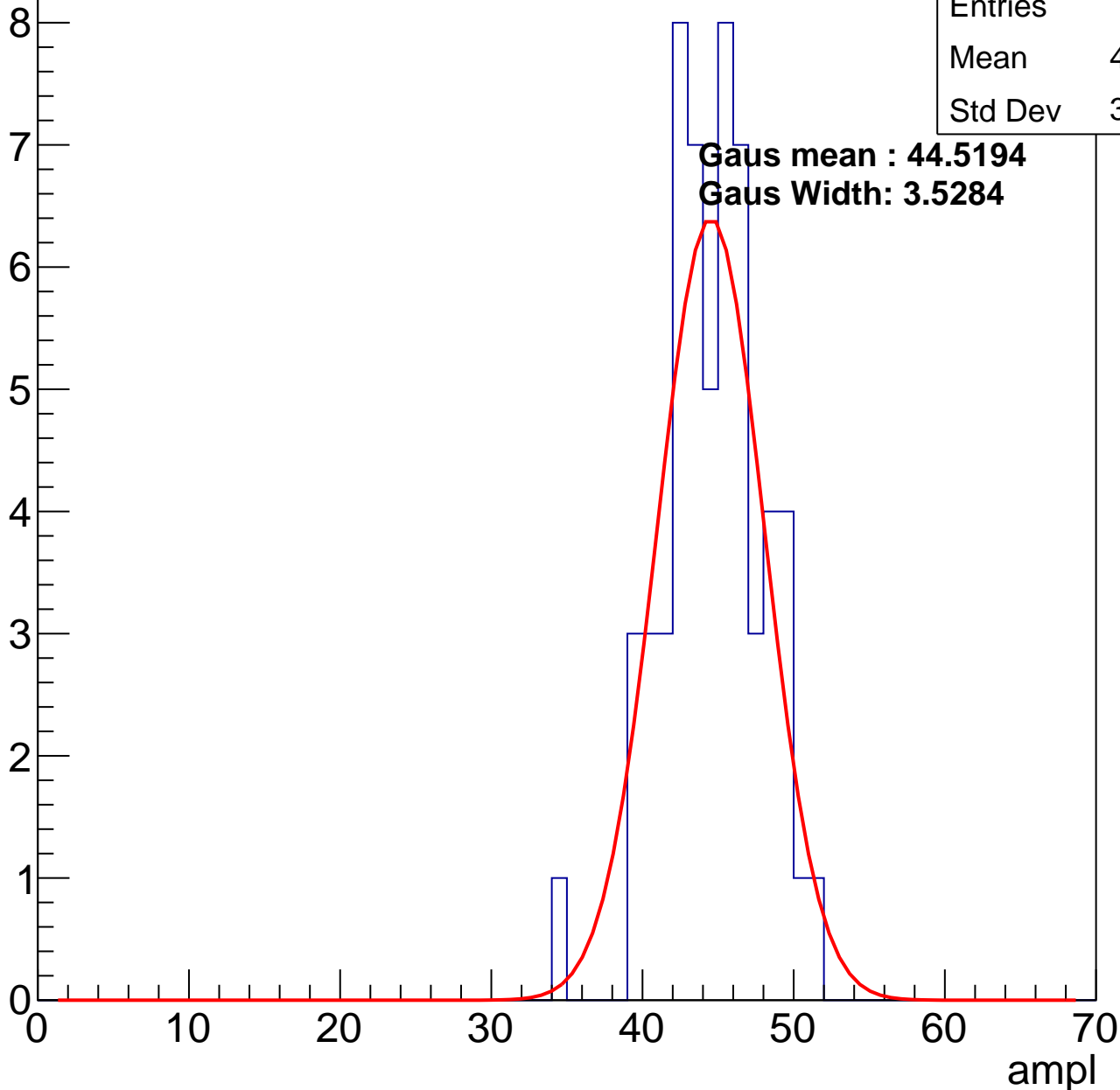
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.19
Std Dev	3.213

Gaus mean : 44.5194

Gaus Width: 3.5284

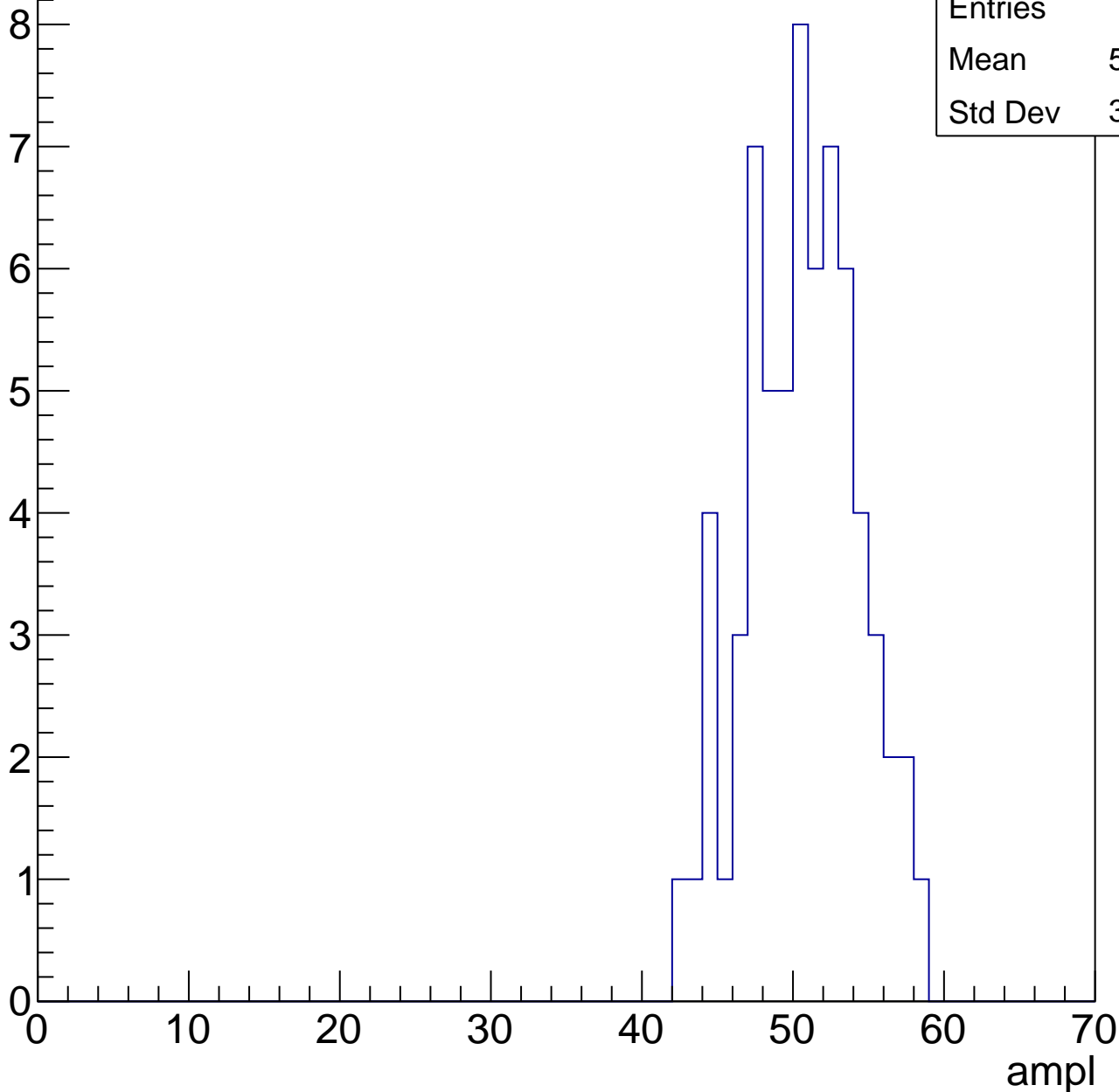


B0L001S, U19-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	50.17
Std Dev	3.646

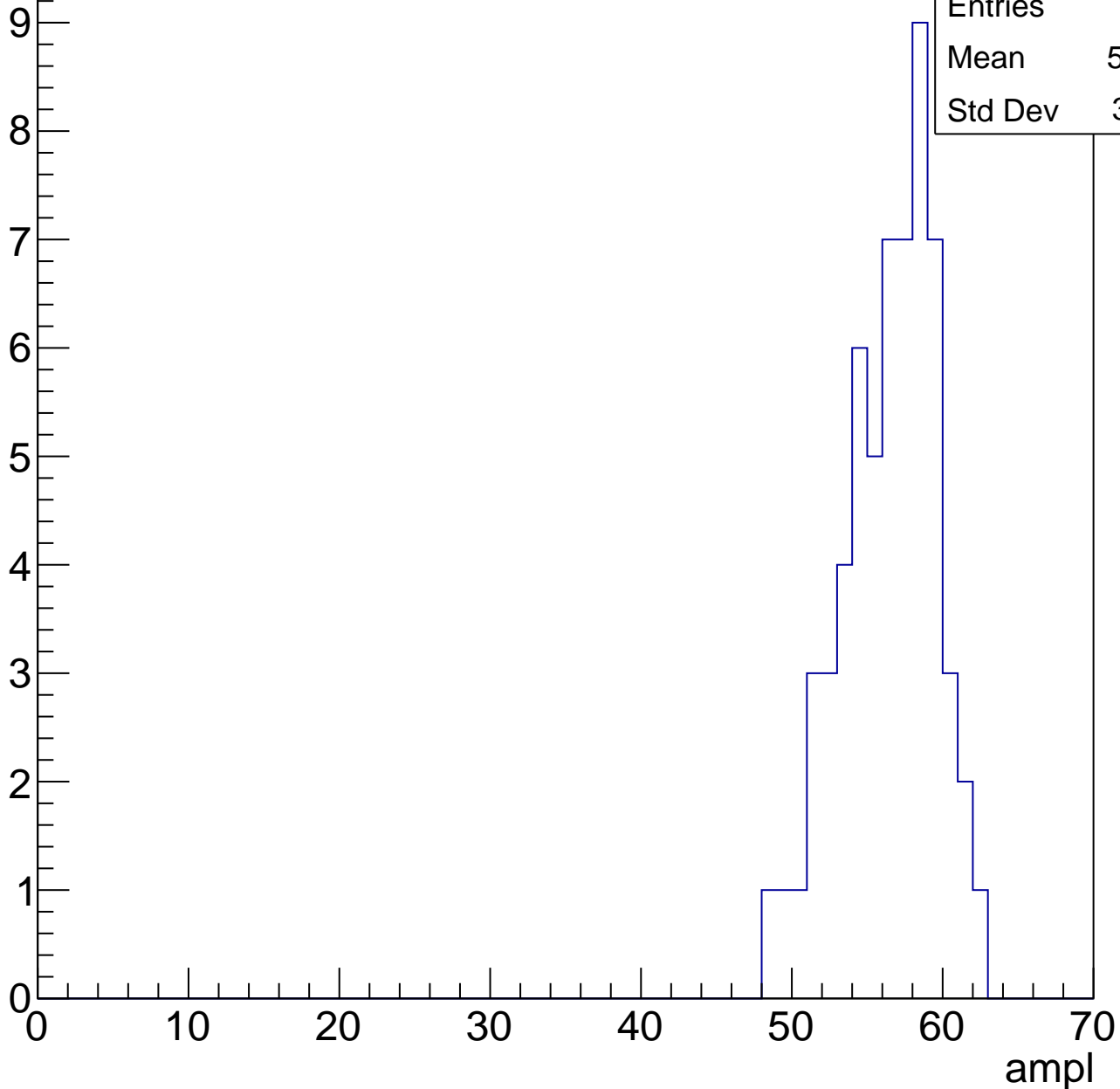


B0L001S, U19-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	55.95
Std Dev	3.101

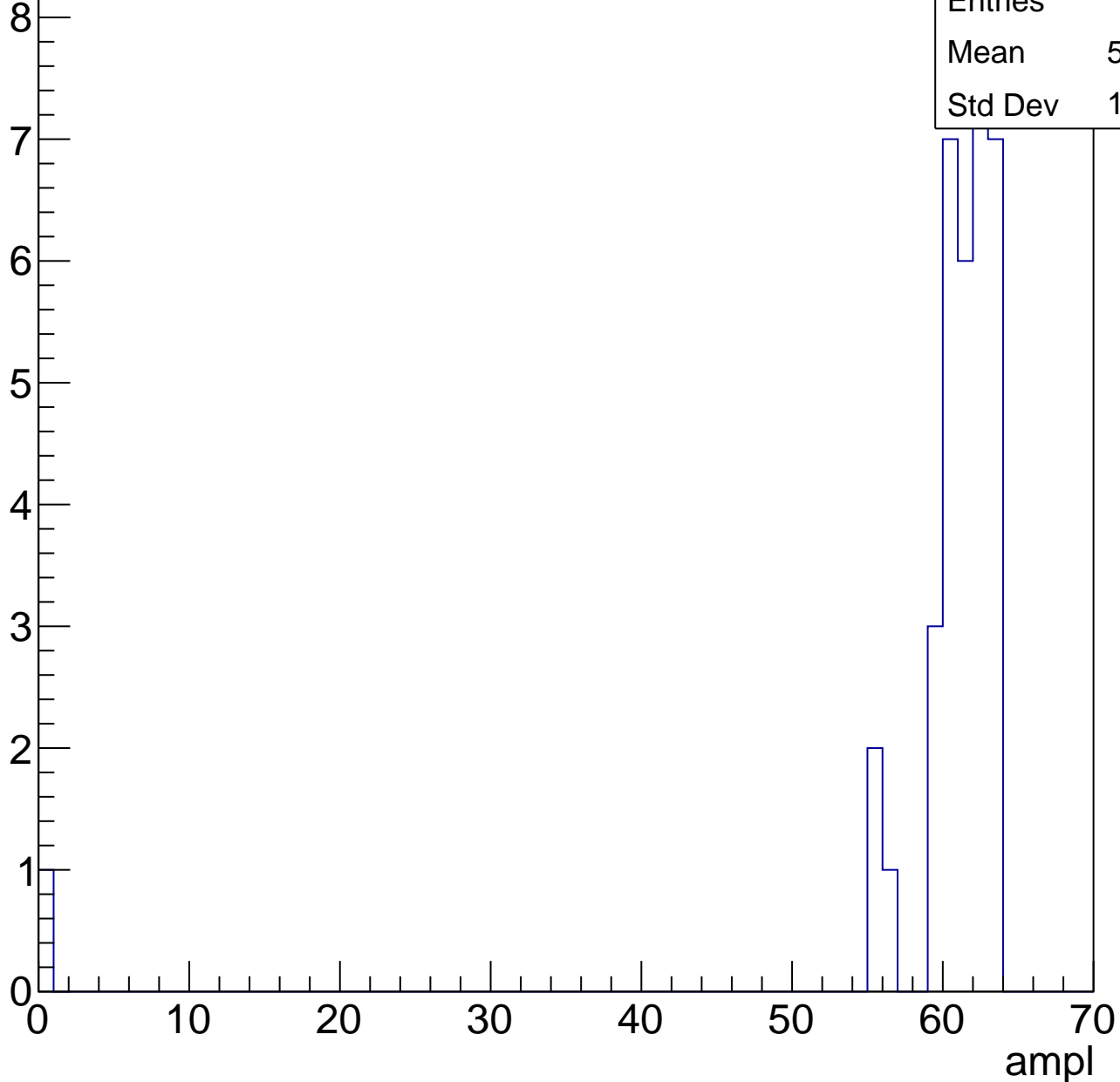


B0L001S, U19-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

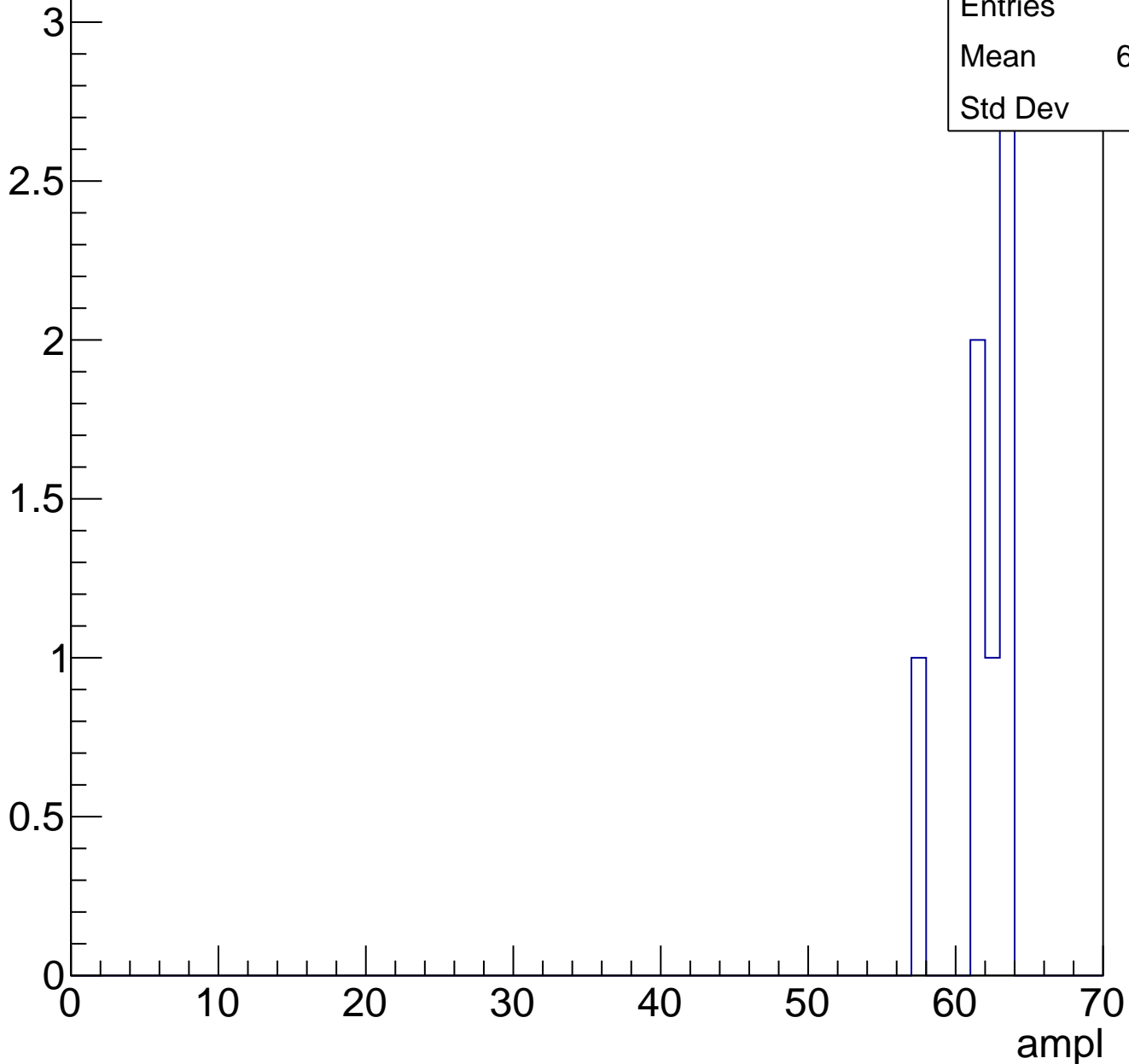
Entries	35
Mean	59.03
Std Dev	10.33



B0L001S, U19-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

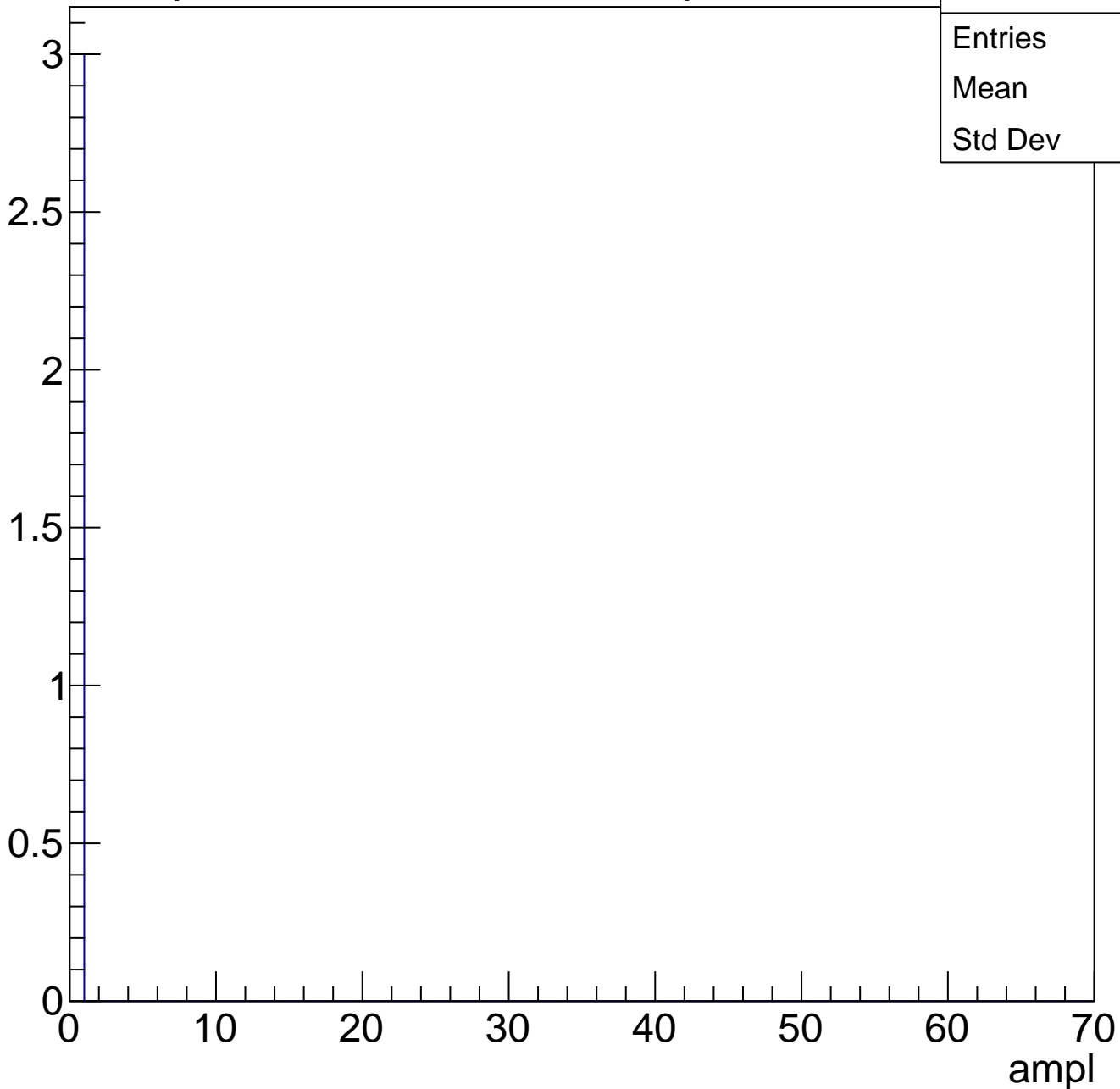
Entry



B0L001S, U19-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U19-ch125, adc0

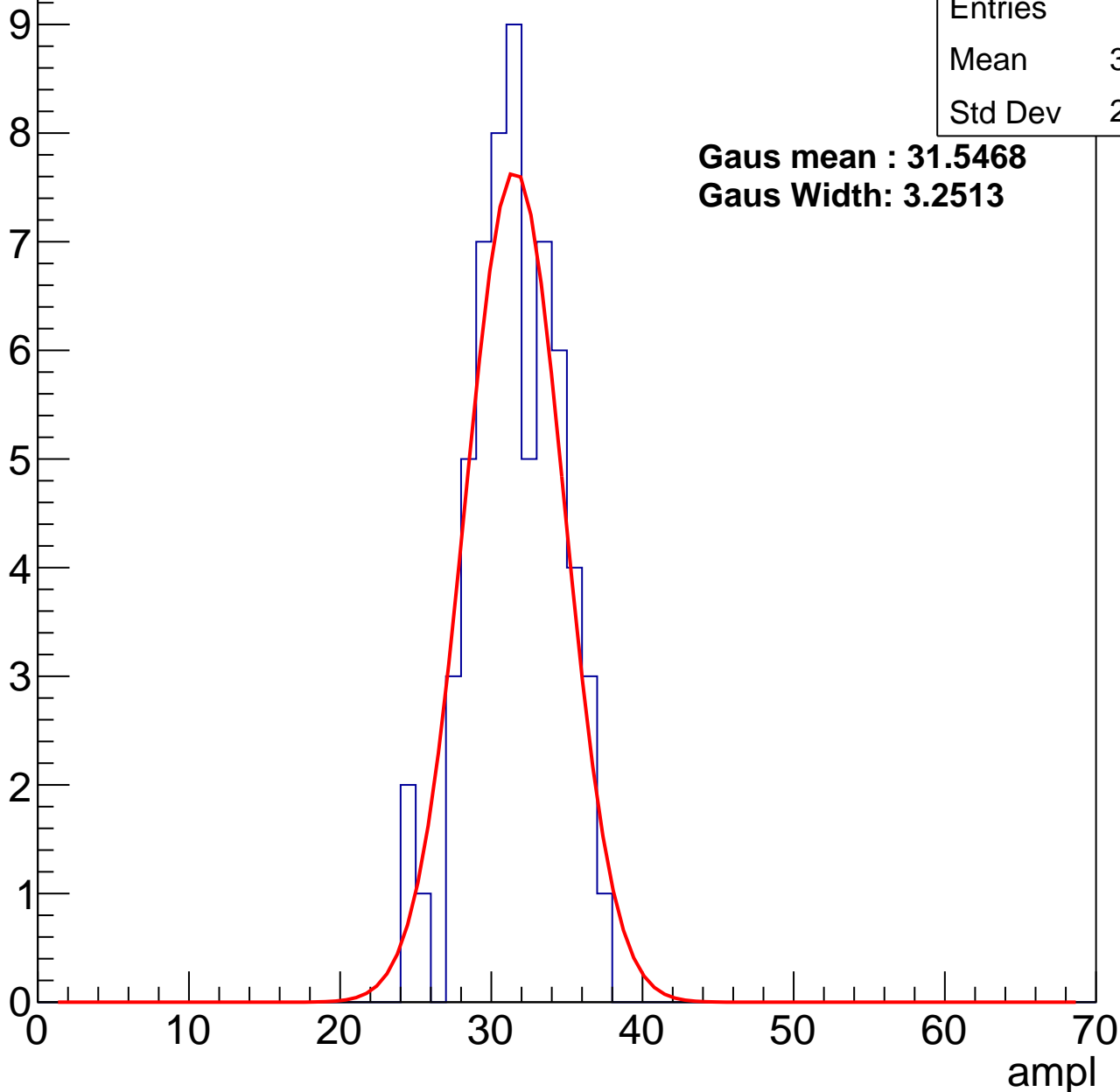
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	31.08
Std Dev	2.933

Gaus mean : 31.5468

Gaus Width: 3.2513



B0L001S, U19-ch125, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	37.49
Std Dev	3.02

Gaus mean : 38.0356

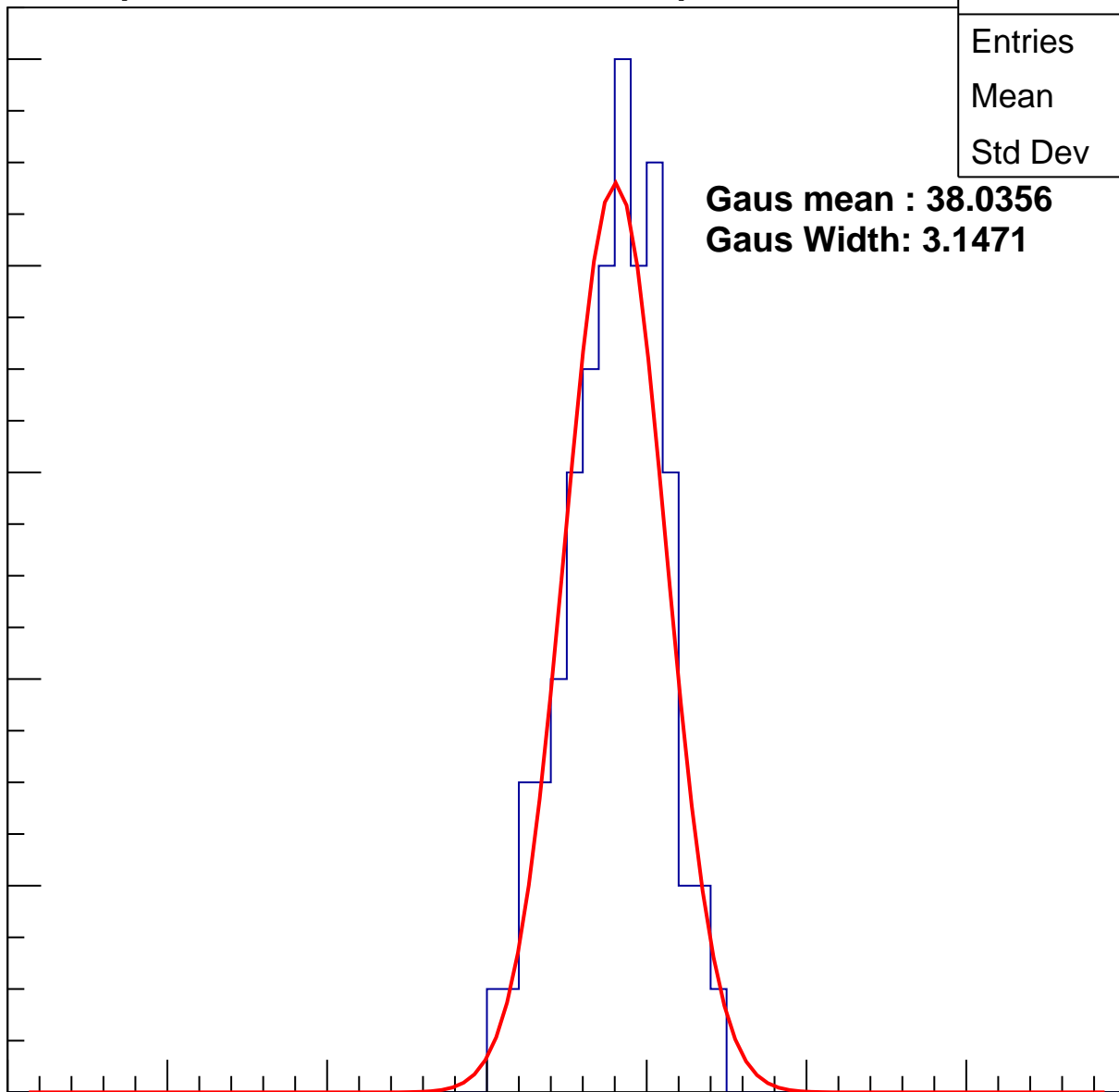
Gaus Width: 3.1471

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U19-ch125, adc2

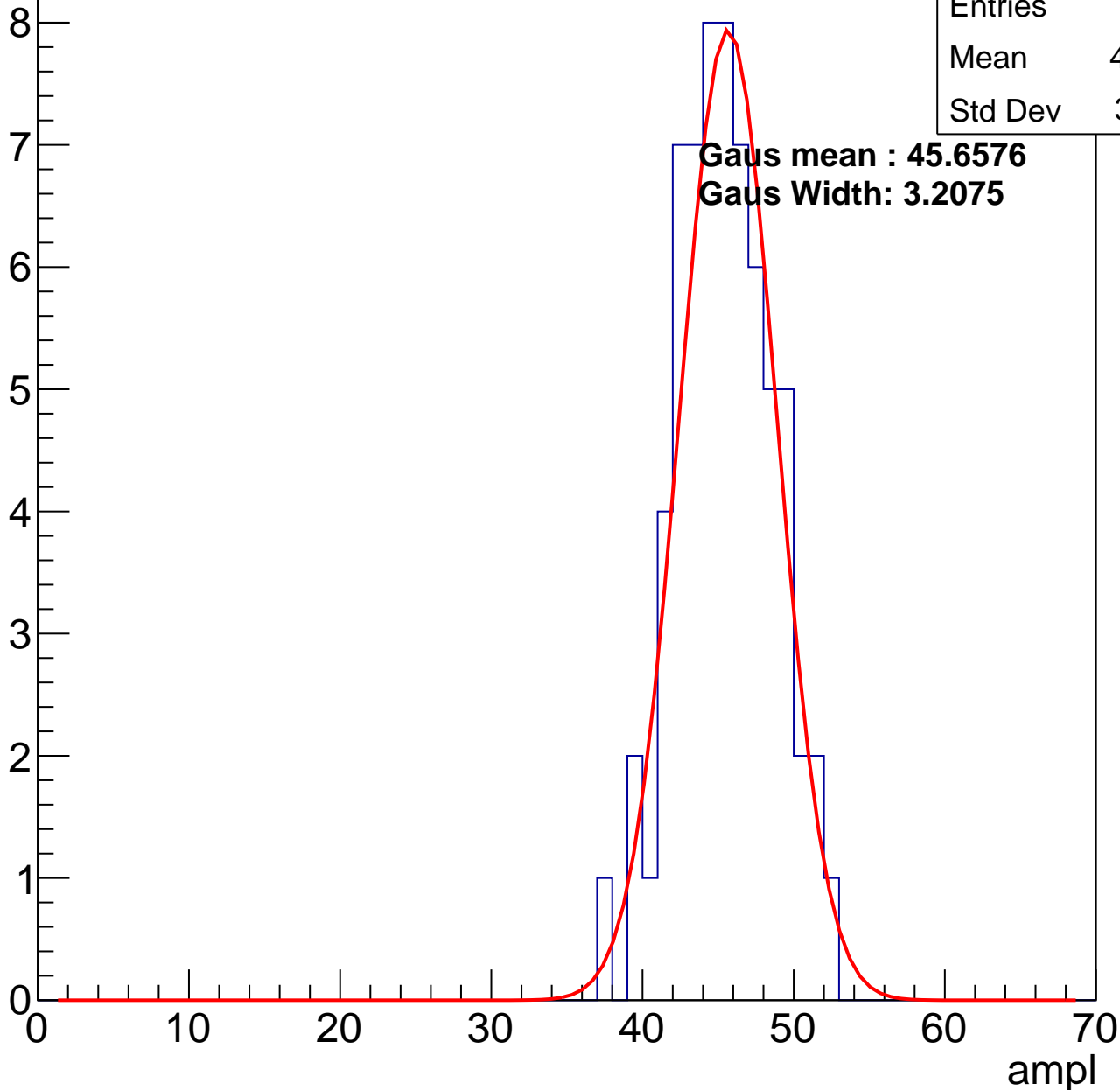
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.98
Std Dev	3.141

Gaus mean : 45.6576

Gaus Width: 3.2075

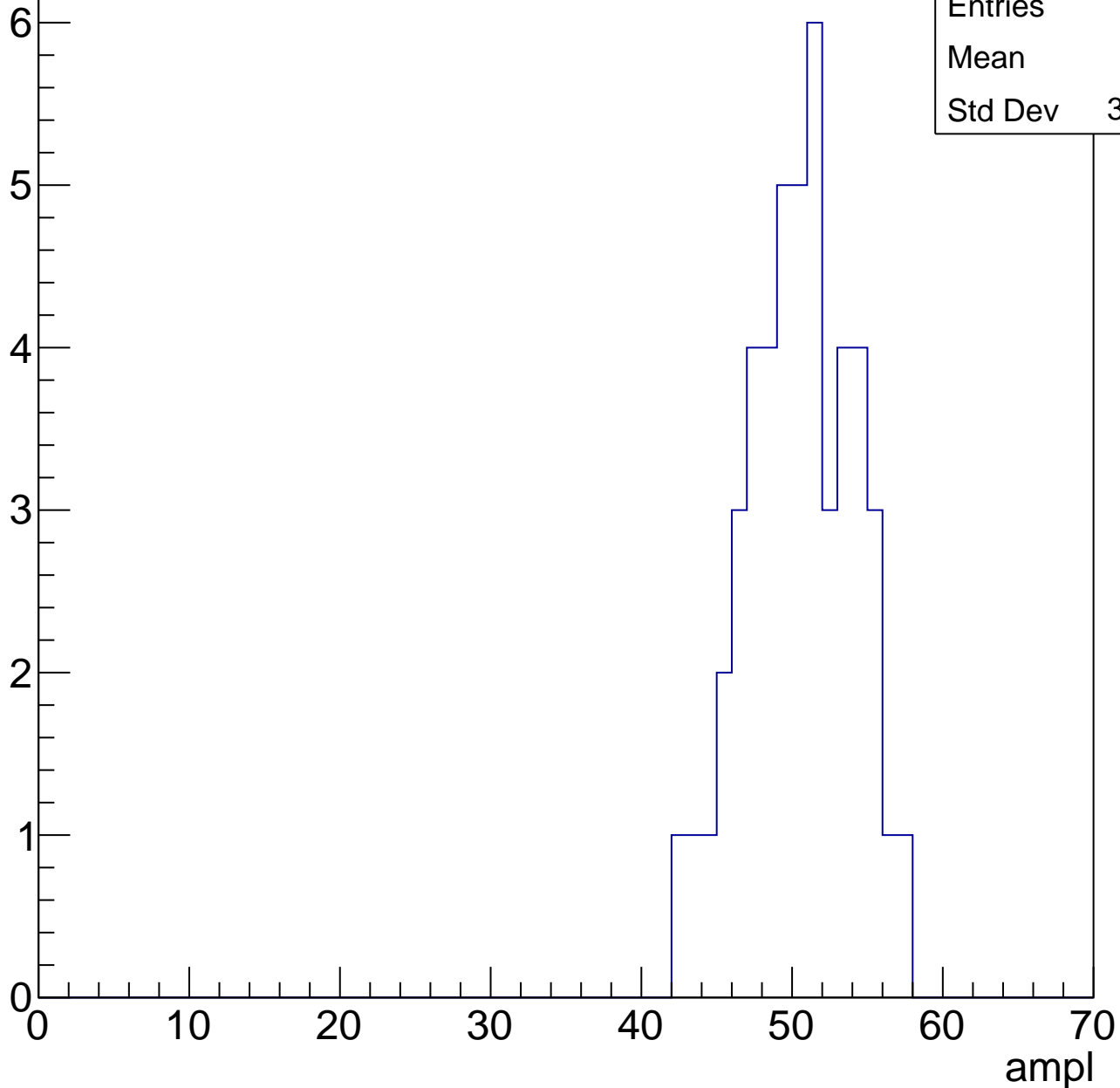


B0L001S, U19-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	50
Std Dev	3.482

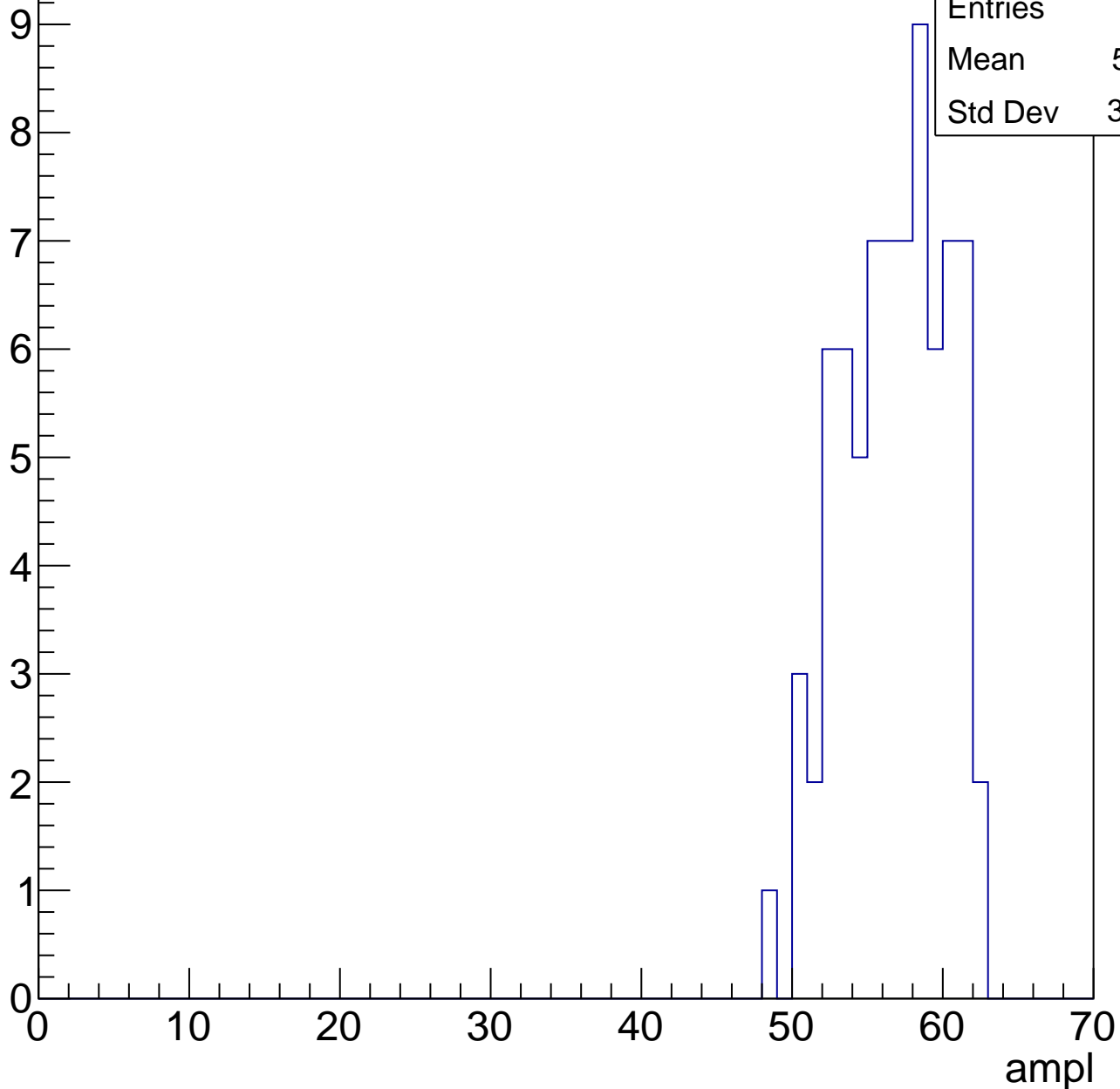


B0L001S, U19-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	56.31
Std Dev	3.362

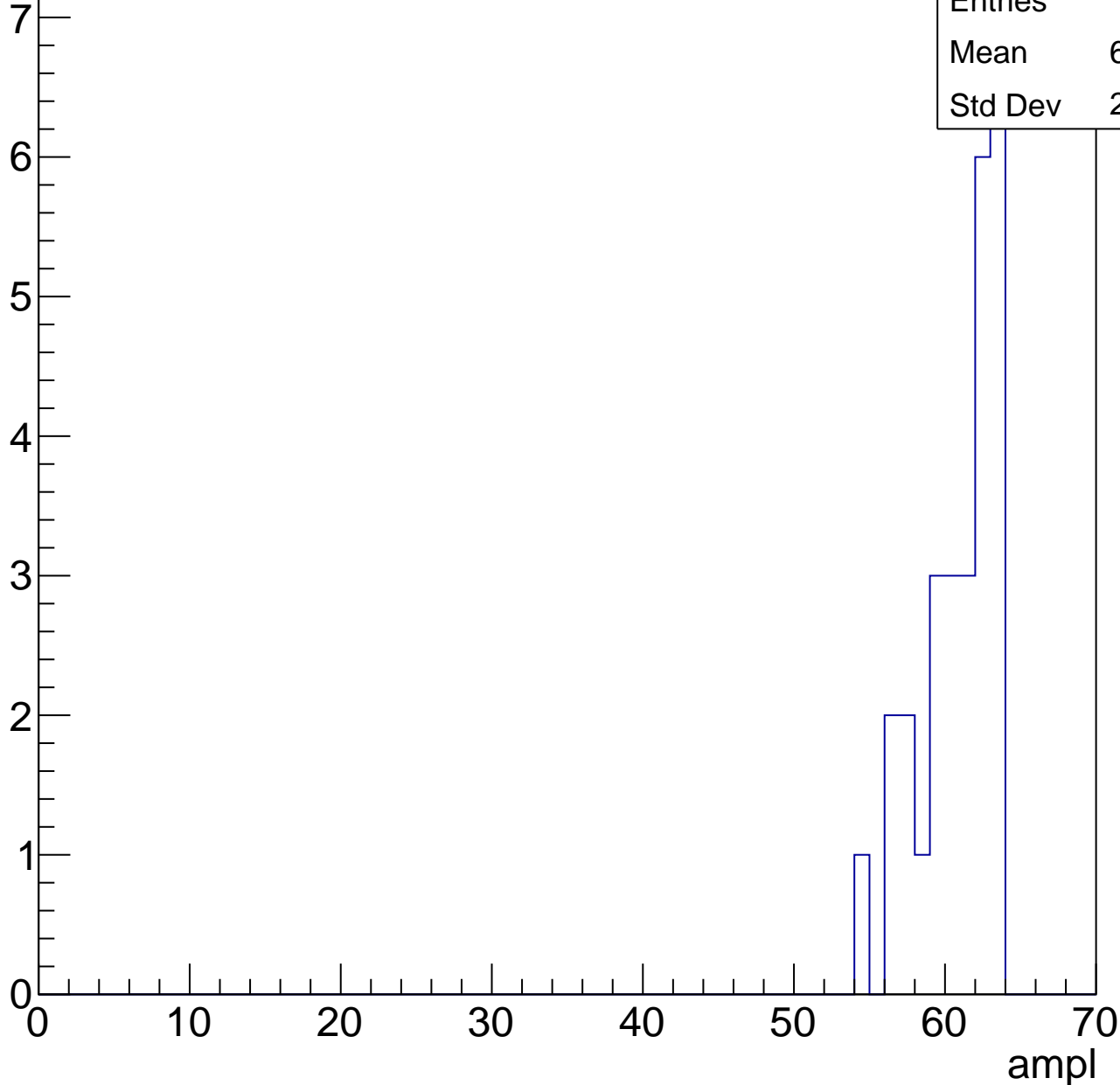


B0L001S, U19-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

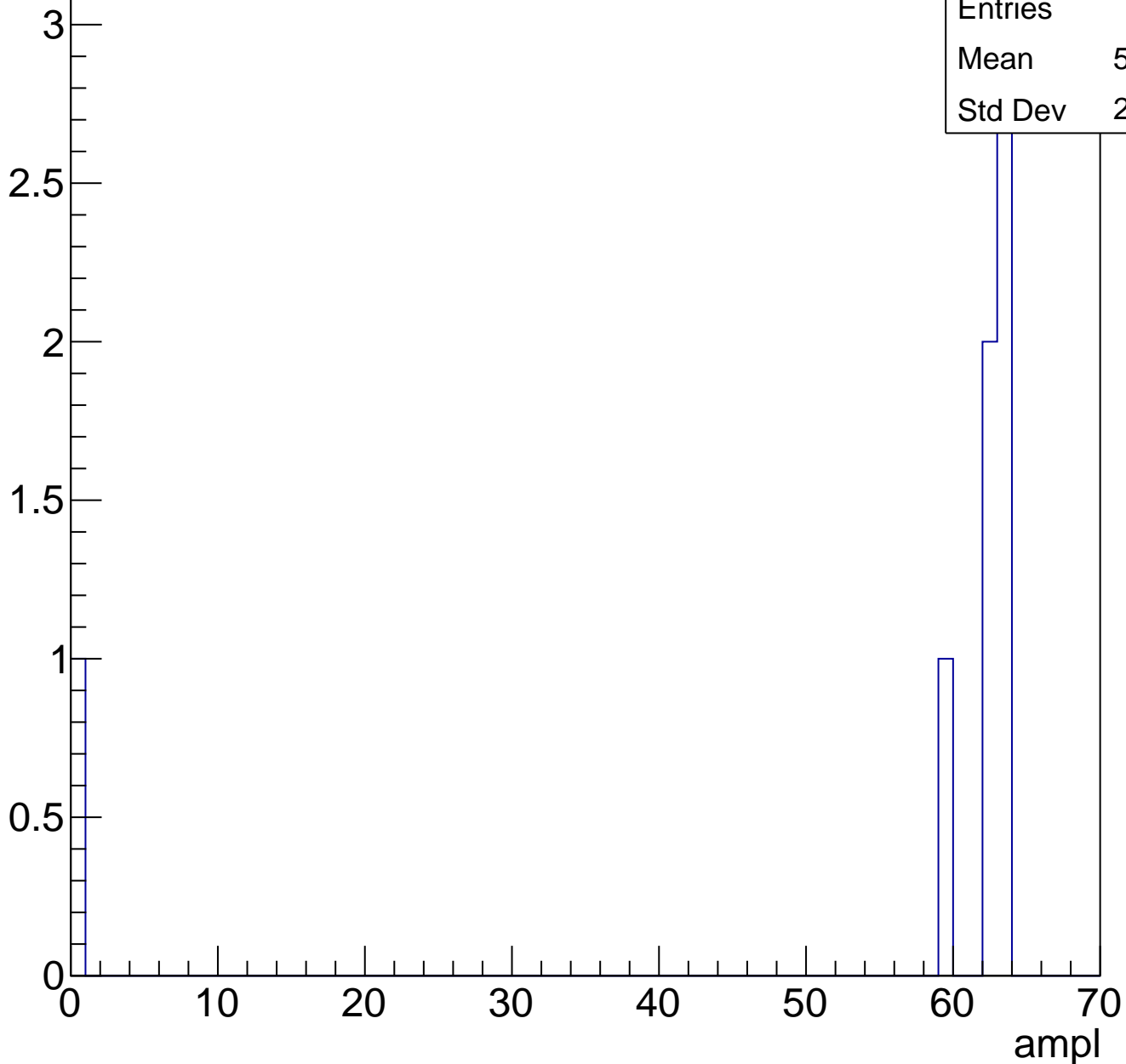
Entries	28
Mean	60.39
Std Dev	2.526



B0L001S, U19-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch126, adc0

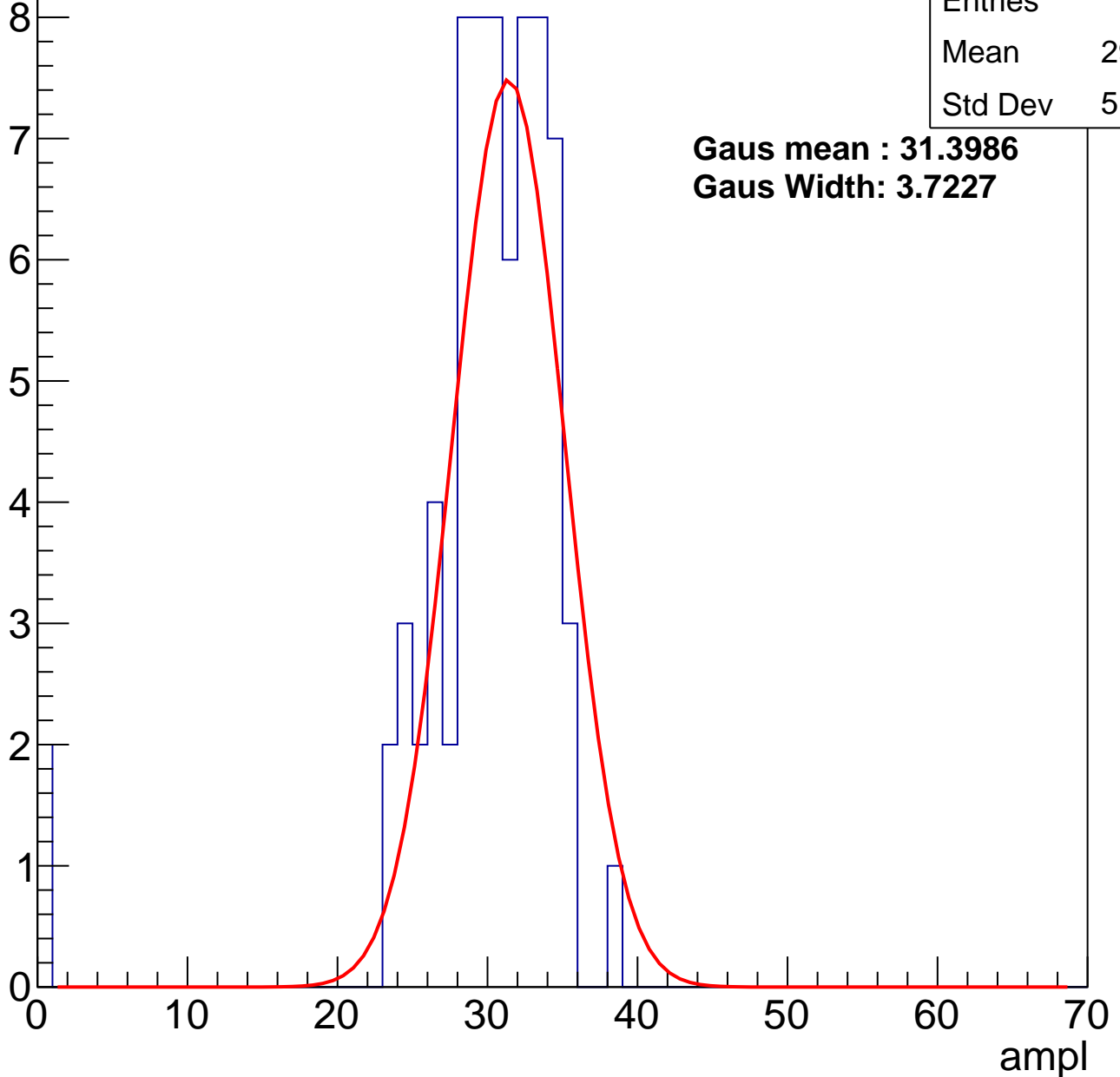
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.29
Std Dev	5.896

Gaus mean : 31.3986

Gaus Width: 3.7227



B0L001S, U19-ch126, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	37.36
Std Dev	2.75

Gaus mean : 37.9804

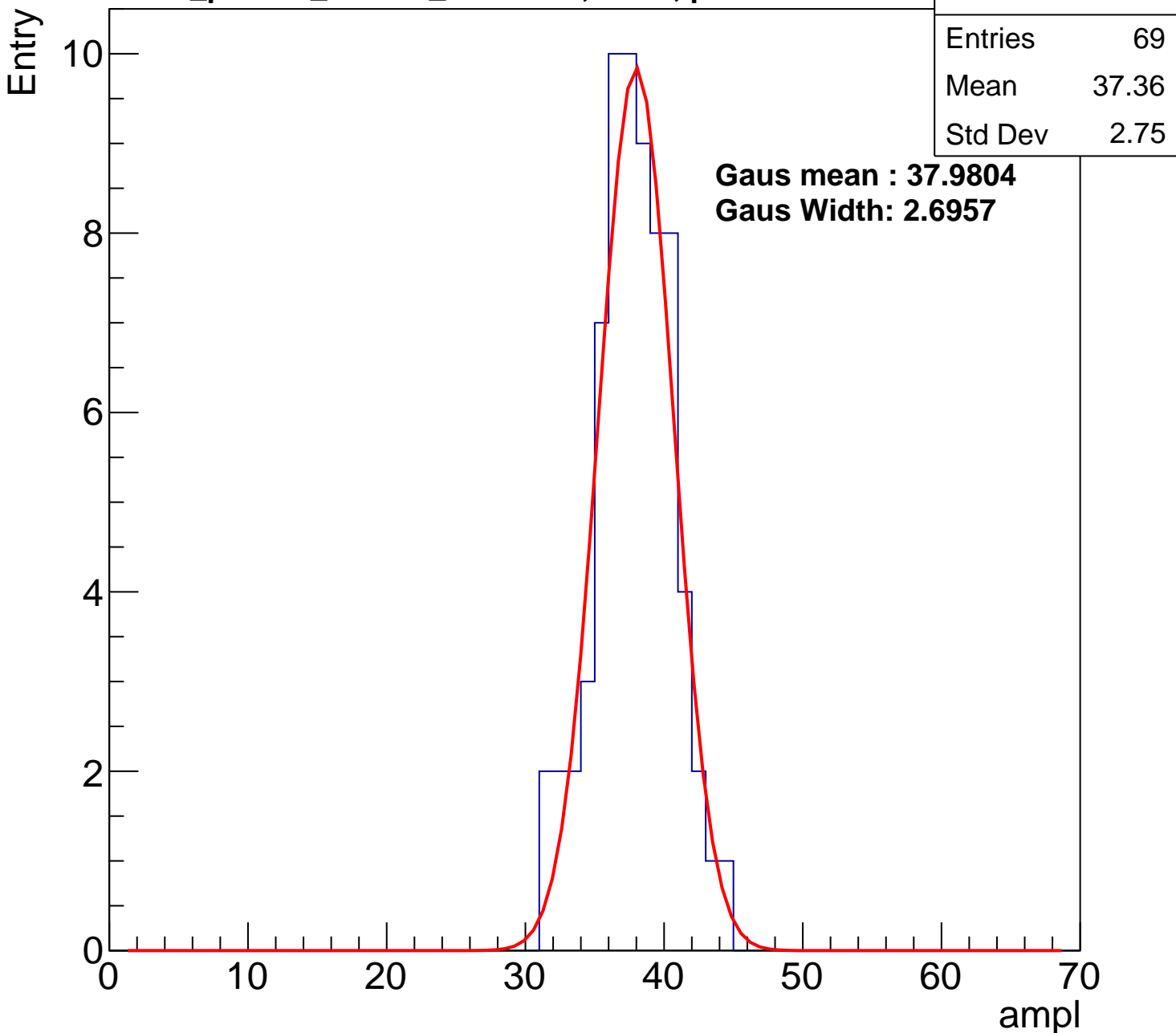
Gaus Width: 2.6957

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

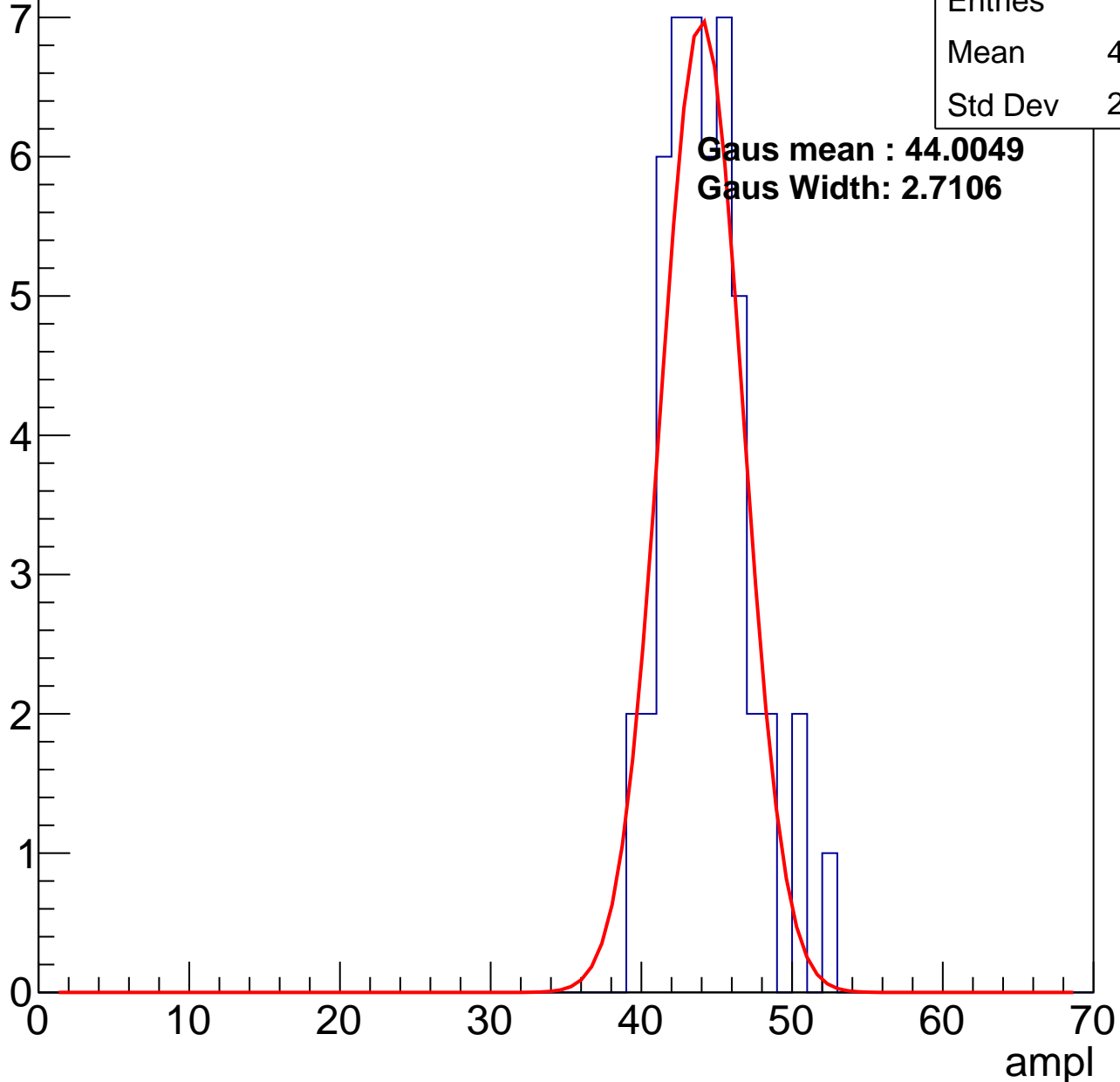


B0L001S, U19-ch126, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	43.88
Std Dev	2.797

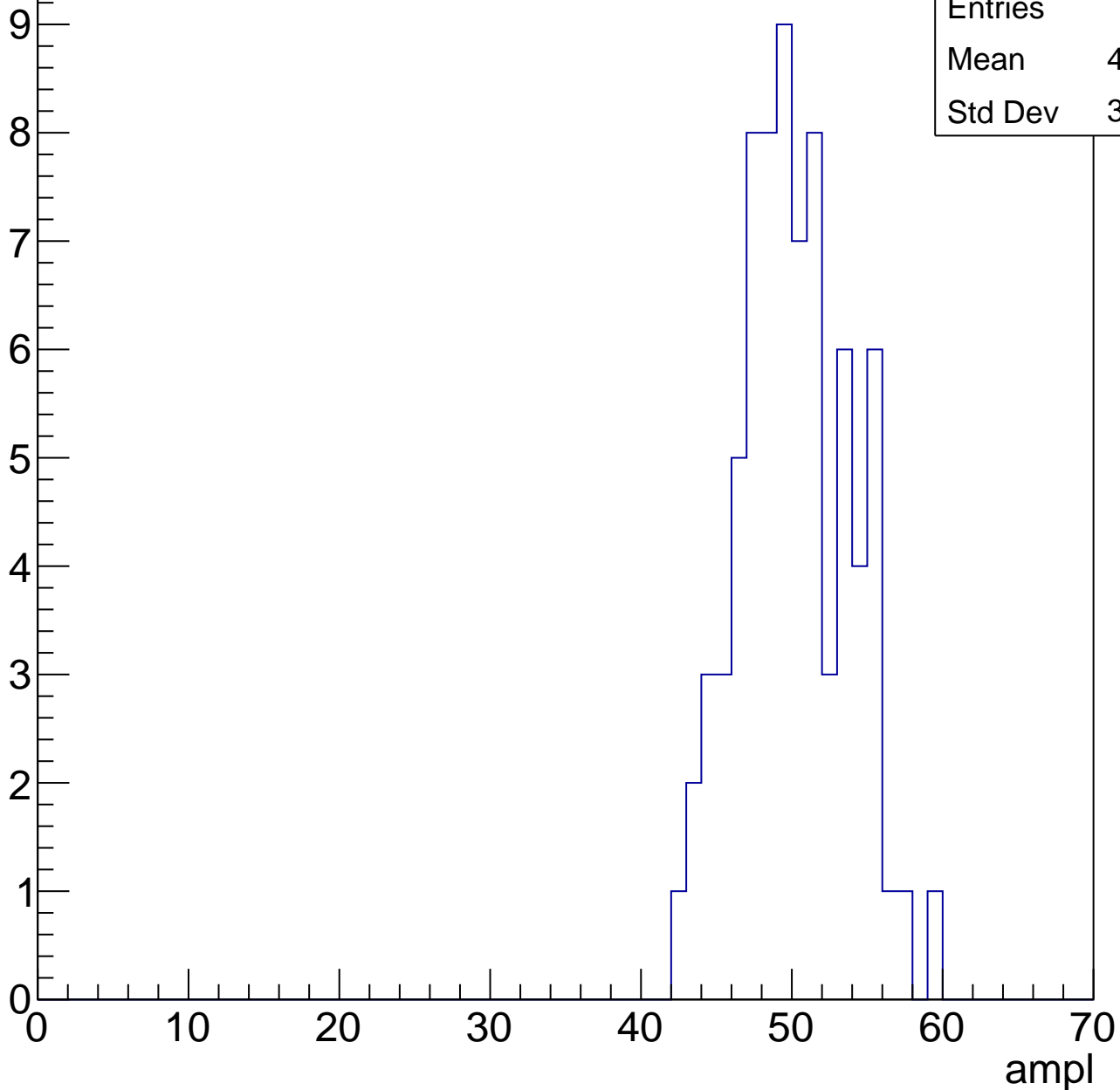


B0L001S, U19-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	49.68
Std Dev	3.599

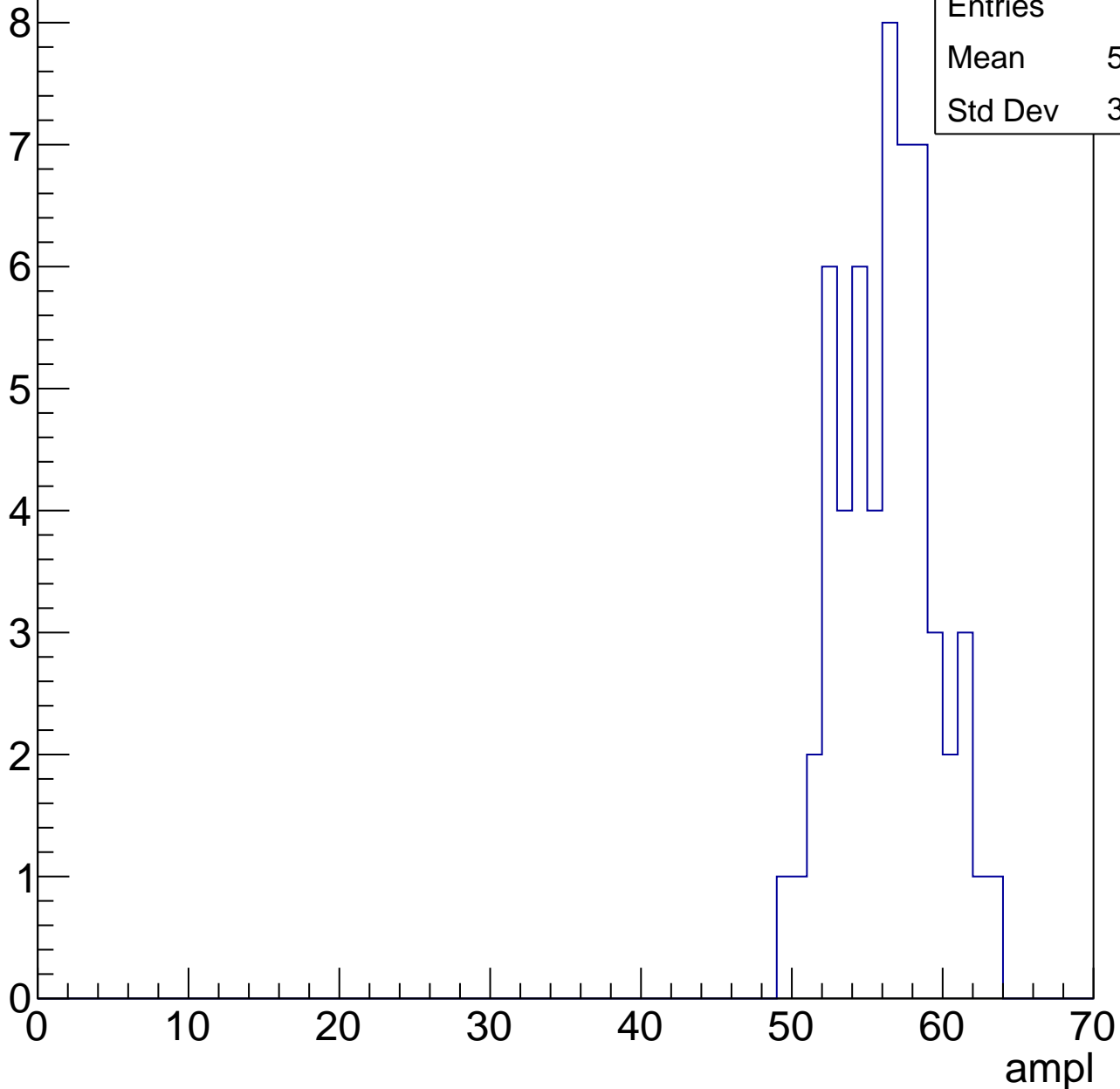


B0L001S, U19-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	55.84
Std Dev	3.127

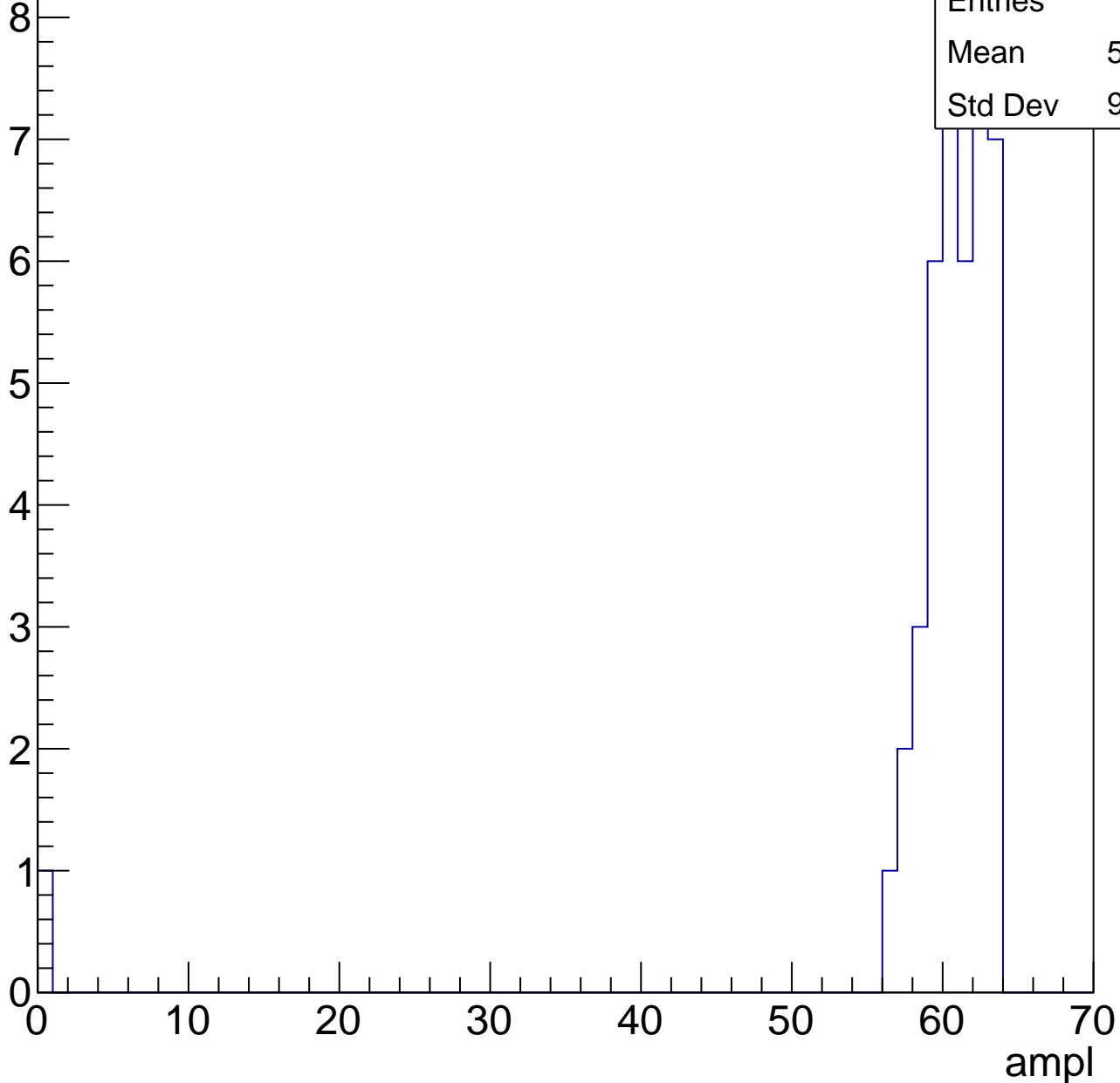


B0L001S, U19-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59.07
Std Dev	9.407



B0L001S, U19-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U19-ch127, adc0

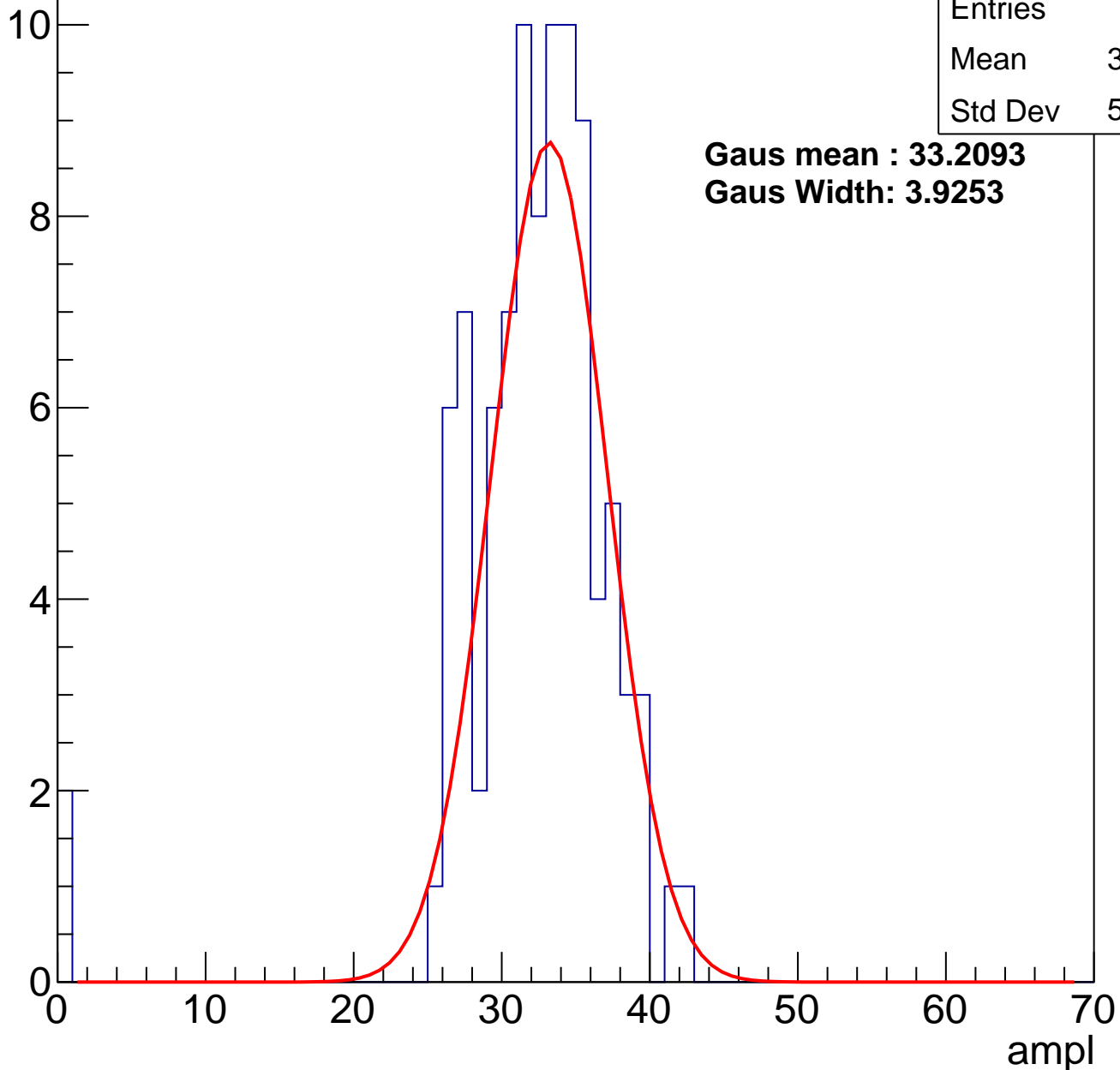
calib_packv5_042523_0143.root, FC#9, port A1

Entries	95
Mean	31.62
Std Dev	5.942

Gaus mean : 33.2093

Gaus Width: 3.9253

Entry



B0L001S, U19-ch127, adc1

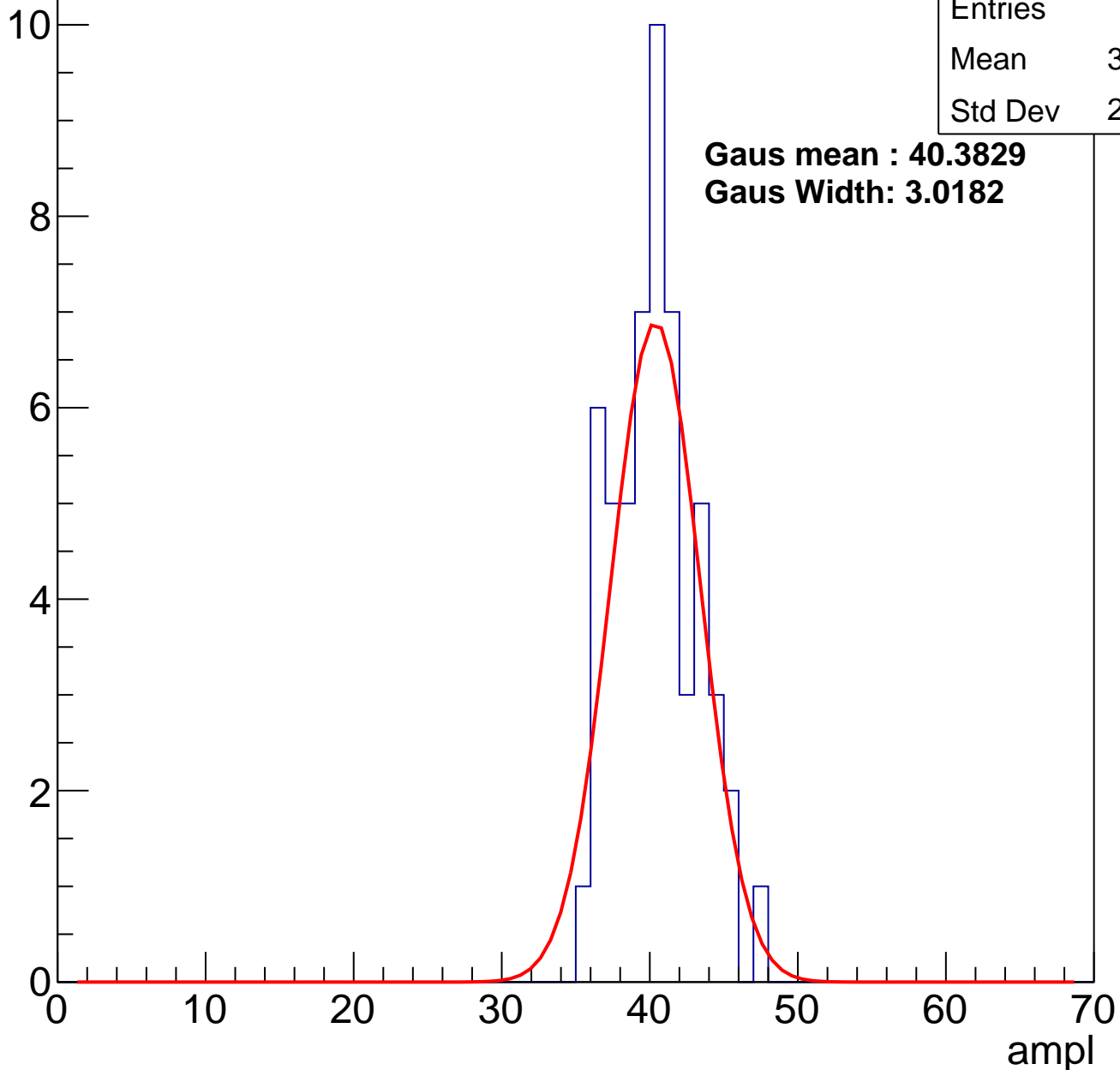
calib_packv5_042523_0143.root, FC#9, port A1

Entries	55
Mean	39.93
Std Dev	2.709

Gaus mean : 40.3829

Gaus Width: 3.0182

Entry



B0L001S, U19-ch127, adc2

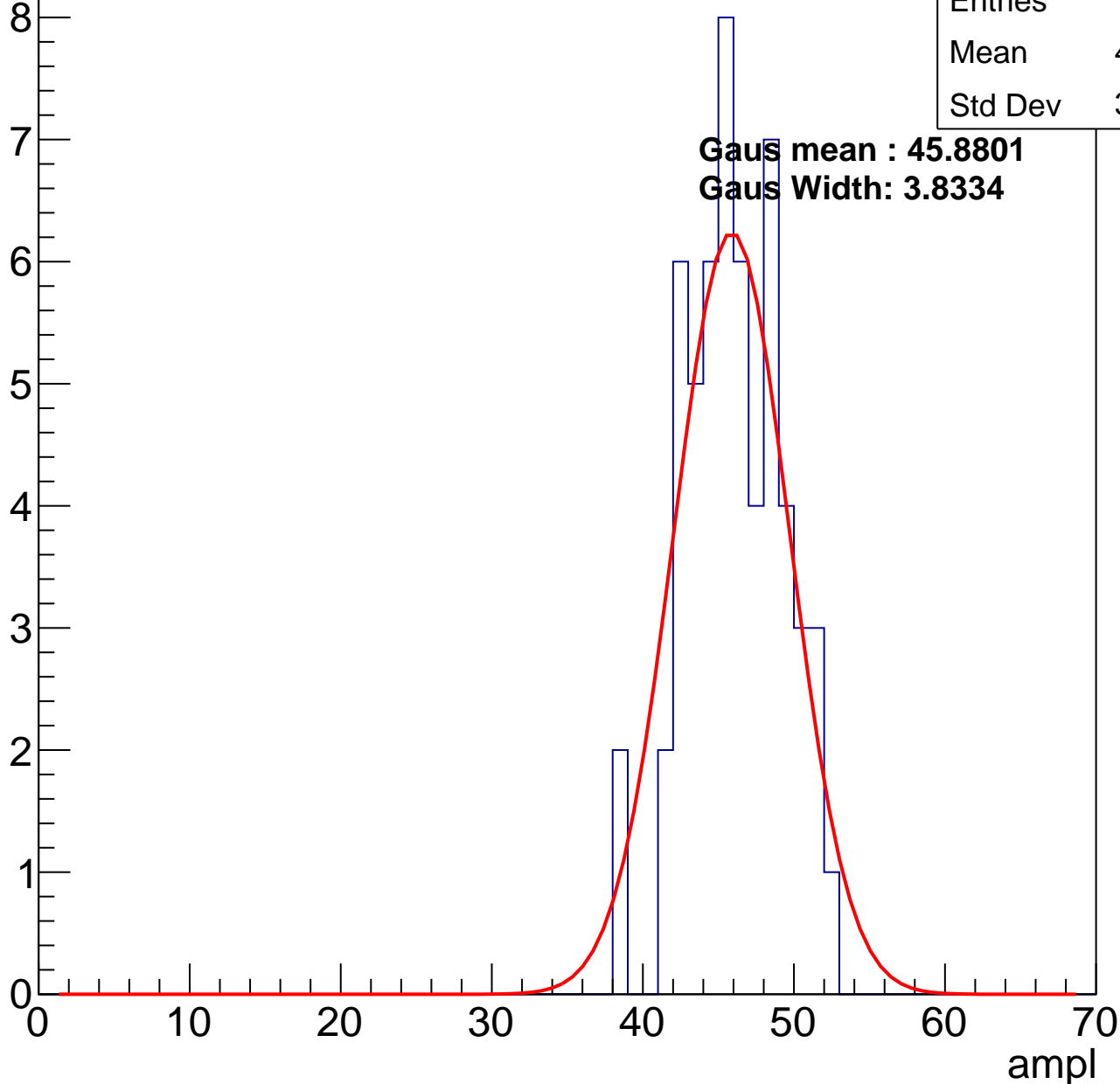
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	45.61
Std Dev	3.161

Gaus mean : 45.8801

Gaus Width: 3.8334

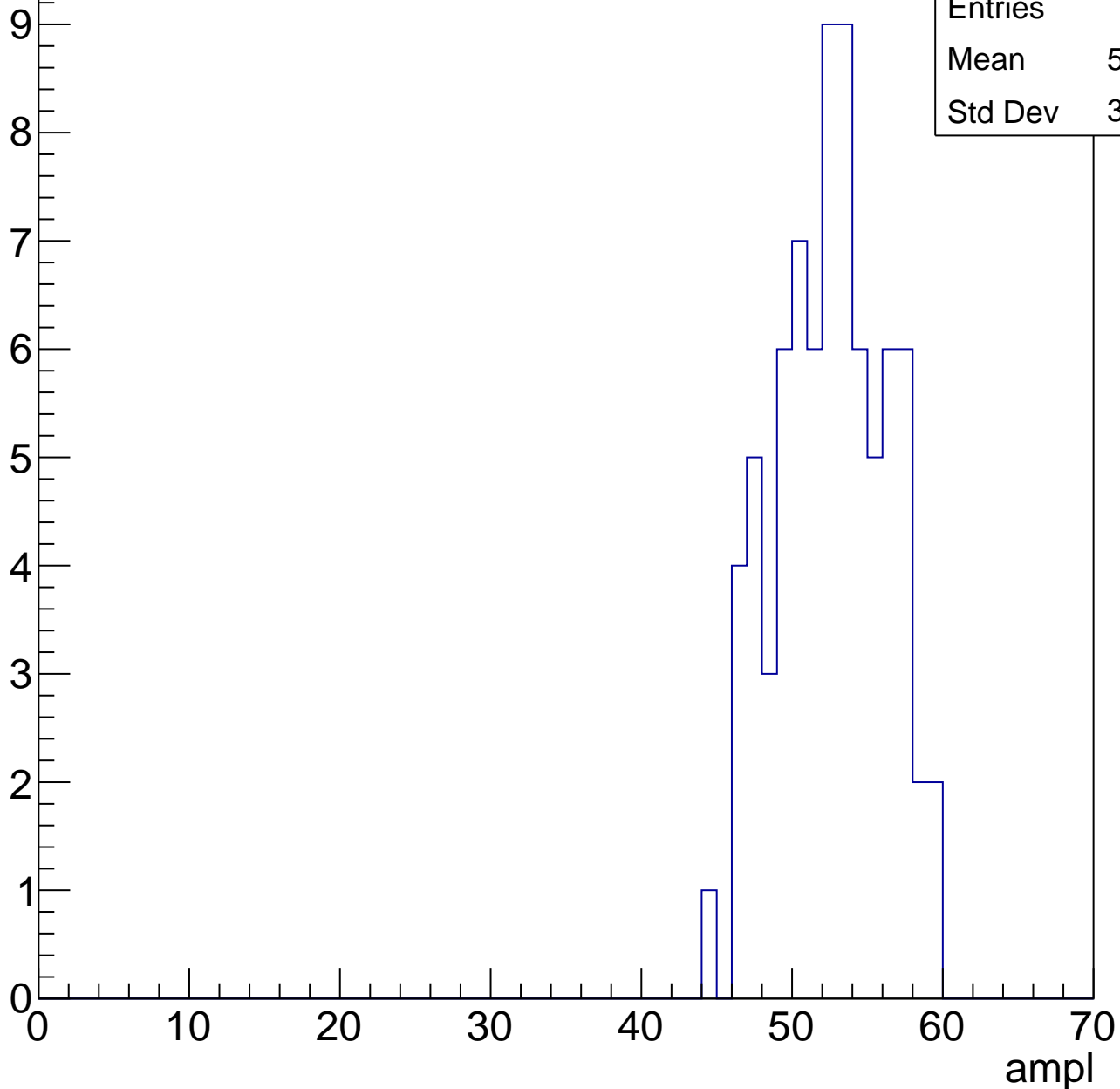


B0L001S, U19-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	52.12
Std Dev	3.535

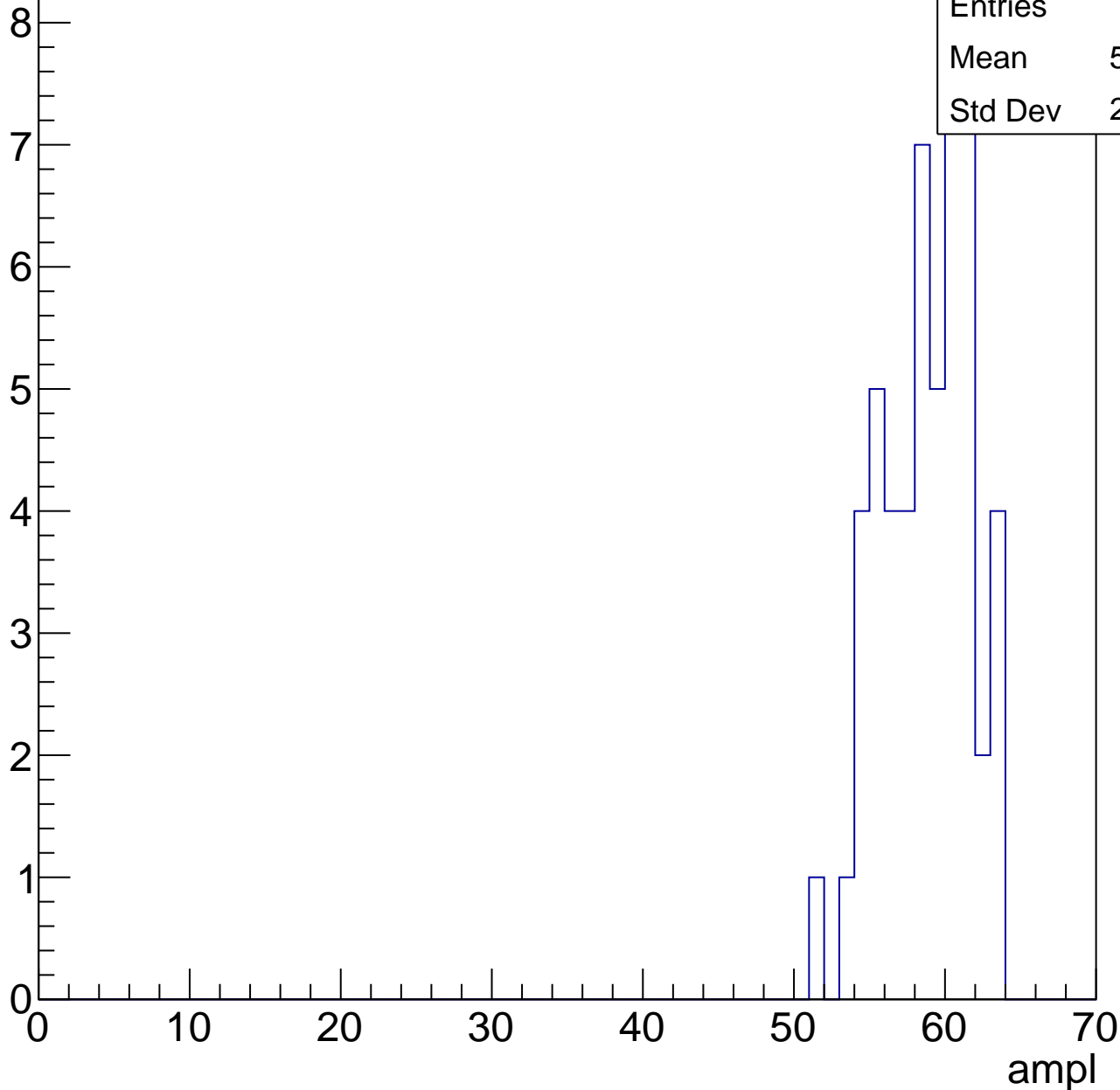


B0L001S, U19-ch127, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	58.34
Std Dev	2.874

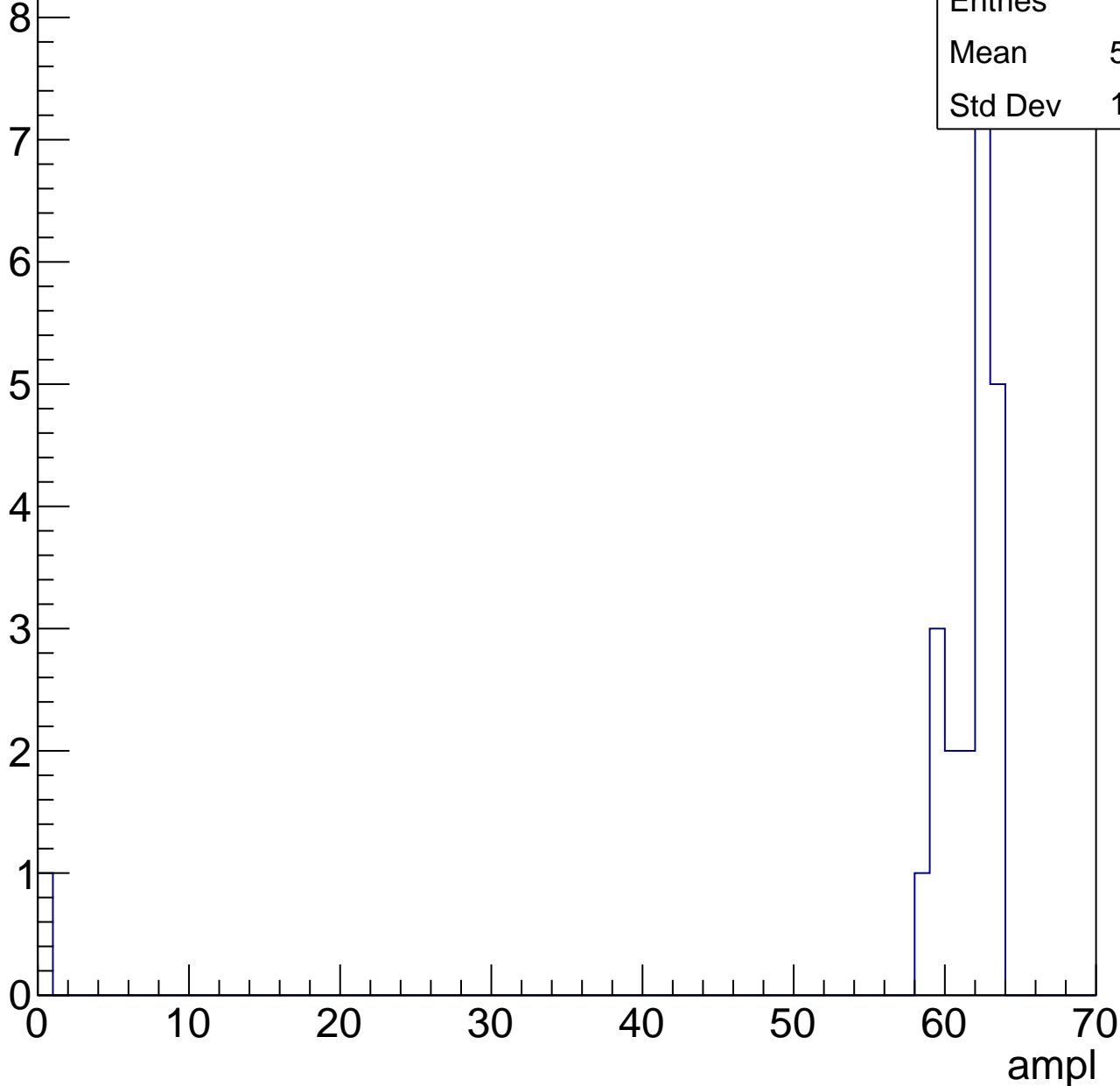


B0L001S, U19-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	58.55
Std Dev	12.86



B0L001S, U19-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

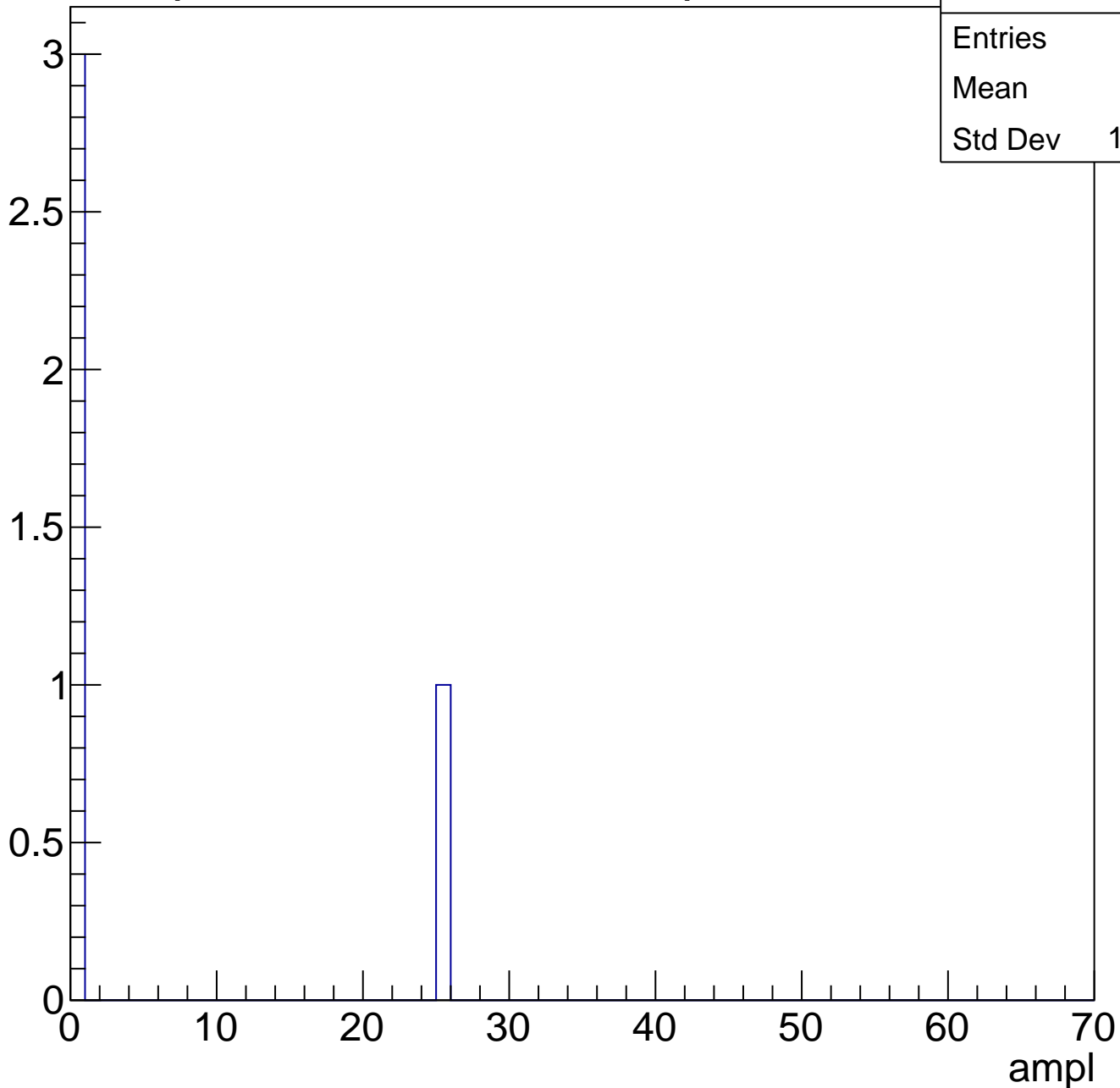
Entries	1
Mean	63
Std Dev	0

ampl

B0L001S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U19-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

