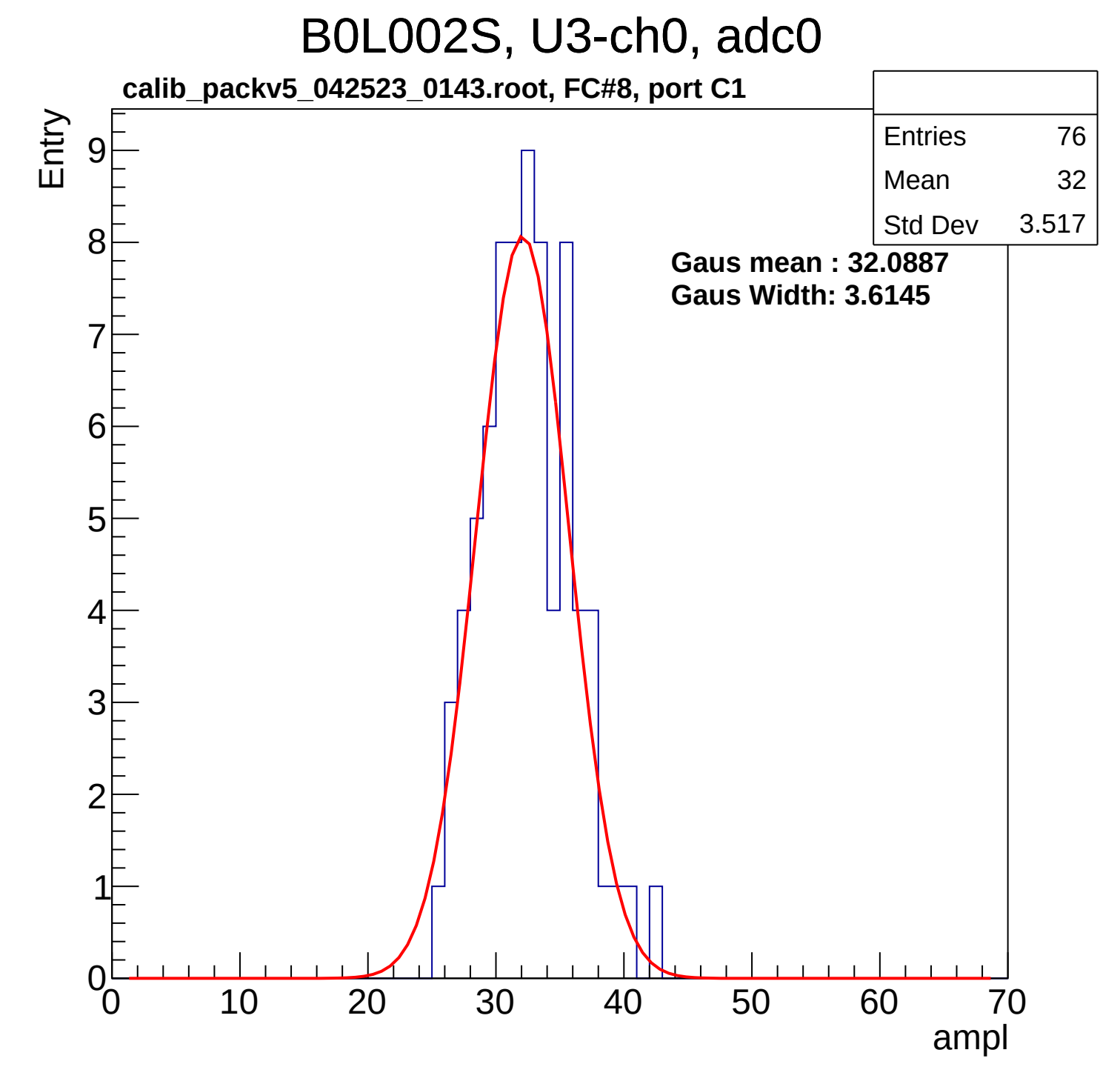
B0L002S, U3-ch0, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 32
Std Dev 3.517
Gaus mean : 32.0887
Gaus Width: 3.6145
Entry
ampl
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2
3
4
5
6
7
8
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0
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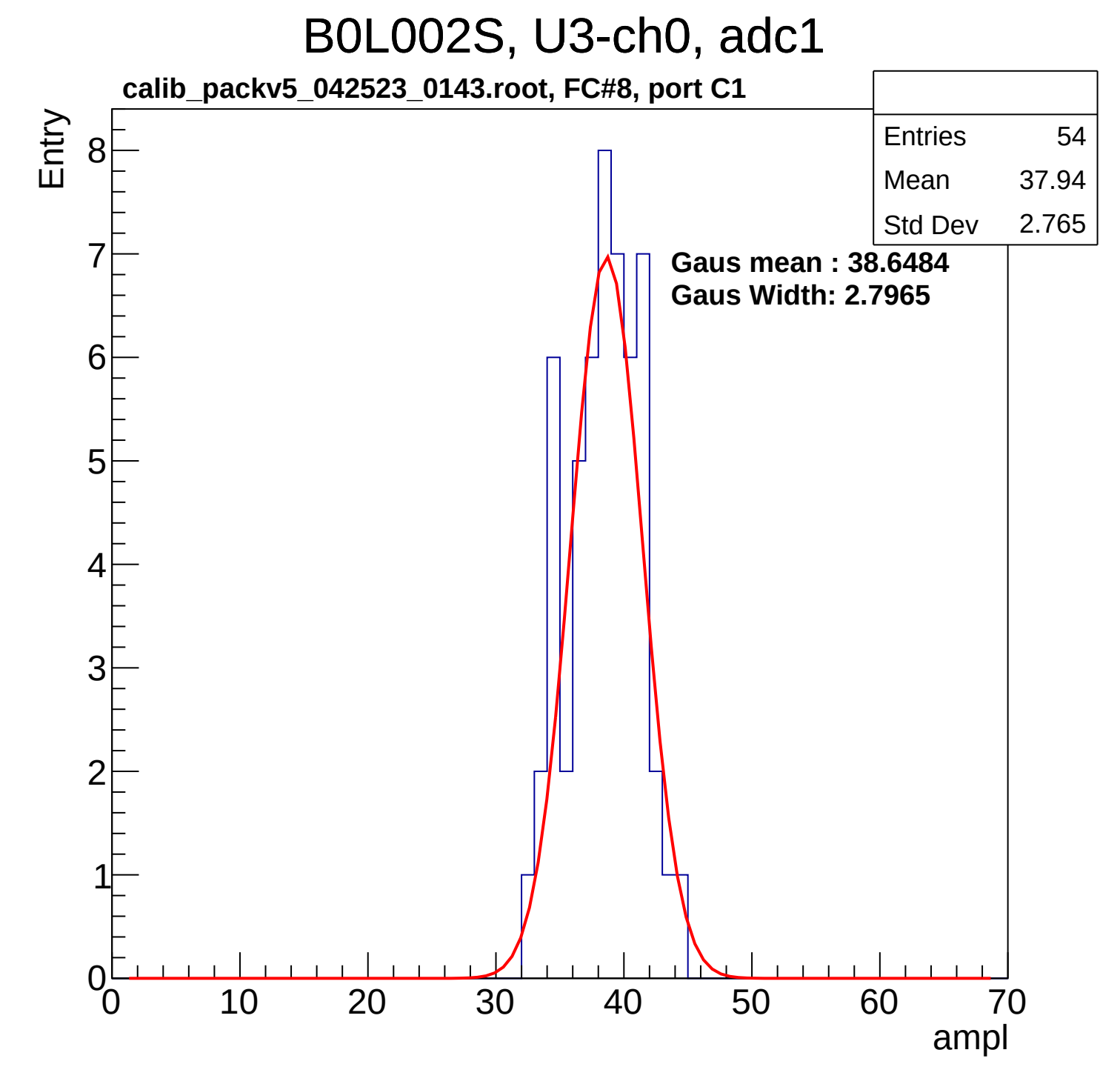
B0L002S, U3-ch0, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 37.94
Std Dev 2.765
Gaus mean : 38.6484
Gaus Width: 2.7965
Entry
ampl
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1
2
3
4
5
6
7
8
0
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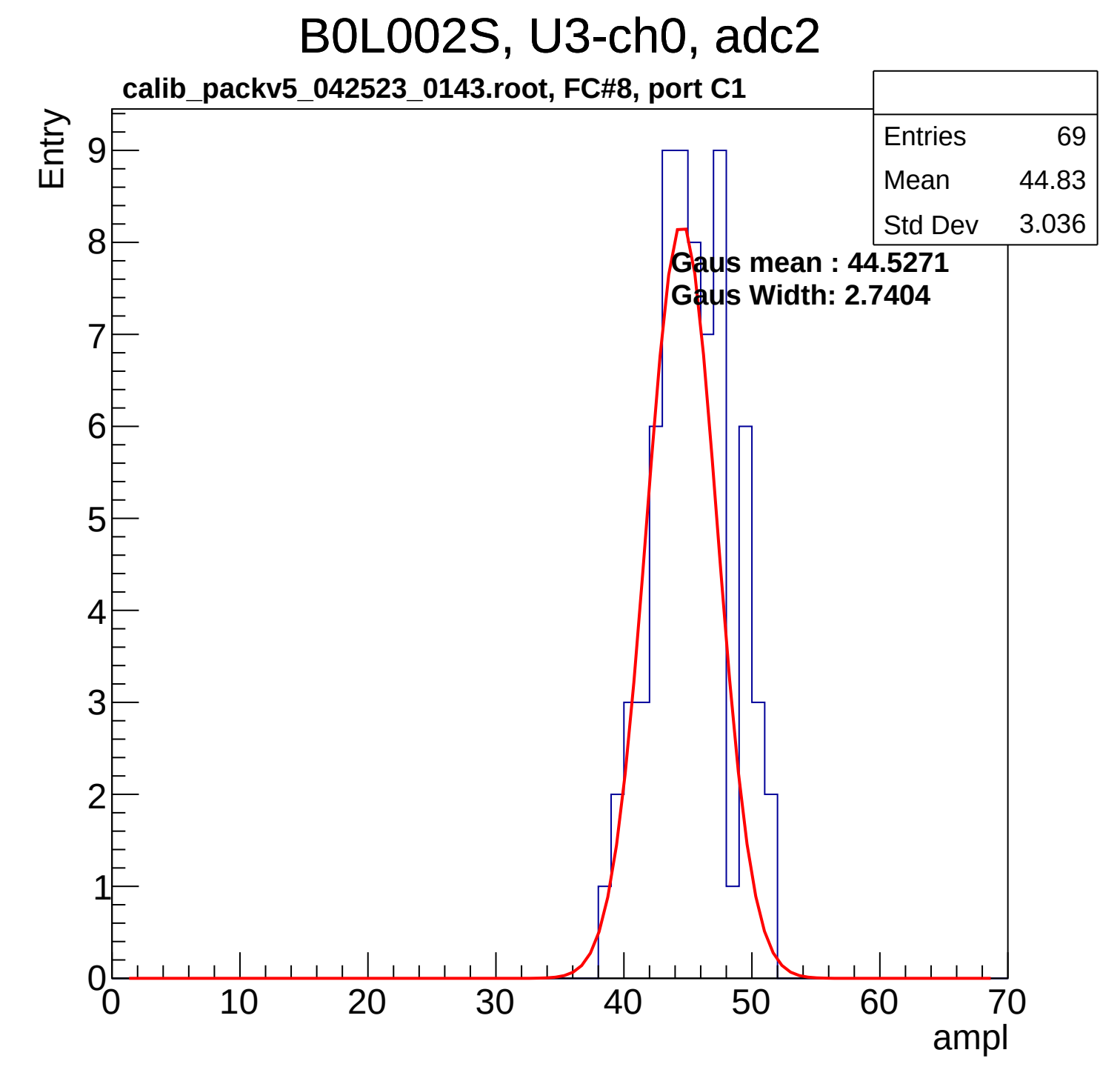
B0L002S, U3-ch0, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.83
Std Dev 3.036
Gaus mean : 44.5271
Gaus Width: 2.7404
Entry
ampl
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2
3
4
5
6
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0
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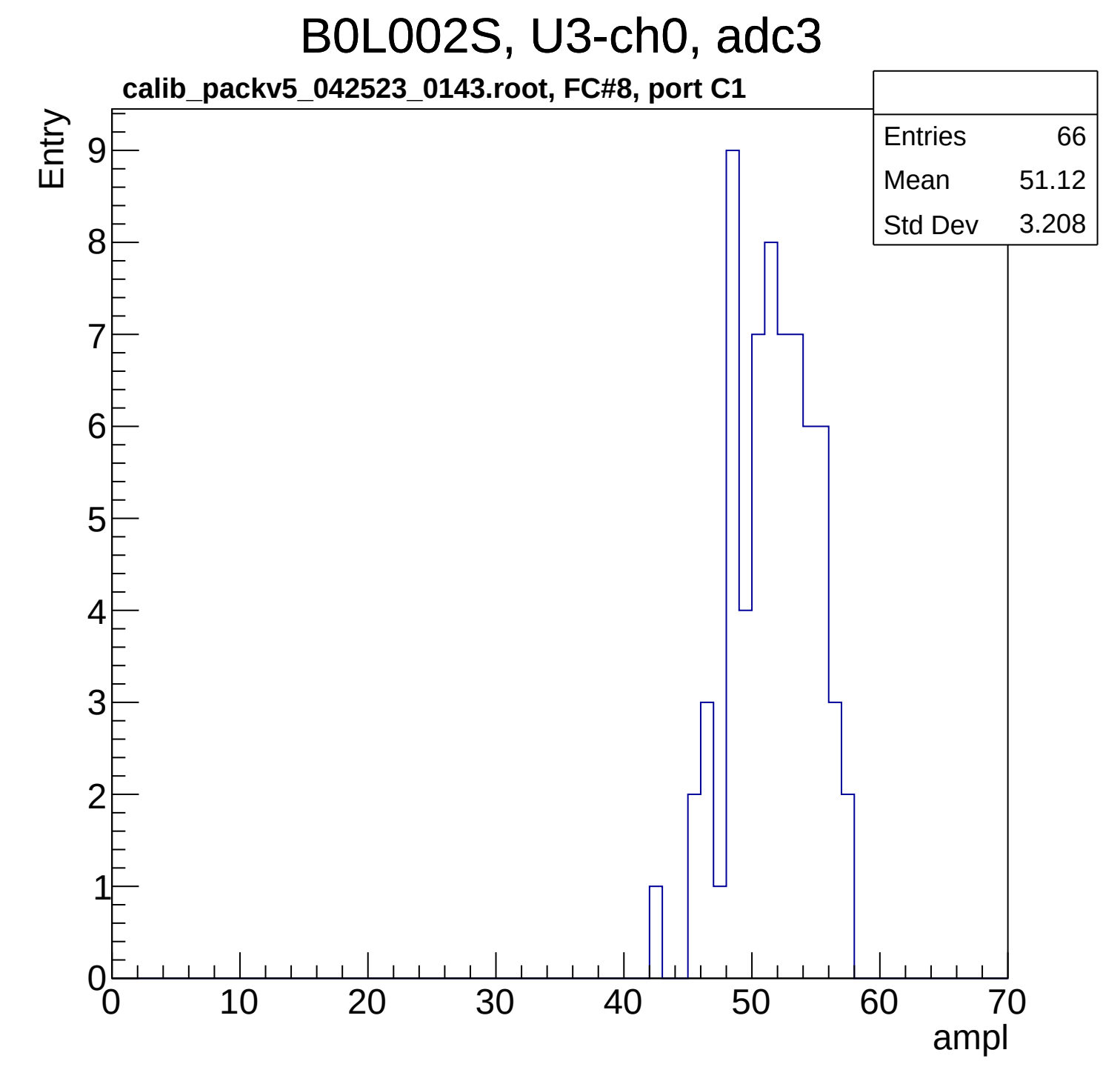
B0L002S, U3-ch0, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.12
Std Dev 3.208
Entry
ampl
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3
4
5
6
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0
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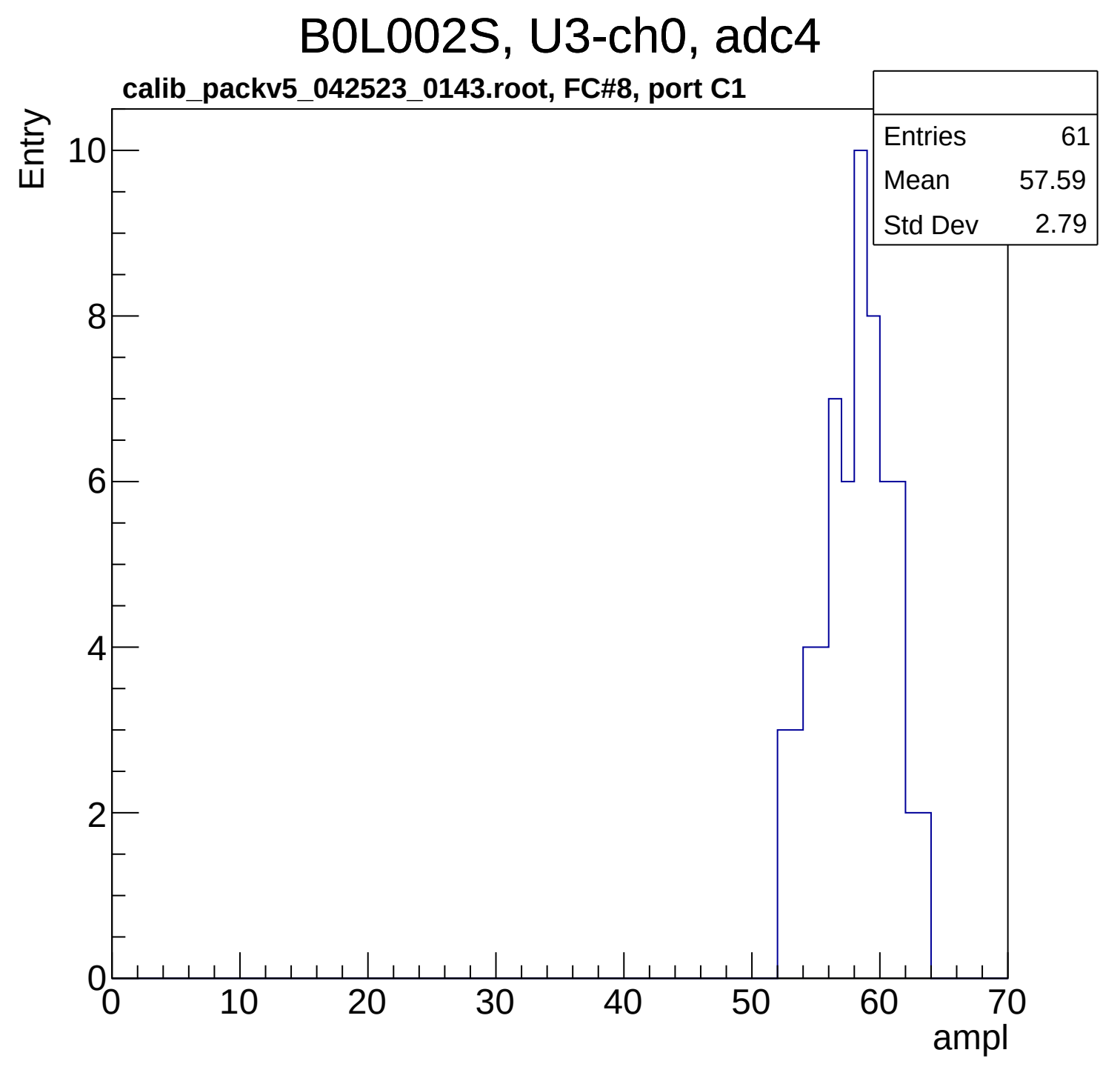
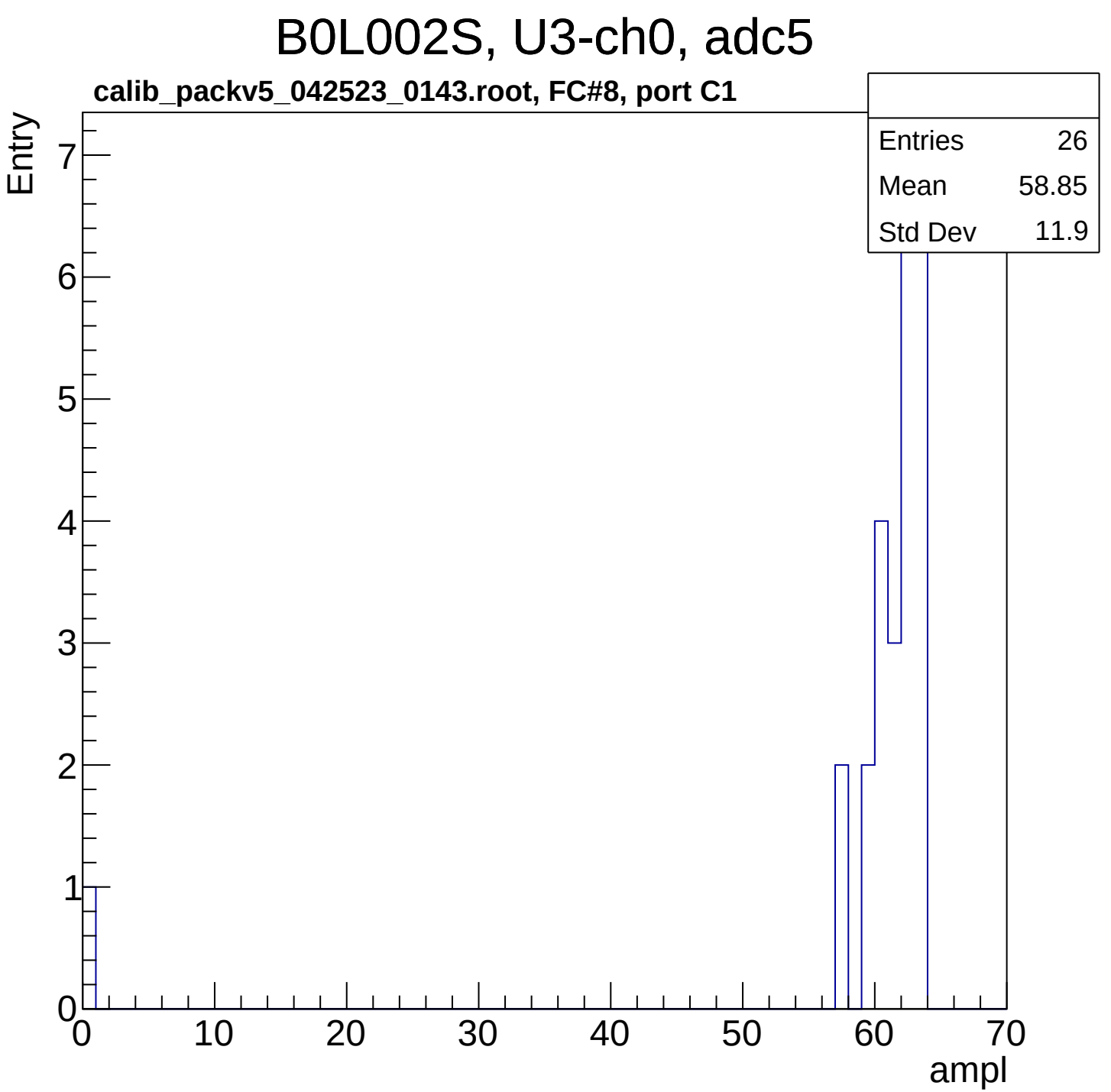

B0L002S, U3-ch0, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.59
Std Dev 2.79
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch0, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.85
Std Dev 11.9
Entry
0
1
2
3
4
5
6
7
0
10
20
30
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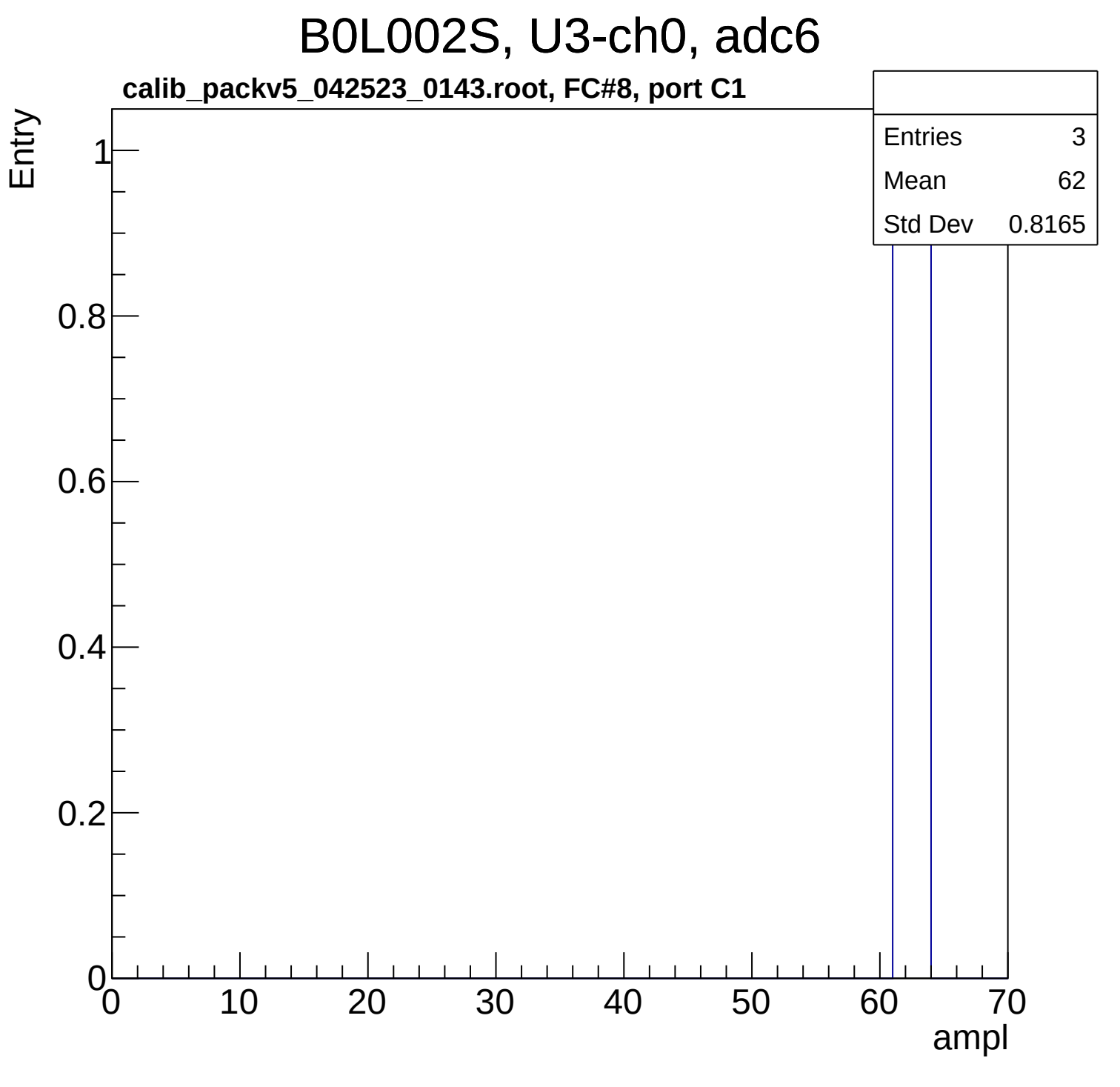
50
60
70
ampl


B0L002S, U3-ch0, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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0.6
0.4
0.2
0
0
10
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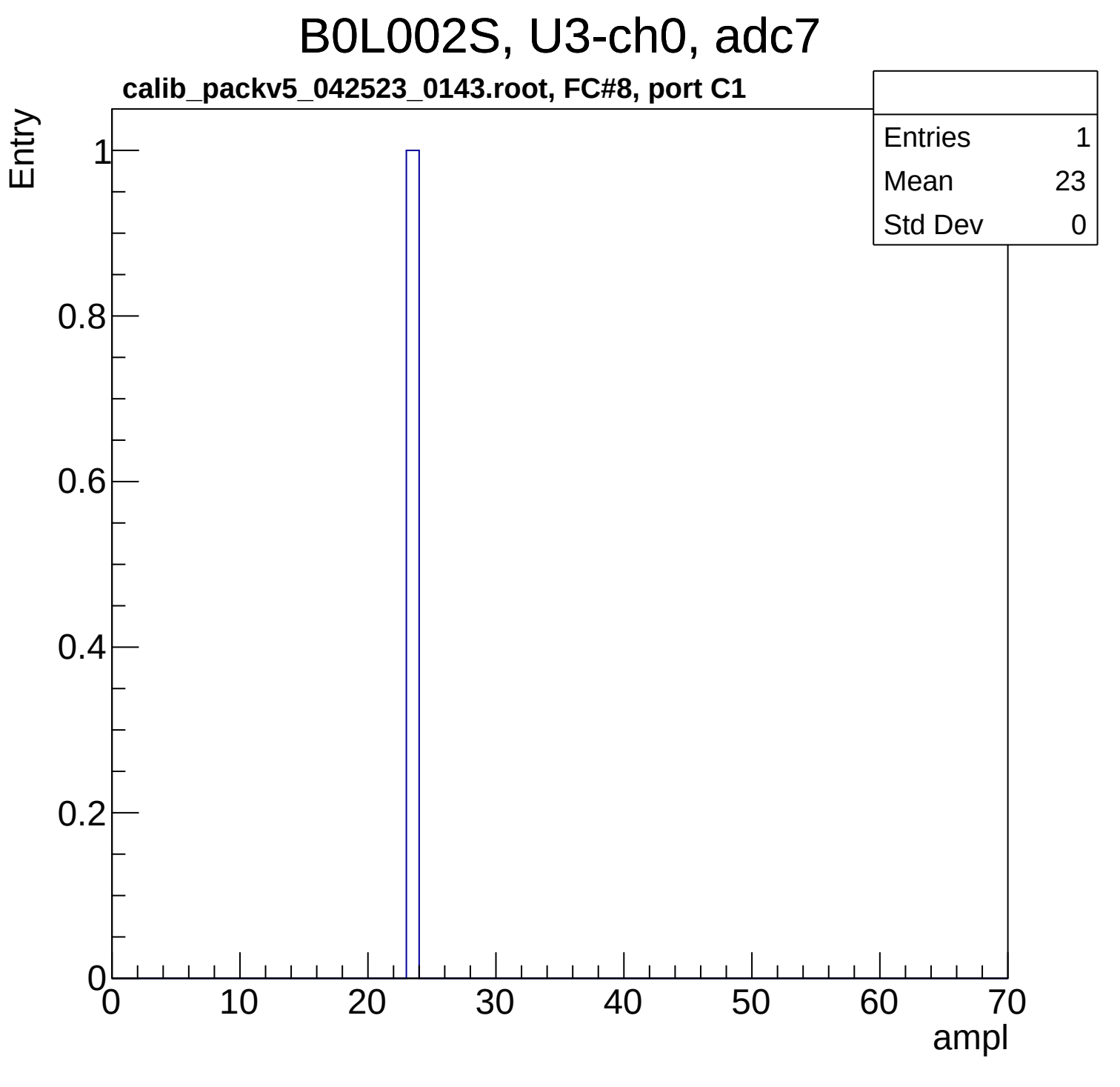
50
60
70
ampl


B0L002S, U3-ch0, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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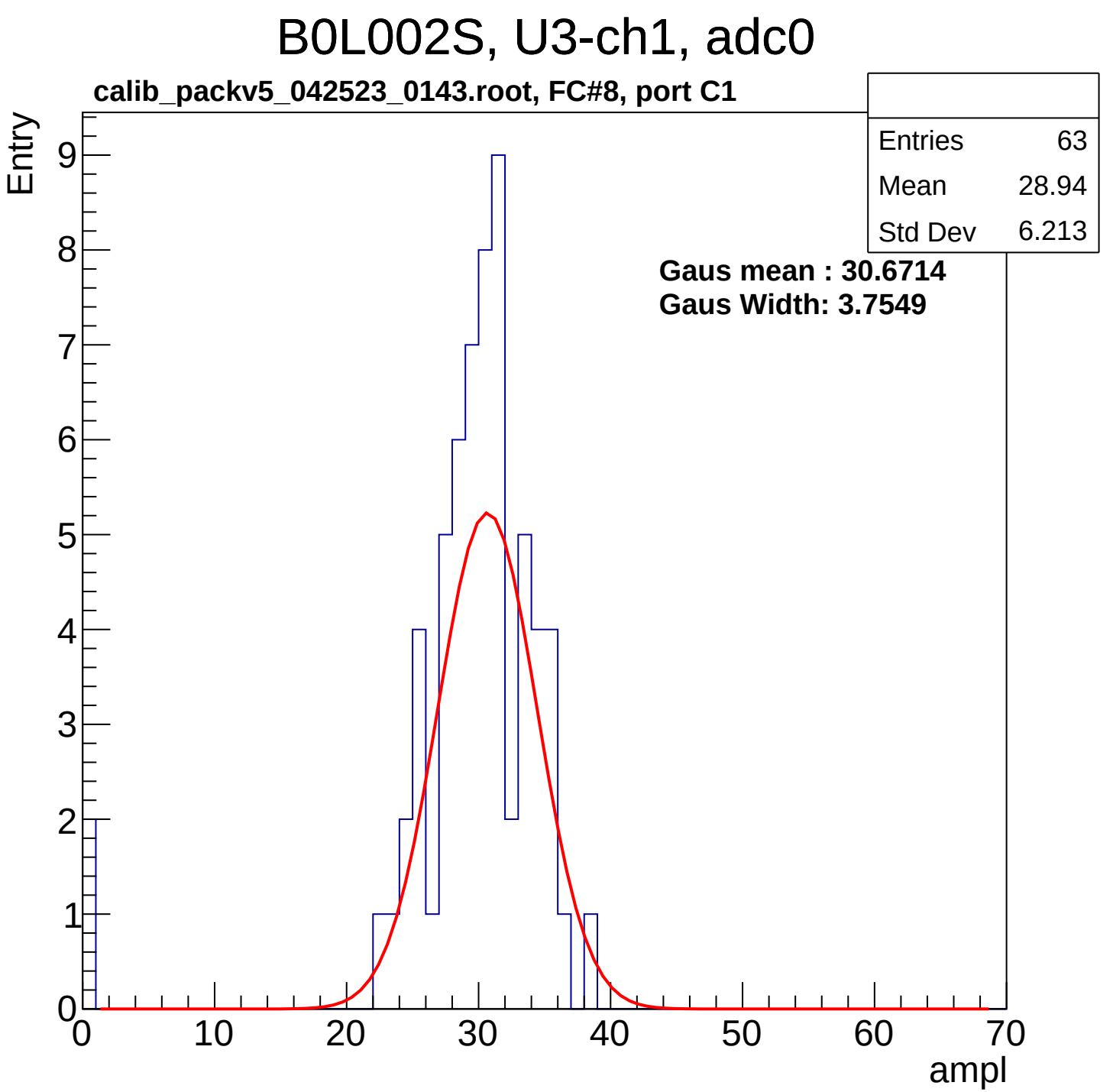
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch1, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 28.94
Std Dev 6.213
Gaus mean : 30.6714
Gaus Width: 3.7549
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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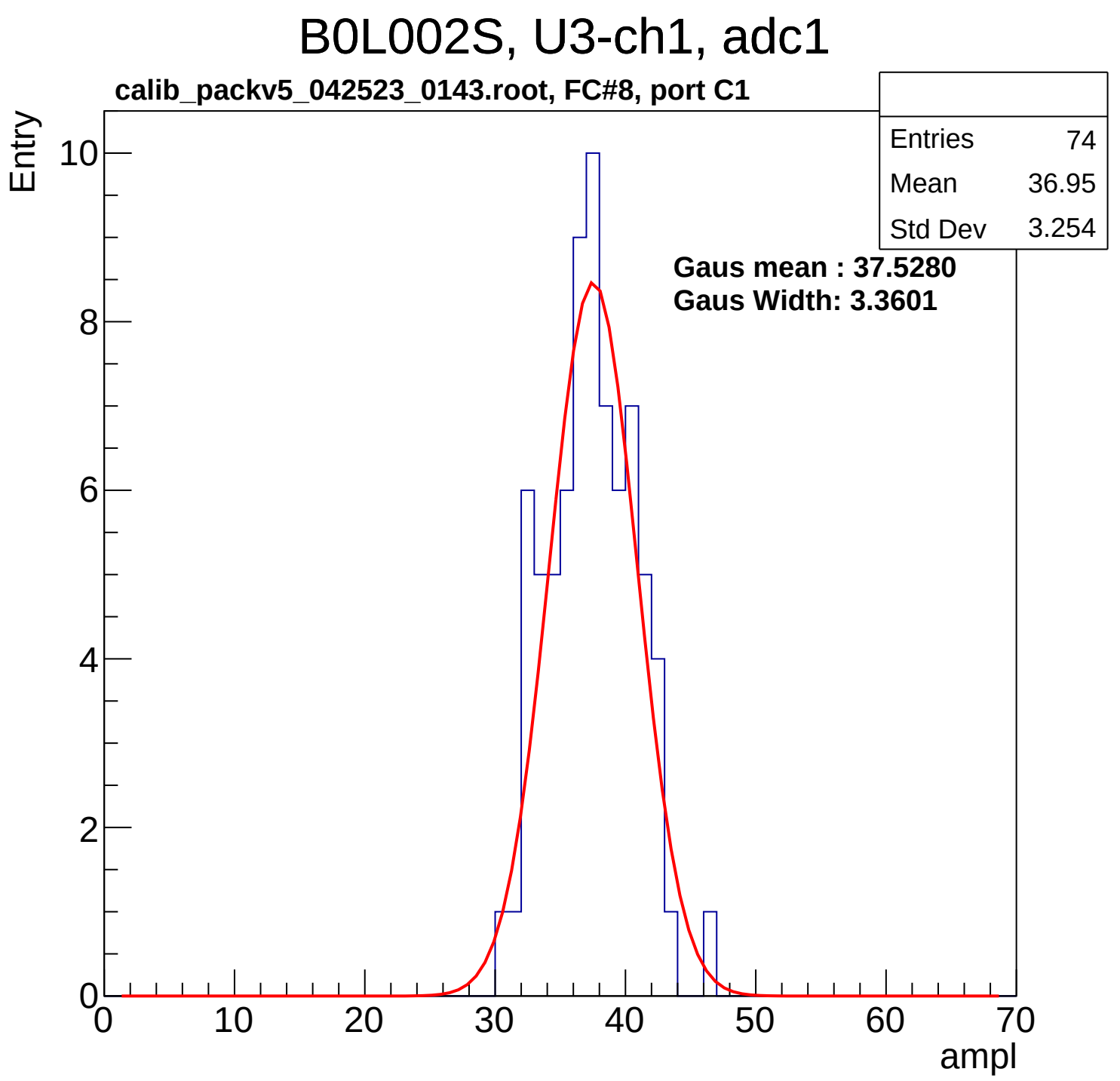
B0L002S, U3-ch1, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 36.95
Std Dev 3.254
Gaus mean : 37.5280
Gaus Width: 3.3601
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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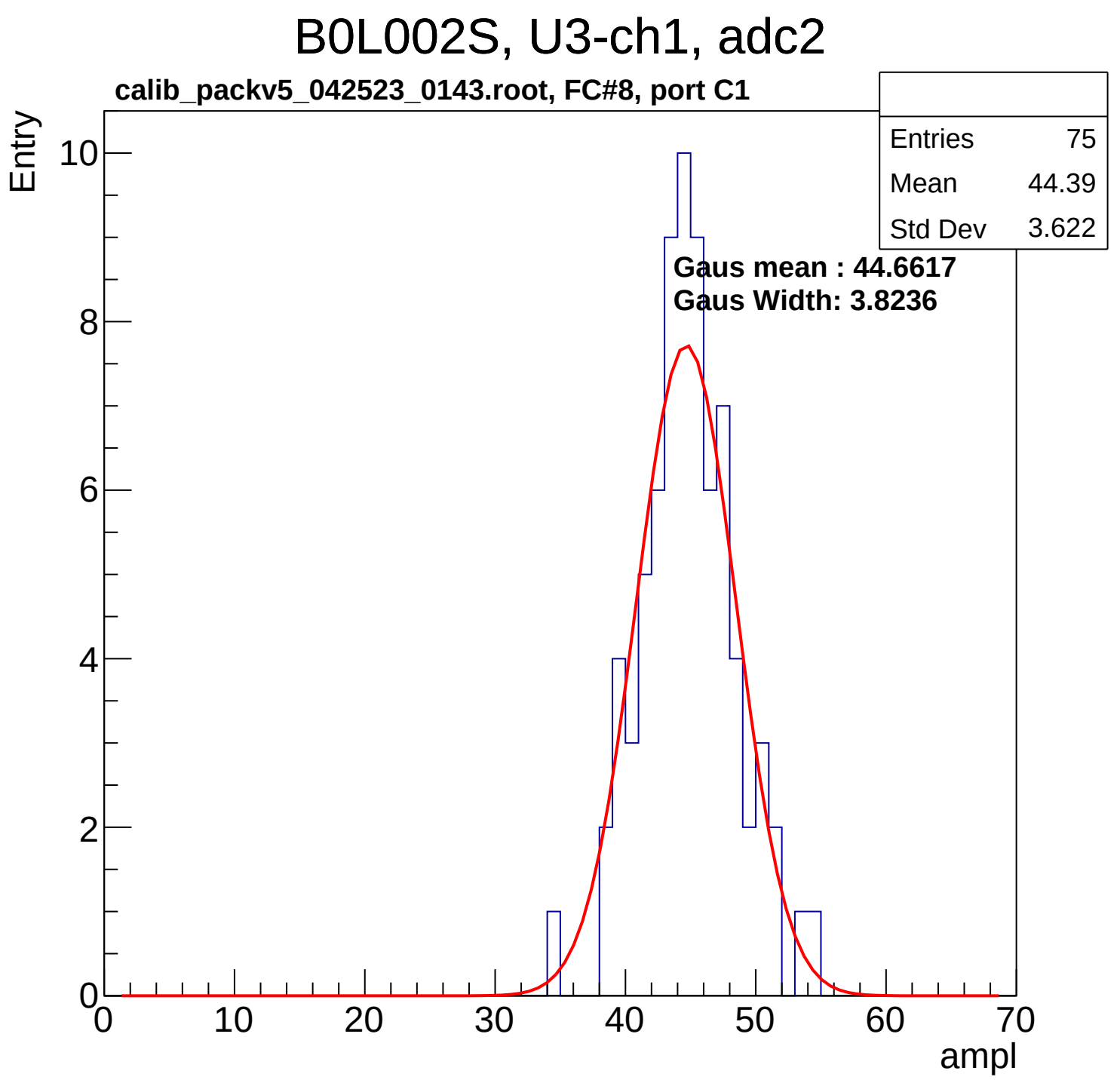
B0L002S, U3-ch1, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 44.39
Std Dev 3.622
Gaus mean : 44.6617
Gaus Width: 3.8236
Entry
ampl
0
2
4
6
8
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0
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40
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60
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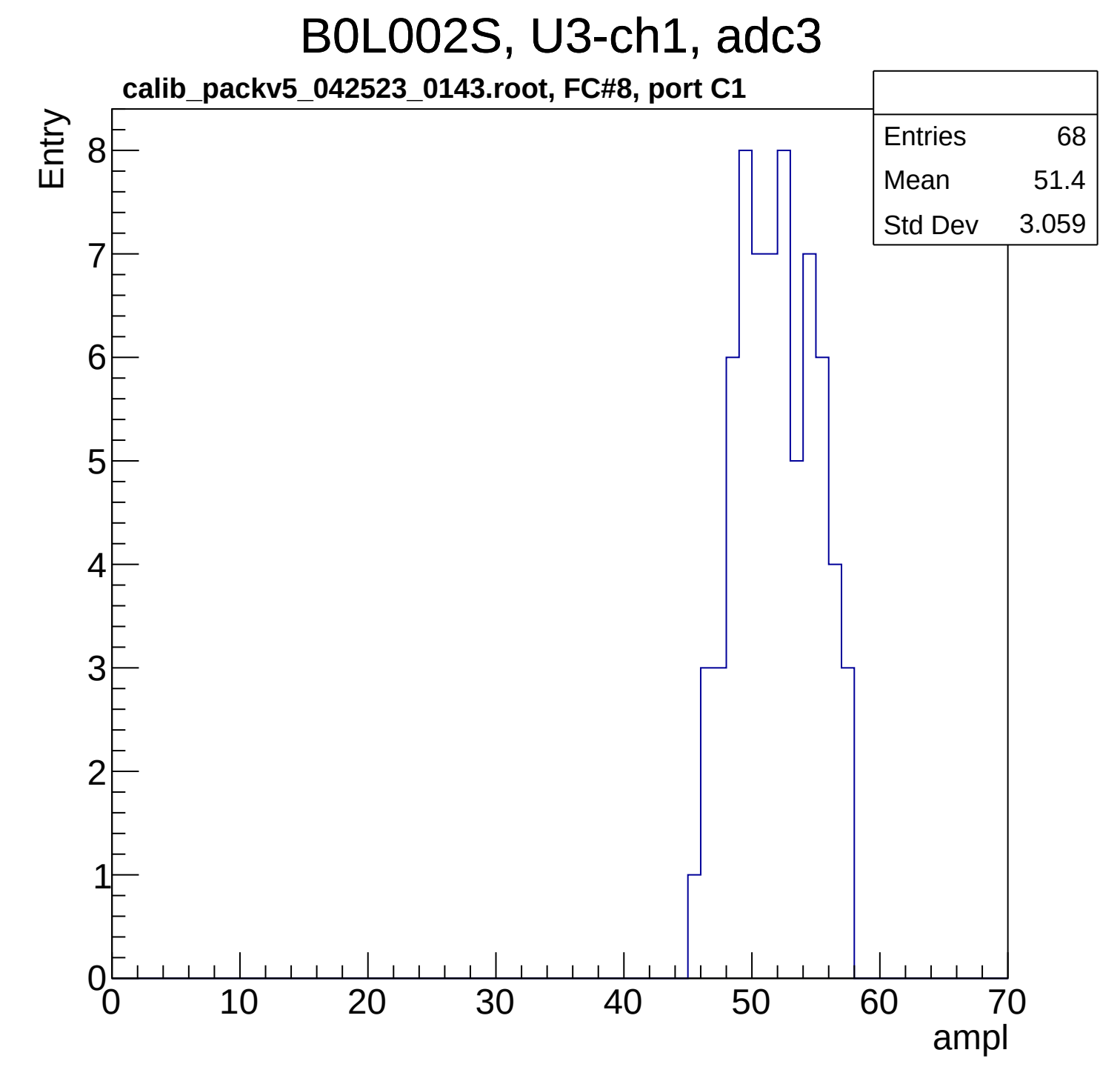
B0L002S, U3-ch1, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.4
Std Dev 3.059
Entry
ampl
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1
2
3
4
5
6
7
8
0
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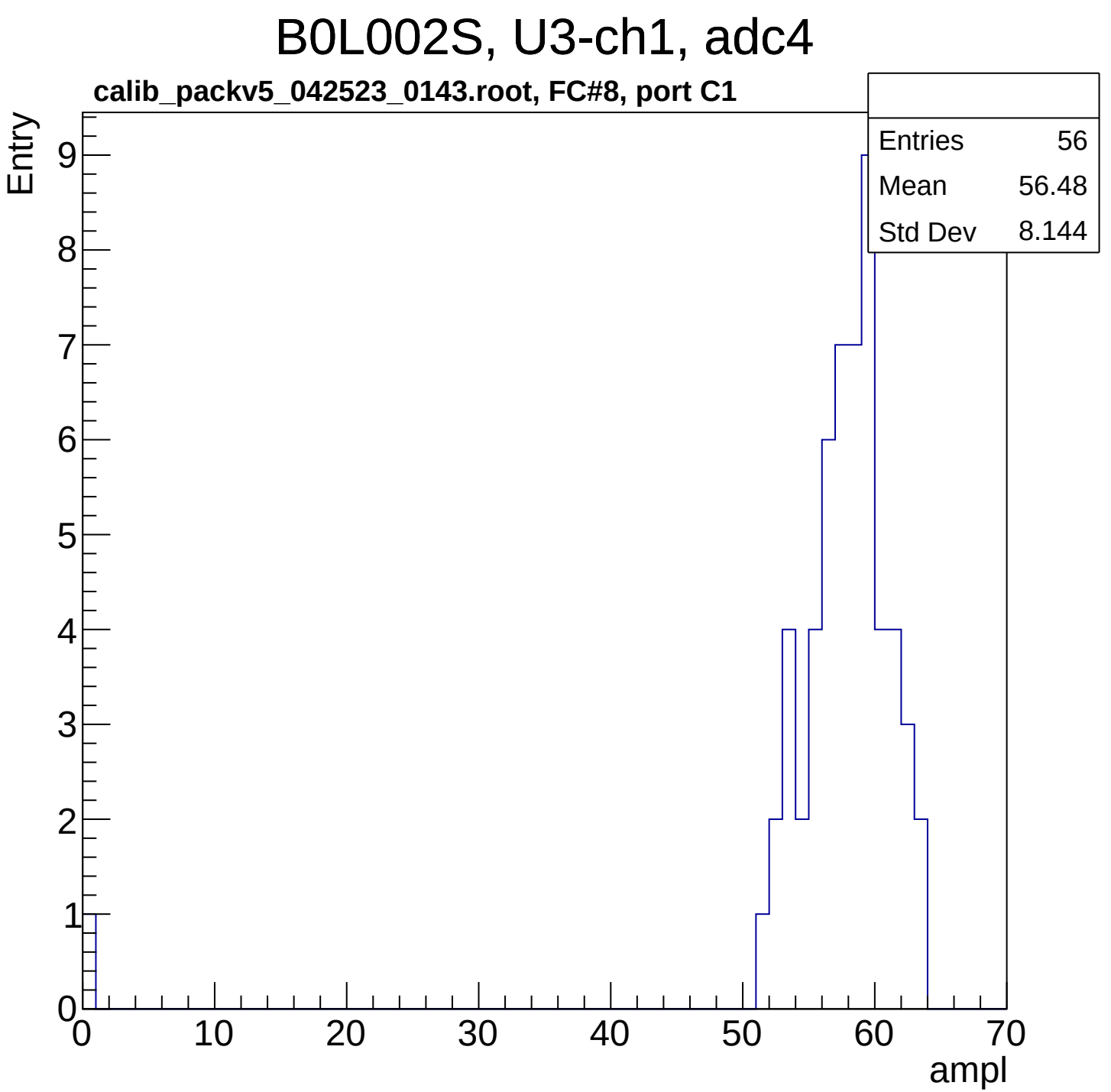
B0L002S, U3-ch1, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.48
Std Dev 8.144
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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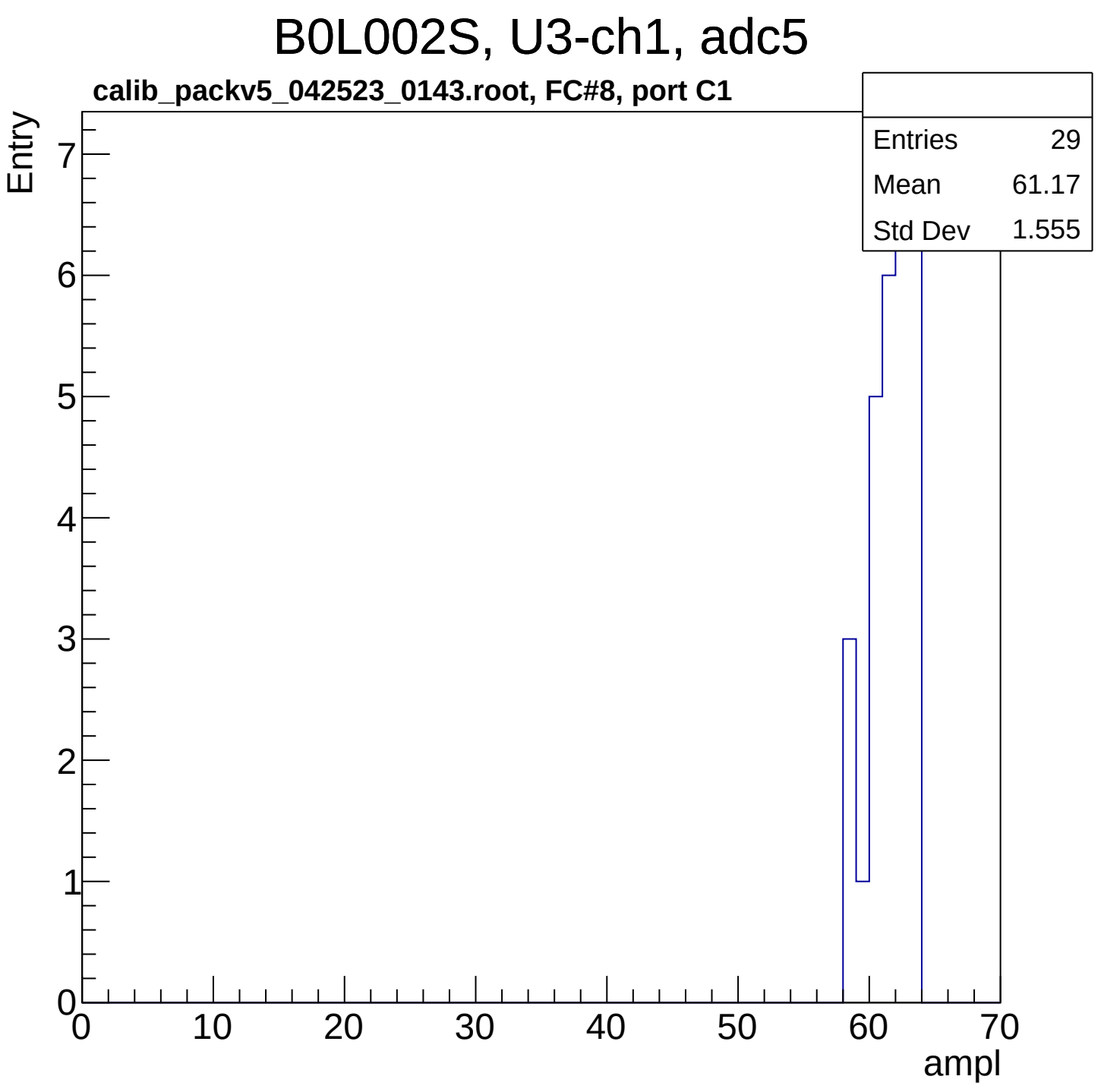
B0L002S, U3-ch1, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 61.17
Std Dev 1.555
Entry
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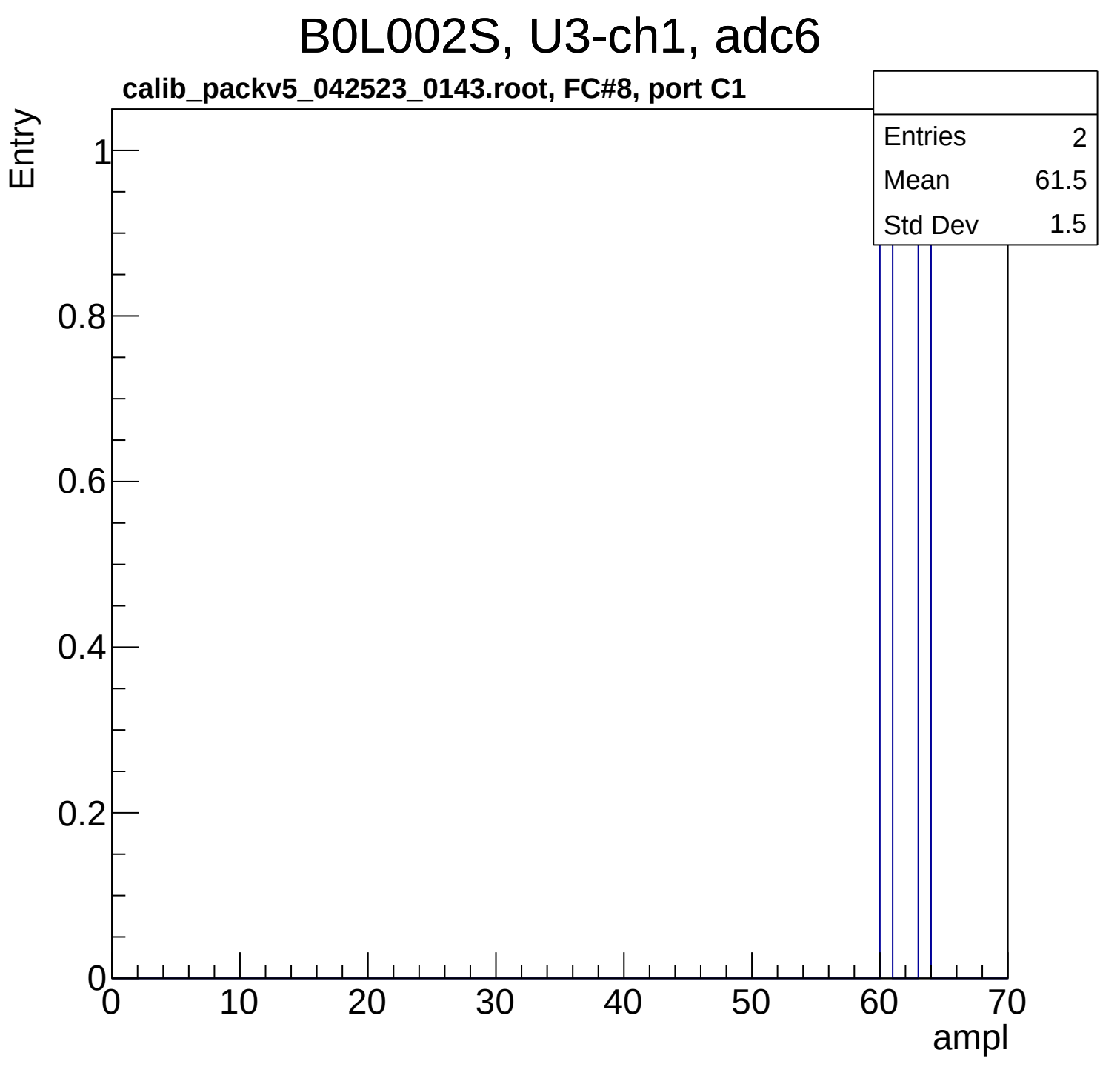
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch1, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
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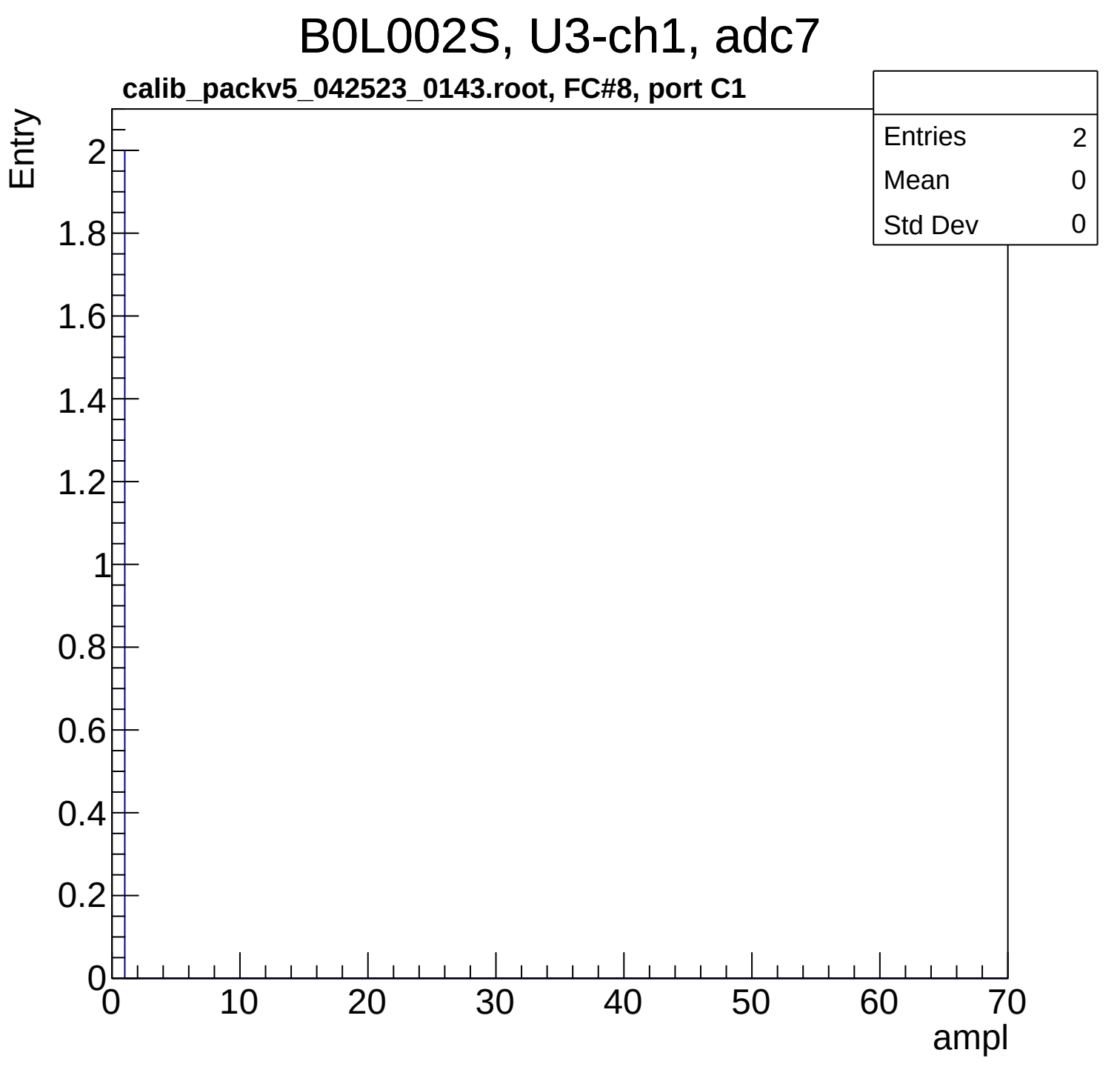
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch1, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
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30
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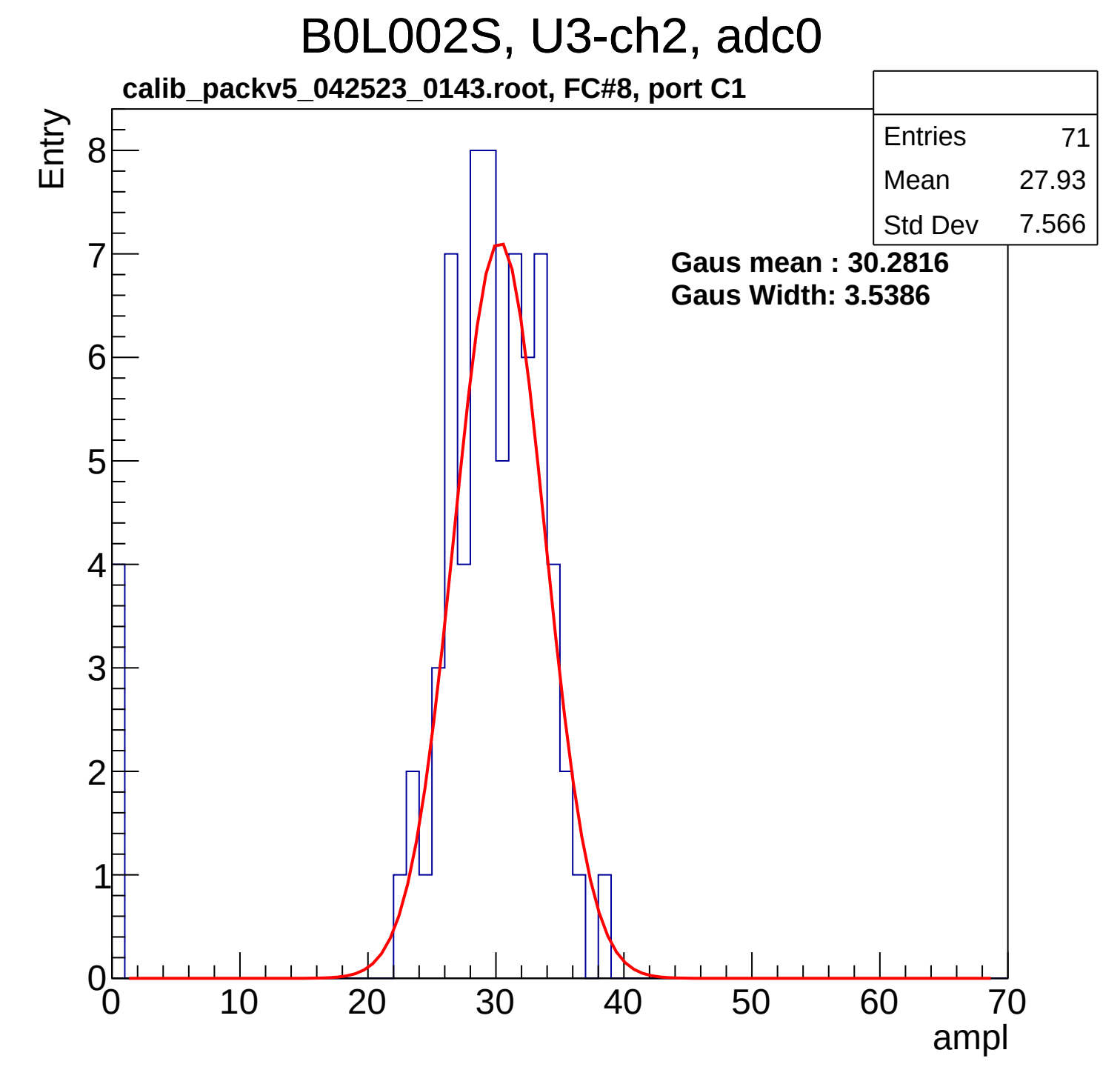
50
60
70
ampl


B0L002S, U3-ch2, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 27.93
Std Dev 7.566
Gaus mean : 30.2816
Gaus Width: 3.5386
Entry
ampl
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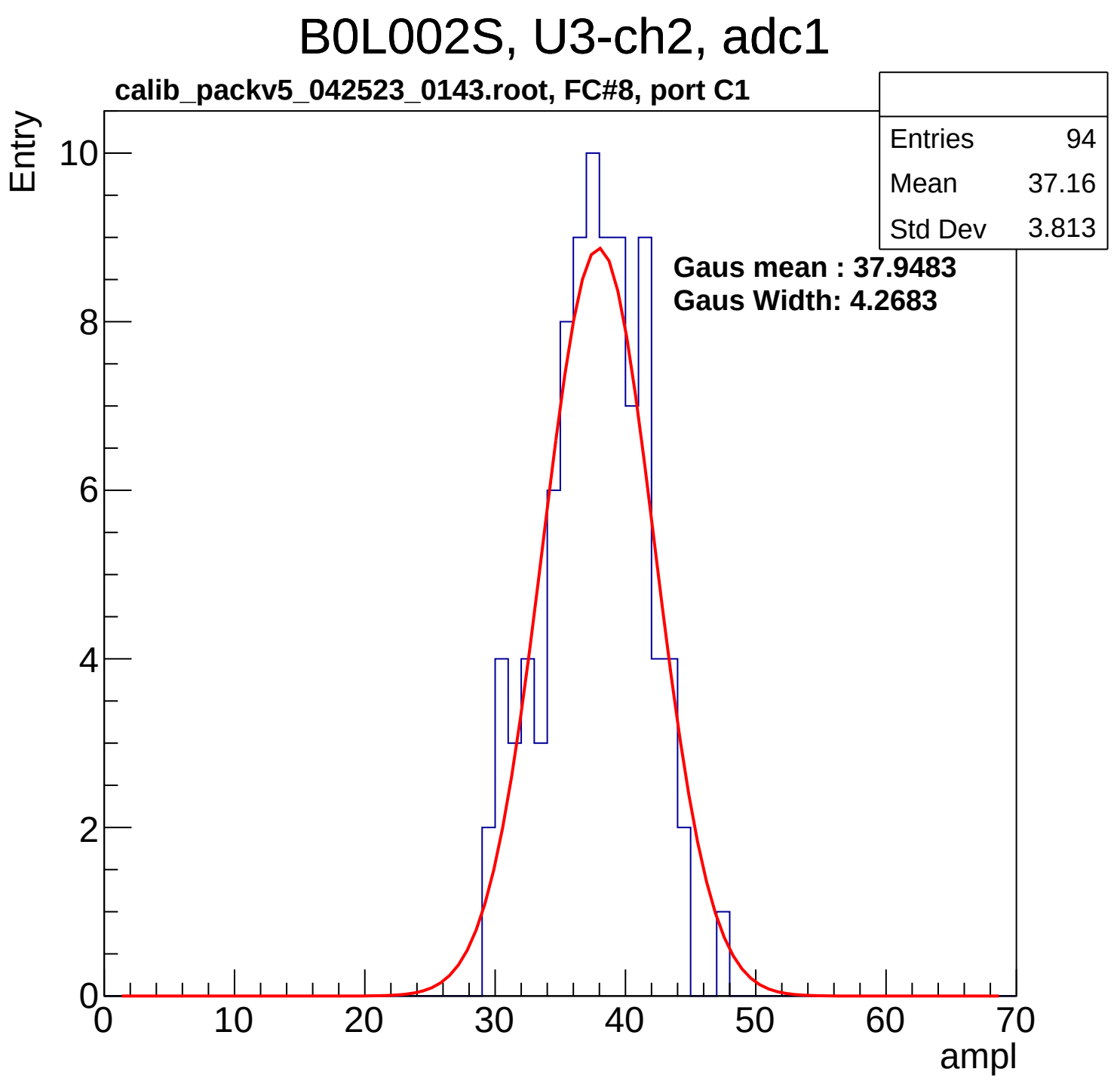
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch2, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 94
Mean 37.16
Std Dev 3.813
Gaus mean : 37.9483
Gaus Width: 4.2683
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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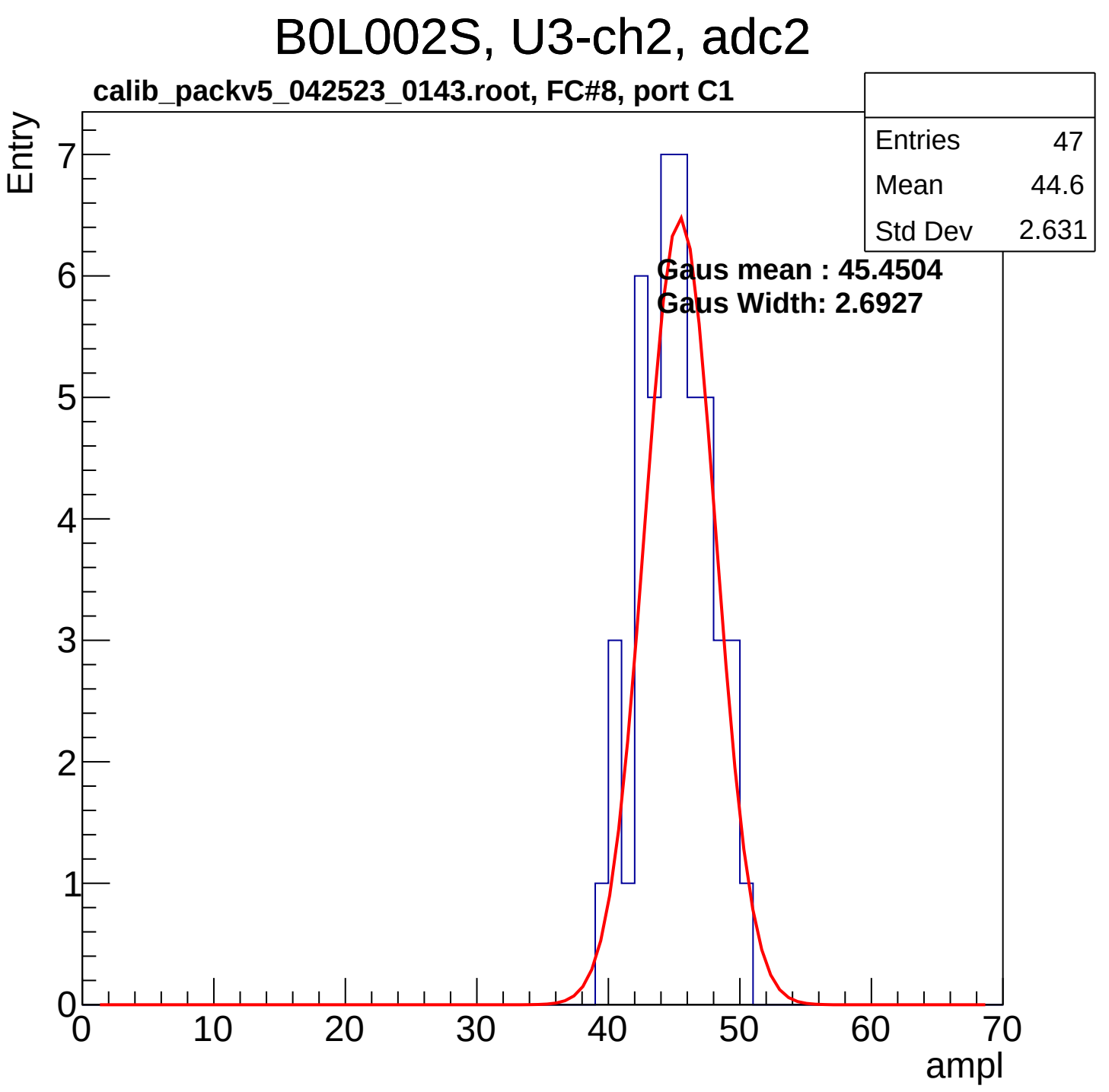
B0L002S, U3-ch2, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 44.6
Std Dev 2.631
Gaus mean : 45.4504
Gaus Width: 2.6927
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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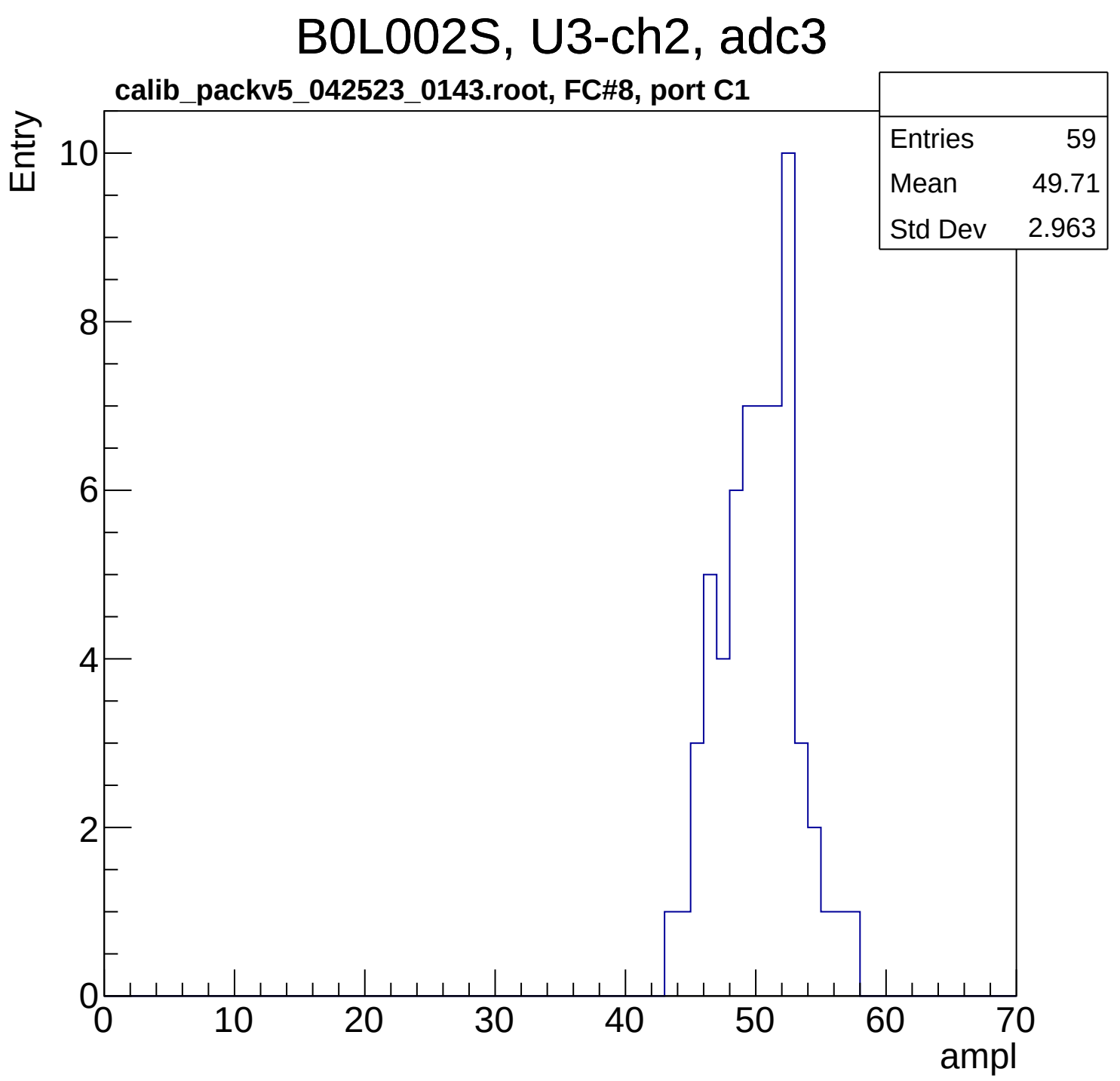
B0L002S, U3-ch2, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 49.71
Std Dev 2.963
Entry
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2
4
6
8
10
0
10
20
30
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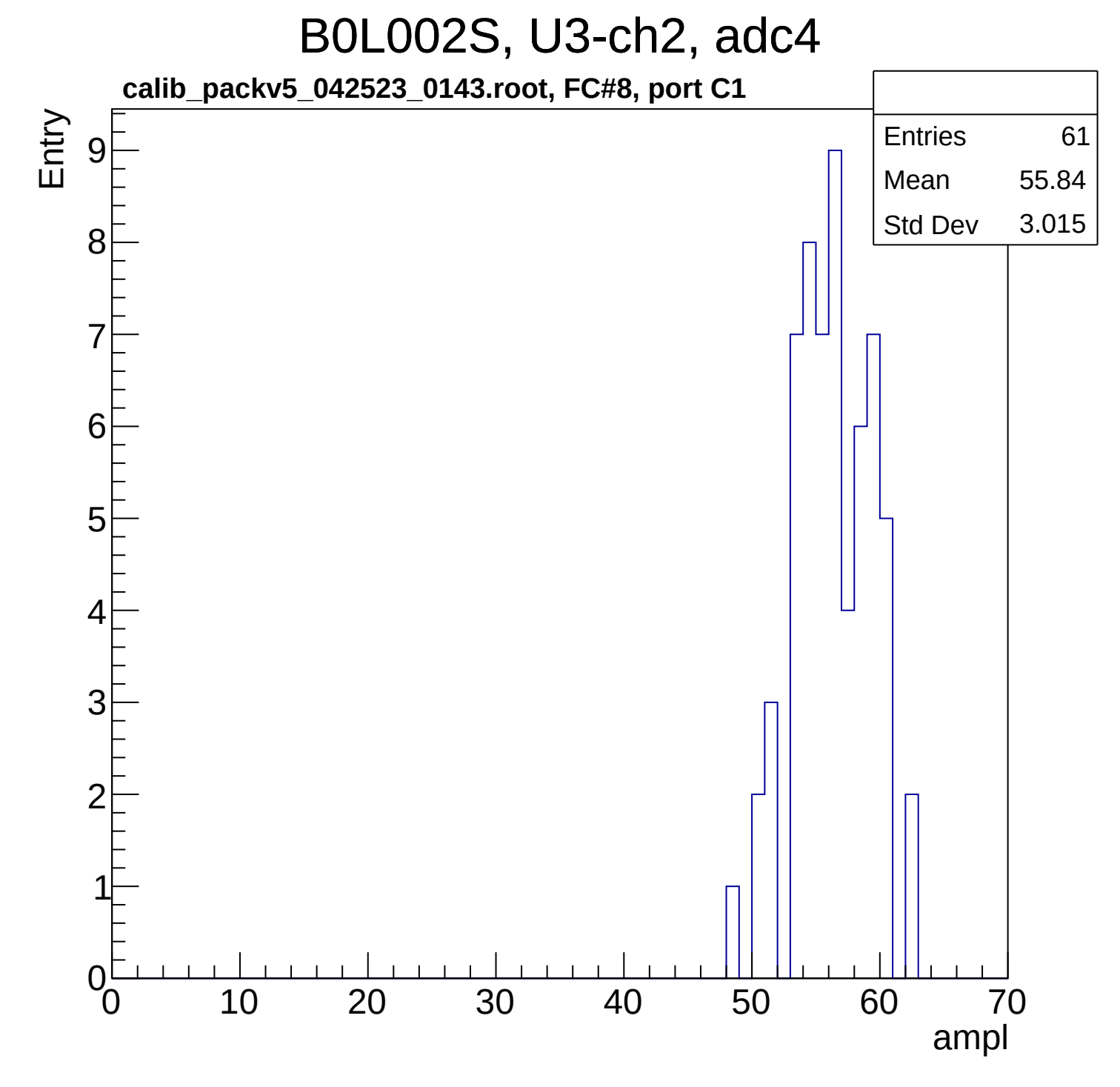
50
60
70
ampl


B0L002S, U3-ch2, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 55.84
Std Dev 3.015
Entry
ampl
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4
5
6
7
8
9
0
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20
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40
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60
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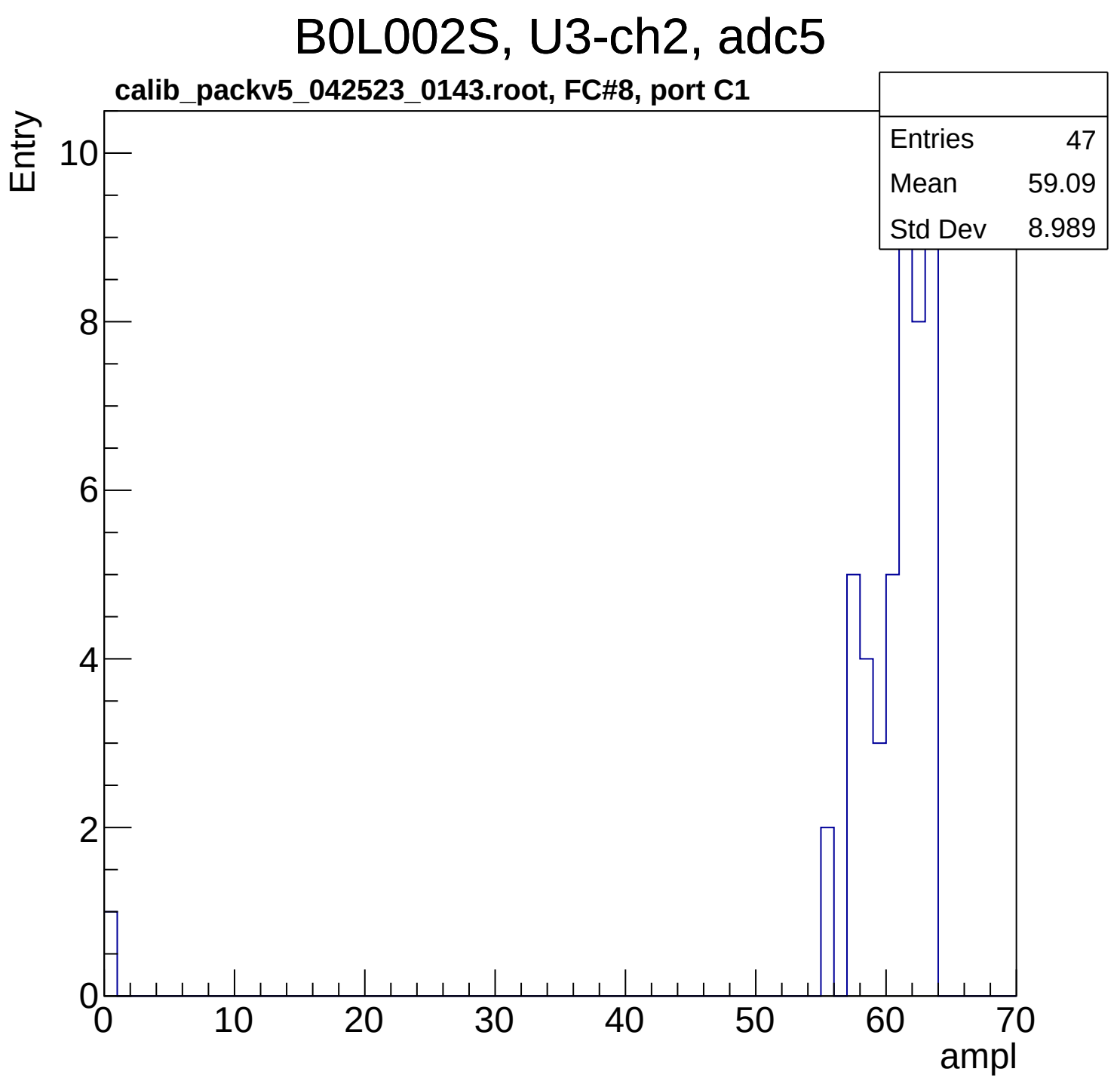
B0L002S, U3-ch2, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 59.09
Std Dev 8.989
Entry
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10
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10
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30
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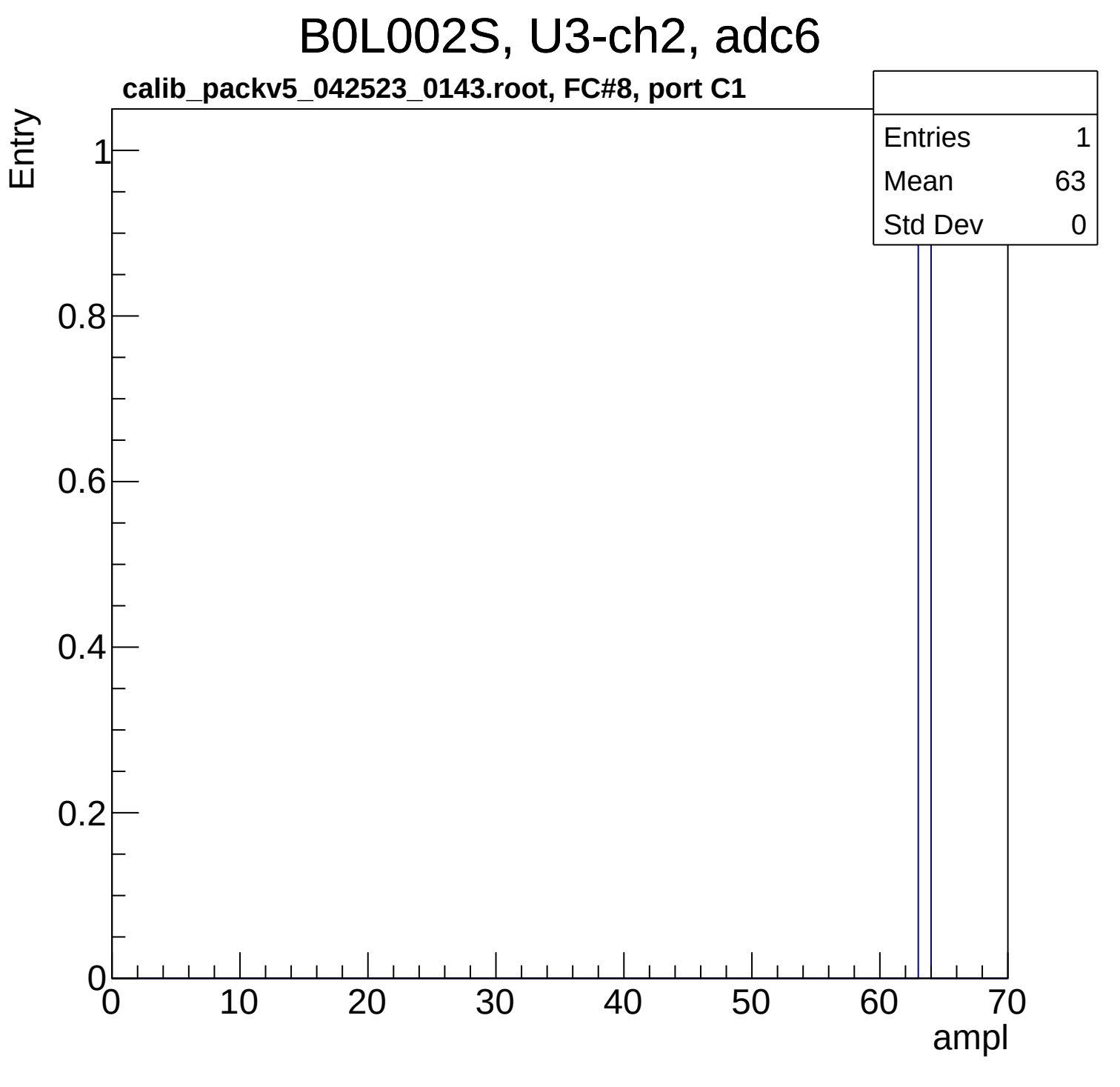
50
60
70
ampl


B0L002S, U3-ch2, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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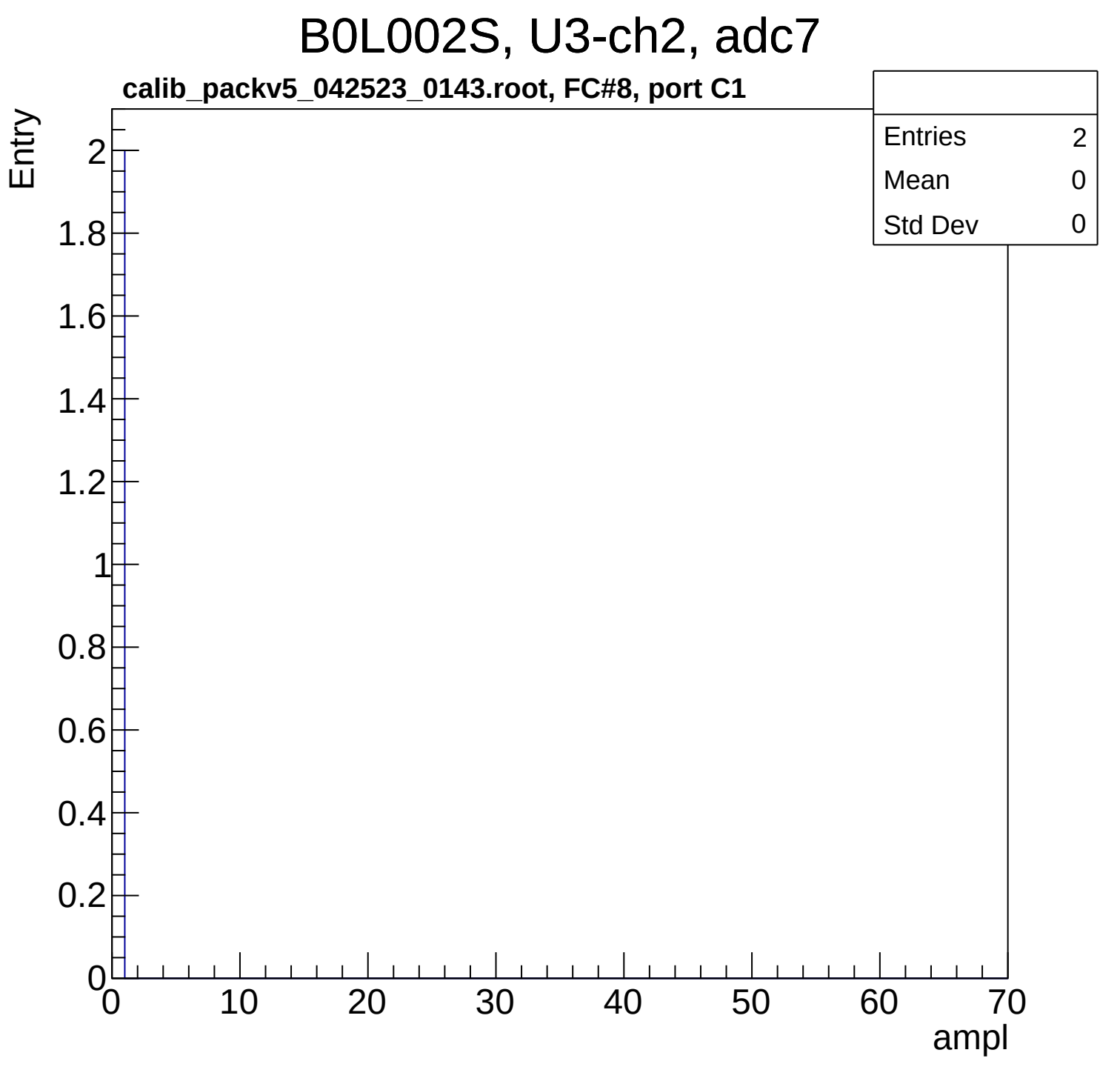
50
60
70
ampl


B0L002S, U3-ch2, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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10
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30
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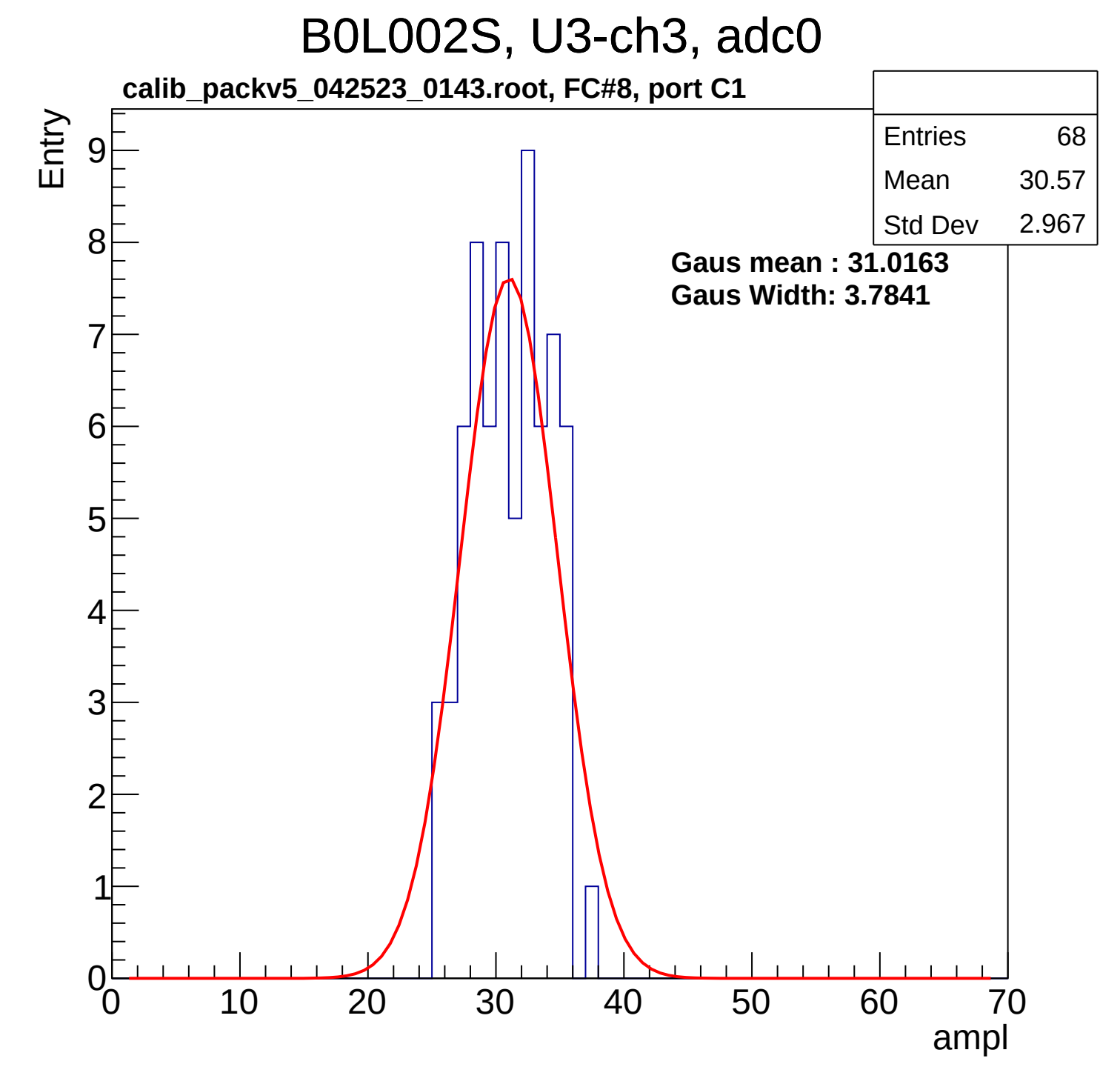
50
60
70
ampl


B0L002S, U3-ch3, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 30.57
Std Dev 2.967
Gaus mean : 31.0163
Gaus Width: 3.7841
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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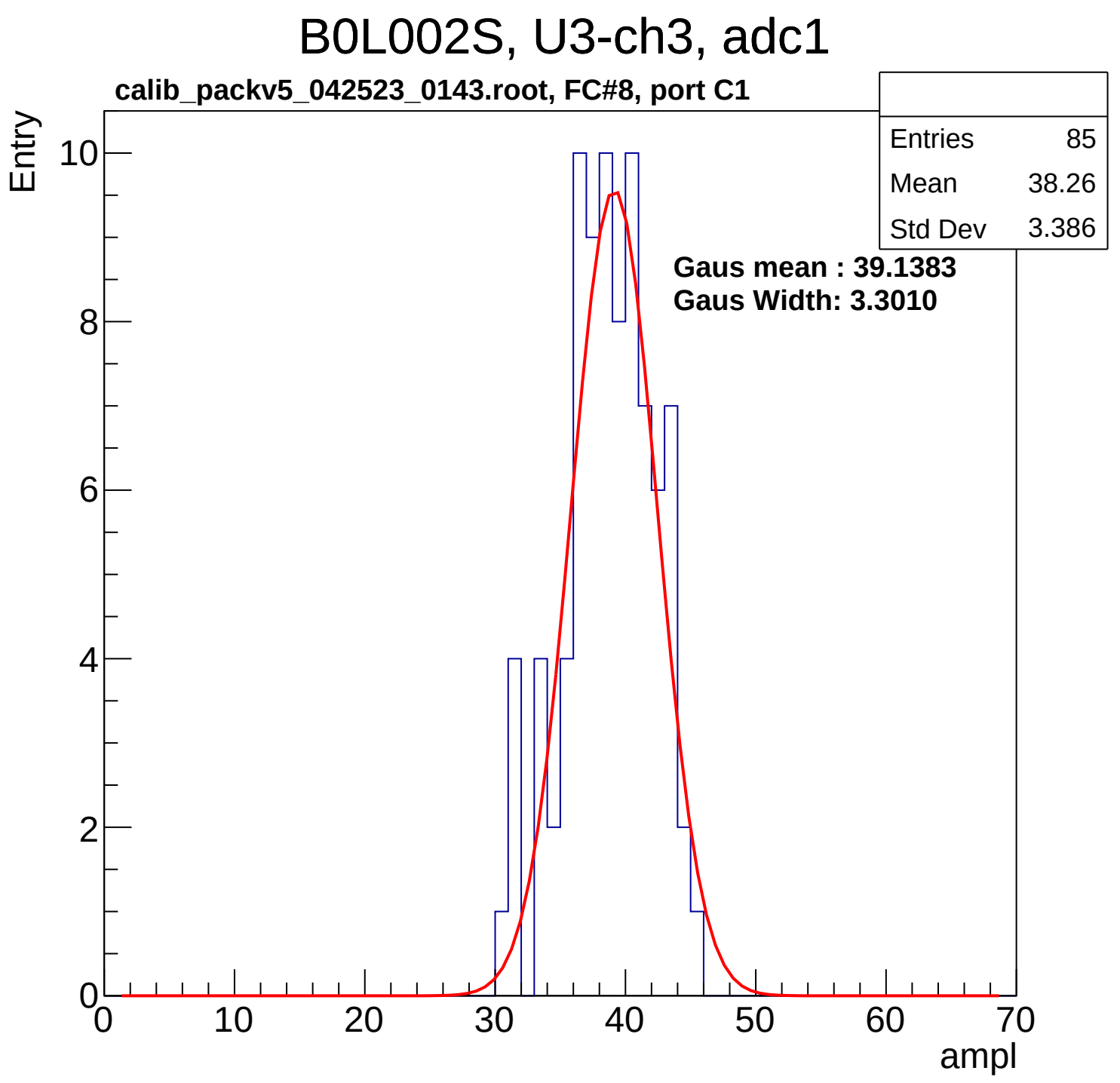
B0L002S, U3-ch3, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 38.26
Std Dev 3.386
Gaus mean : 39.1383
Gaus Width: 3.3010
Entry
ampl
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4
6
8
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0
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20
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40
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60
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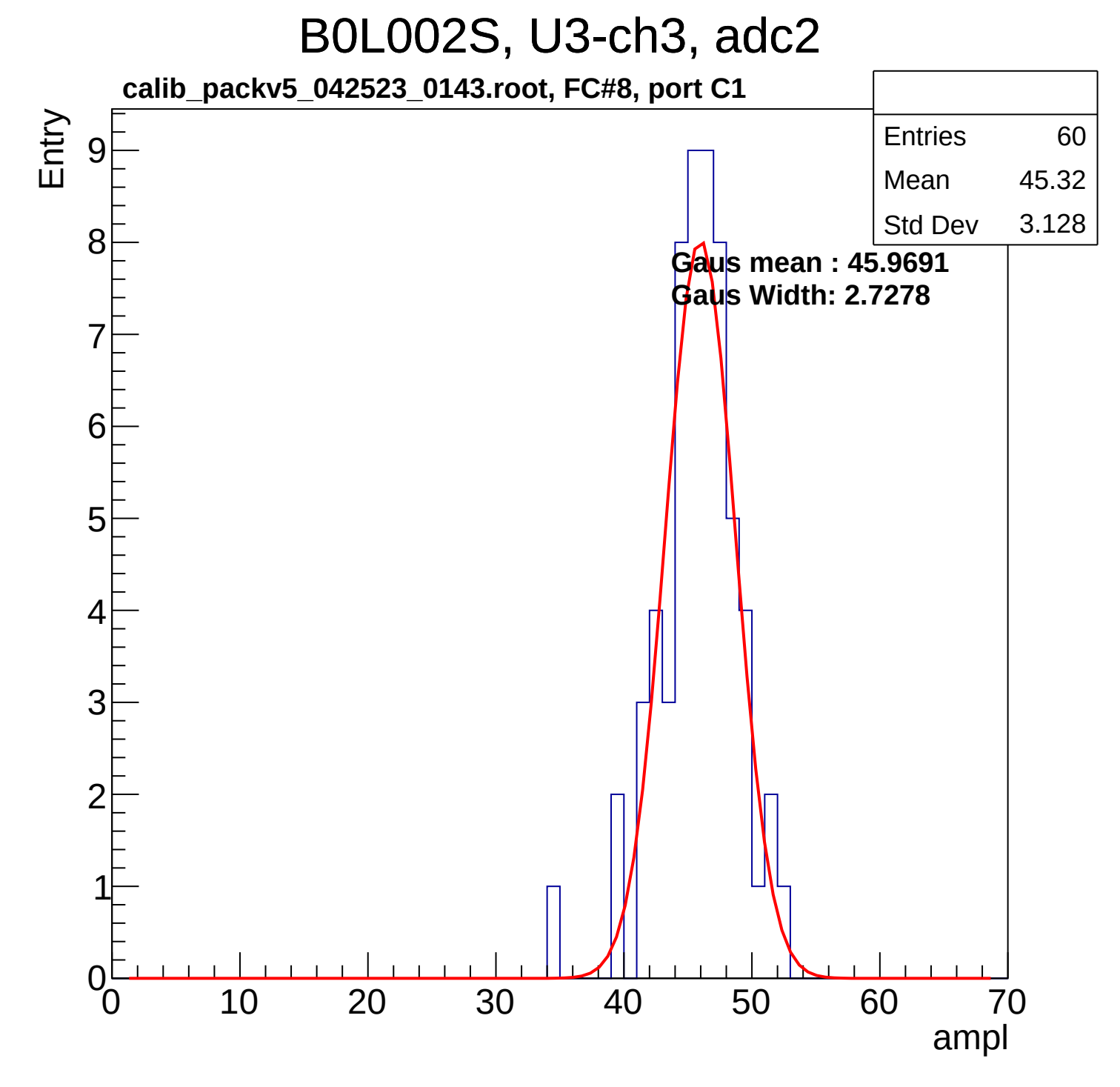
B0L002S, U3-ch3, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.32
Std Dev 3.128
Gaus mean : 45.9691
Gaus Width: 2.7278
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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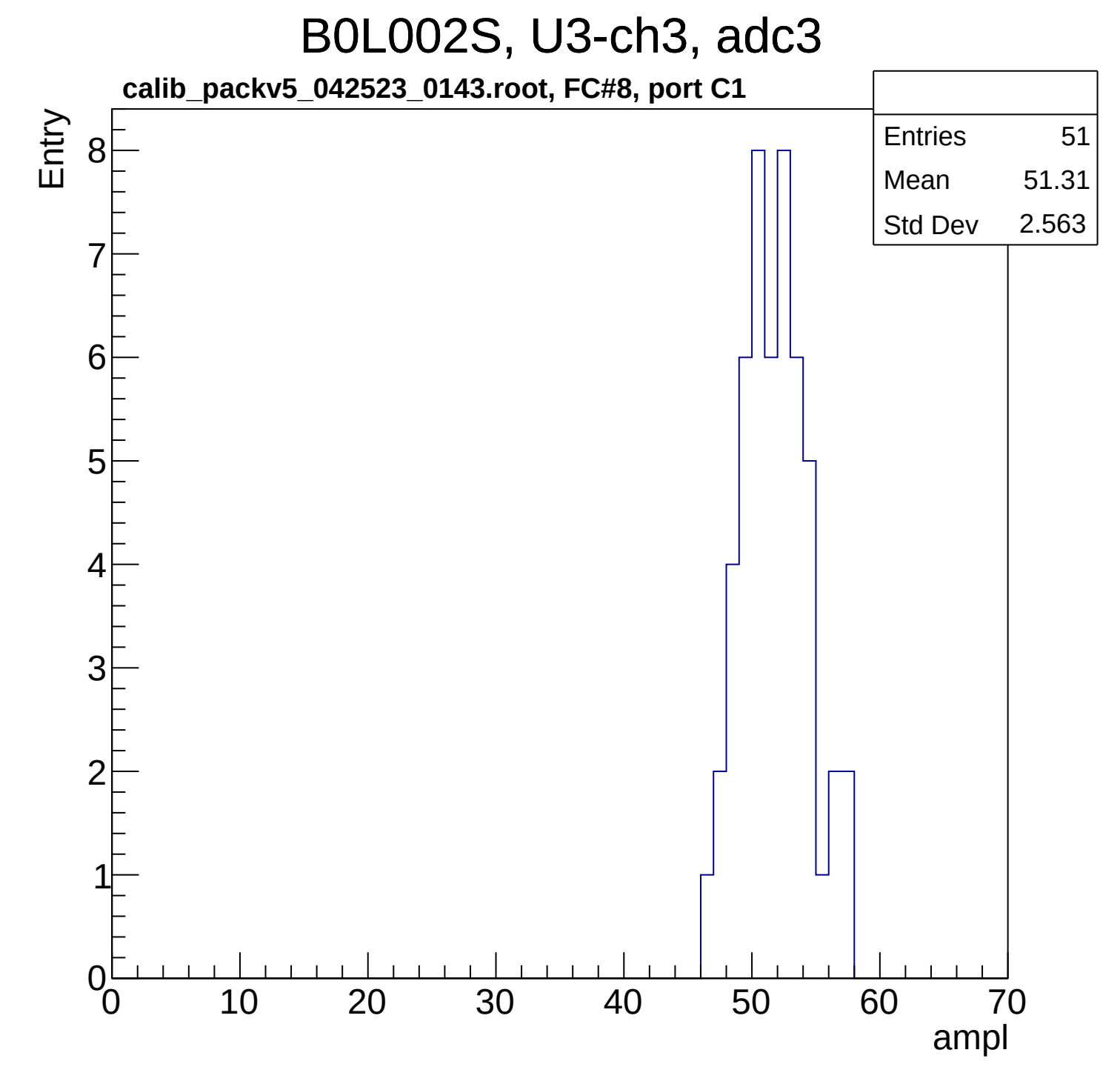
B0L002S, U3-ch3, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 51.31
Std Dev 2.563
Entry
ampl
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4
5
6
7
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0
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20
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60
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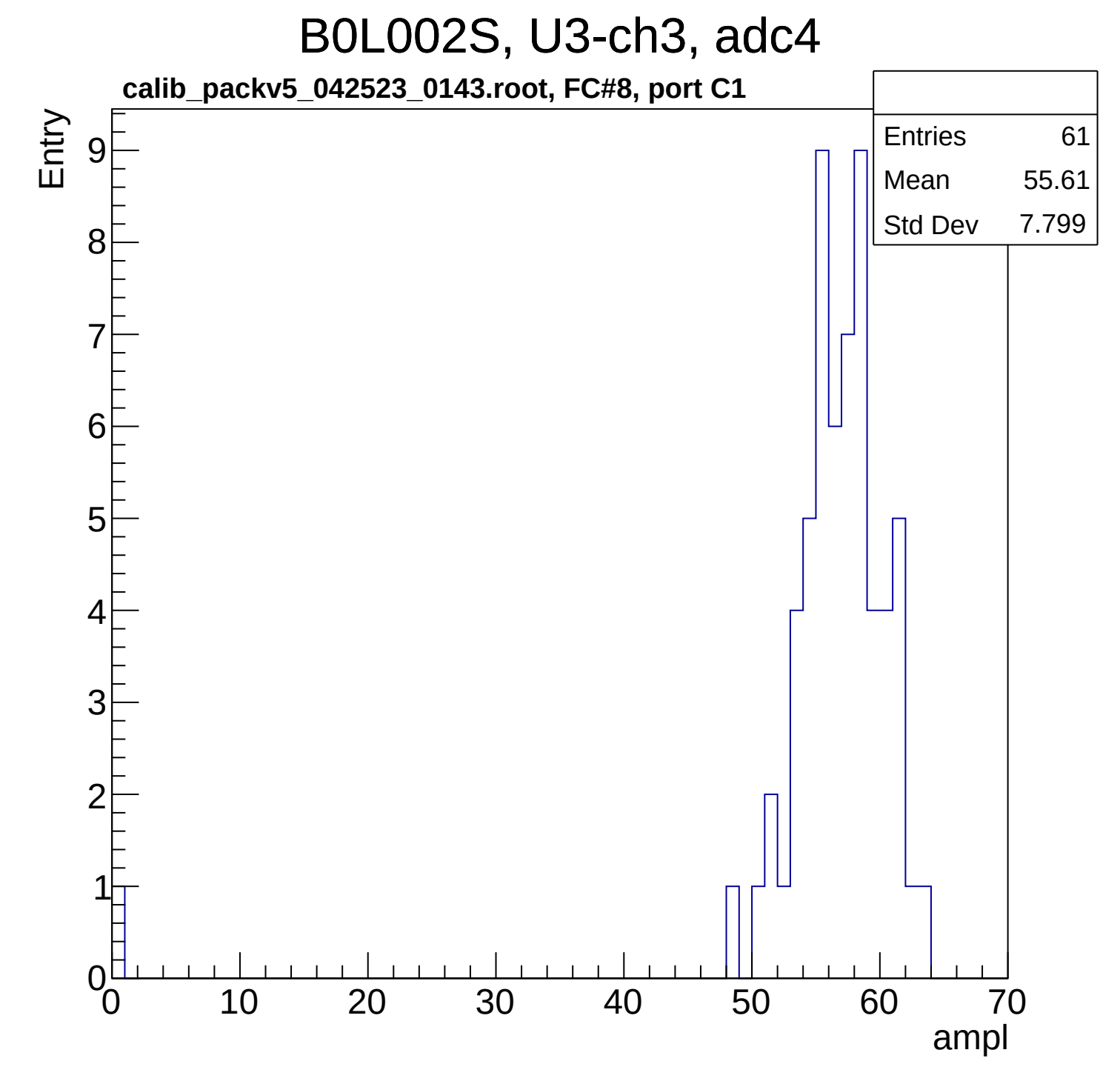
B0L002S, U3-ch3, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 55.61
Std Dev 7.799
Entry
ampl
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3
4
5
6
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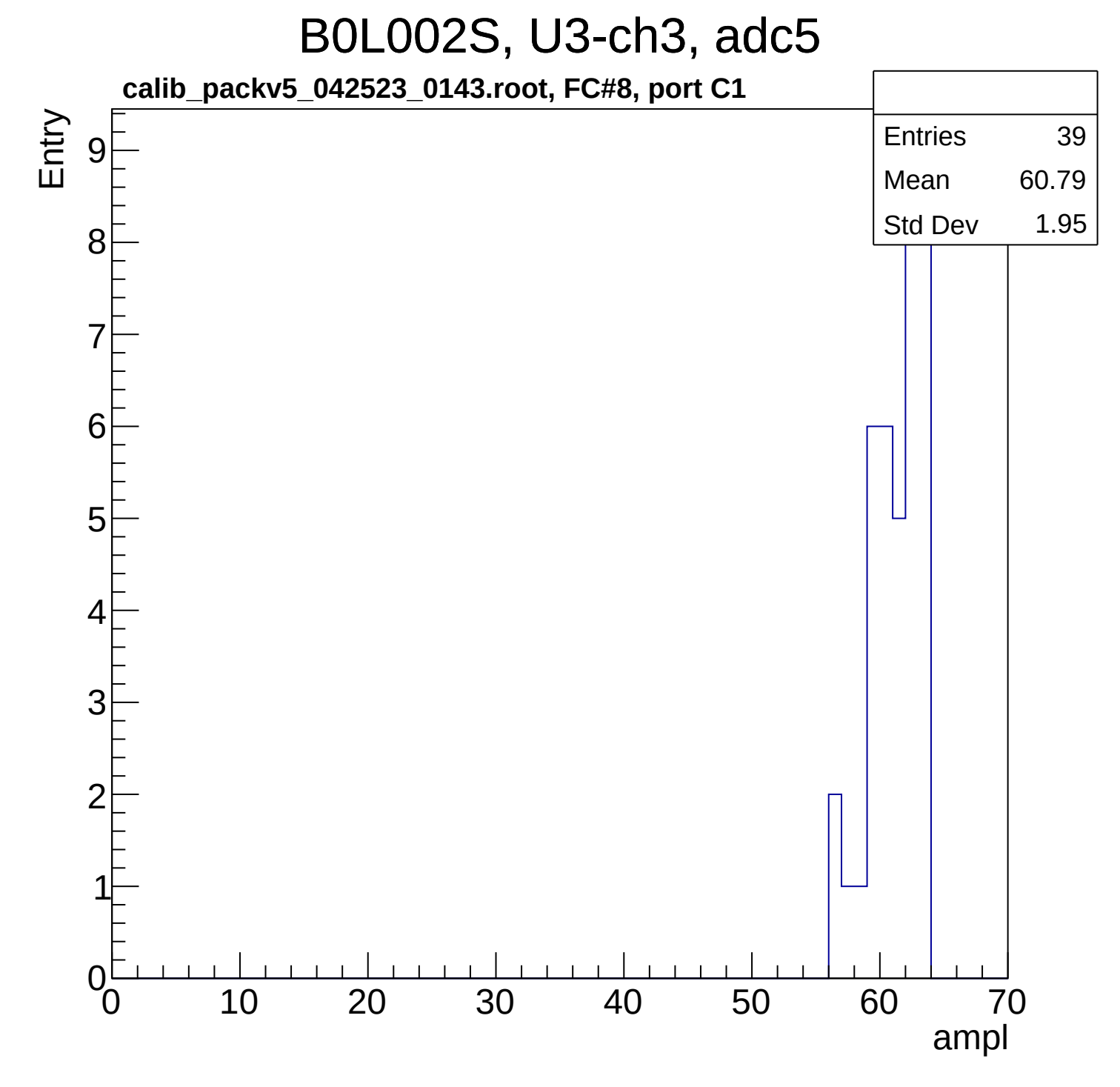
B0L002S, U3-ch3, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 60.79
Std Dev 1.95
Entry
ampl
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3
4
5
6
7
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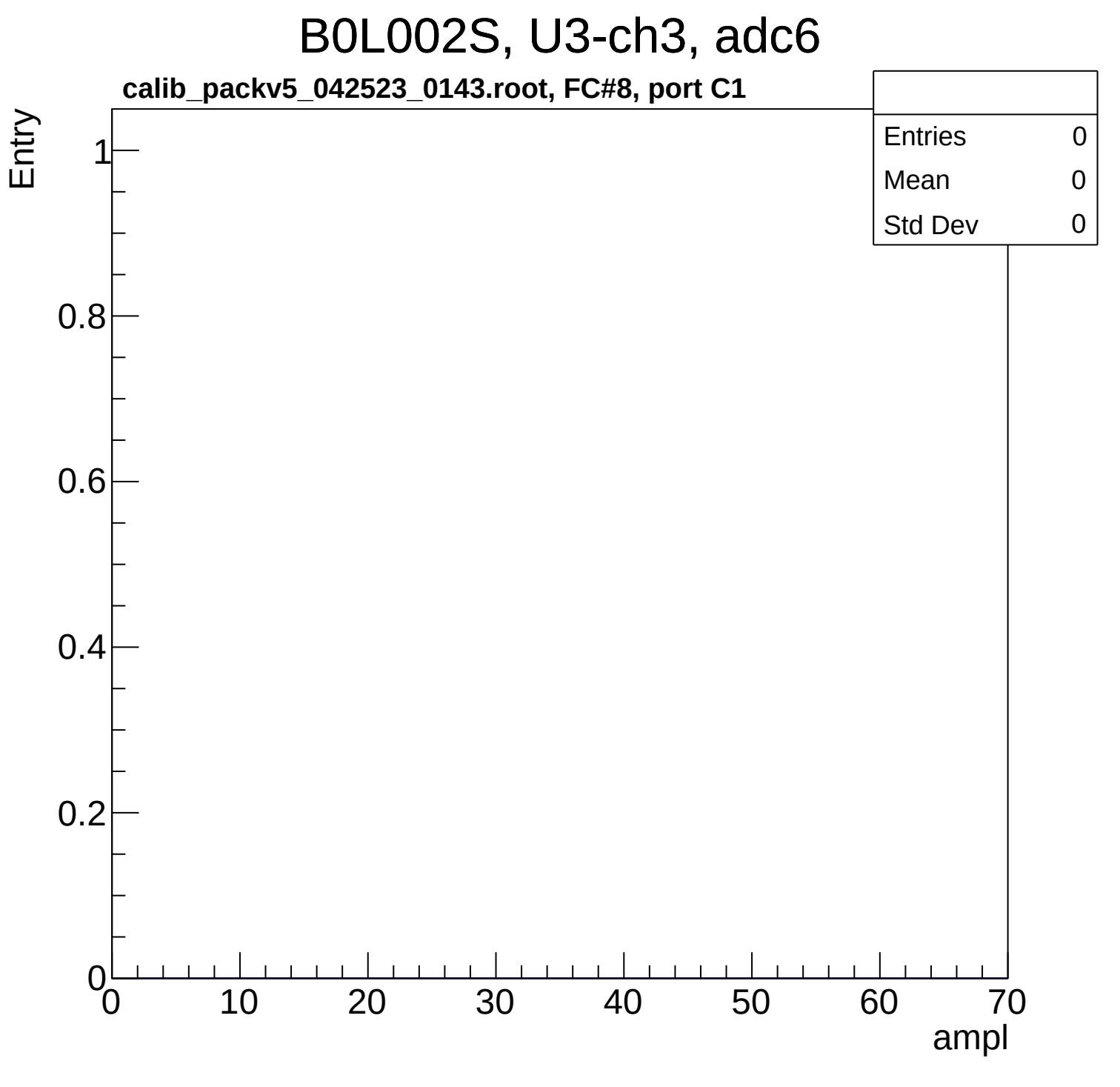
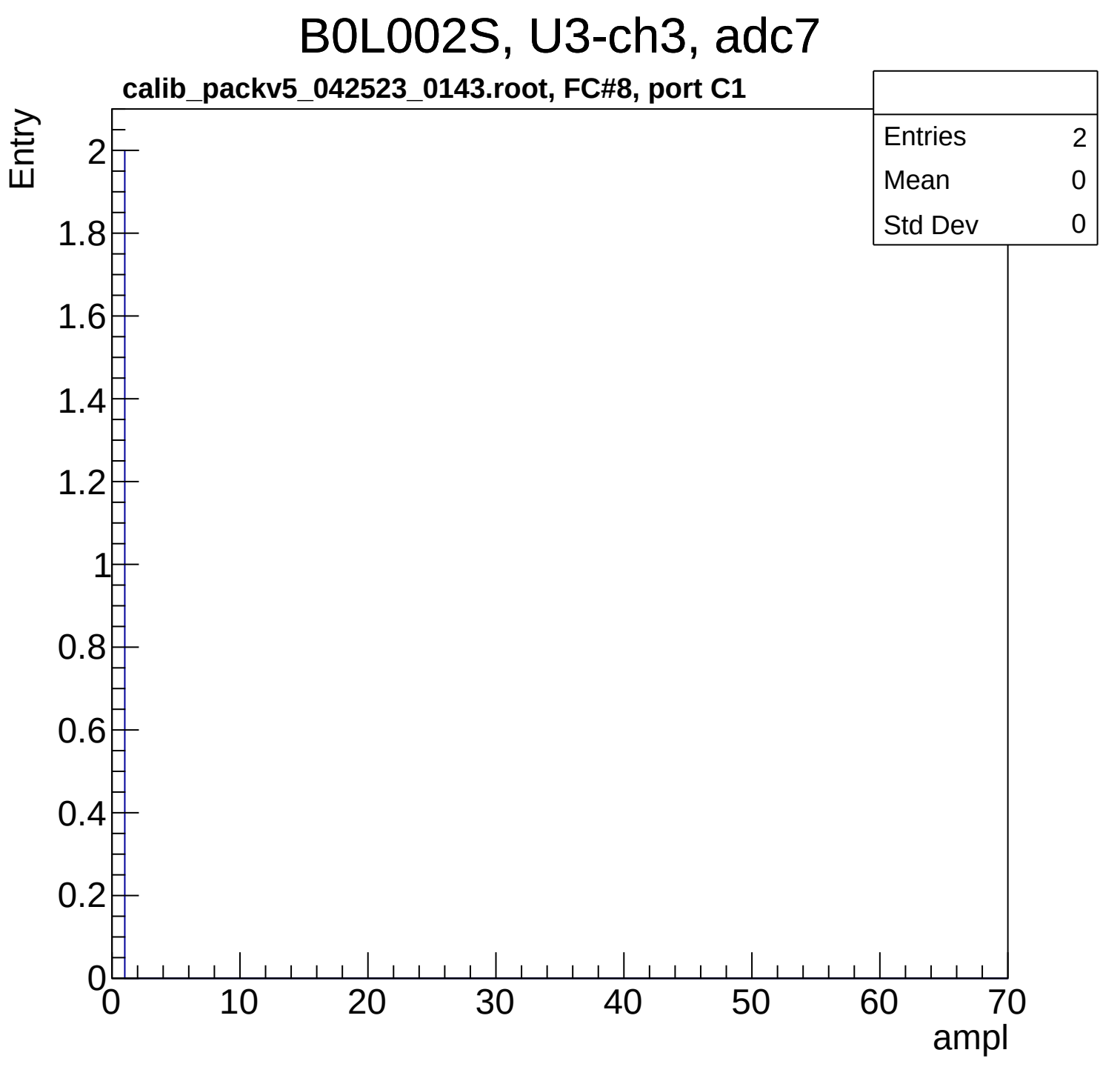

B0L002S, U3-ch3, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch3, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.8
0.6
0.4
0.2
0
0
10
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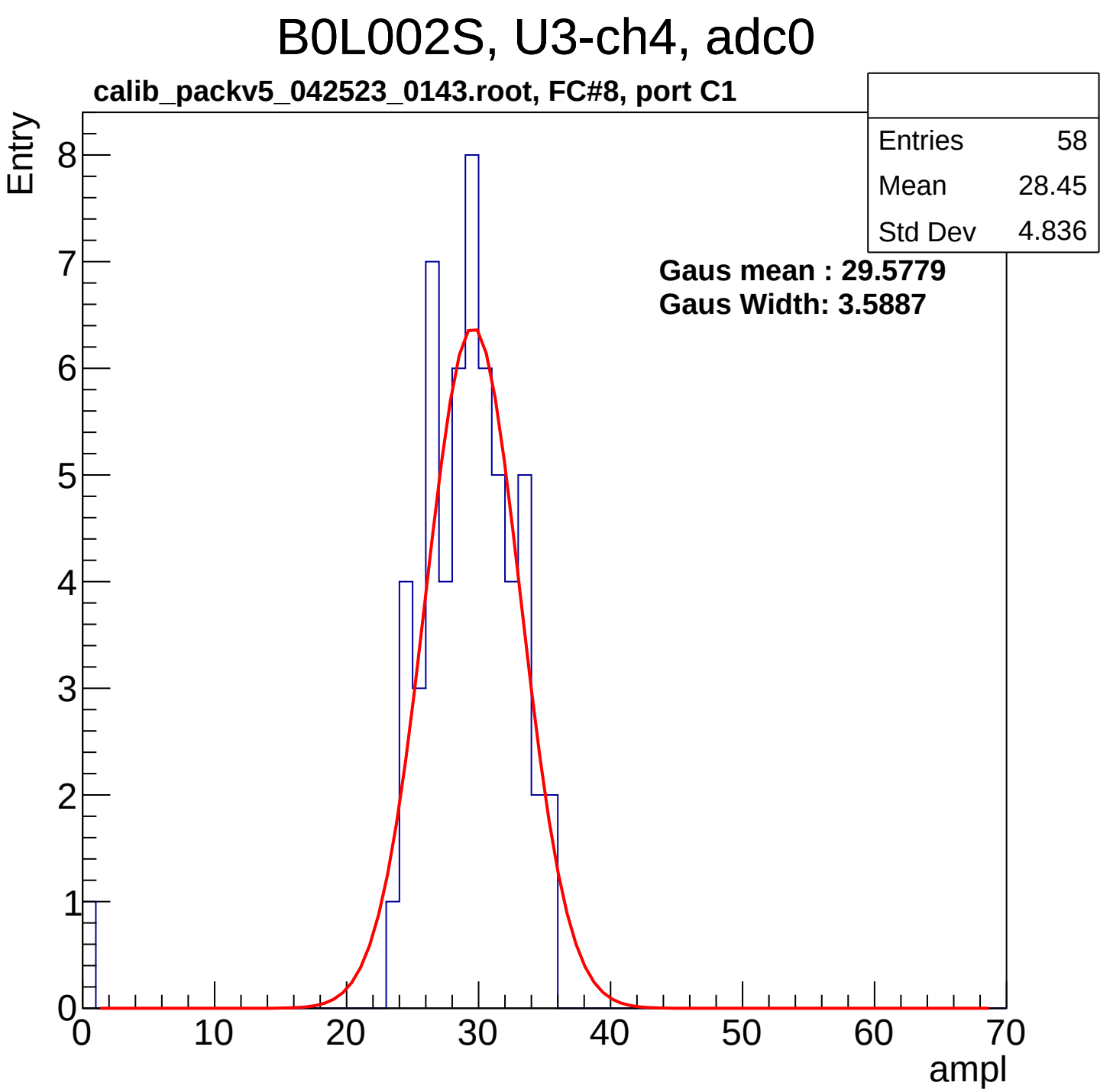
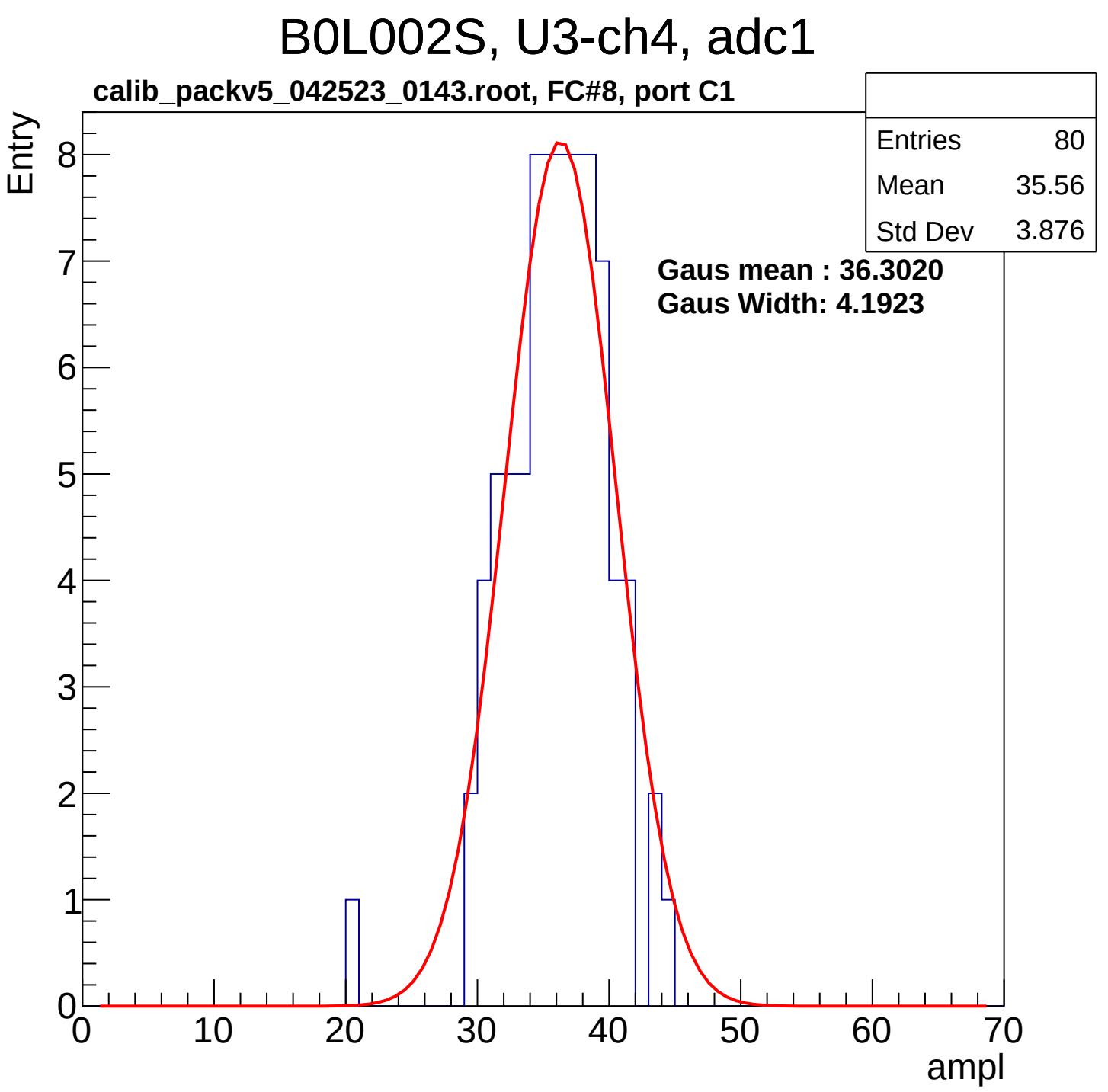
40
50
60
70
ampl


B0L002S, U3-ch4, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 28.45
Std Dev 4.836
Gaus mean : 29.5779
Gaus Width: 3.5887
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch4, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 35.56
Std Dev 3.876
Gaus mean : 36.3020
Gaus Width: 4.1923
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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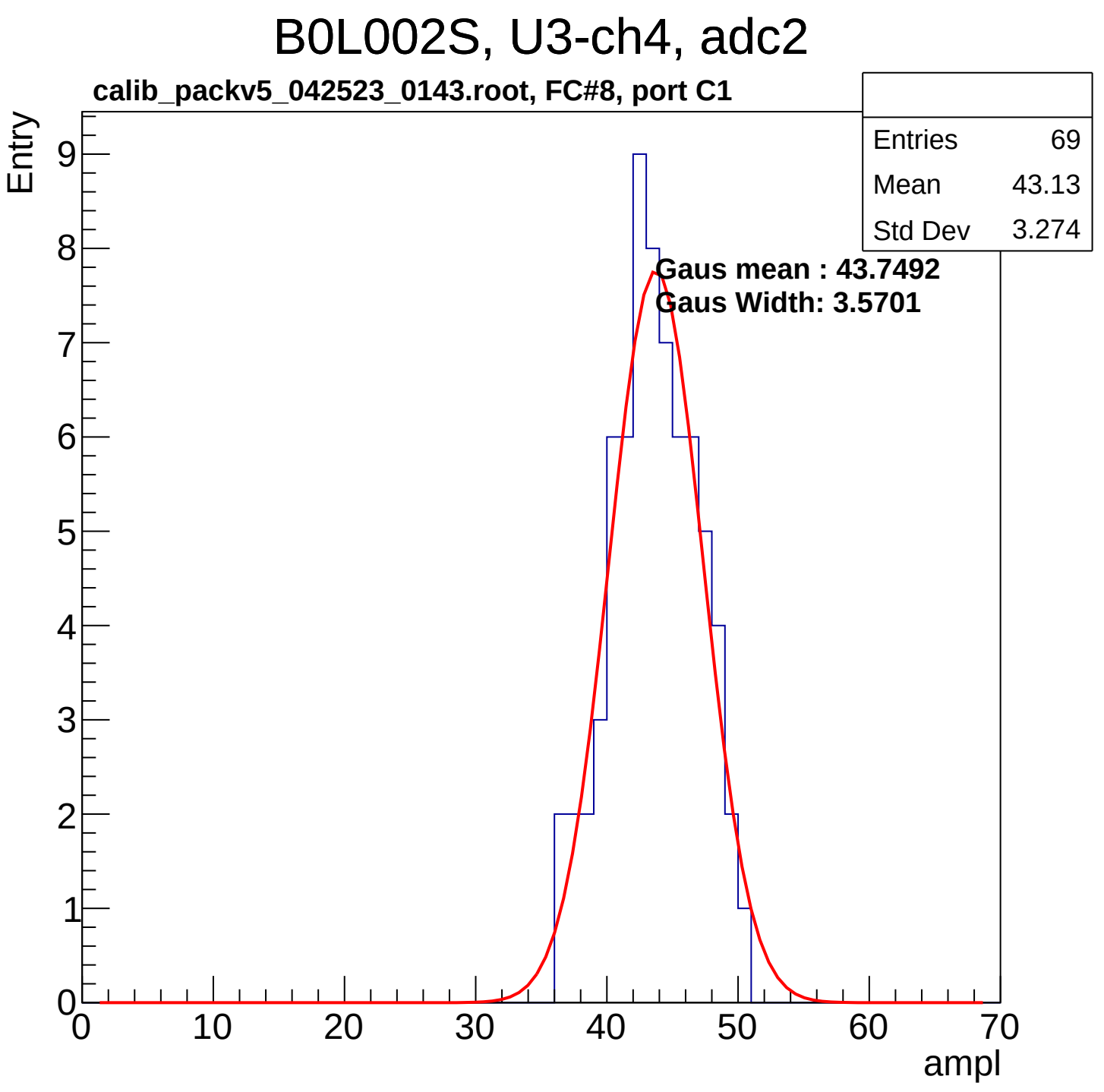
B0L002S, U3-ch4, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.13
Std Dev 3.274
Gaus mean : 43.7492
Gaus Width: 3.5701
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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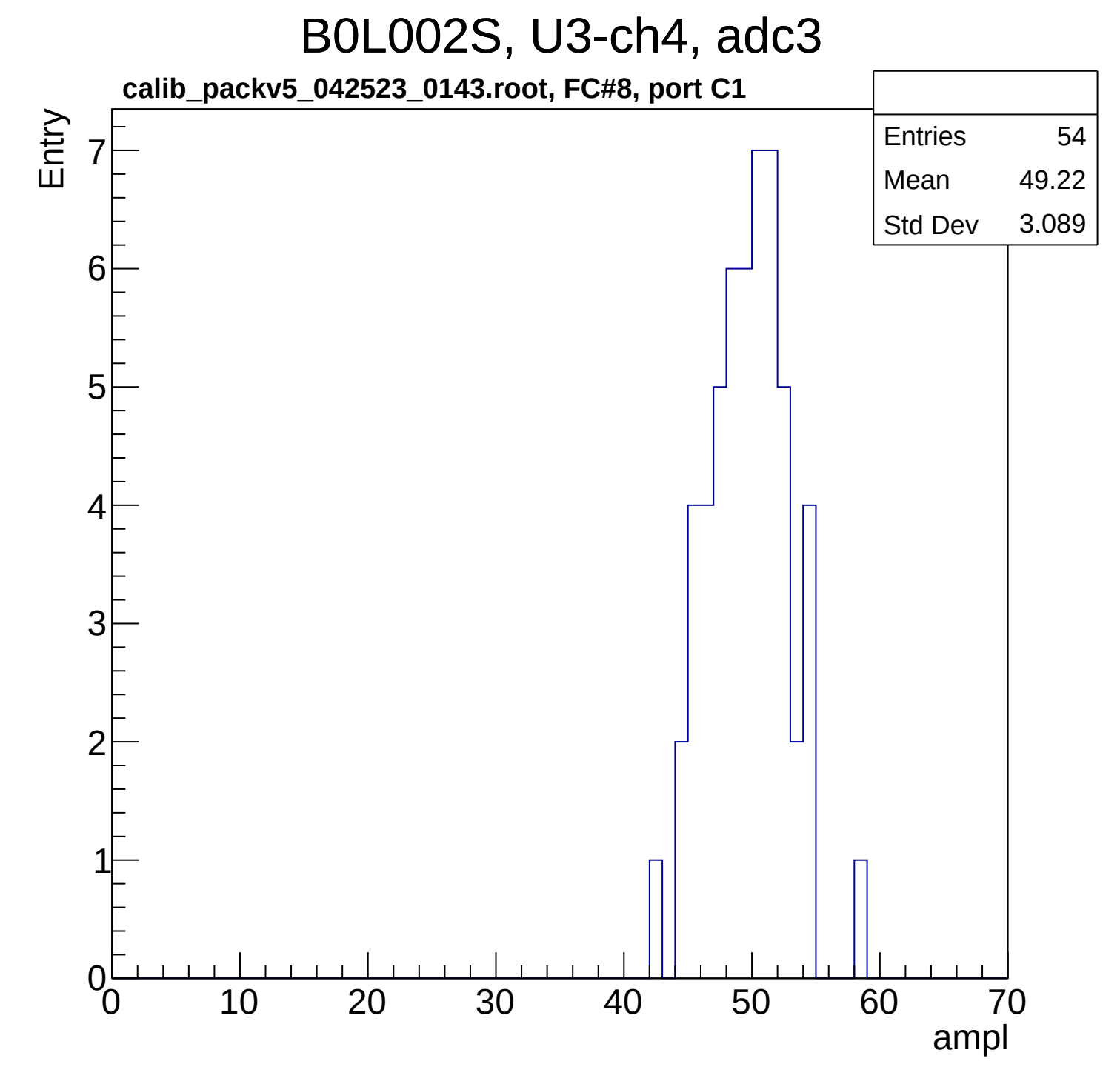
B0L002S, U3-ch4, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 49.22
Std Dev 3.089
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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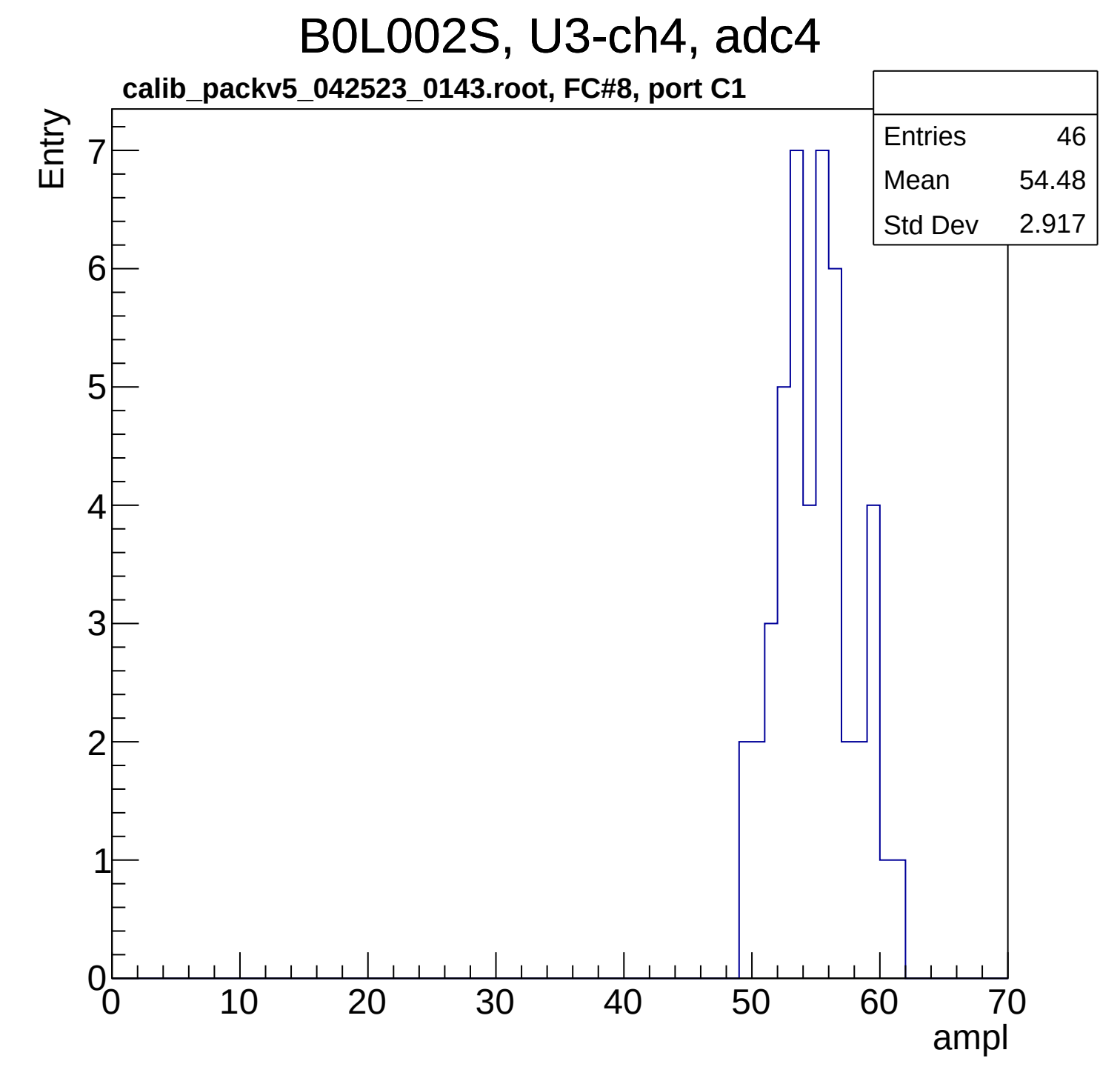
B0L002S, U3-ch4, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 54.48
Std Dev 2.917
Entry
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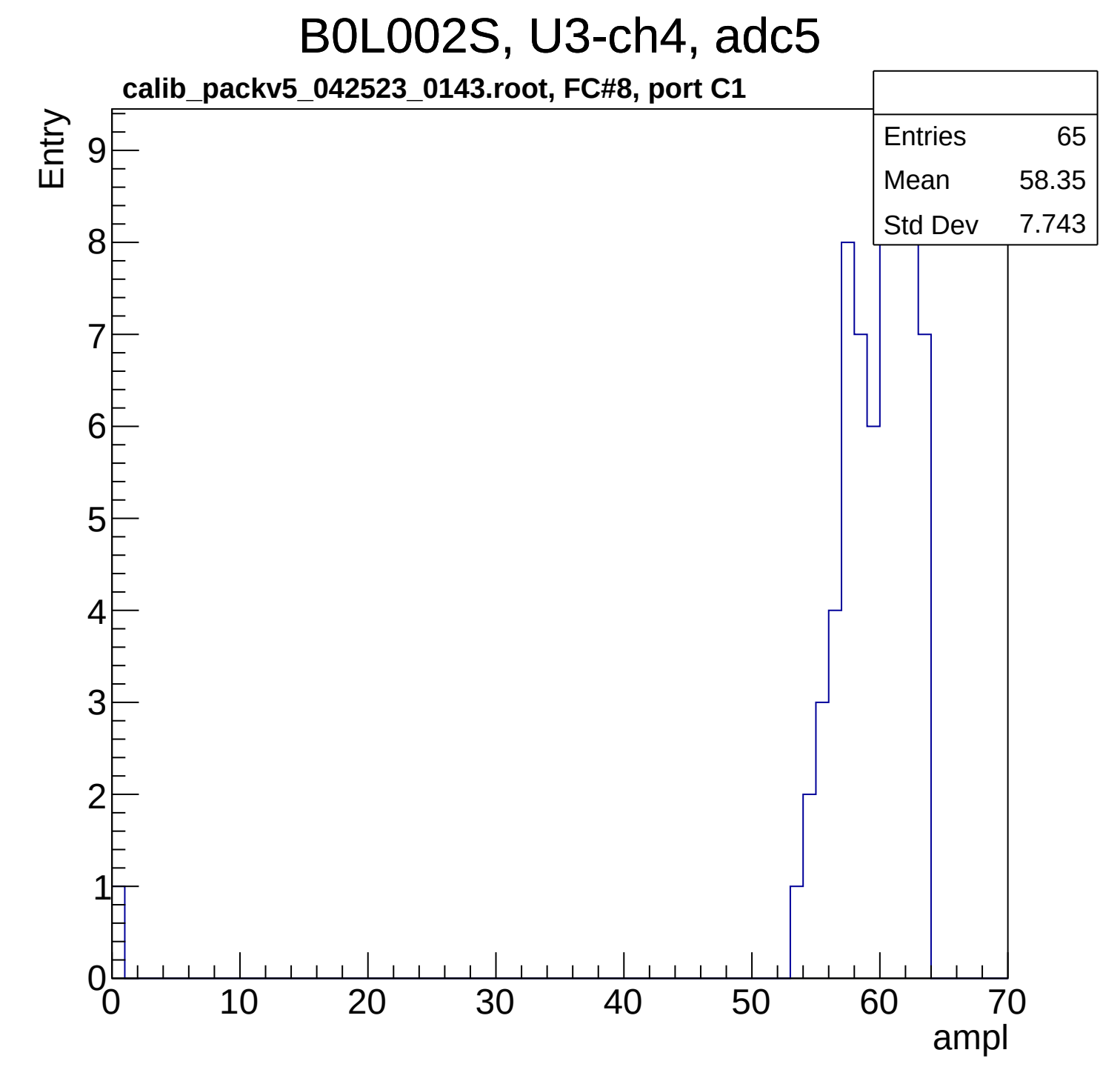
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch4, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 58.35
Std Dev 7.743
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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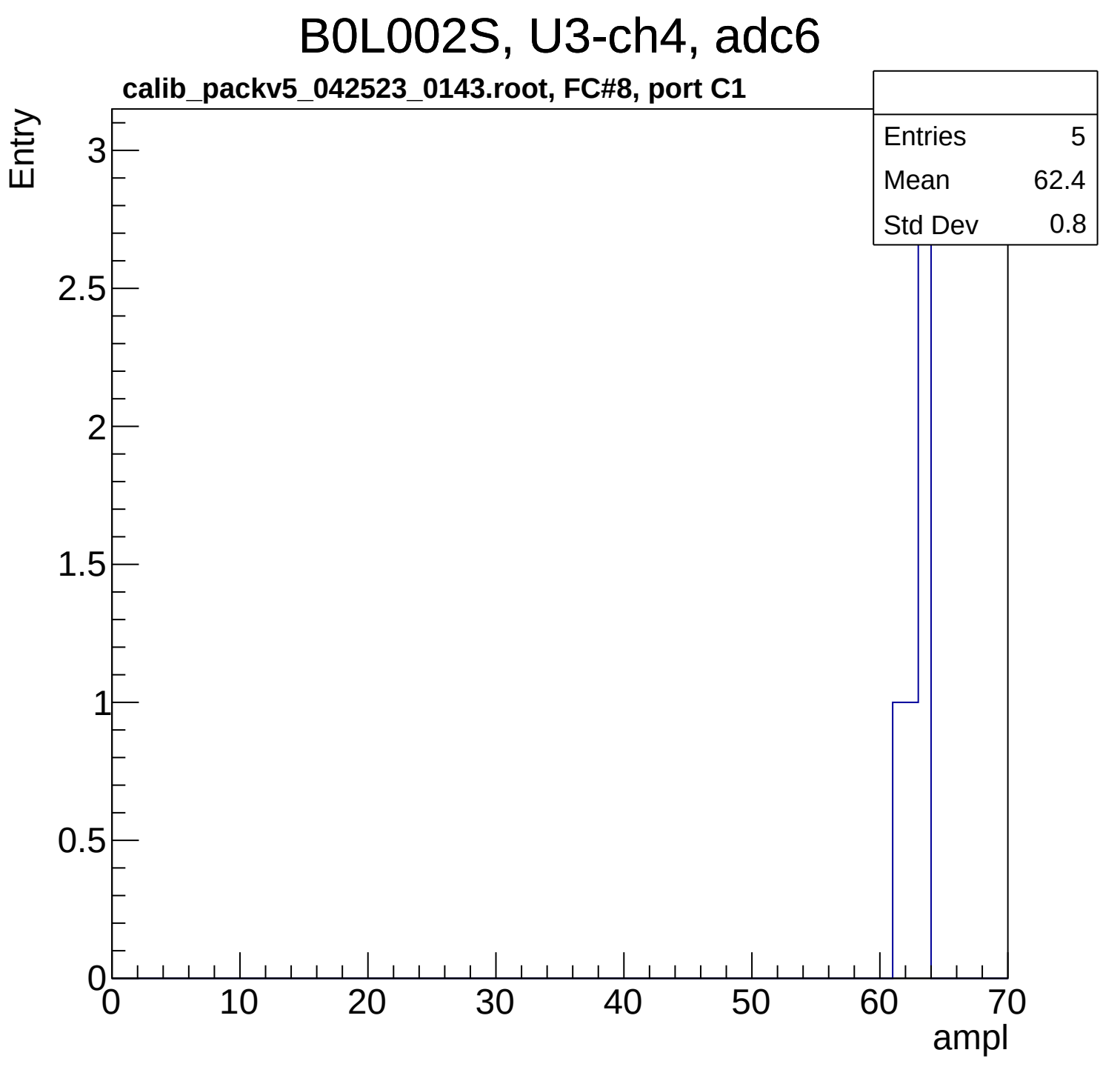
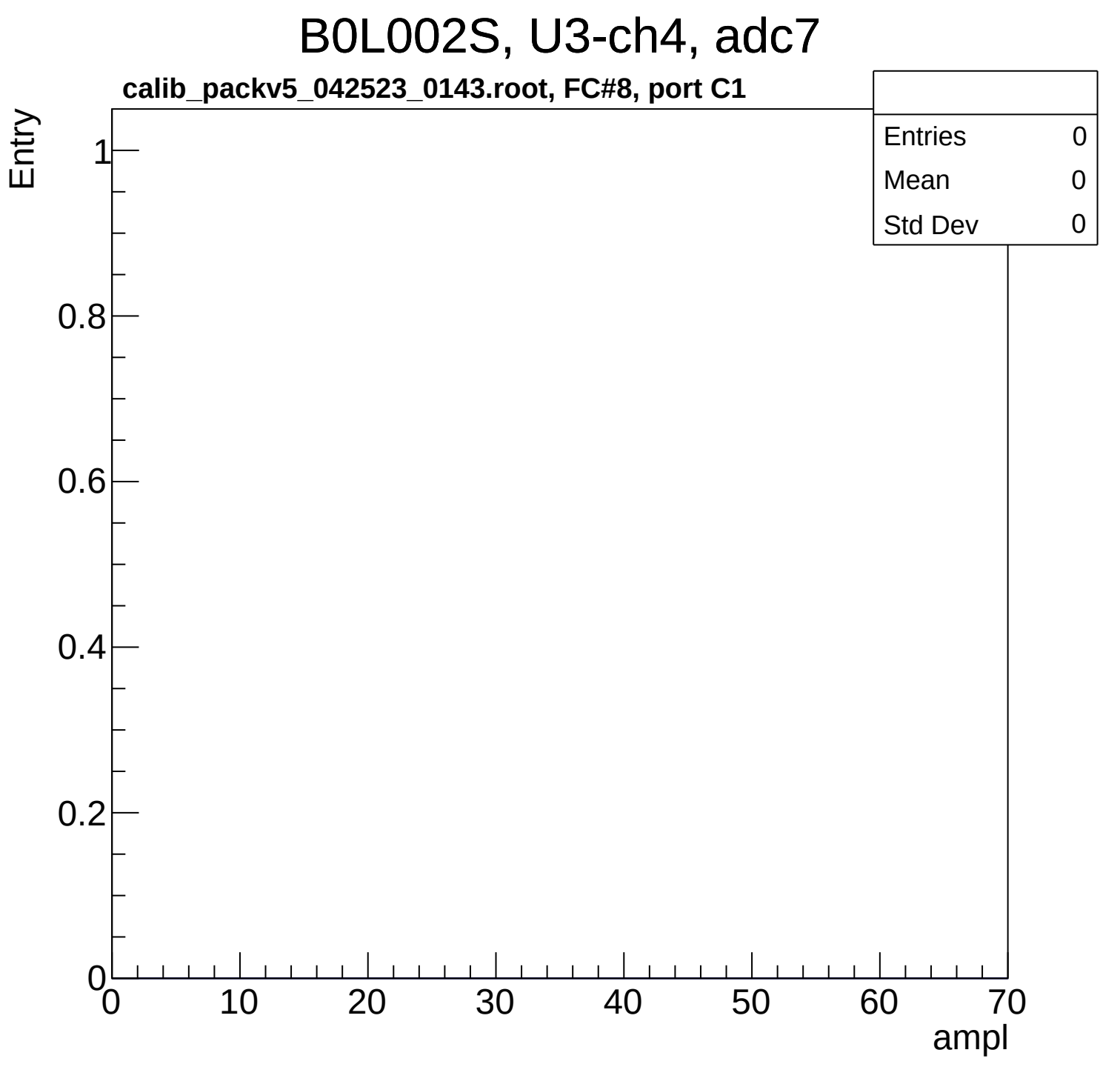
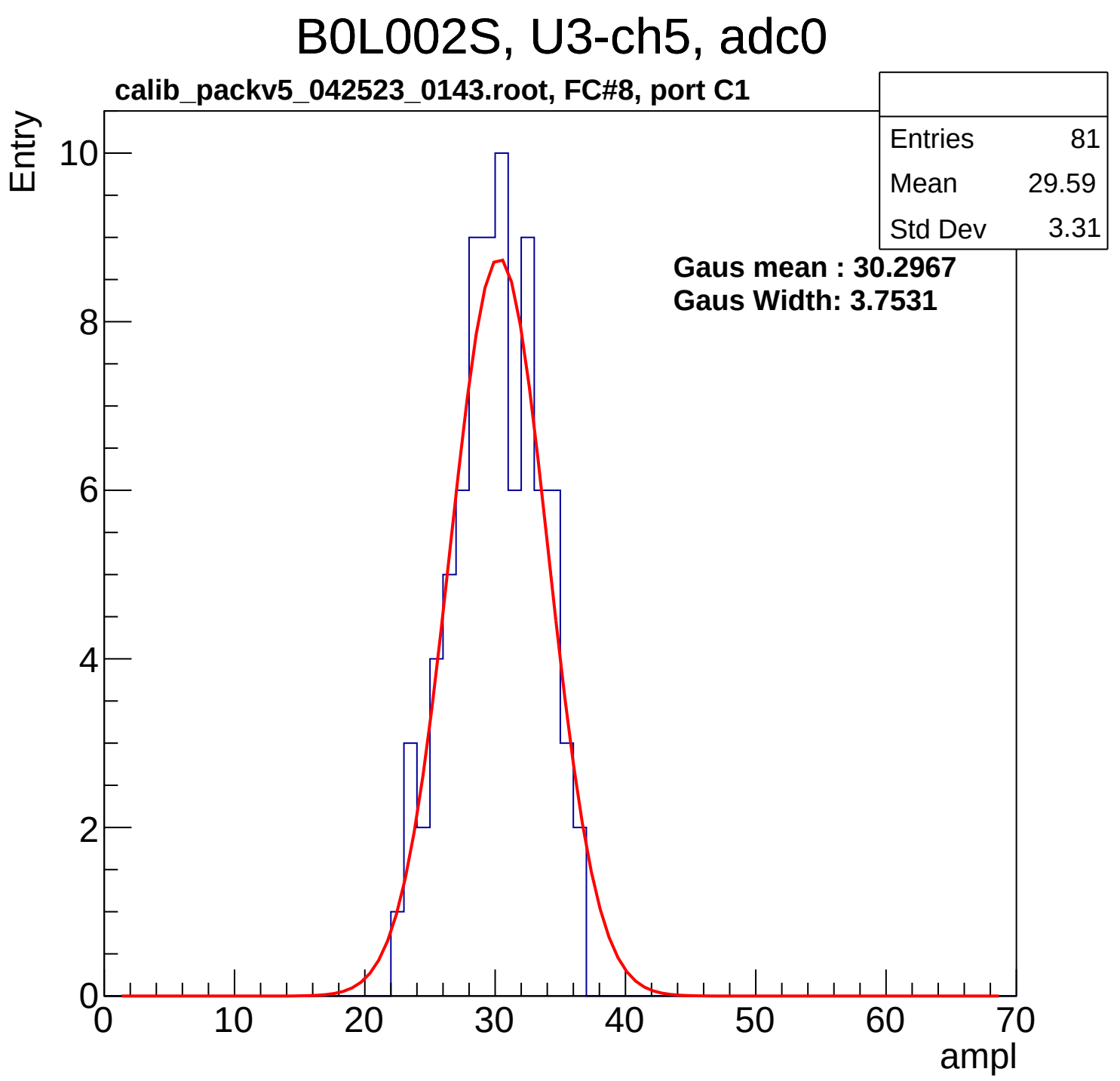
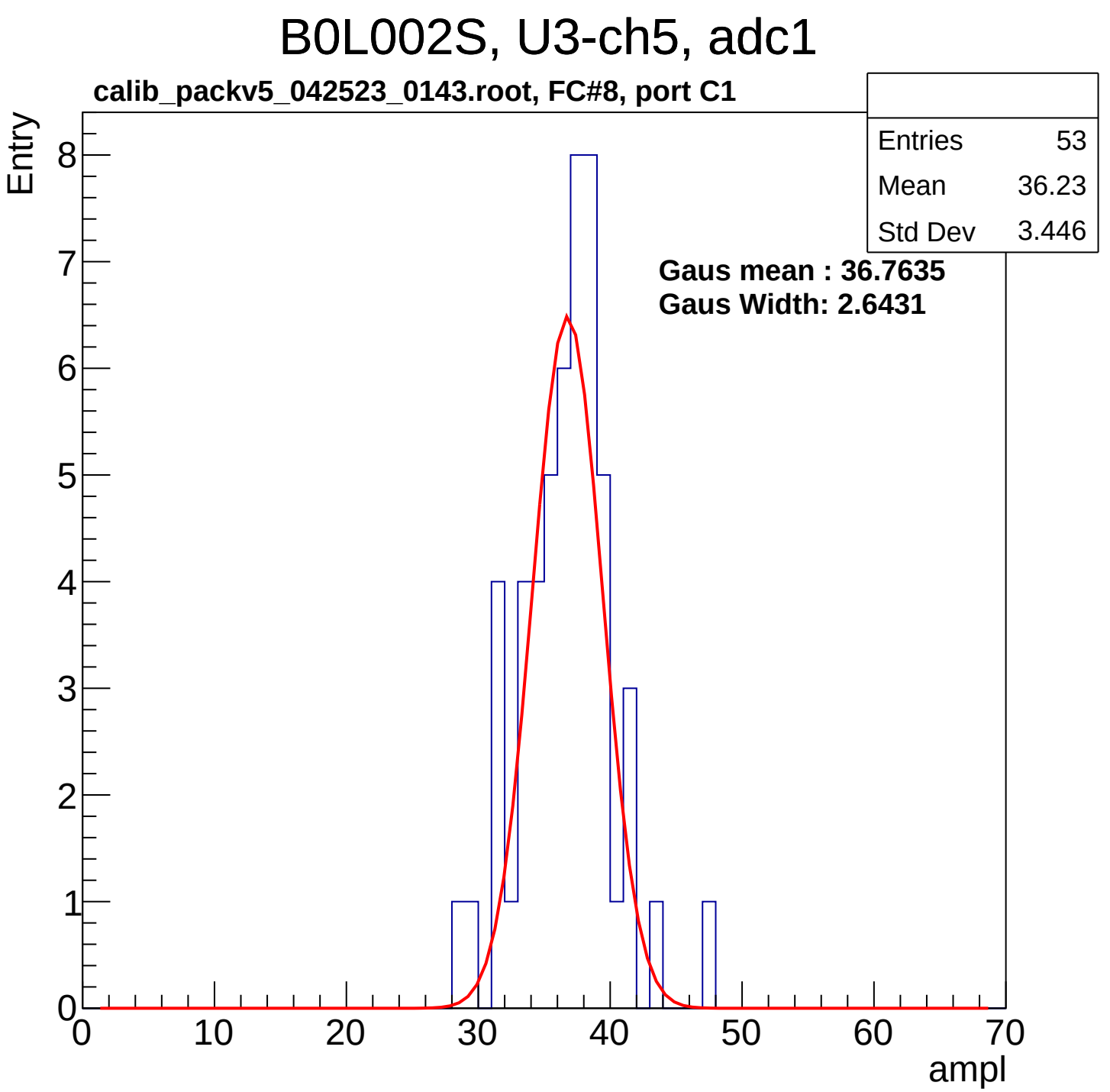
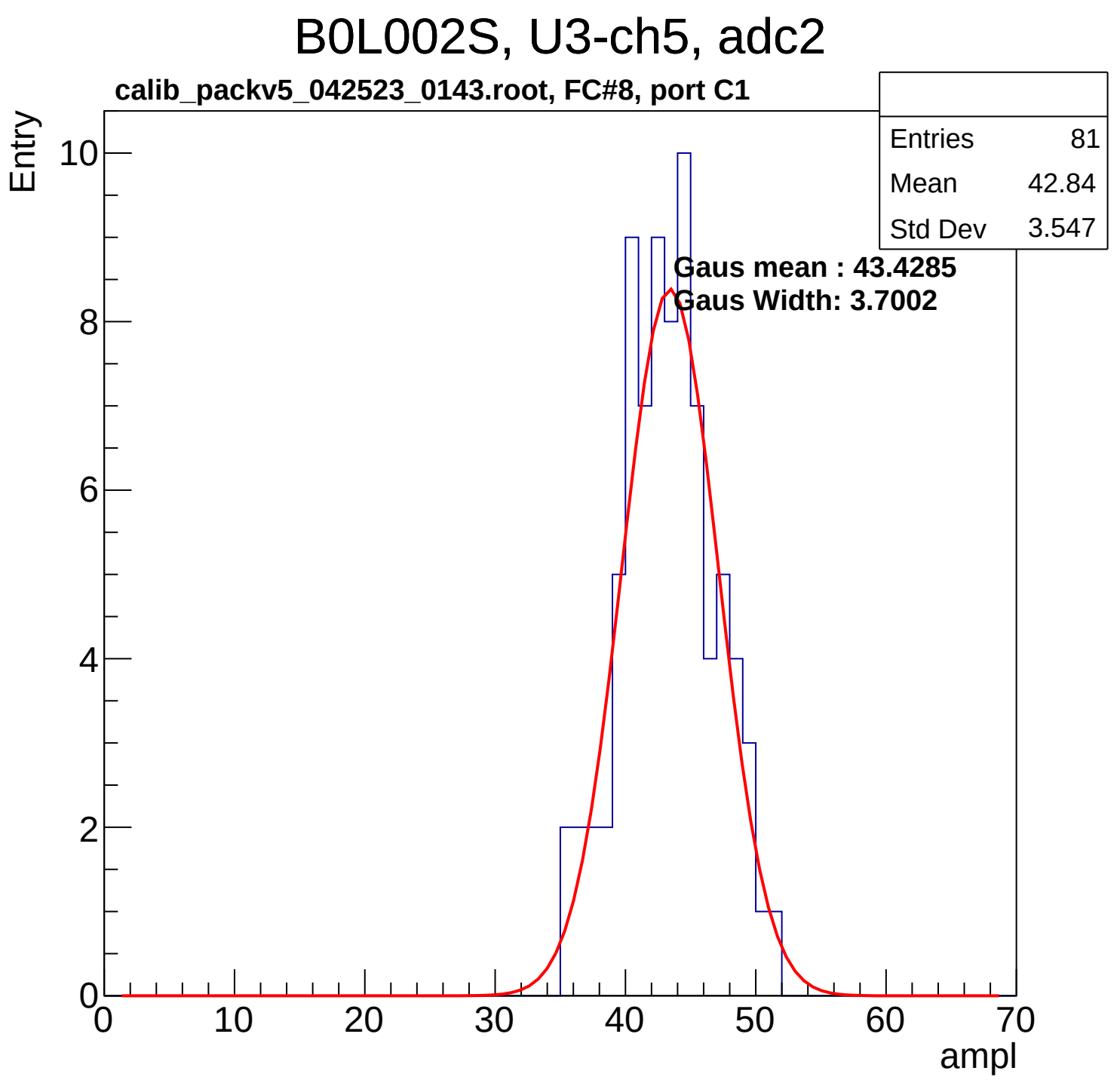

B0L002S, U3-ch4, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch4, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch5, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 29.59
Std Dev 3.31
Gaus mean : 30.2967
Gaus Width: 3.7531
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch5, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 36.23
Std Dev 3.446
Gaus mean : 36.7635
Gaus Width: 2.6431
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch5, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 42.84
Std Dev 3.547
Gaus mean : 43.4285
Gaus Width: 3.7002
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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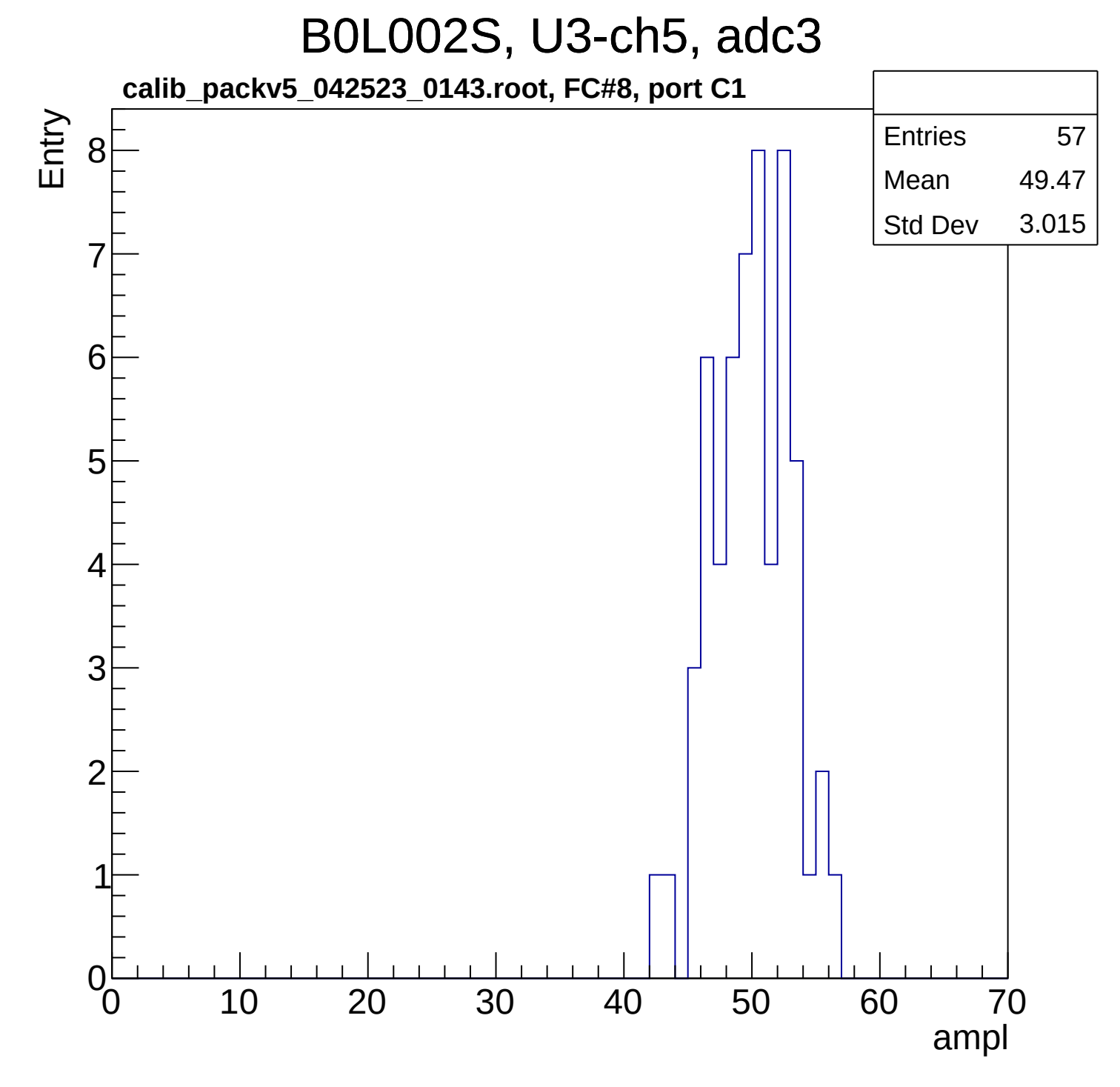
B0L002S, U3-ch5, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 49.47
Std Dev 3.015
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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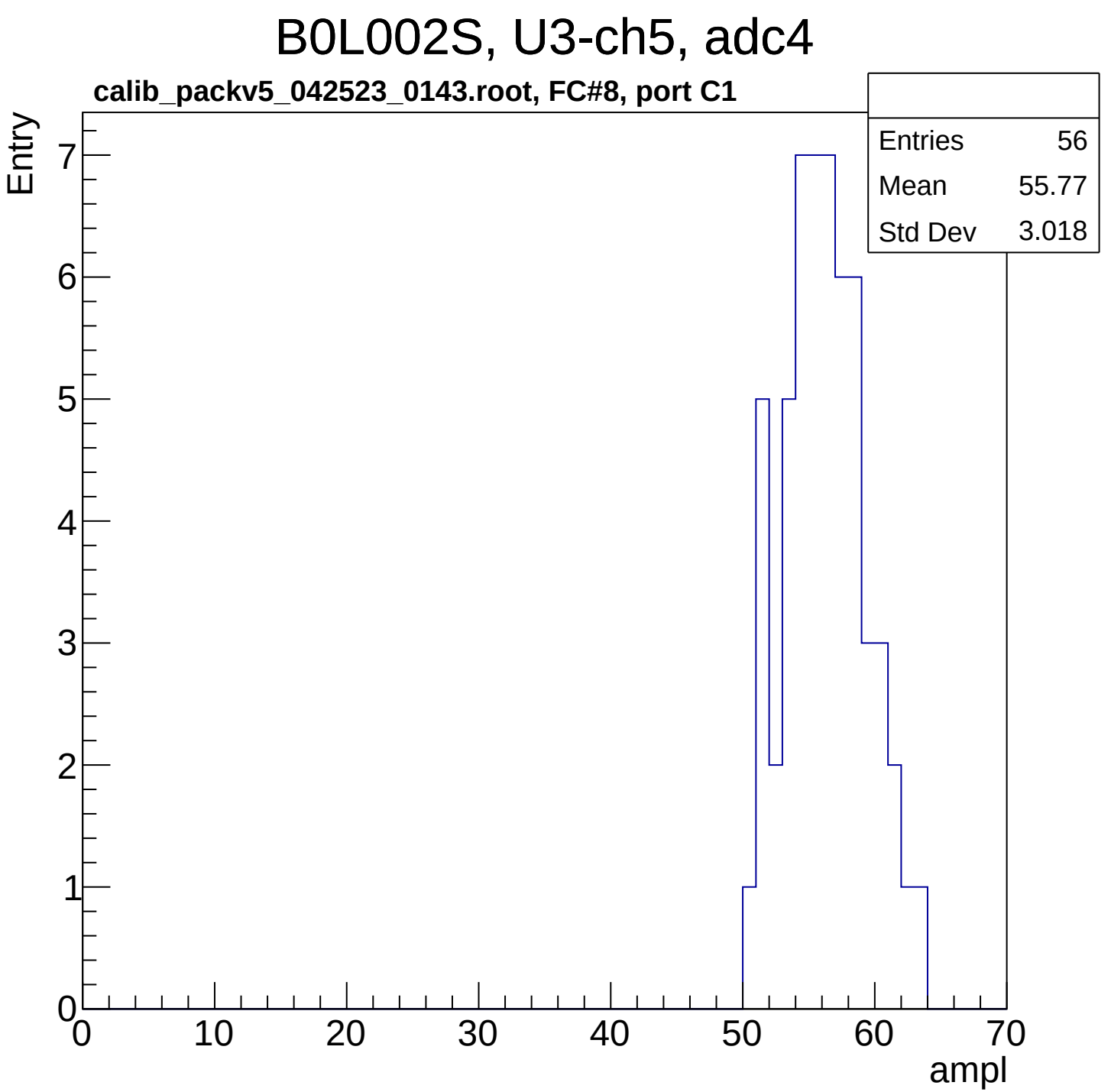
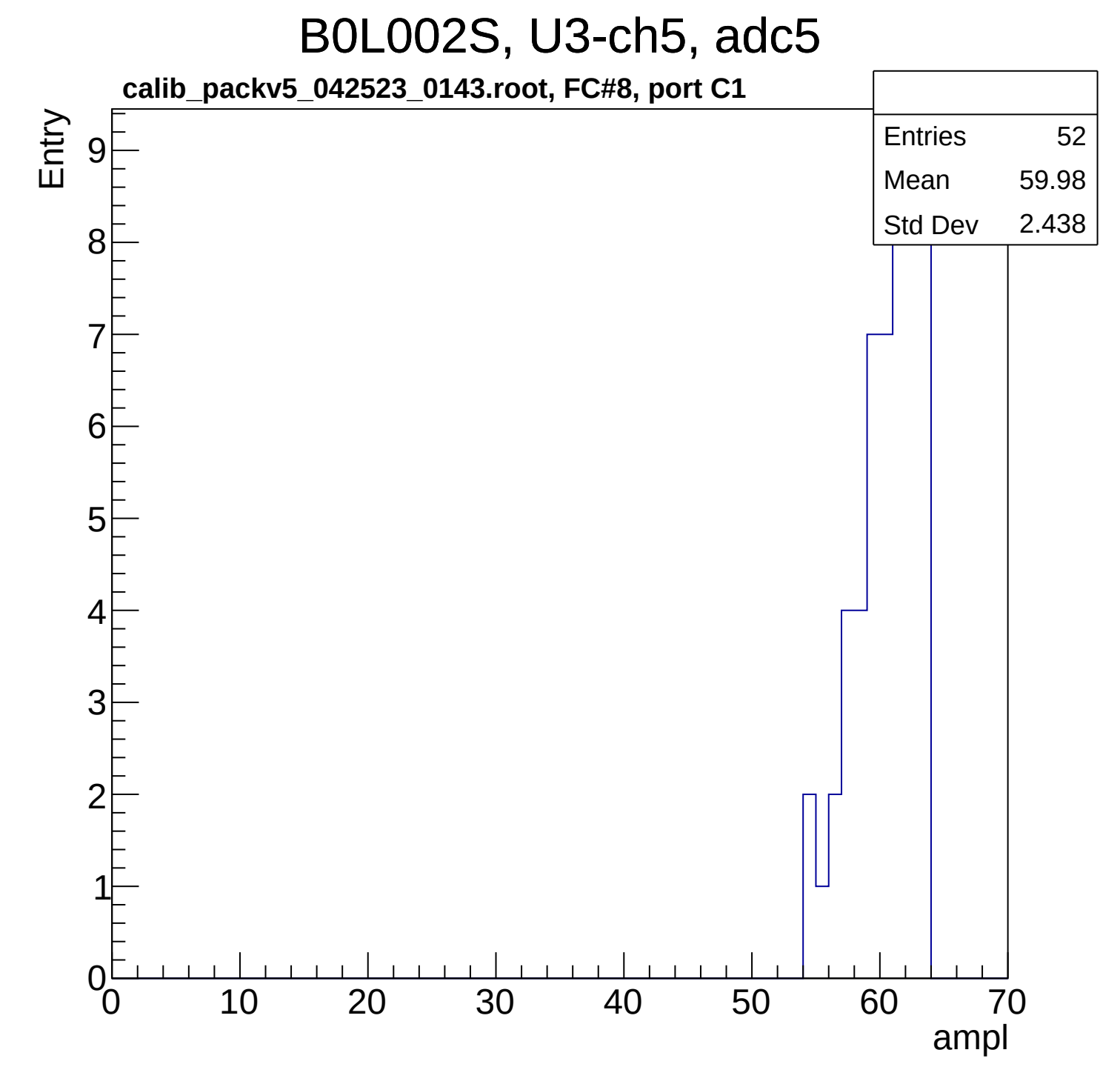

B0L002S, U3-ch5, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.77
Std Dev 3.018
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch5, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 59.98
Std Dev 2.438
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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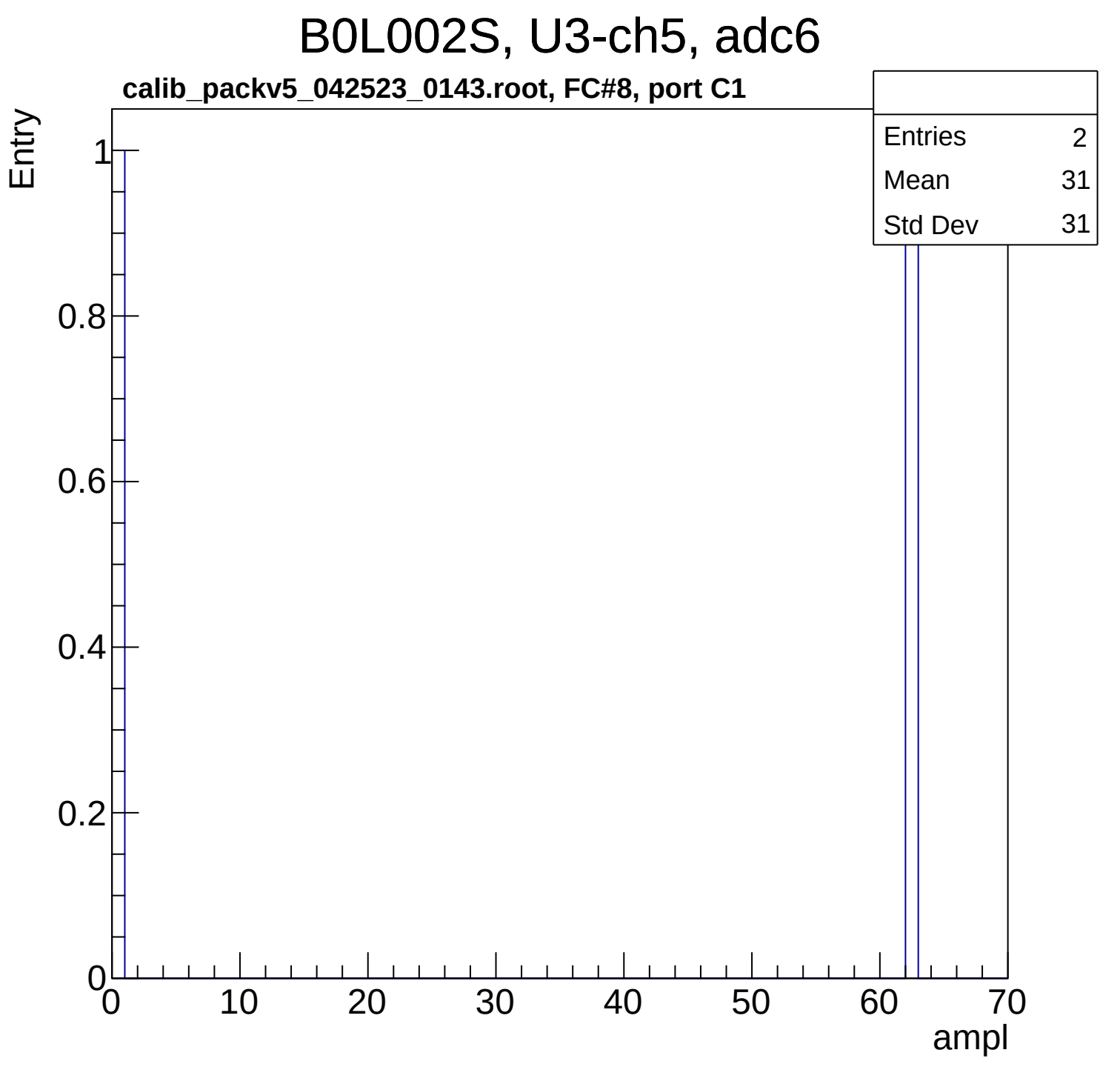
40
50
60
70


B0L002S, U3-ch5, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31
Std Dev 31
Entry
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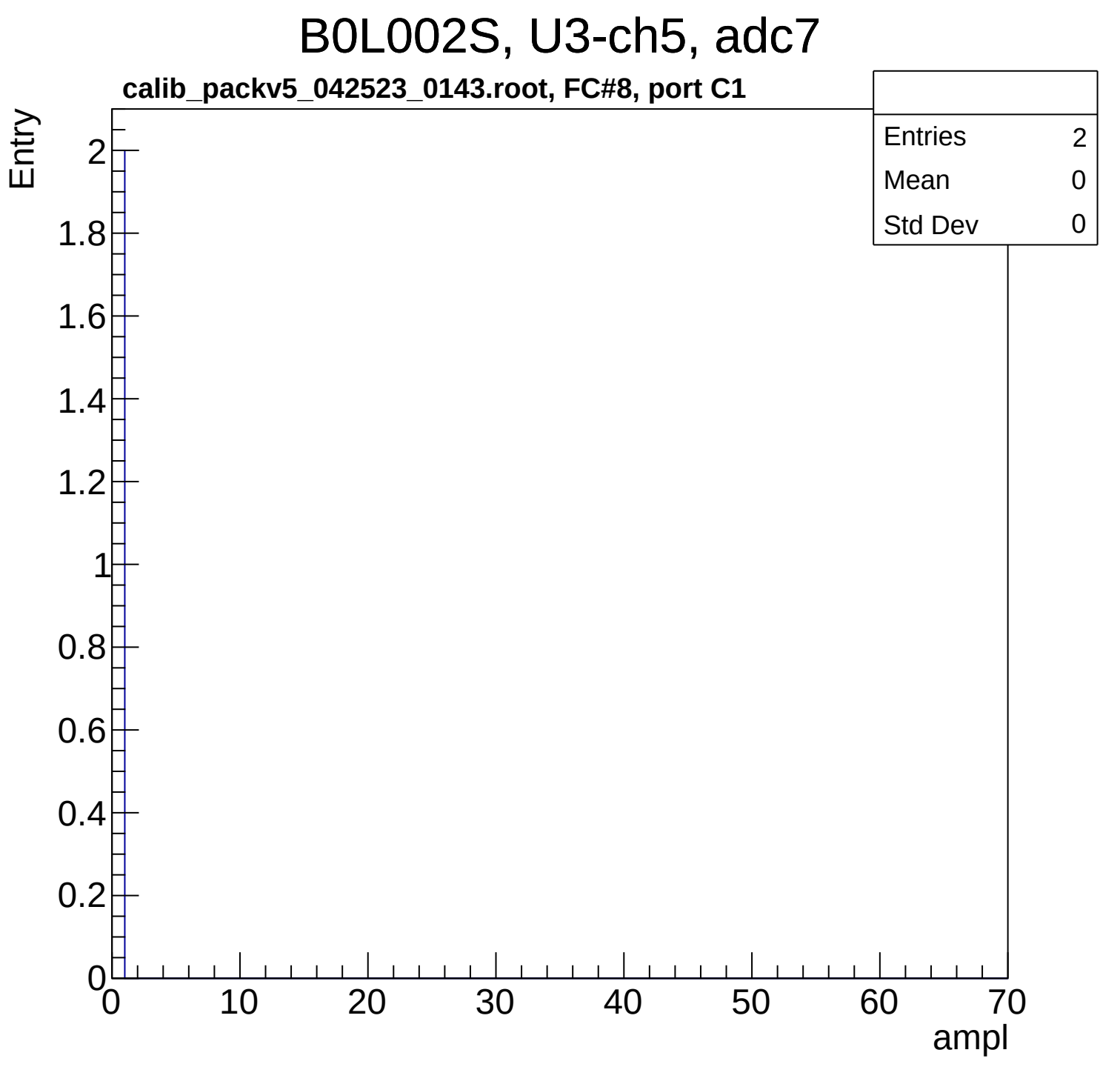
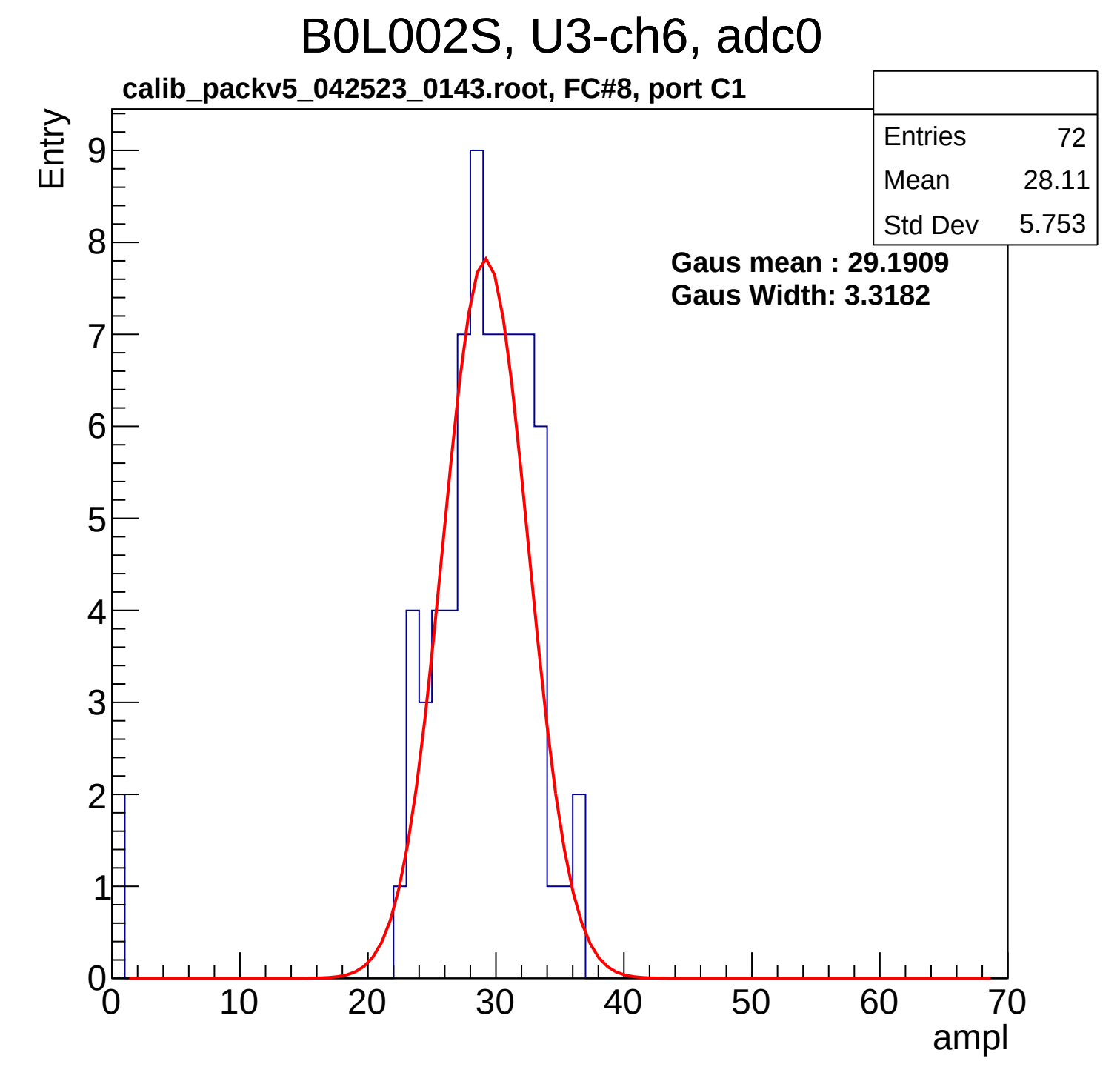
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch5, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch6, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 28.11
Std Dev 5.753
Gaus mean : 29.1909
Gaus Width: 3.3182
Entry
ampl
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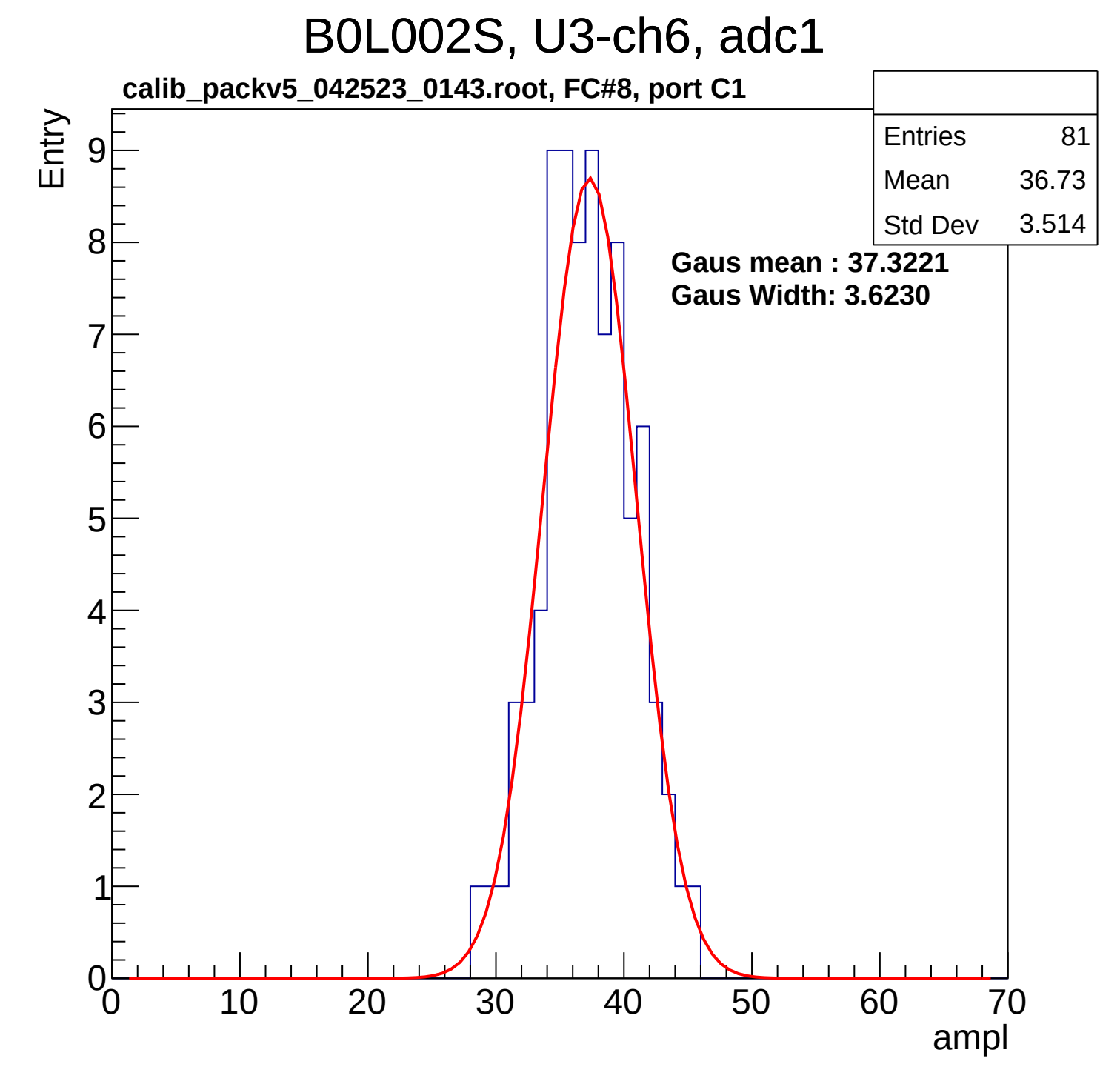
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch6, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 36.73
Std Dev 3.514
Gaus mean : 37.3221
Gaus Width: 3.6230
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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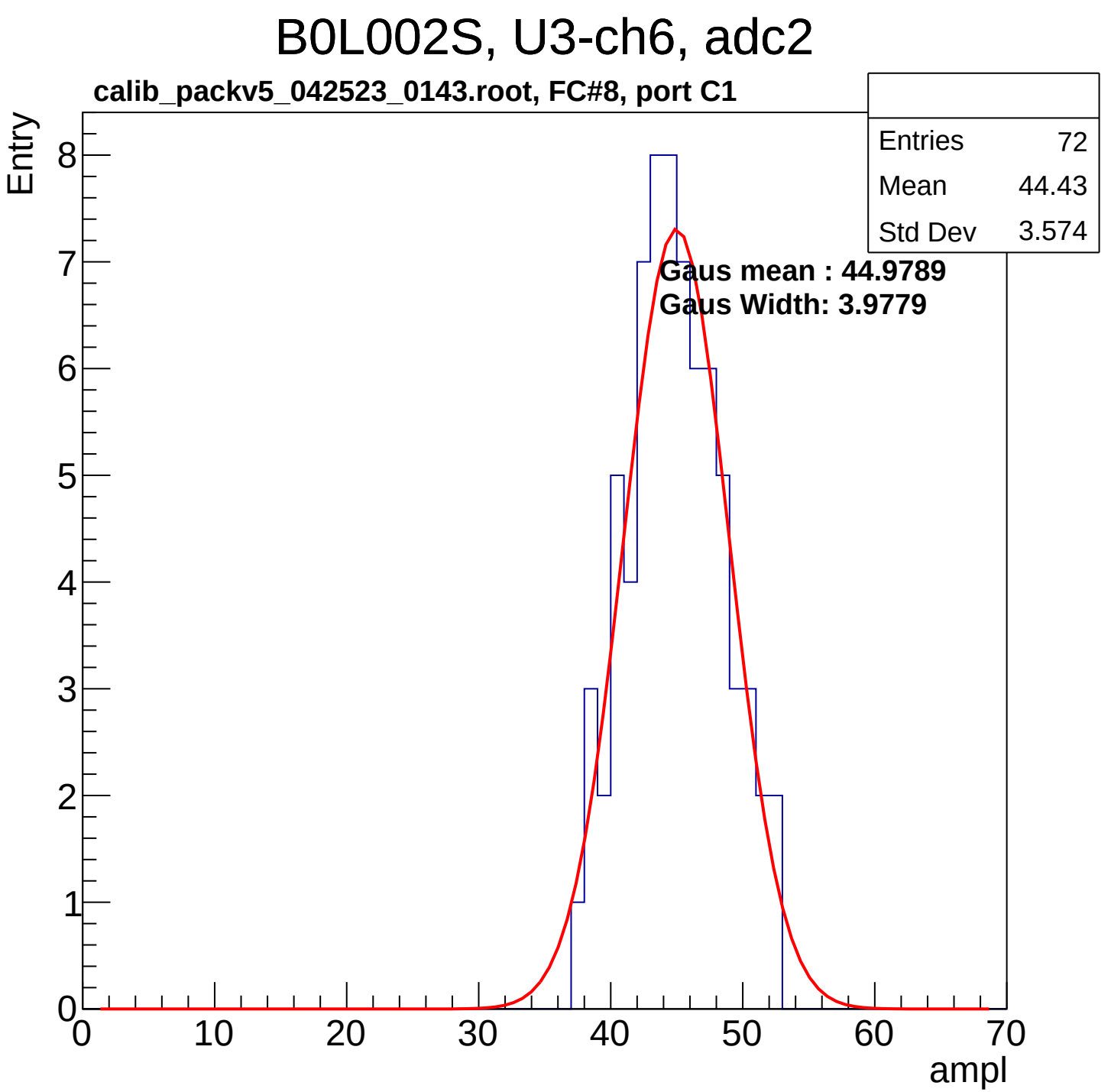
B0L002S, U3-ch6, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.43
Std Dev 3.574
Gaus mean : 44.9789
Gaus Width: 3.9779
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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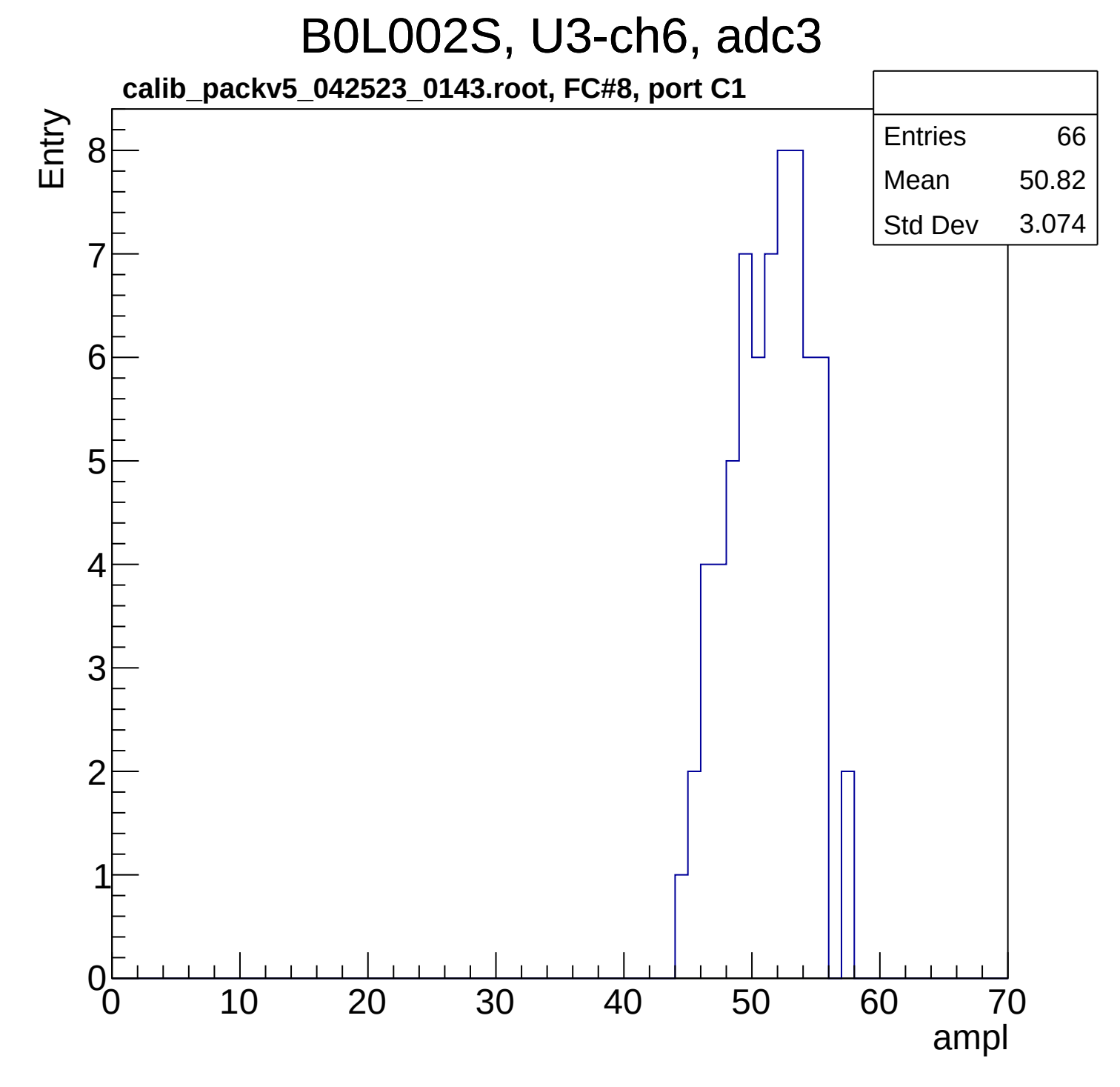
B0L002S, U3-ch6, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.82
Std Dev 3.074
Entry
ampl
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2
3
4
5
6
7
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0
10
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40
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60
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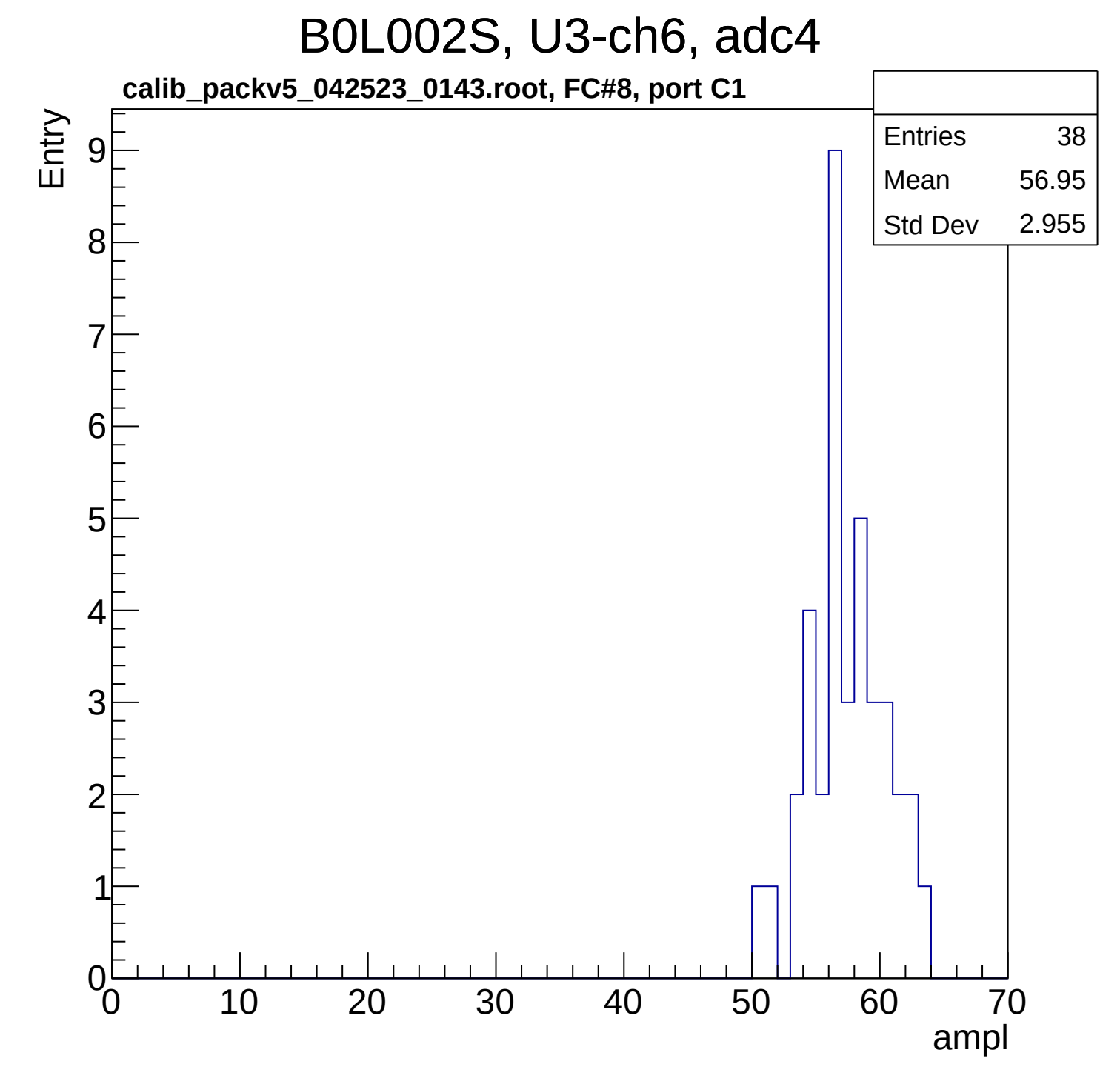
B0L002S, U3-ch6, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 56.95
Std Dev 2.955
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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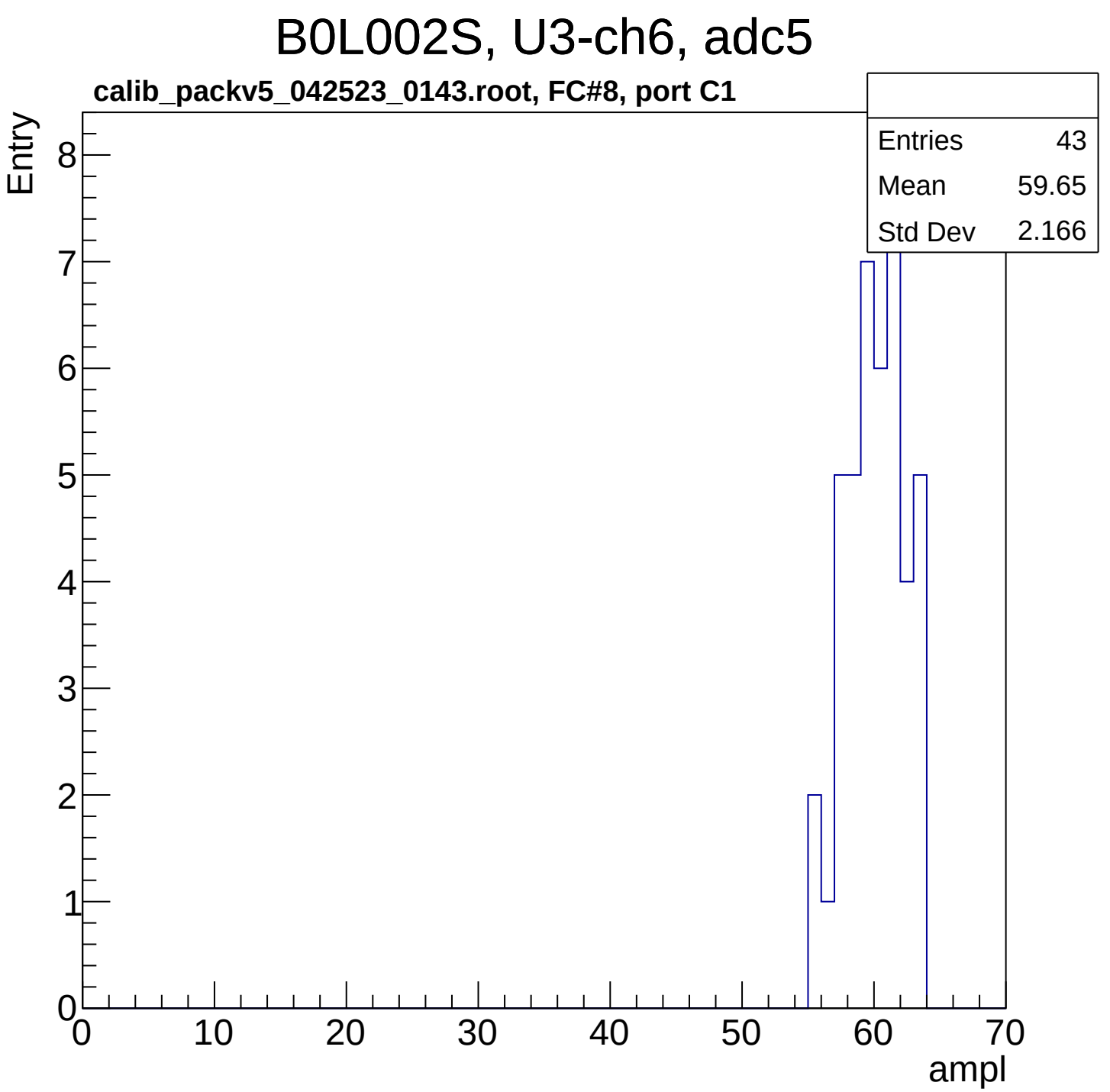
B0L002S, U3-ch6, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 59.65
Std Dev 2.166
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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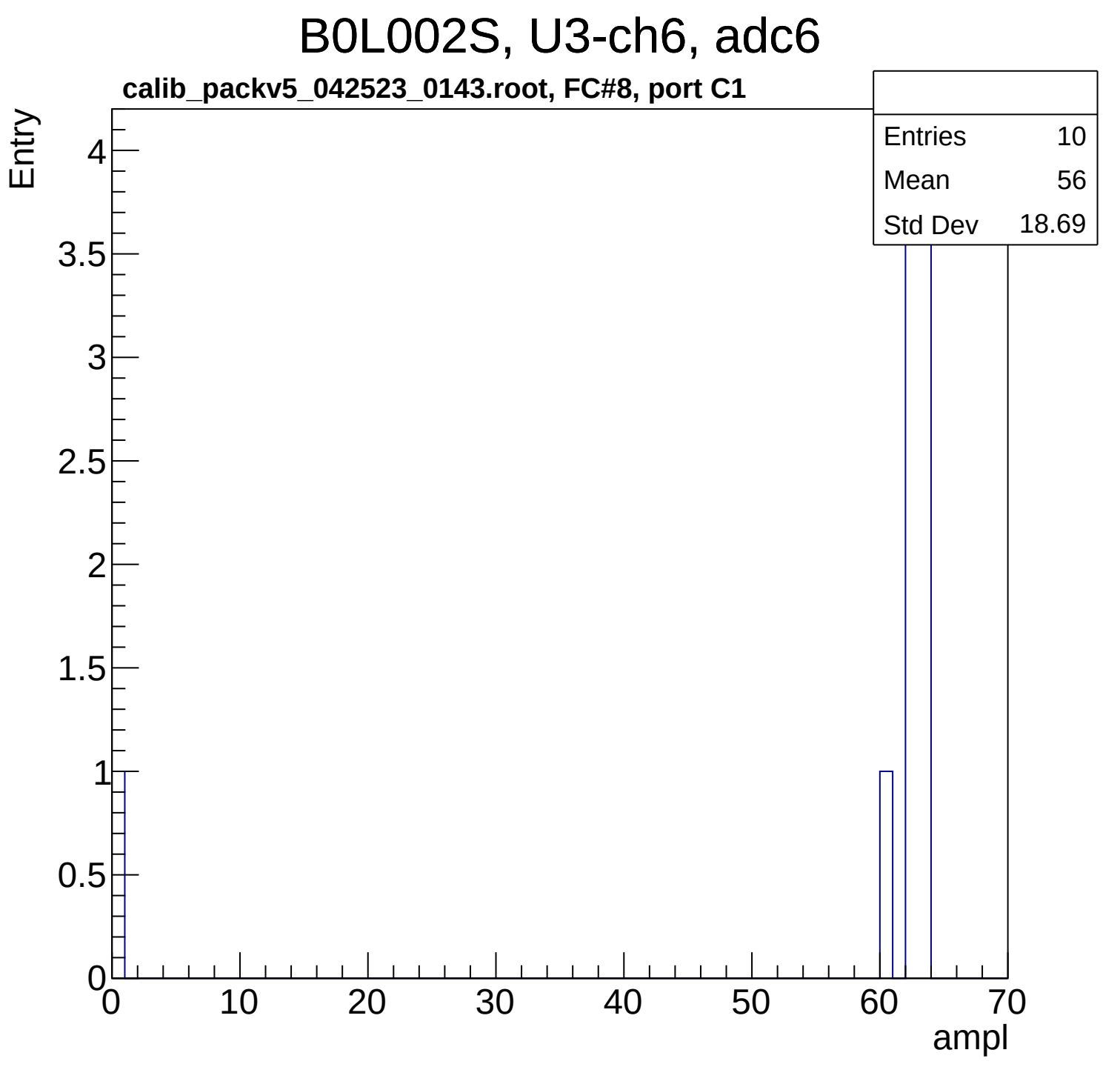
B0L002S, U3-ch6, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 56
Std Dev 18.69
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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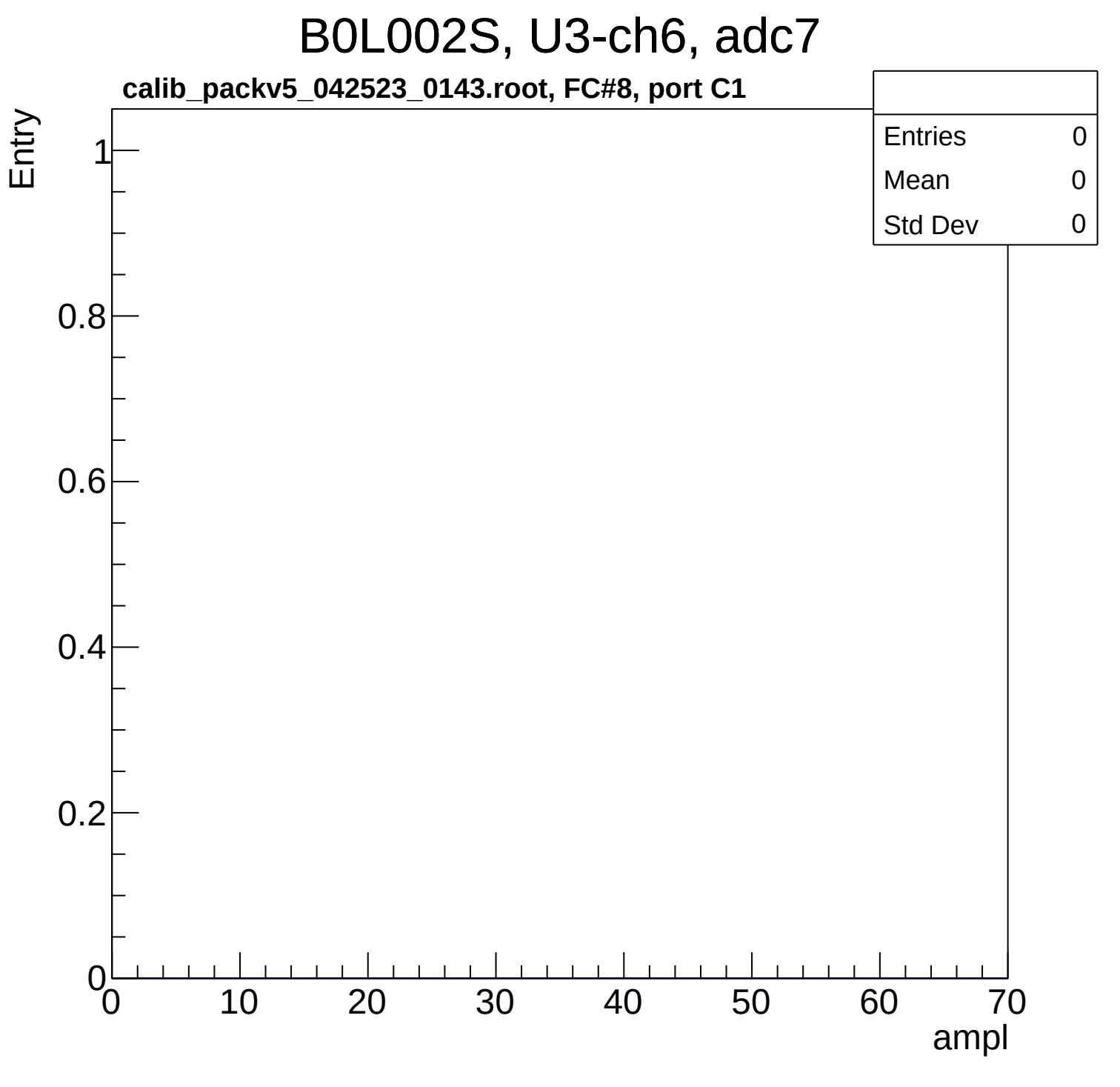
40
50
60
70


B0L002S, U3-ch6, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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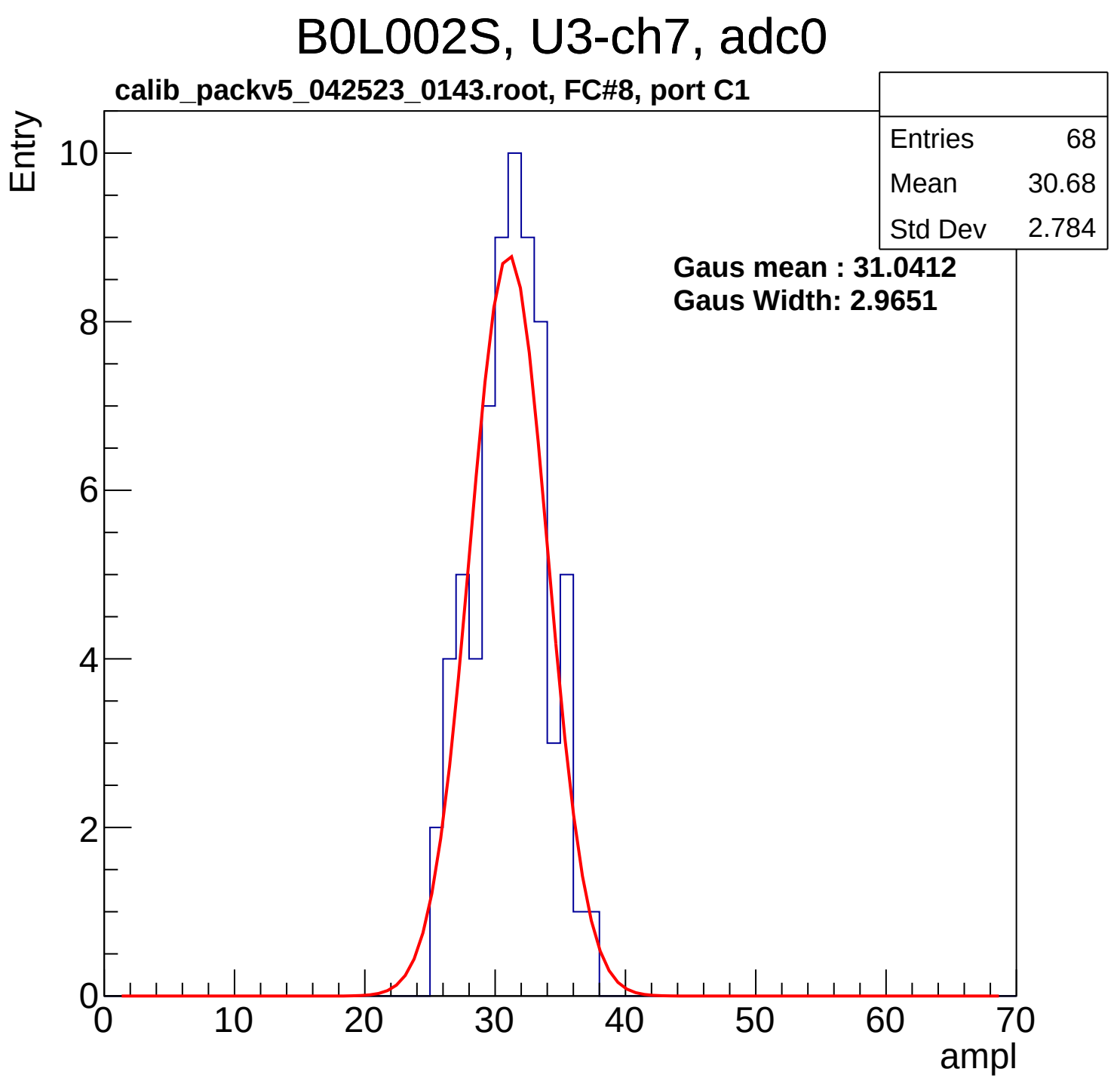
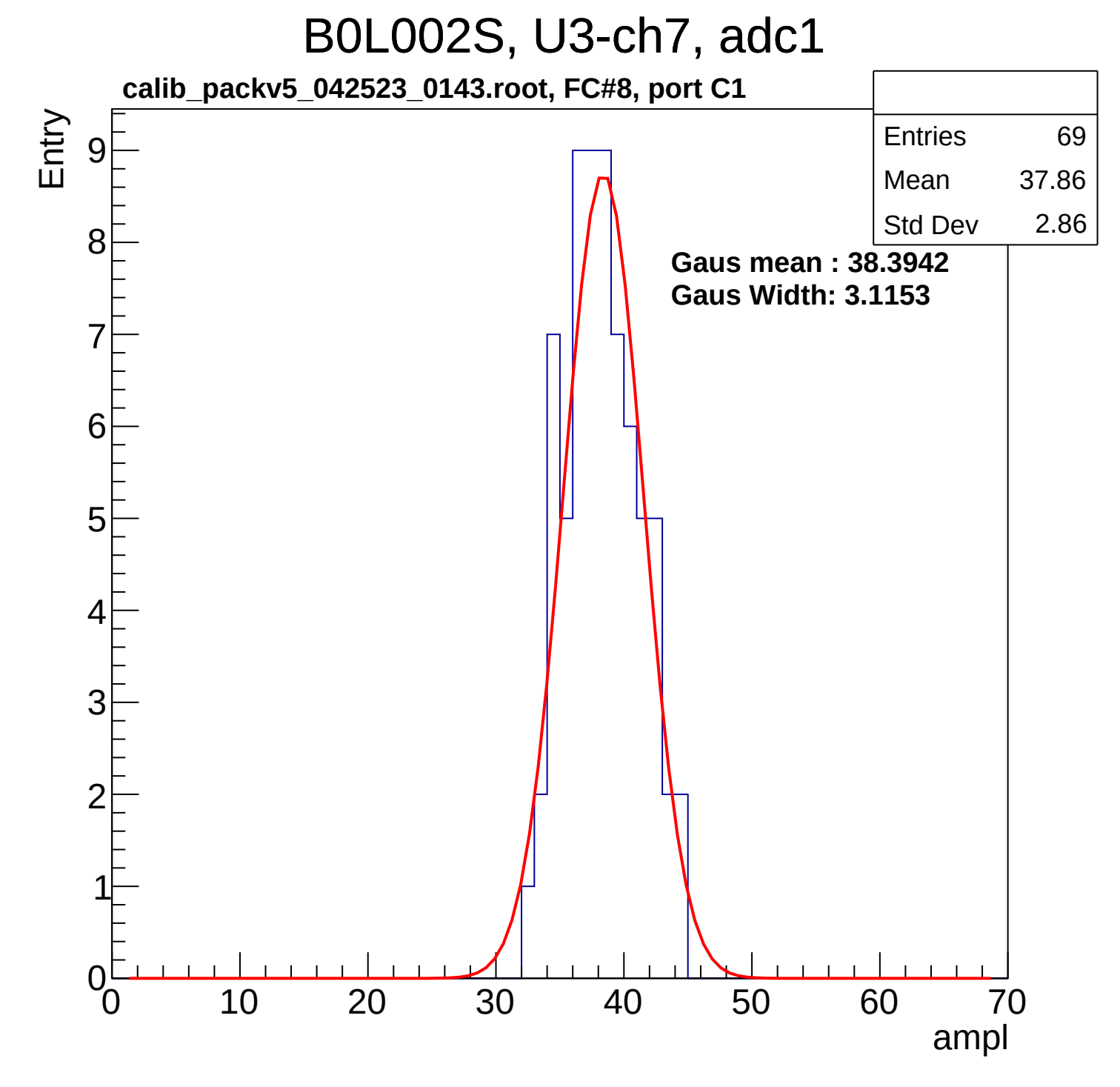
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch7, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 30.68
Std Dev 2.784
Gaus mean : 31.0412
Gaus Width: 2.9651
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch7, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.86
Std Dev 2.86
Gaus mean : 38.3942
Gaus Width: 3.1153
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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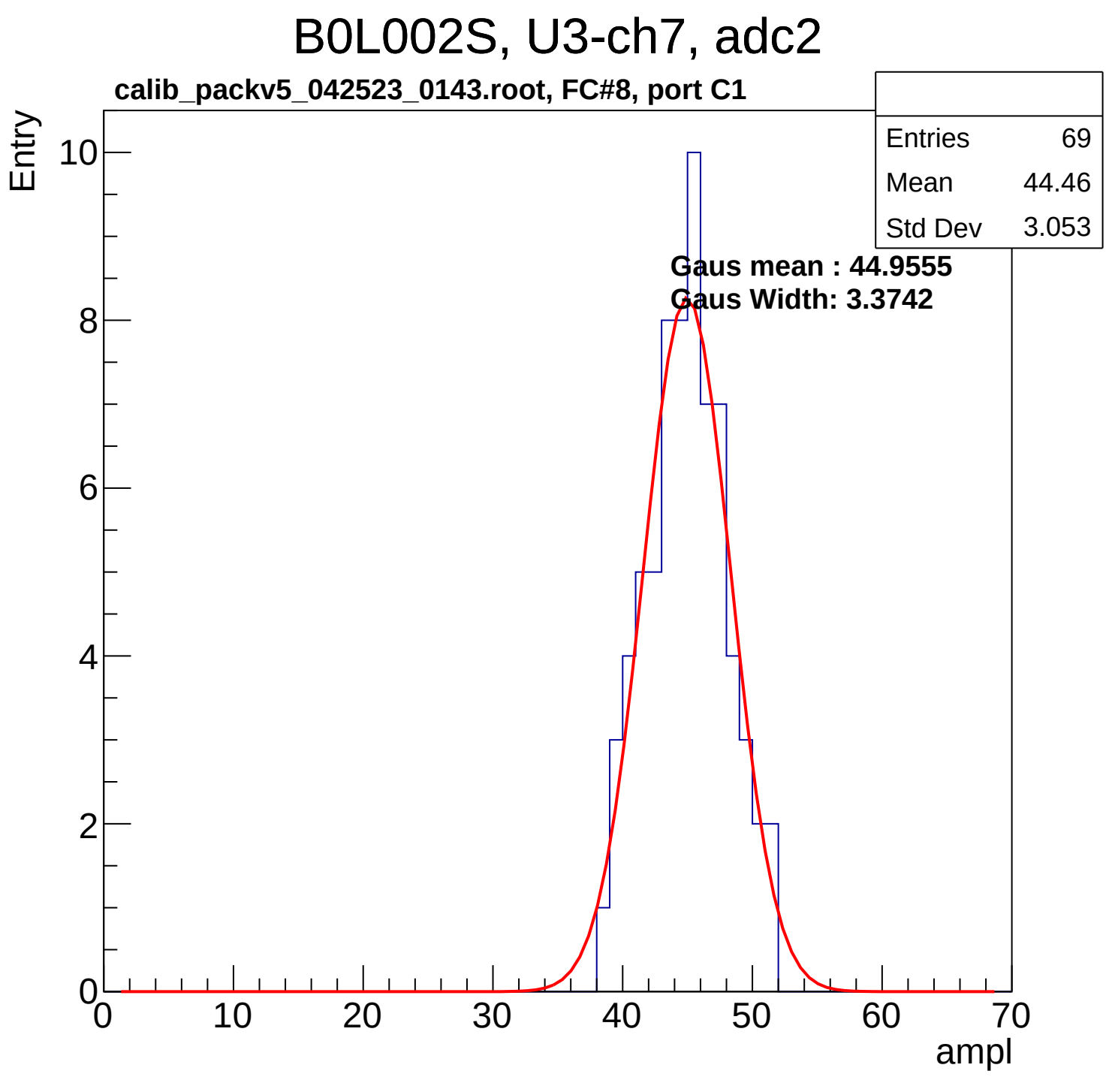
40
50
60
70


B0L002S, U3-ch7, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.46
Std Dev 3.053
Gaus mean : 44.9555
Gaus Width: 3.3742
Entry
ampl
0
2
4
6
8
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40
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60
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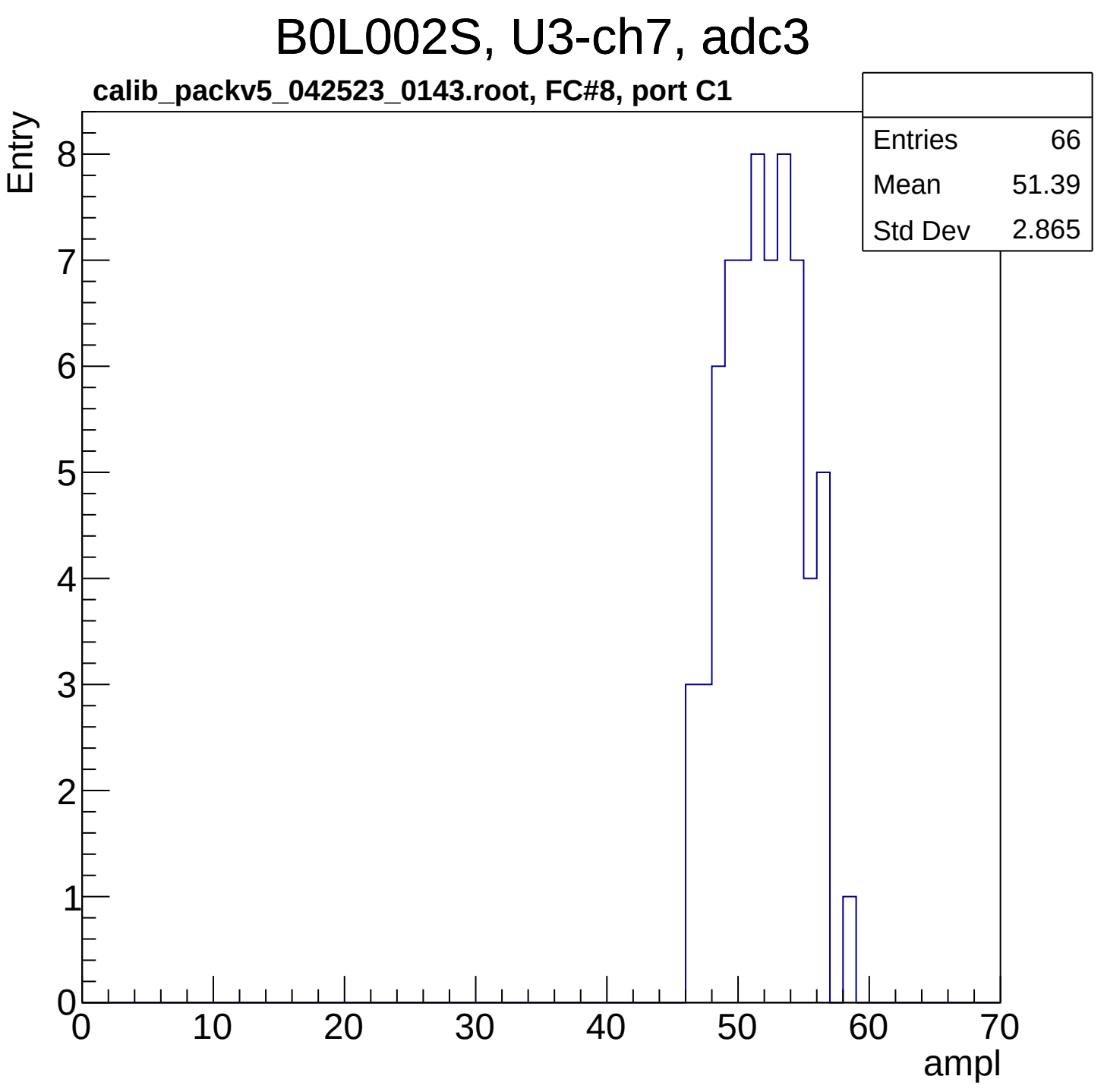
B0L002S, U3-ch7, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.39
Std Dev 2.865
Entry
ampl
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2
3
4
5
6
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0
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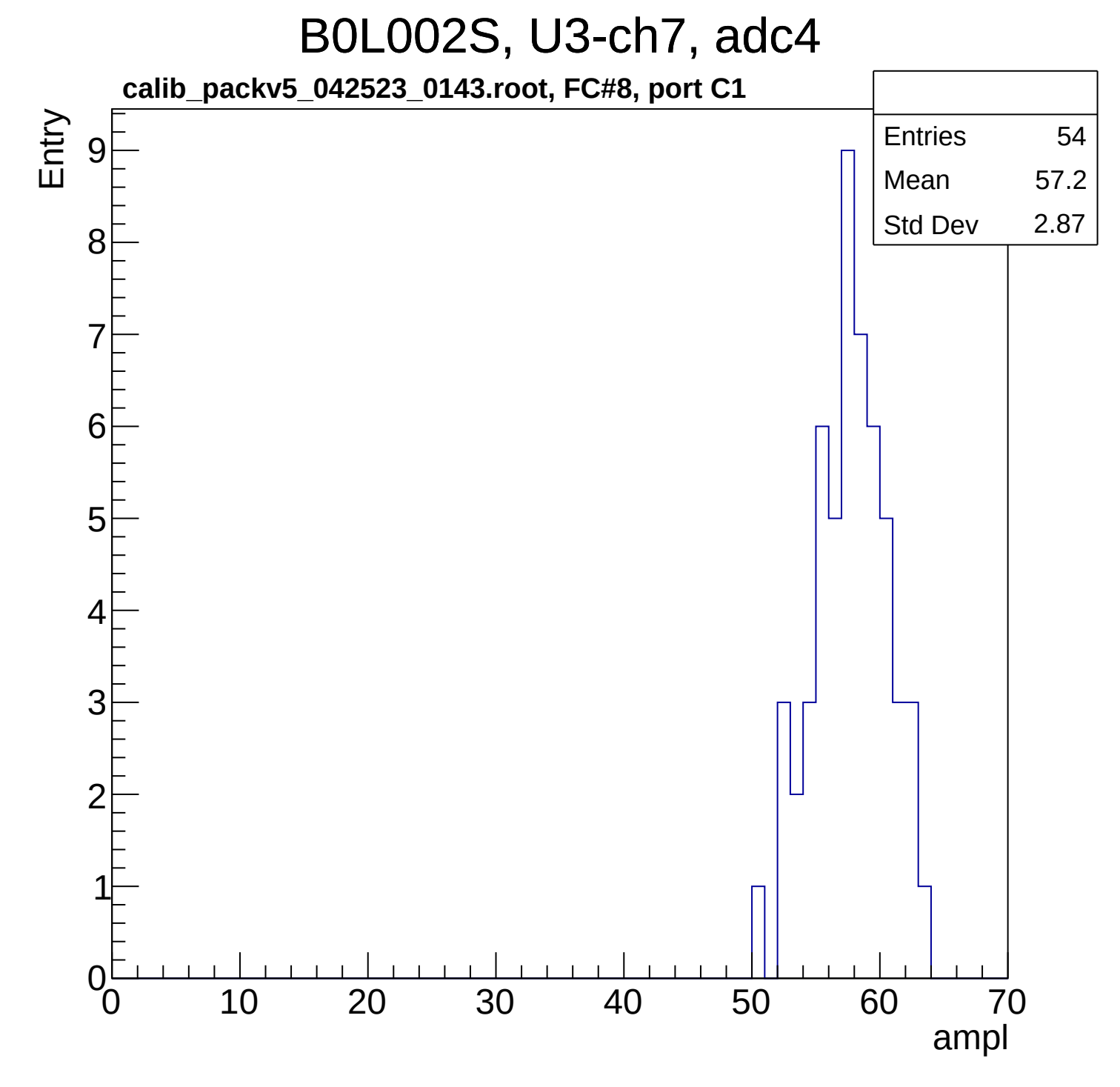
B0L002S, U3-ch7, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.2
Std Dev 2.87
Entry
ampl
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2
3
4
5
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7
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0
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40
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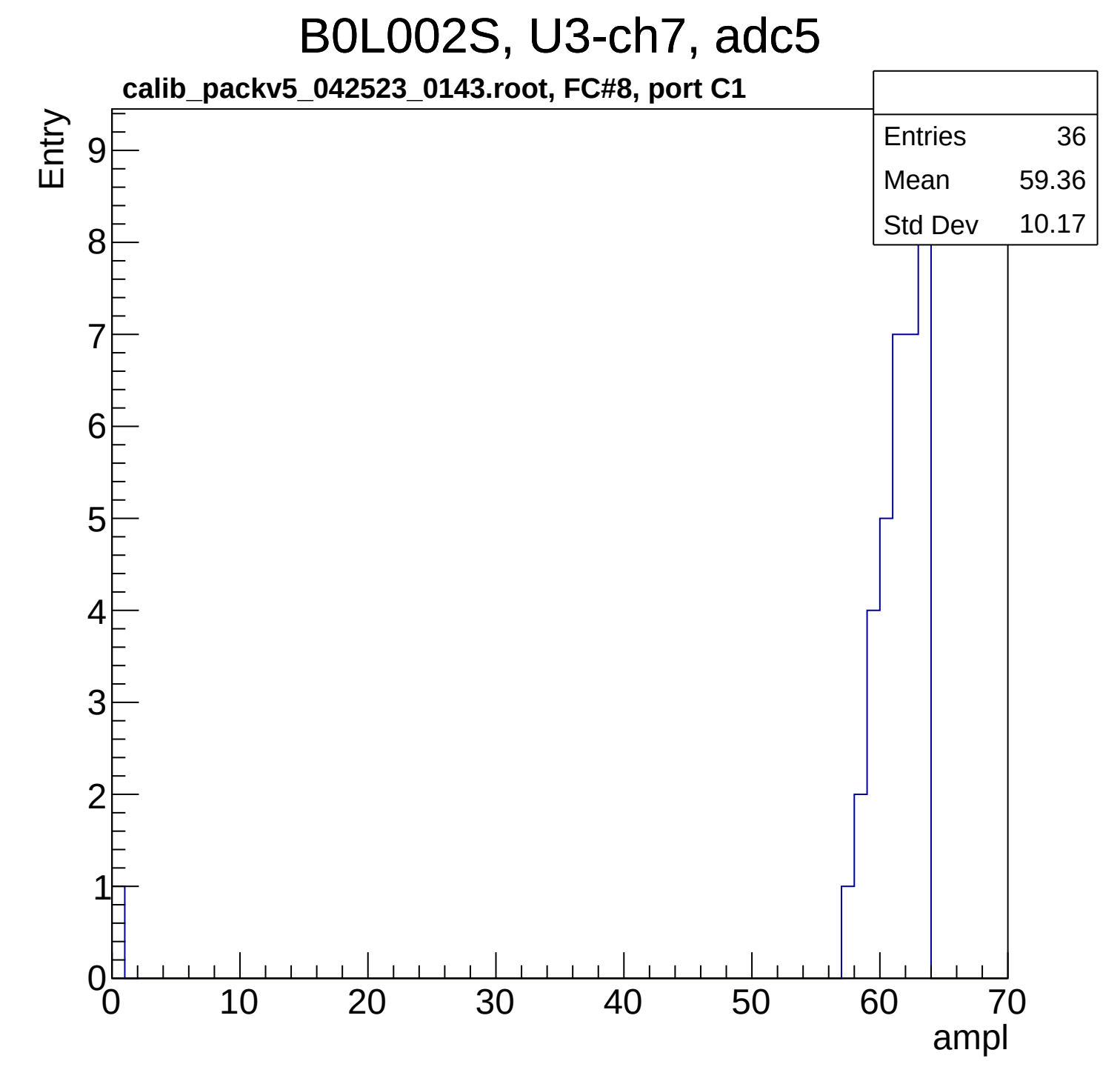
B0L002S, U3-ch7, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.36
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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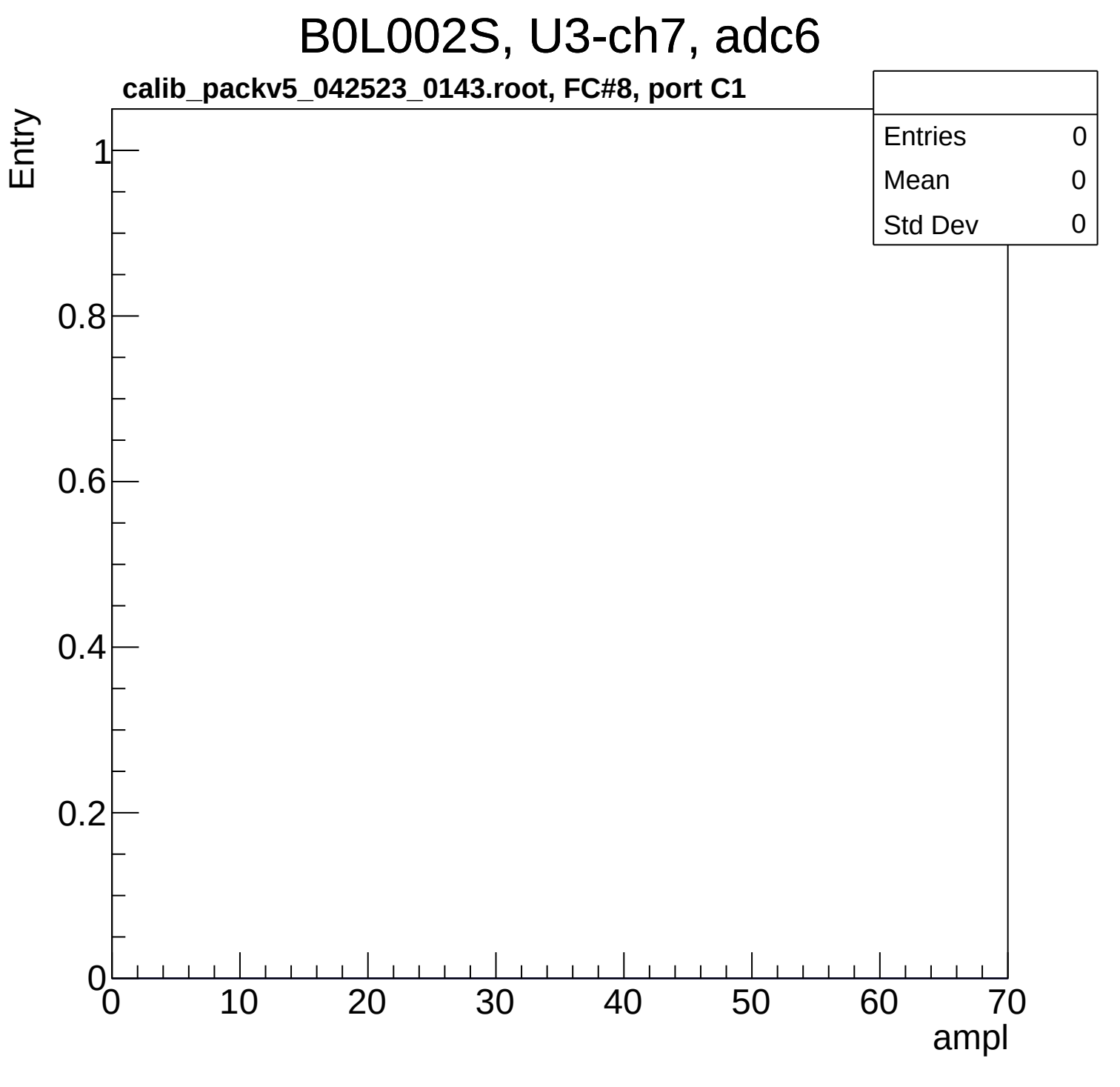
60
70


B0L002S, U3-ch7, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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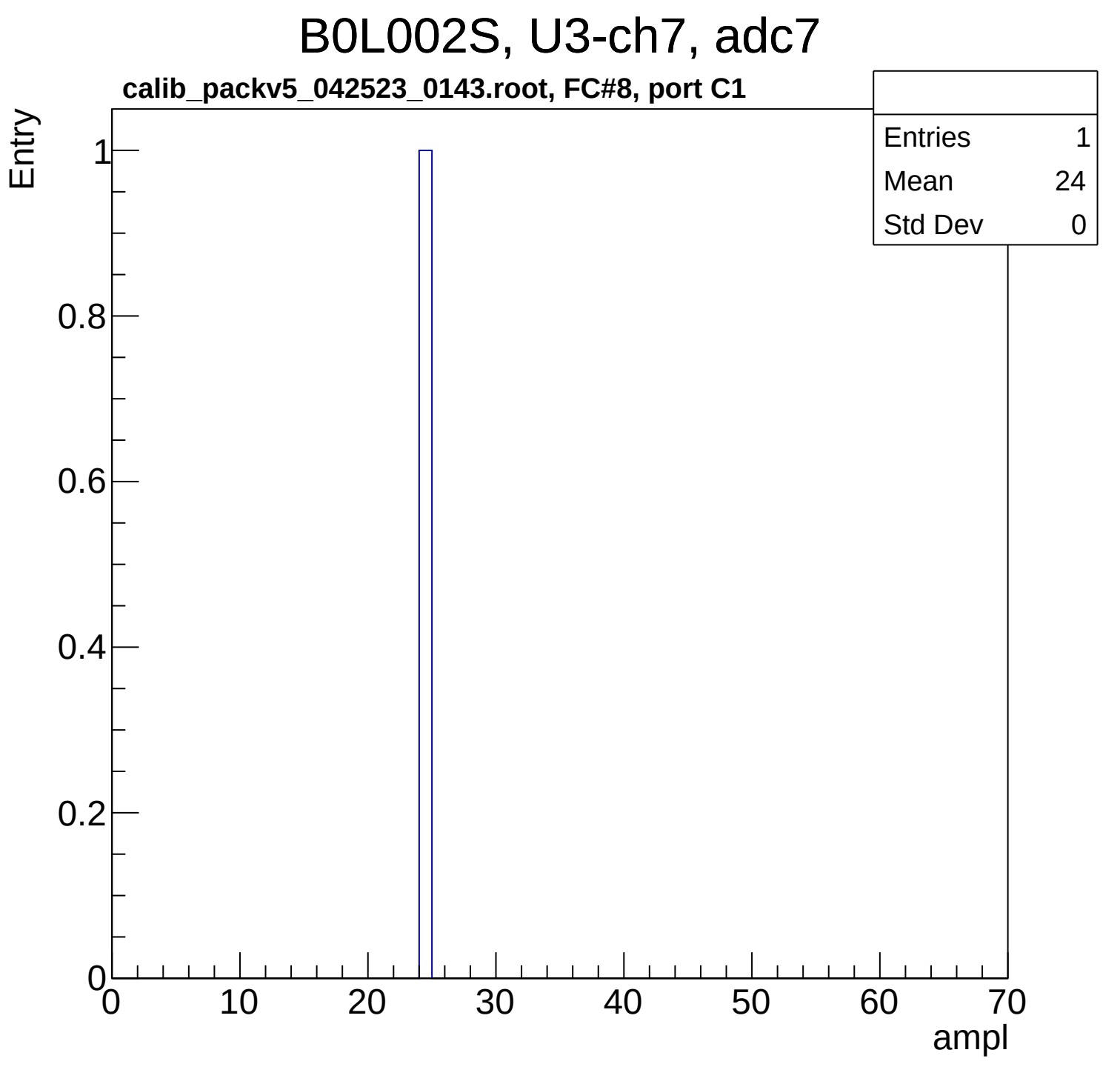
50
60
70
ampl


B0L002S, U3-ch7, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
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0
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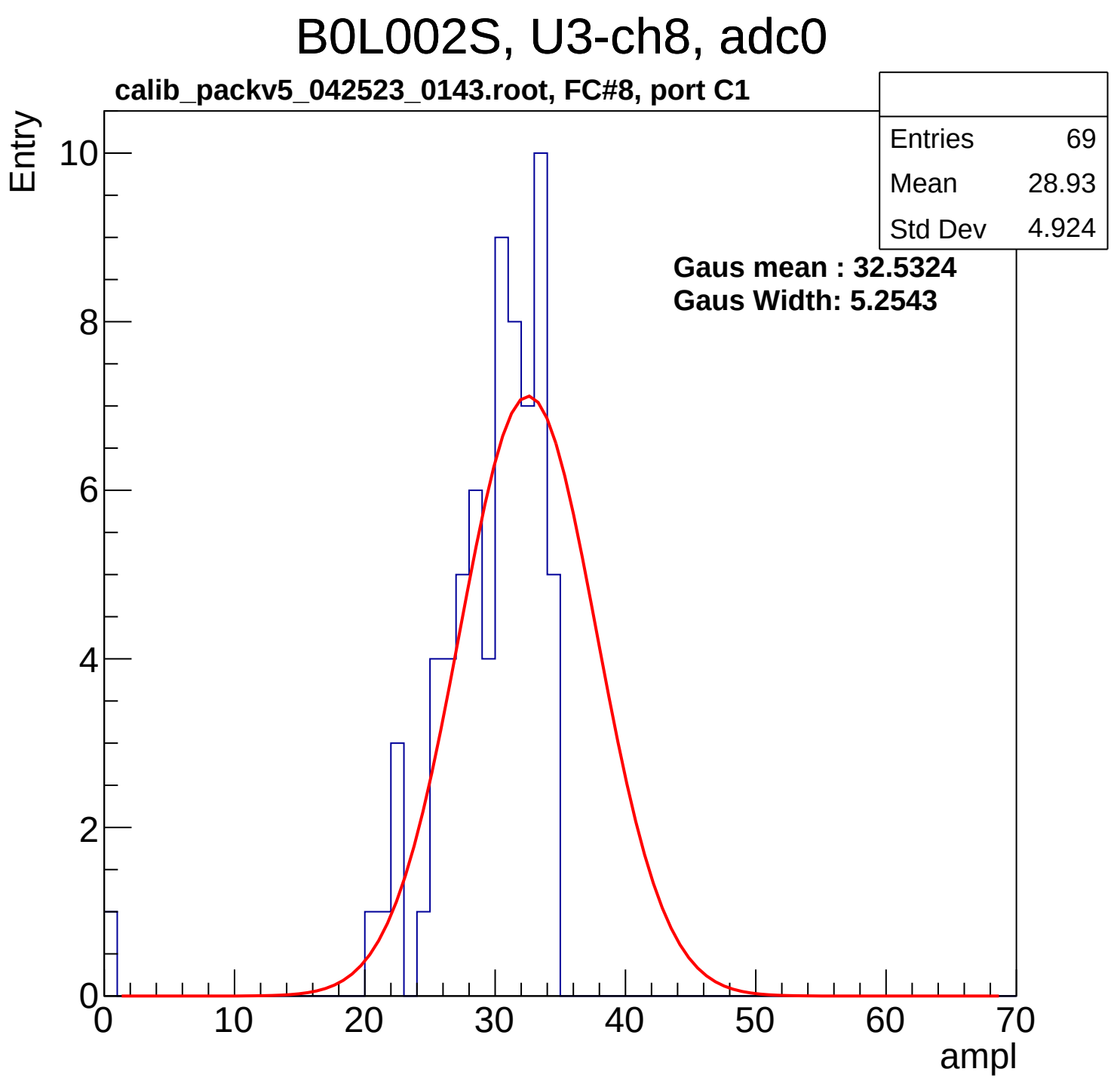
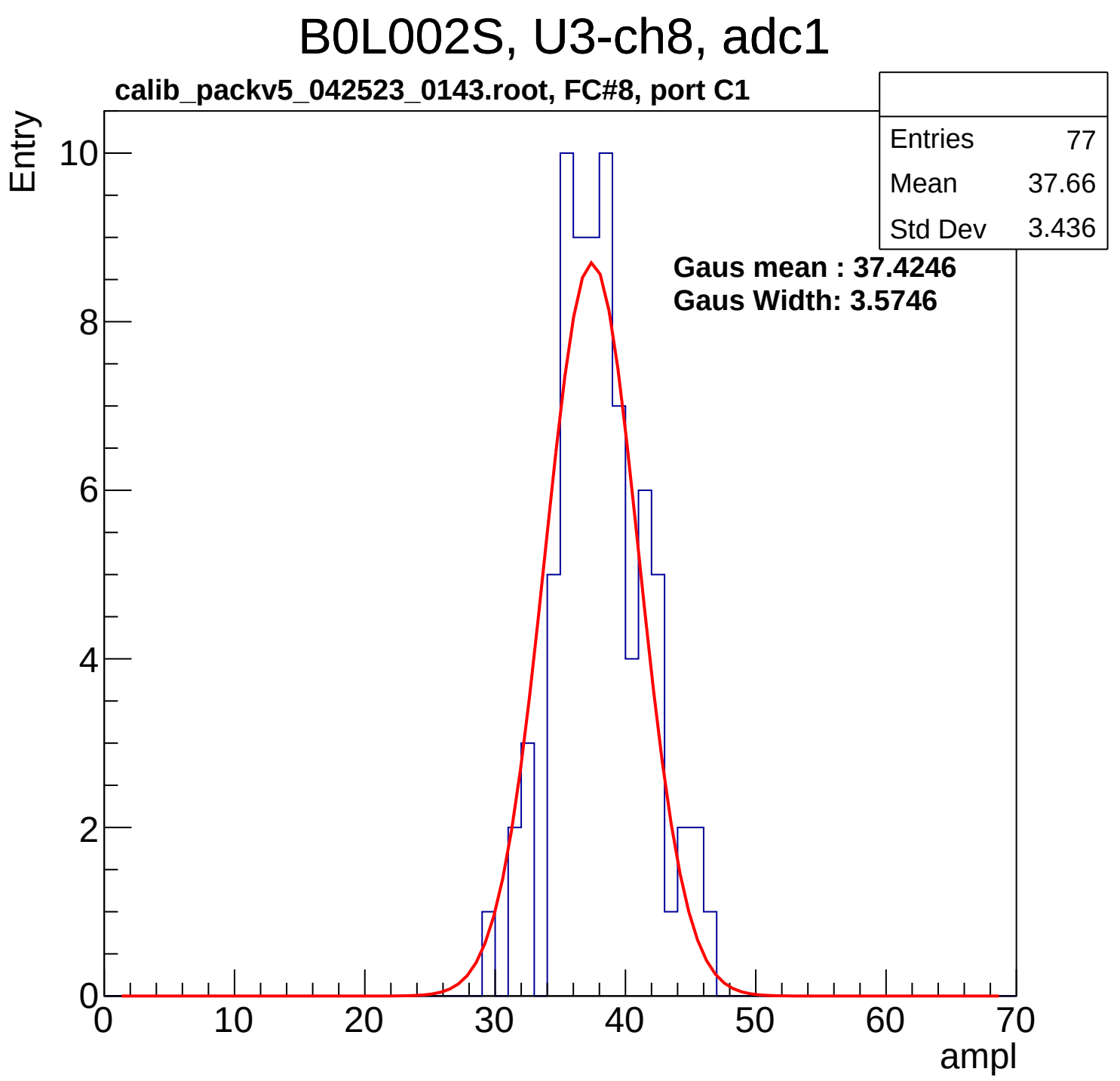
50
60
70
ampl


B0L002S, U3-ch8, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 28.93
Std Dev 4.924
Gaus mean : 32.5324
Gaus Width: 5.2543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch8, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.66
Std Dev 3.436
Gaus mean : 37.4246
Gaus Width: 3.5746
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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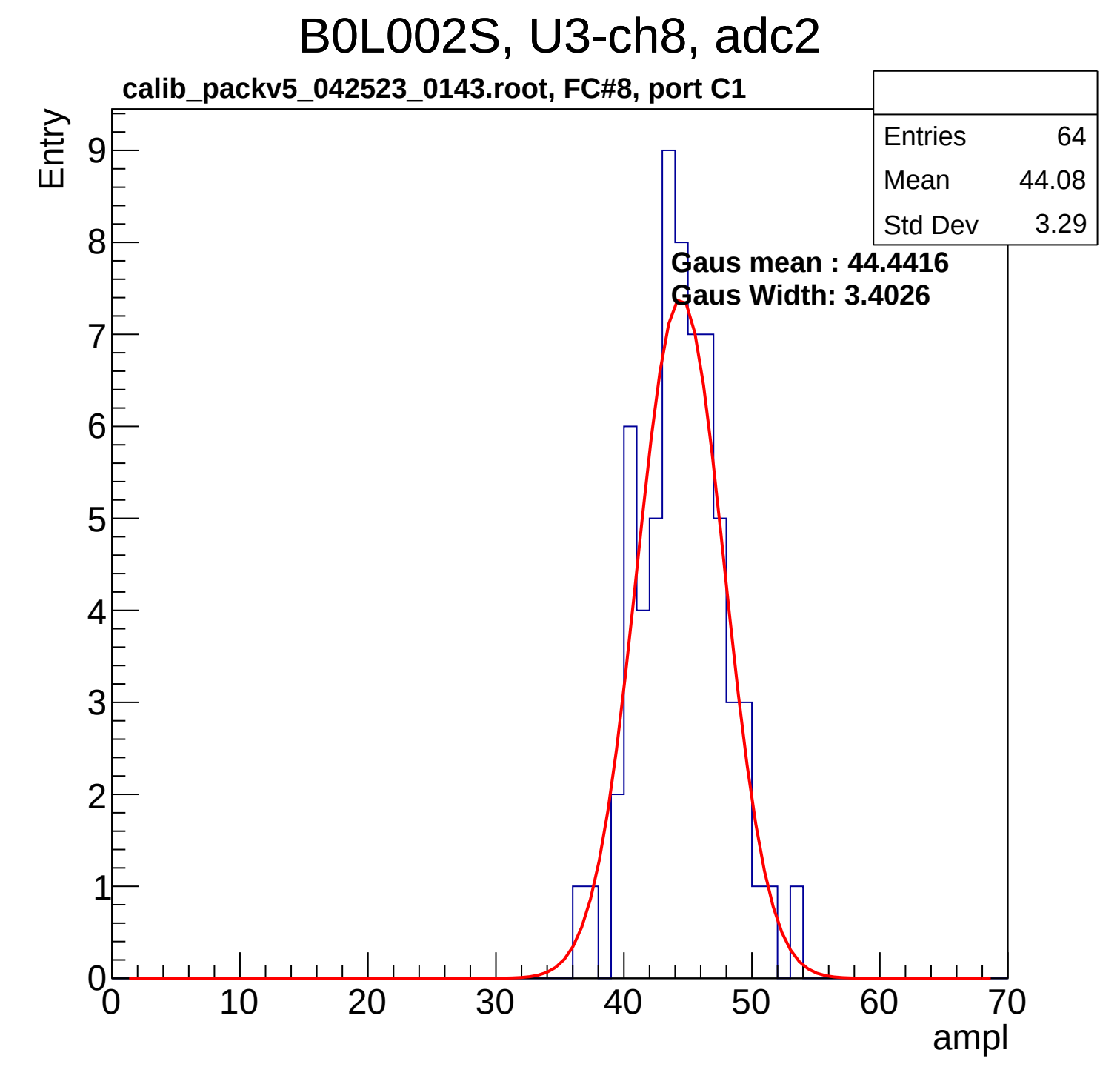
B0L002S, U3-ch8, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.08
Std Dev 3.29
Gaus mean : 44.4416
Gaus Width: 3.4026
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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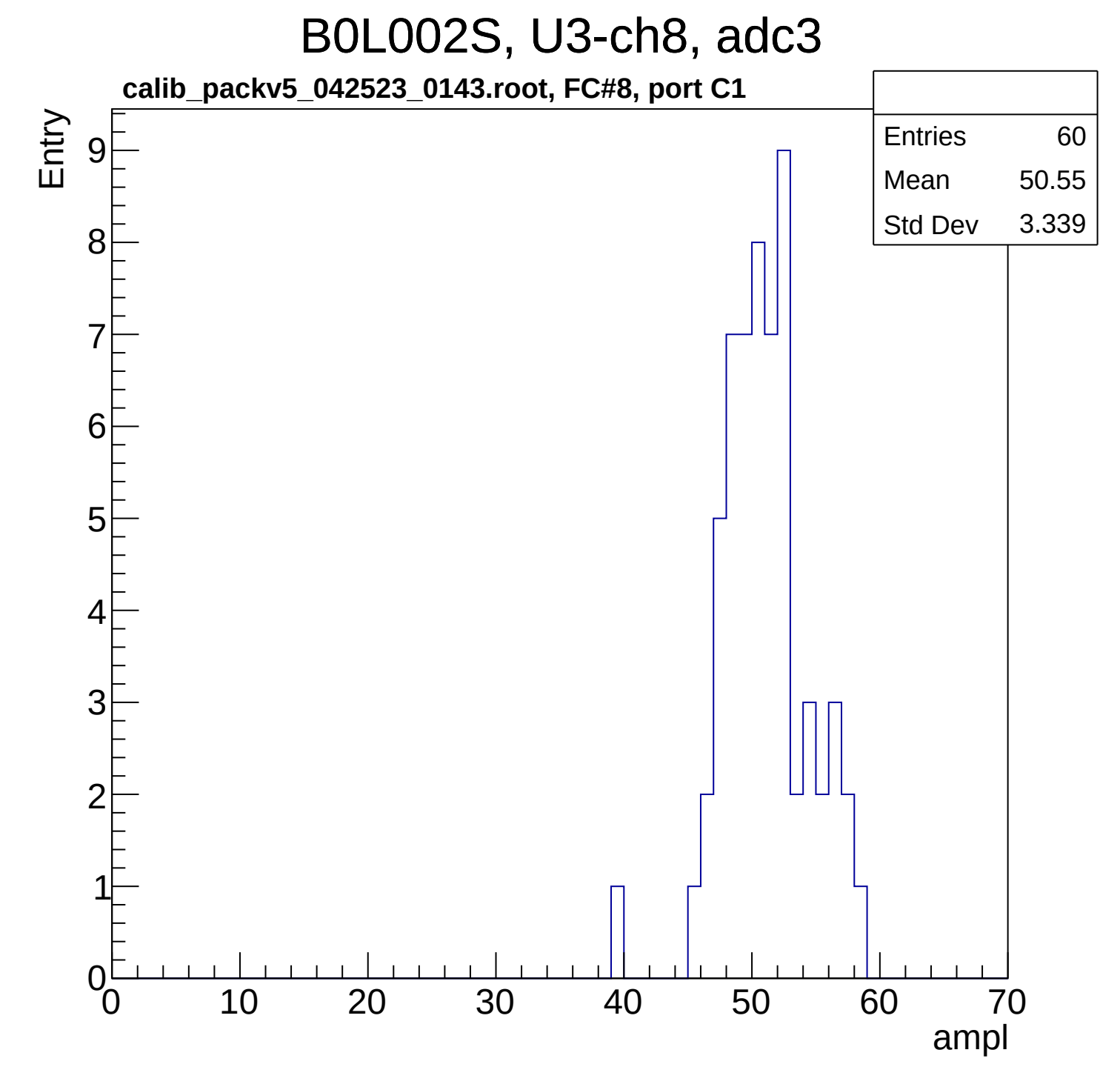
B0L002S, U3-ch8, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.55
Std Dev 3.339
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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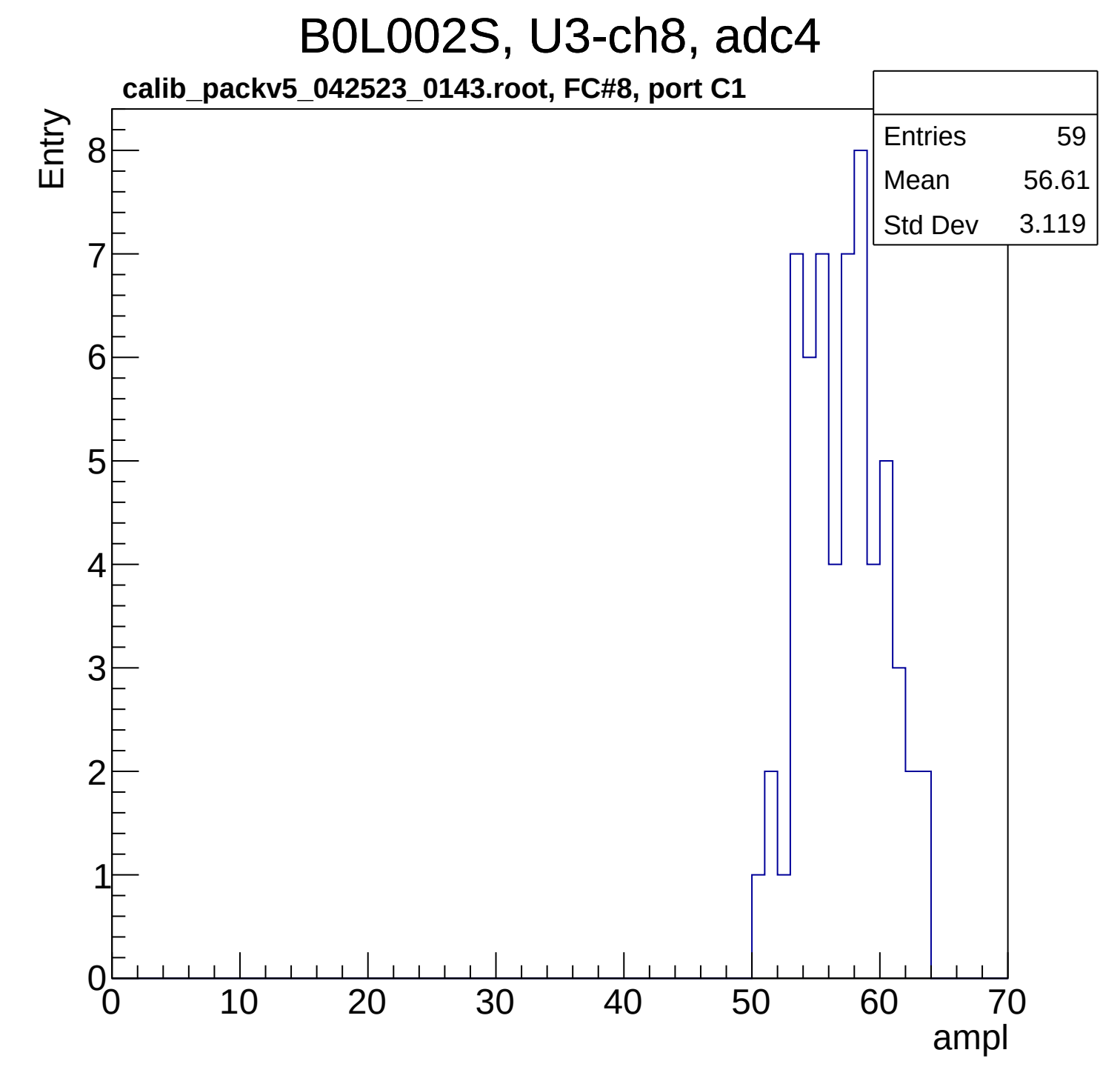
B0L002S, U3-ch8, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.61
Std Dev 3.119
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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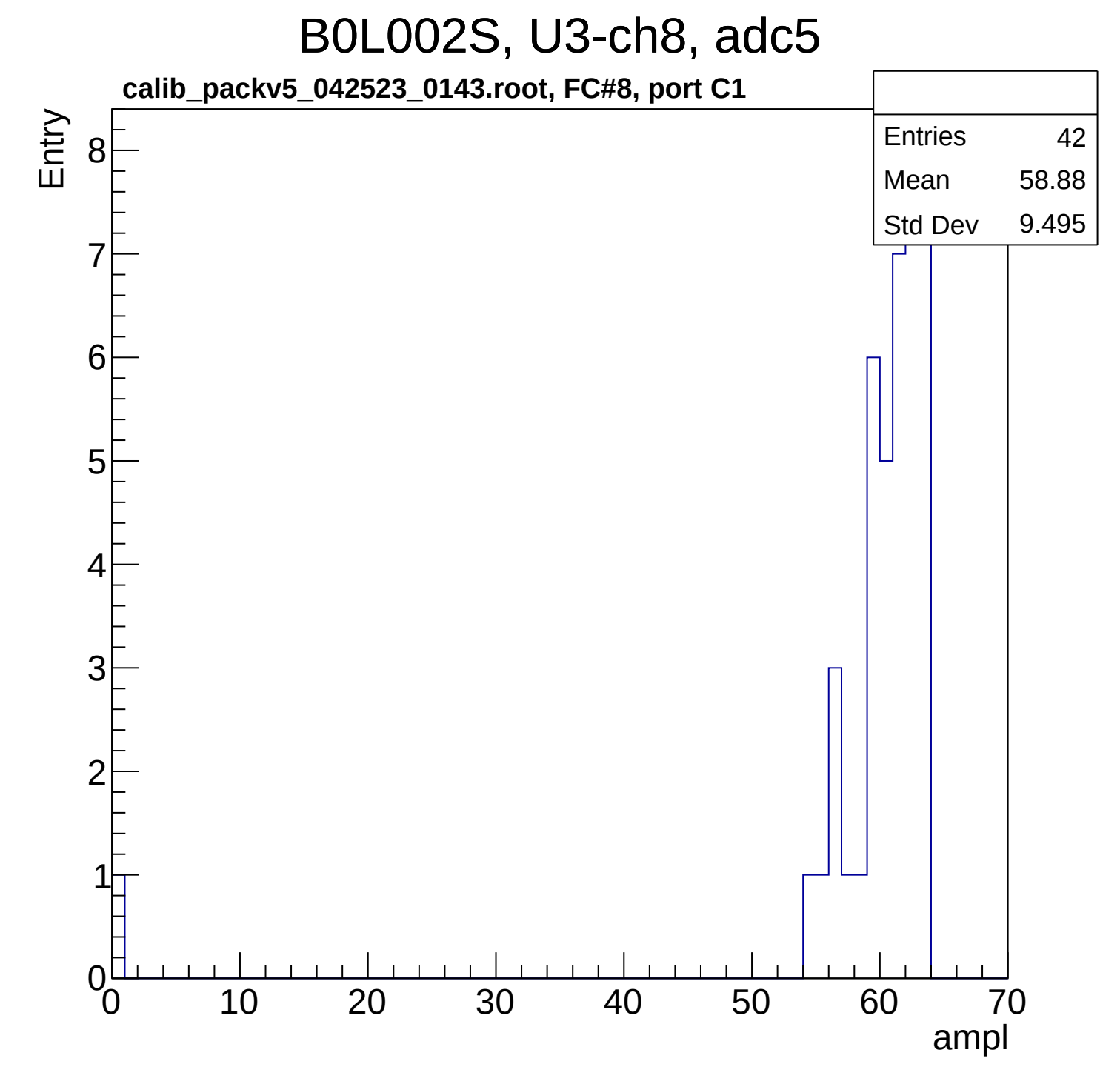
B0L002S, U3-ch8, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.88
Std Dev 9.495
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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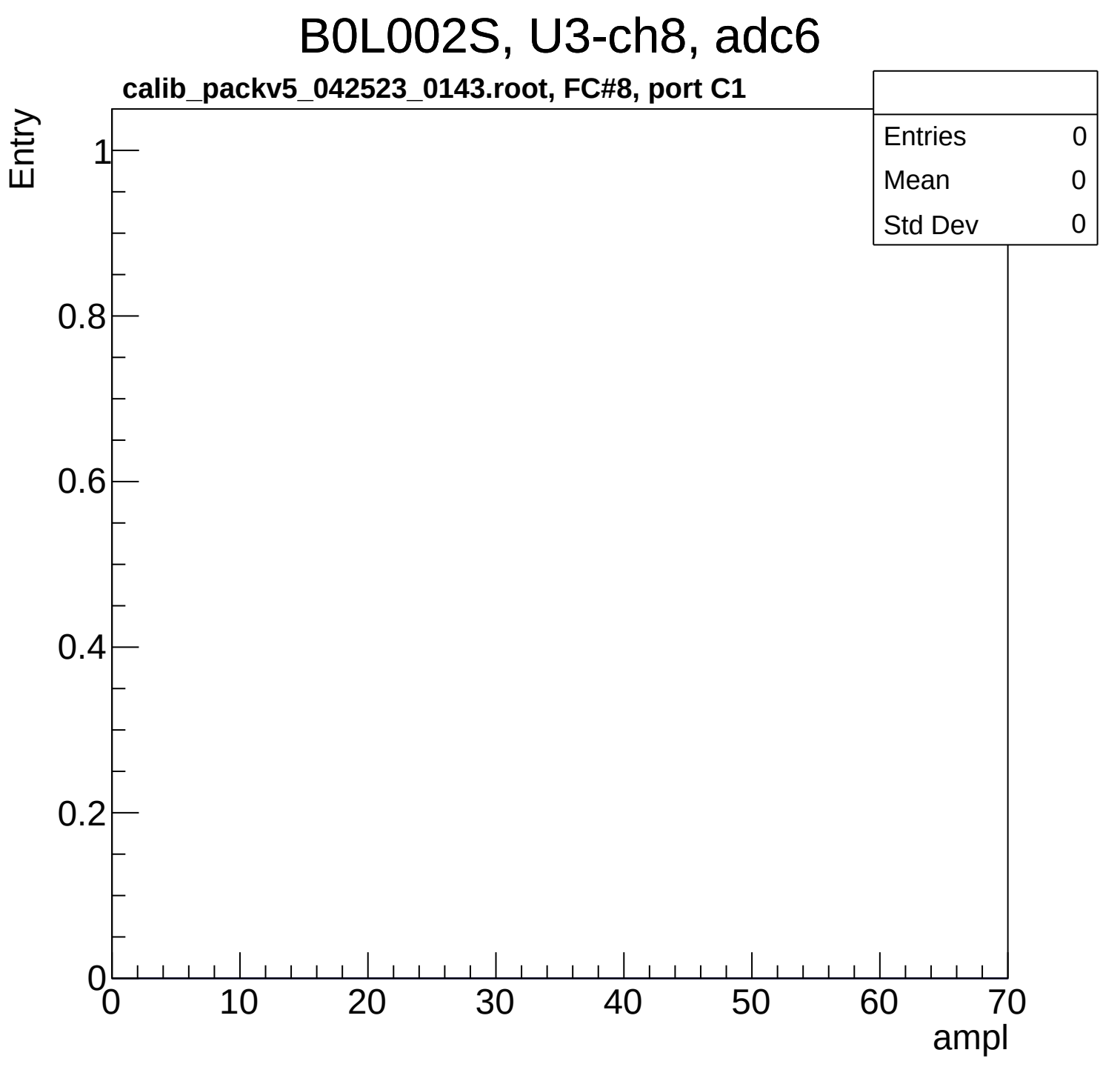
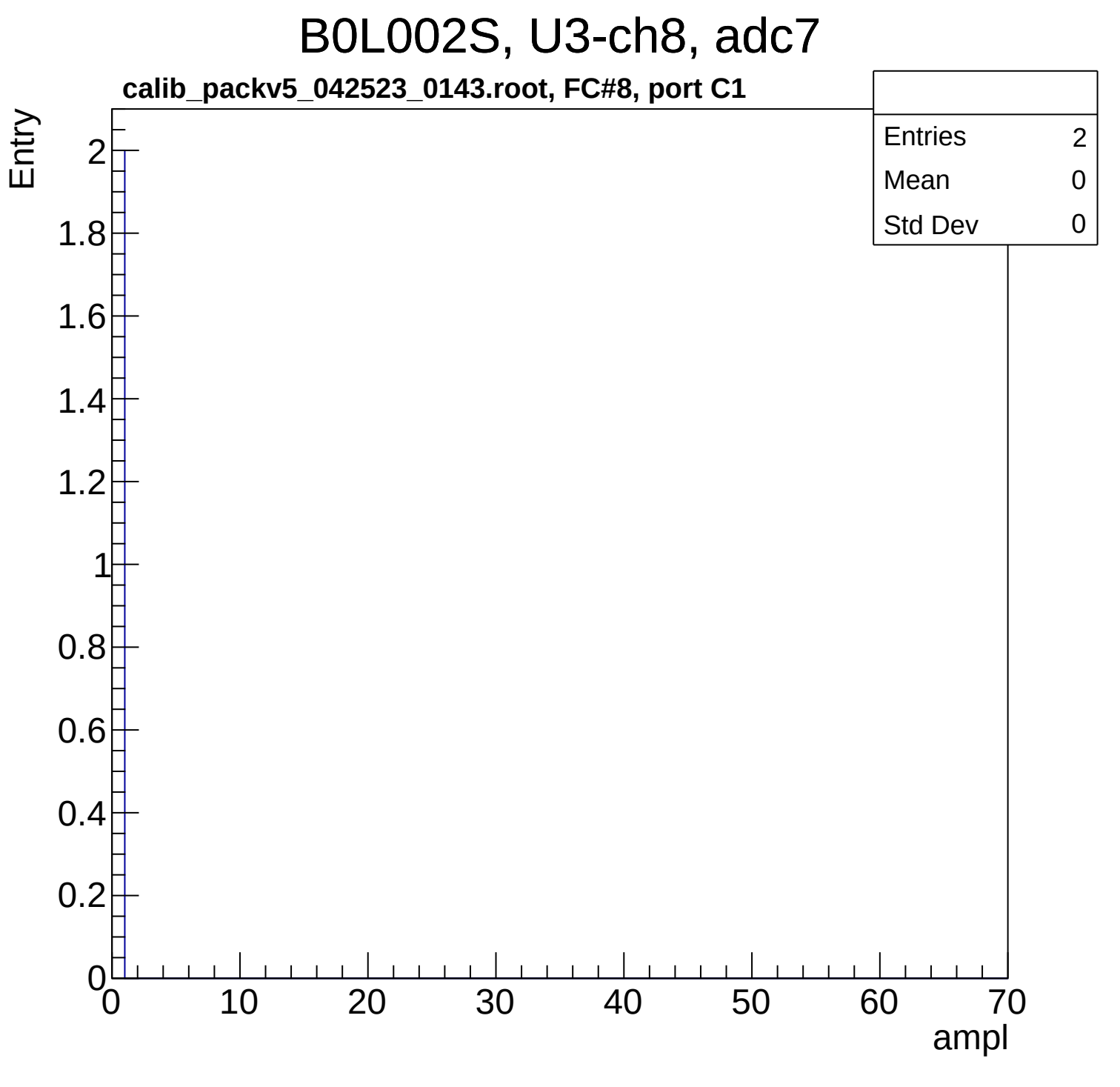
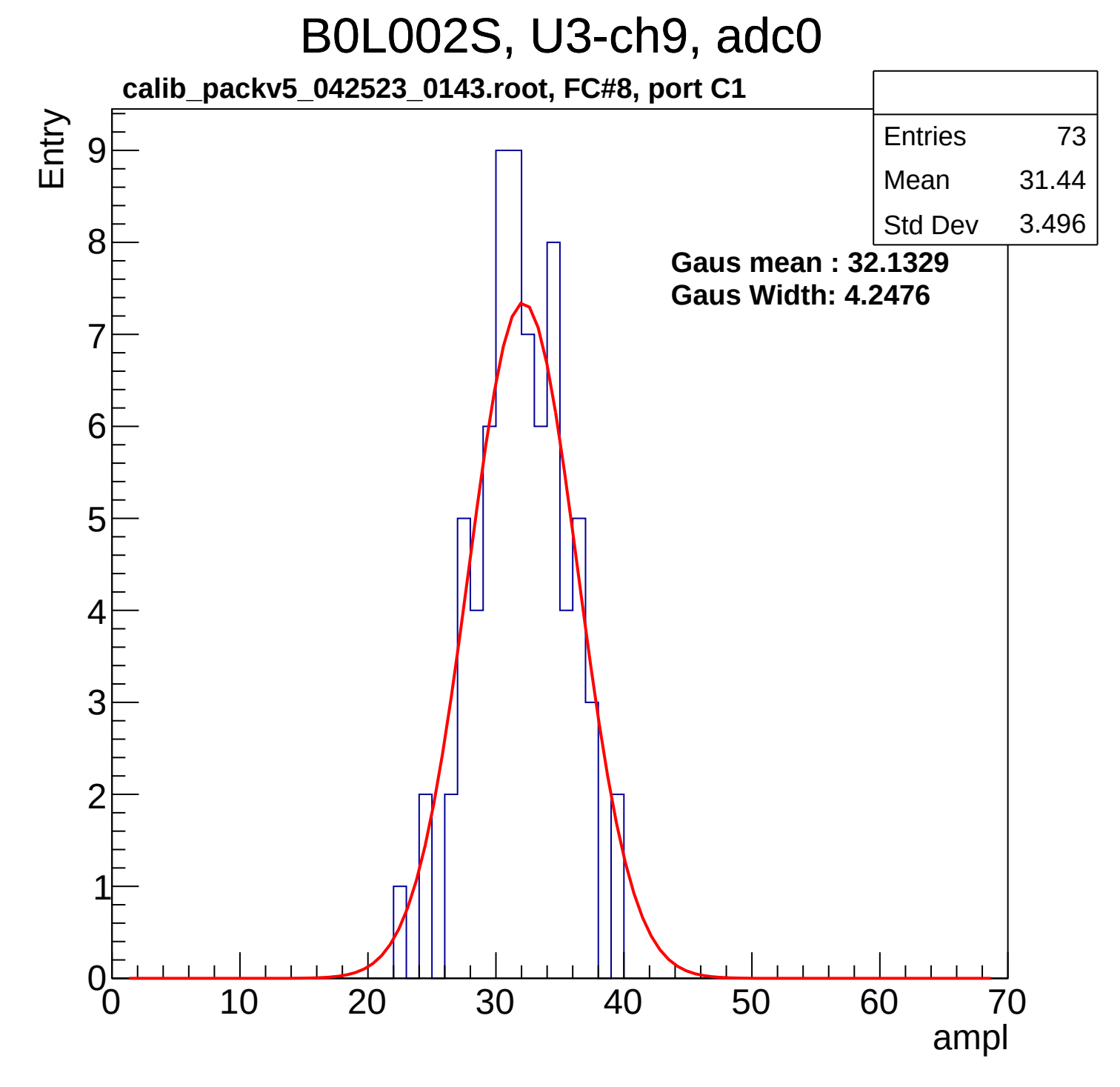

B0L002S, U3-ch8, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch8, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch9, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 31.44
Std Dev 3.496
Gaus mean : 32.1329
Gaus Width: 4.2476
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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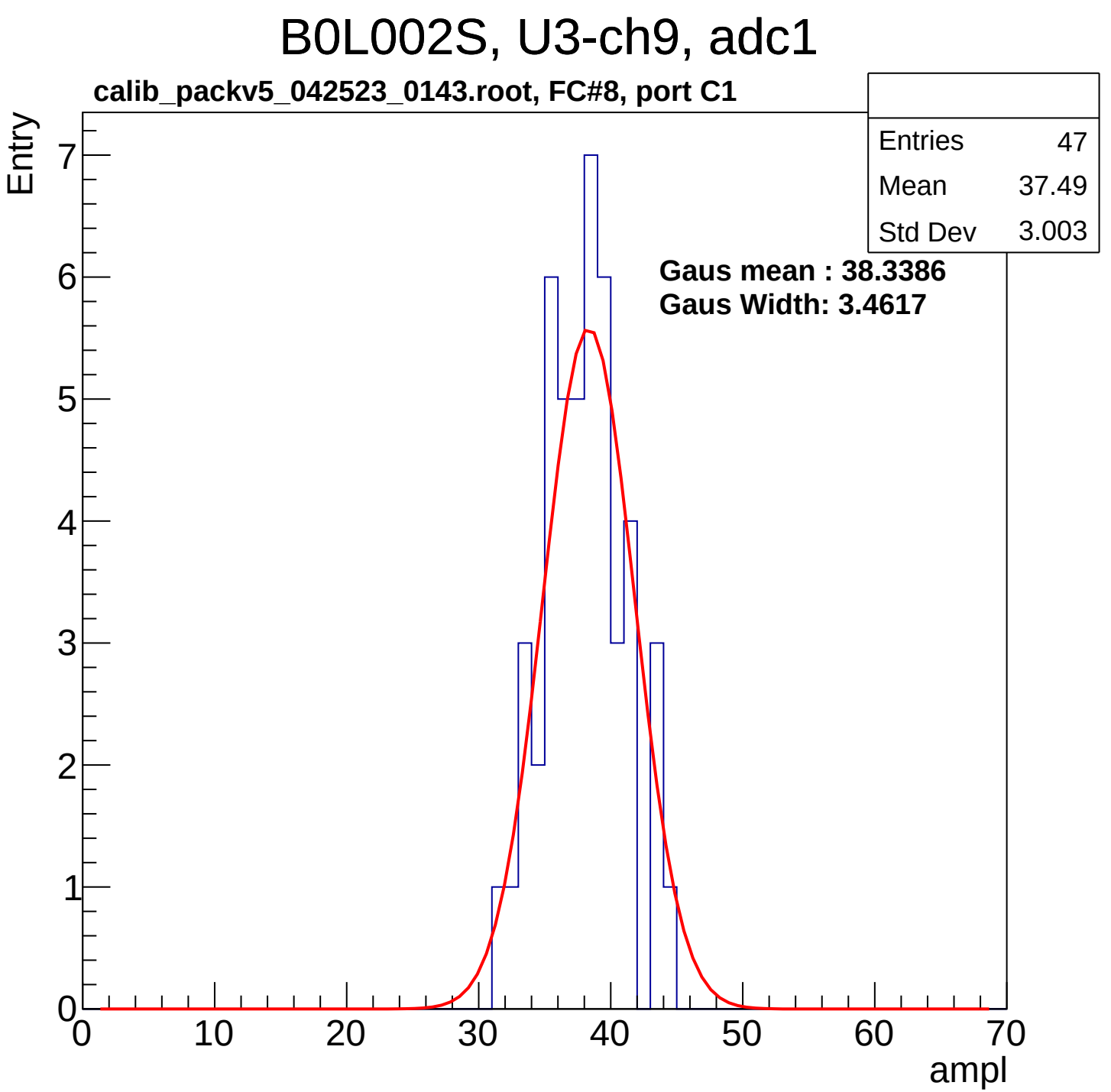
B0L002S, U3-ch9, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 37.49
Std Dev 3.003
Gaus mean : 38.3386
Gaus Width: 3.4617
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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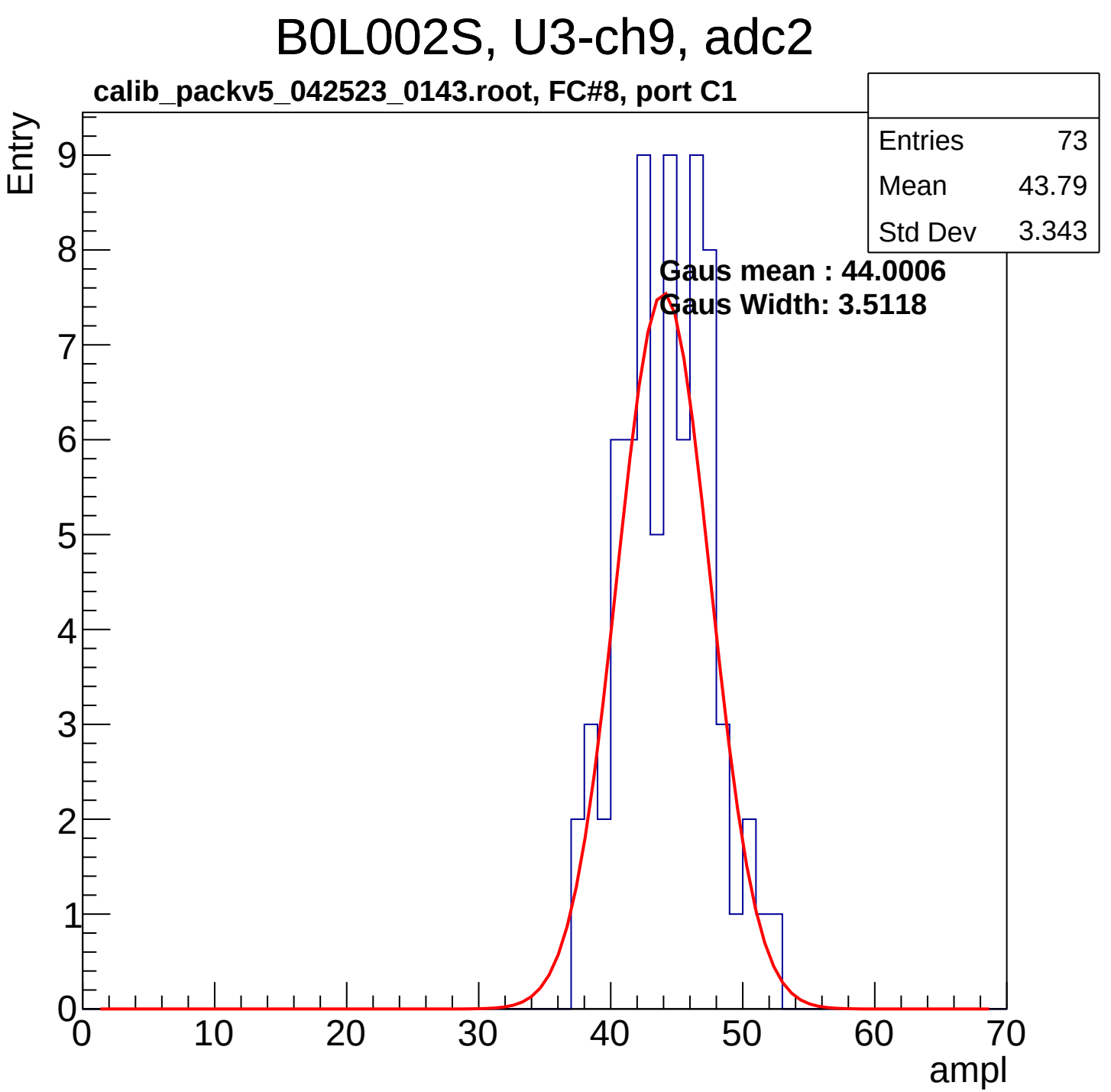
B0L002S, U3-ch9, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.79
Std Dev 3.343
Gaus mean : 44.0006
Gaus Width: 3.5118
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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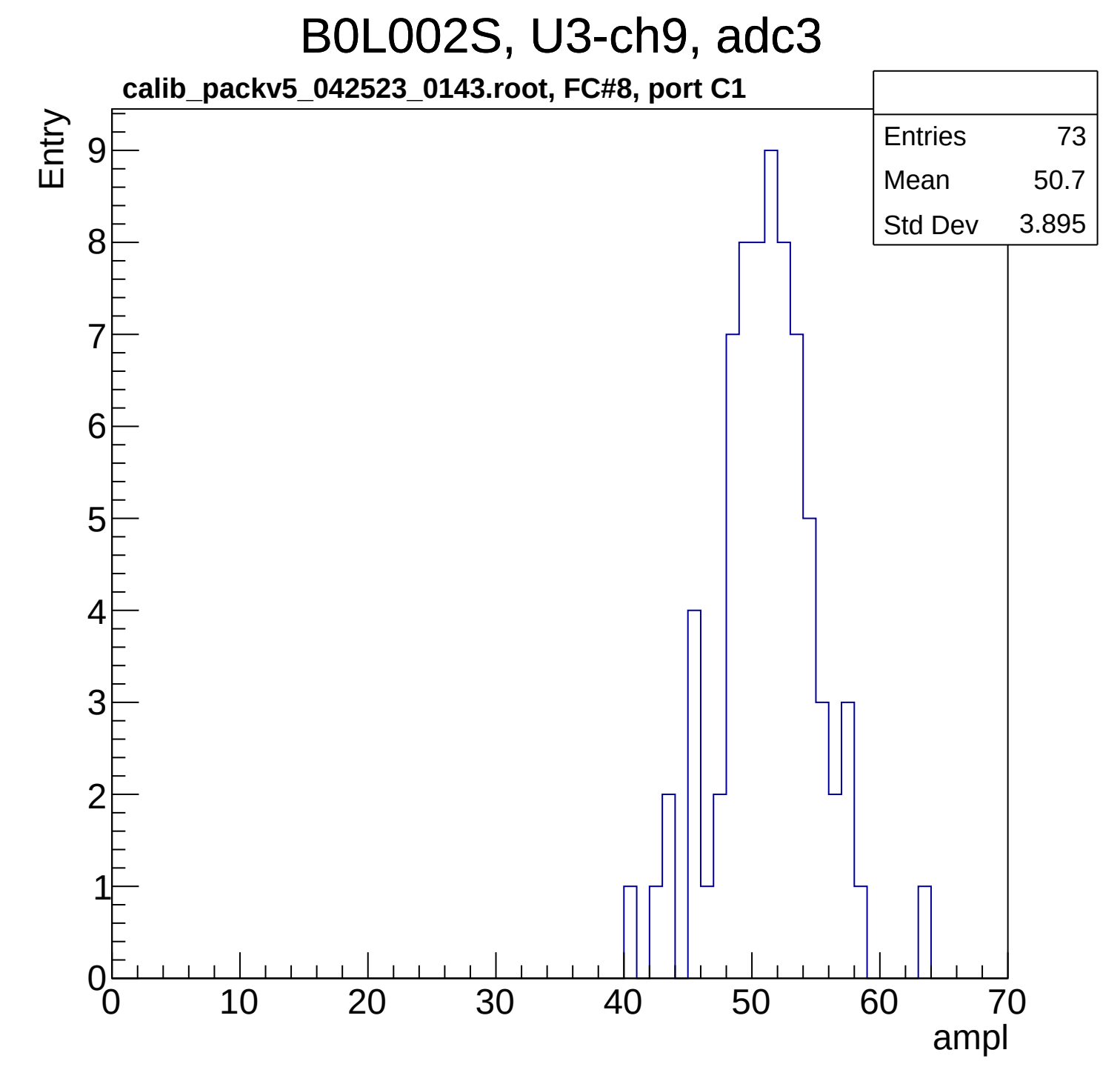
B0L002S, U3-ch9, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.7
Std Dev 3.895
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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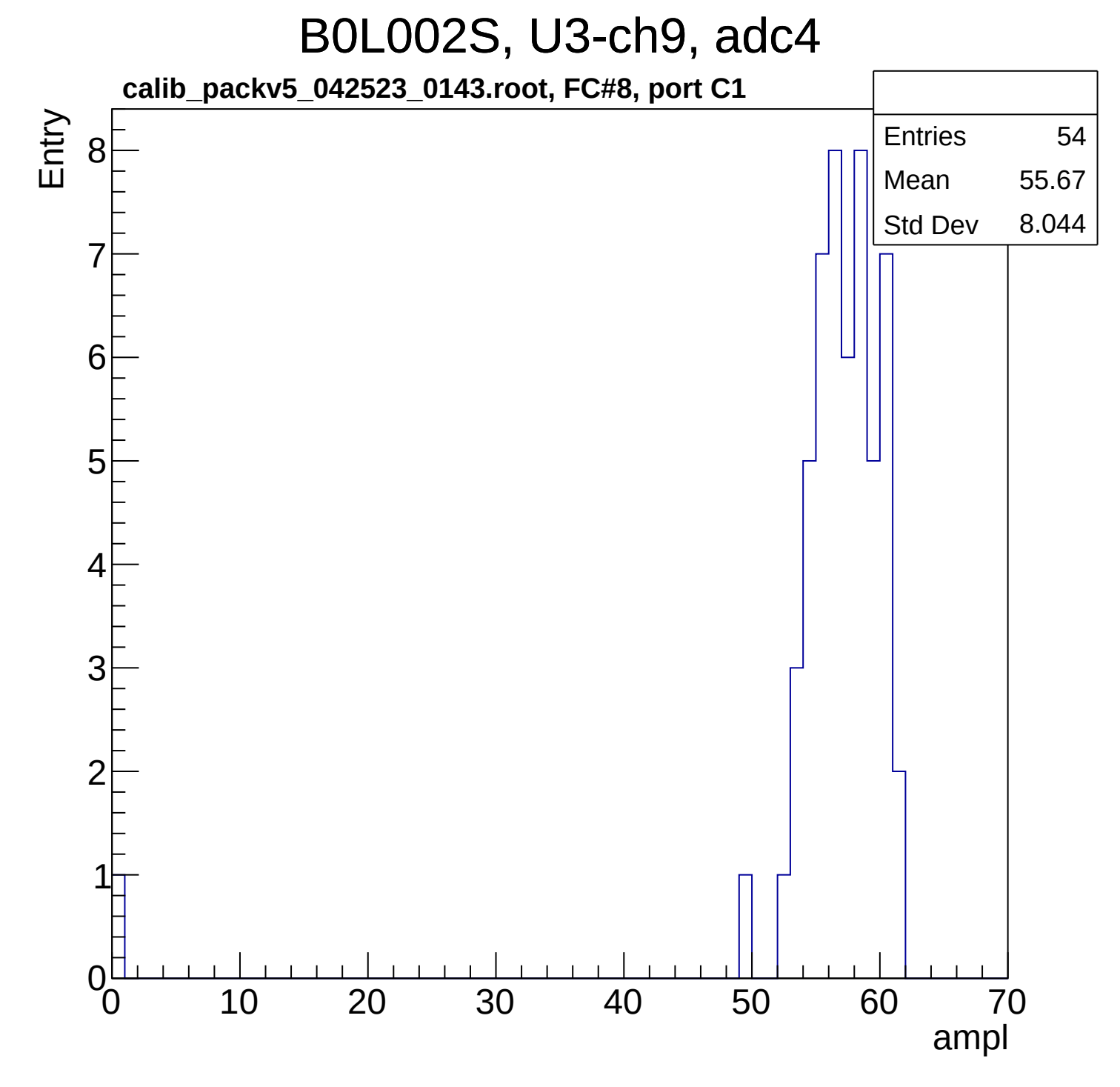
B0L002S, U3-ch9, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 55.67
Std Dev 8.044
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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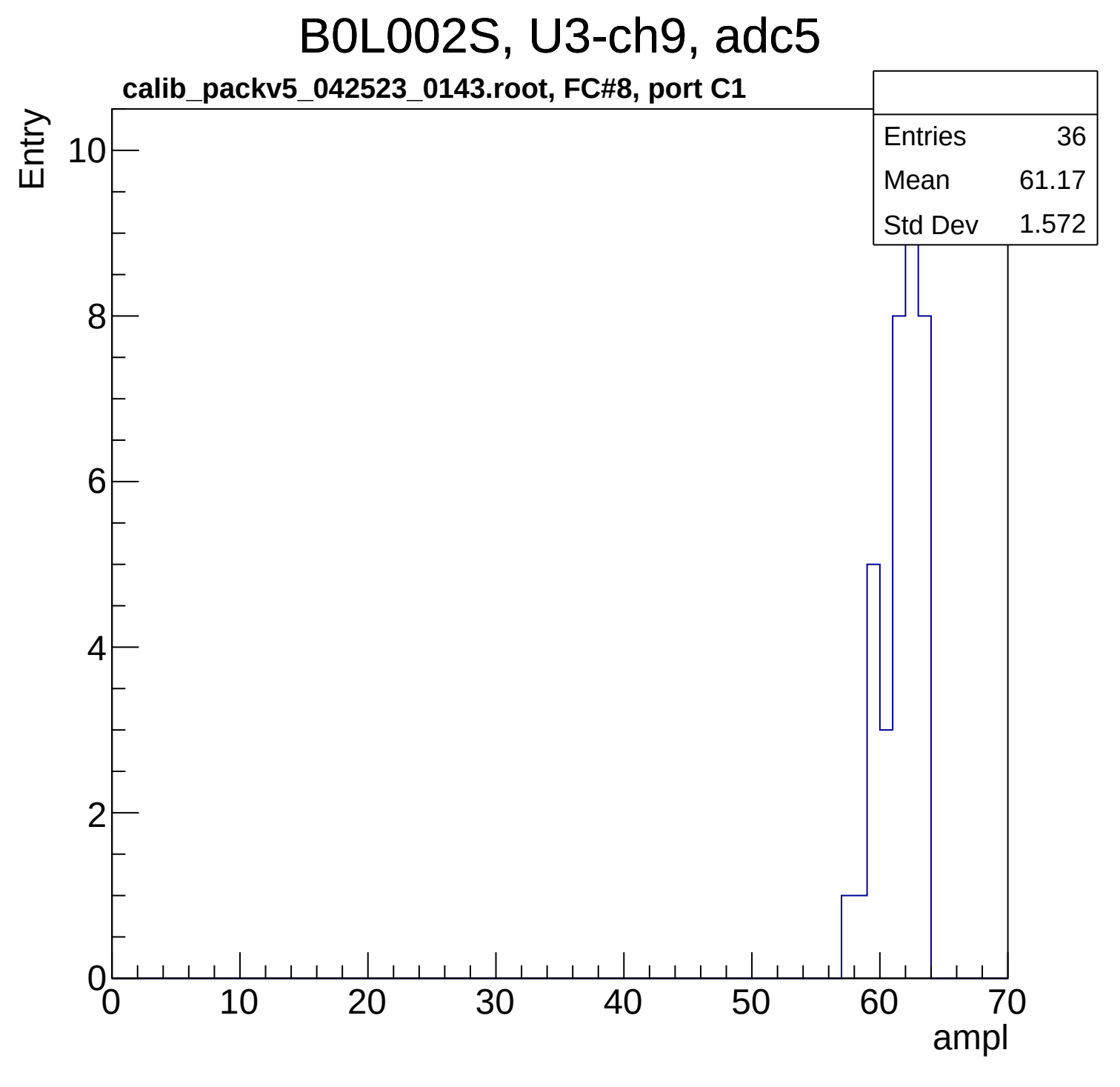
B0L002S, U3-ch9, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 61.17
Std Dev 1.572
Entry
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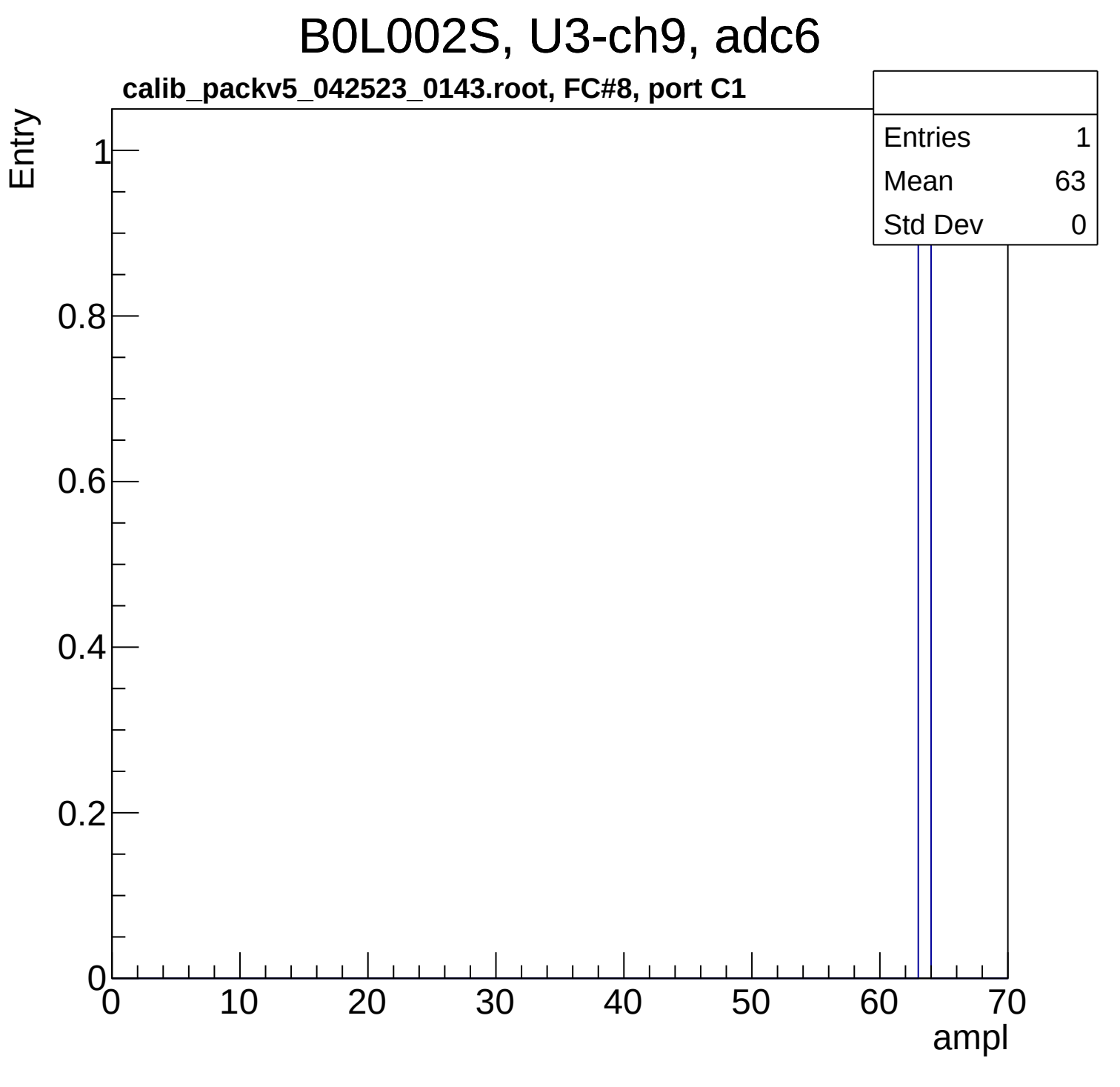
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch9, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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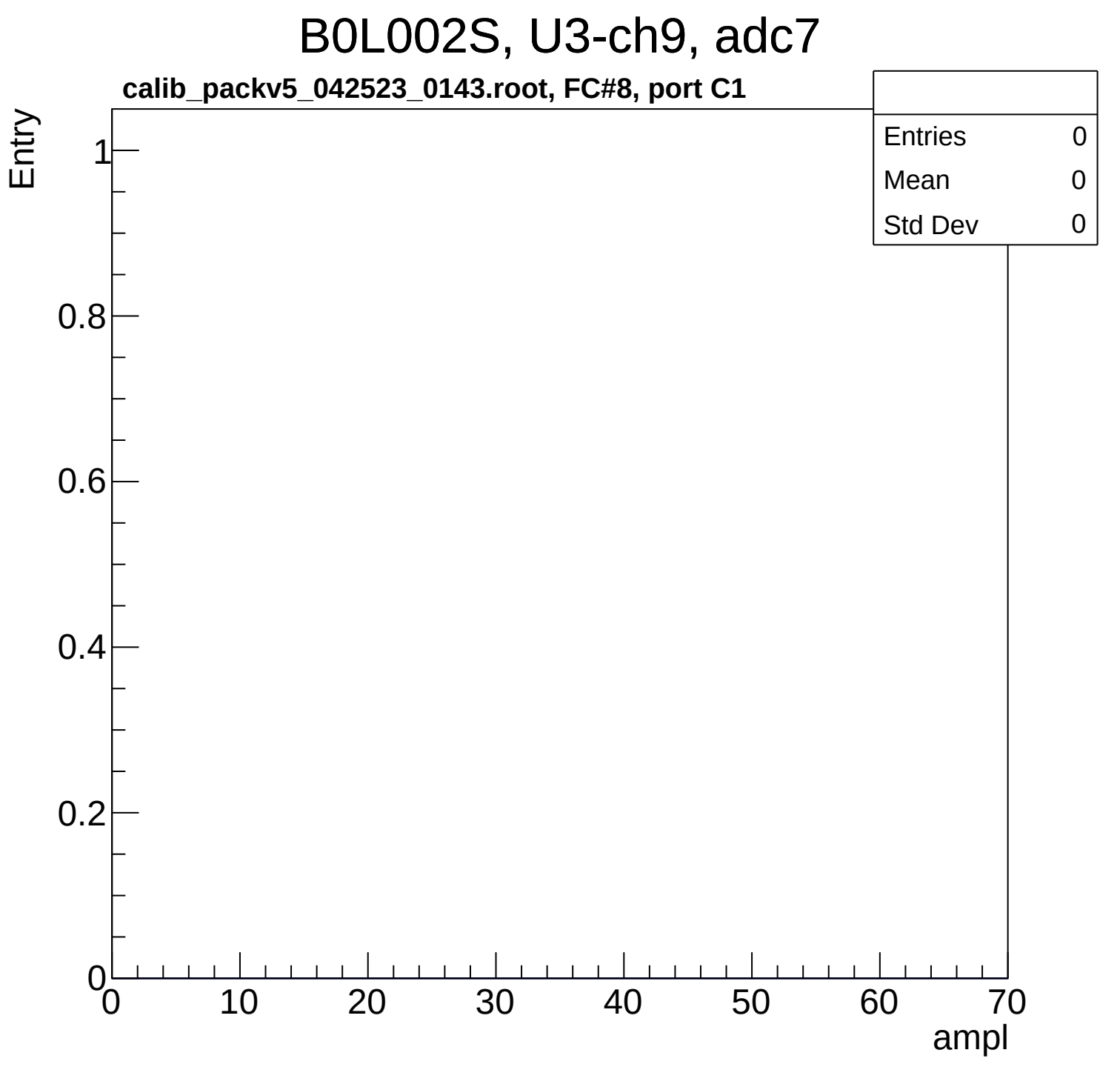
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch9, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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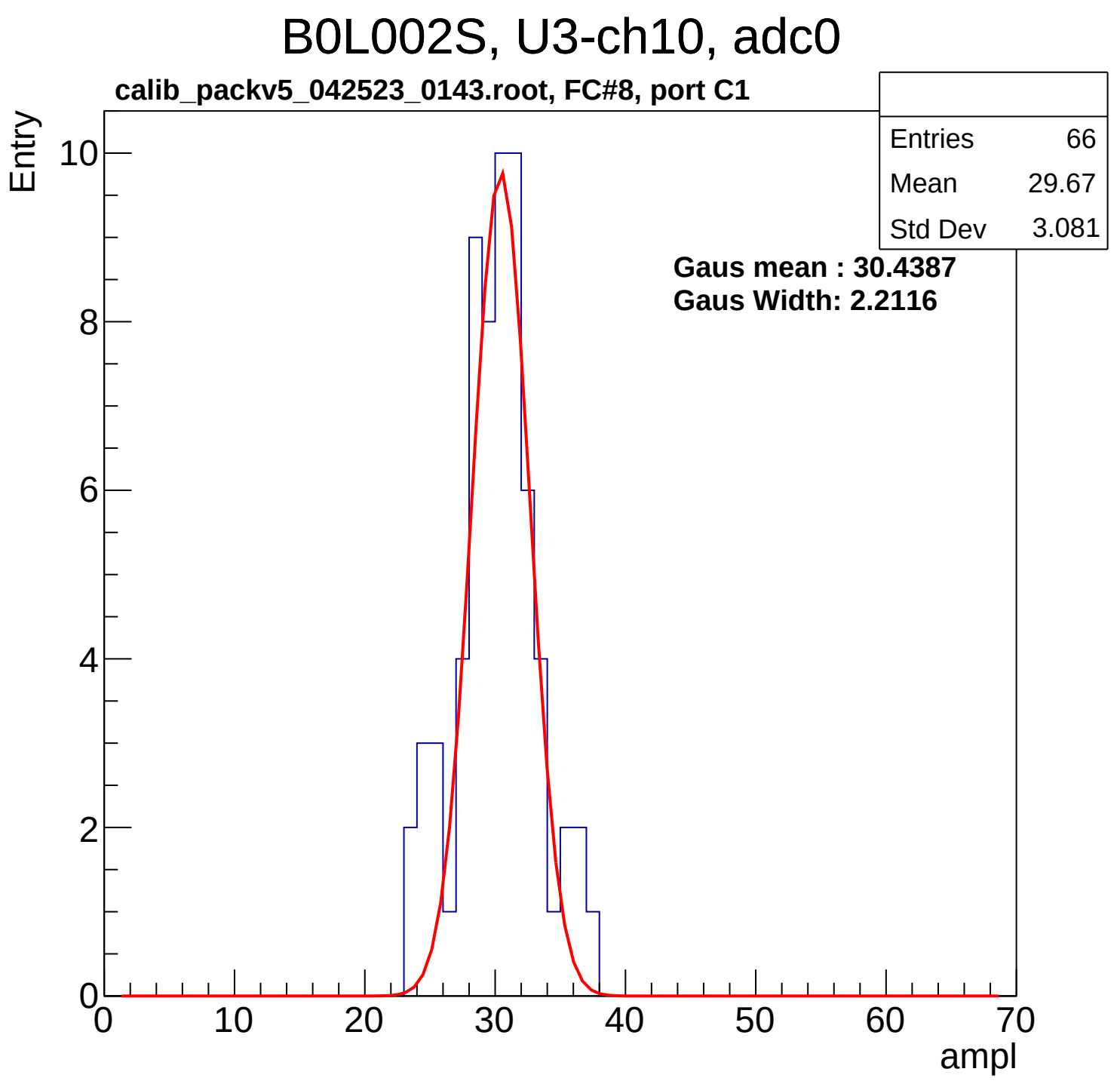
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch10, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 29.67
Std Dev 3.081
Gaus mean : 30.4387
Gaus Width: 2.2116
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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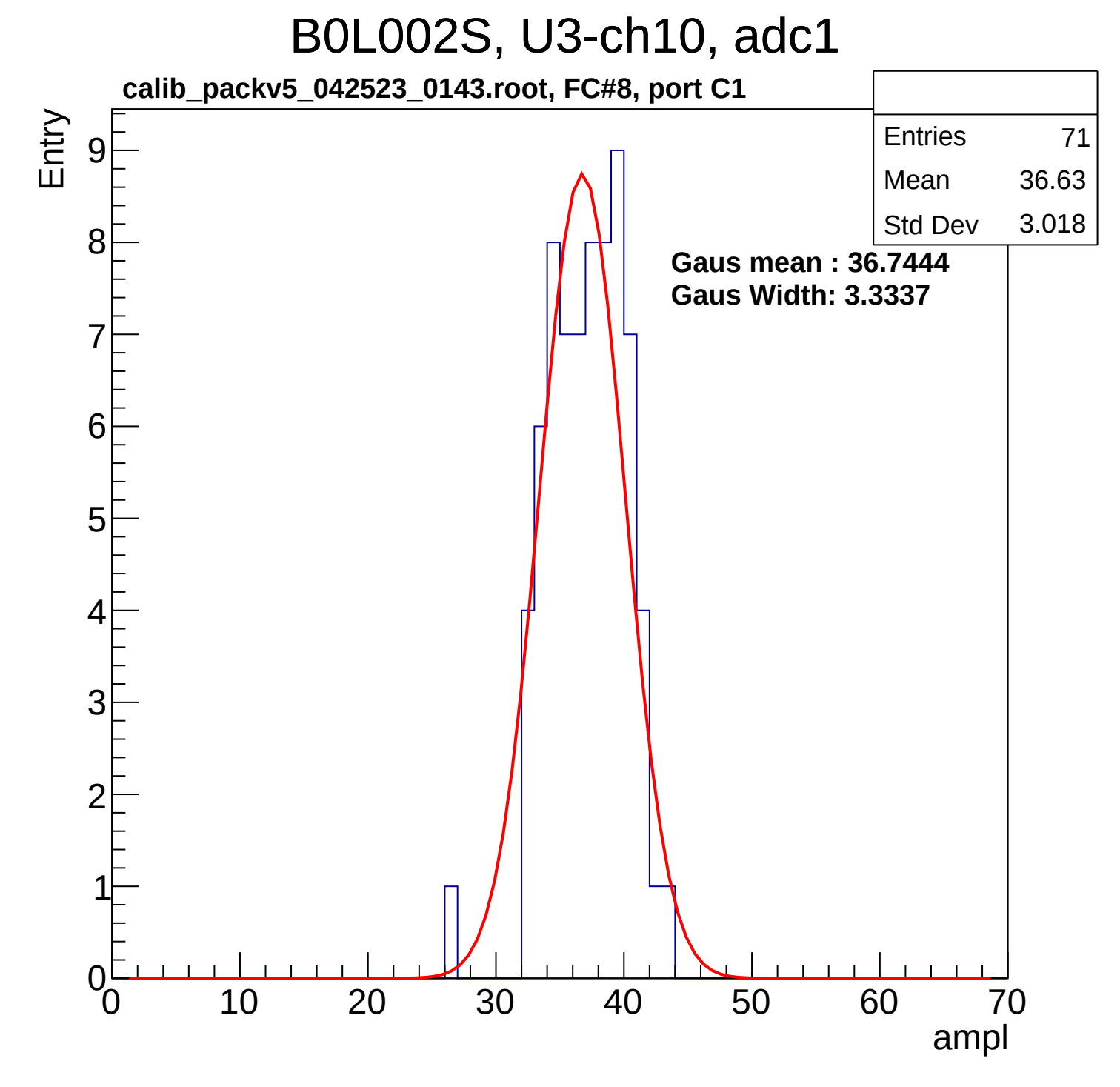
B0L002S, U3-ch10, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 36.63
Std Dev 3.018
Gaus mean : 36.7444
Gaus Width: 3.3337
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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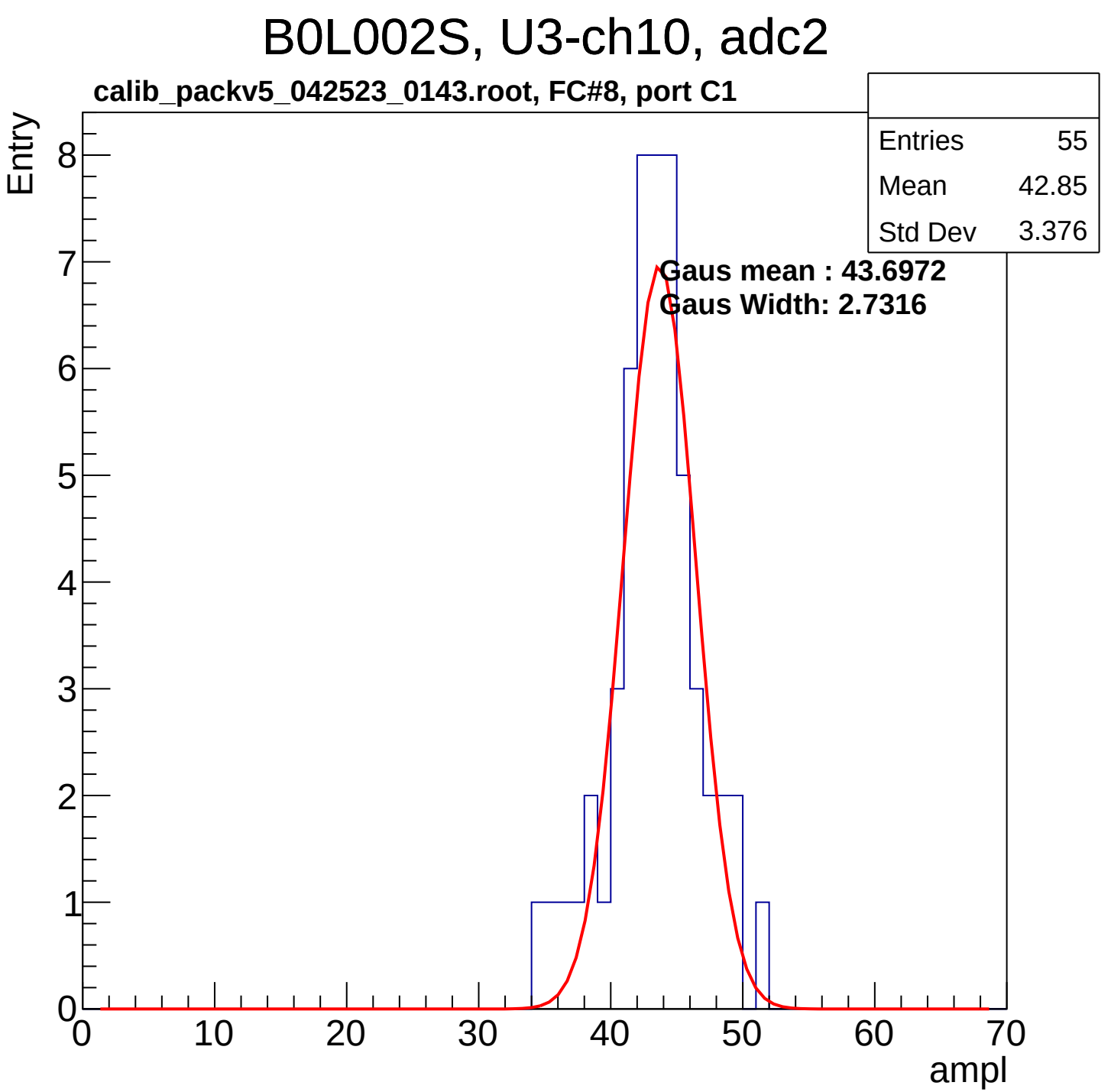
40
50
60
70


B0L002S, U3-ch10, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 42.85
Std Dev 3.376
Gaus mean : 43.6972
Gaus Width: 2.7316
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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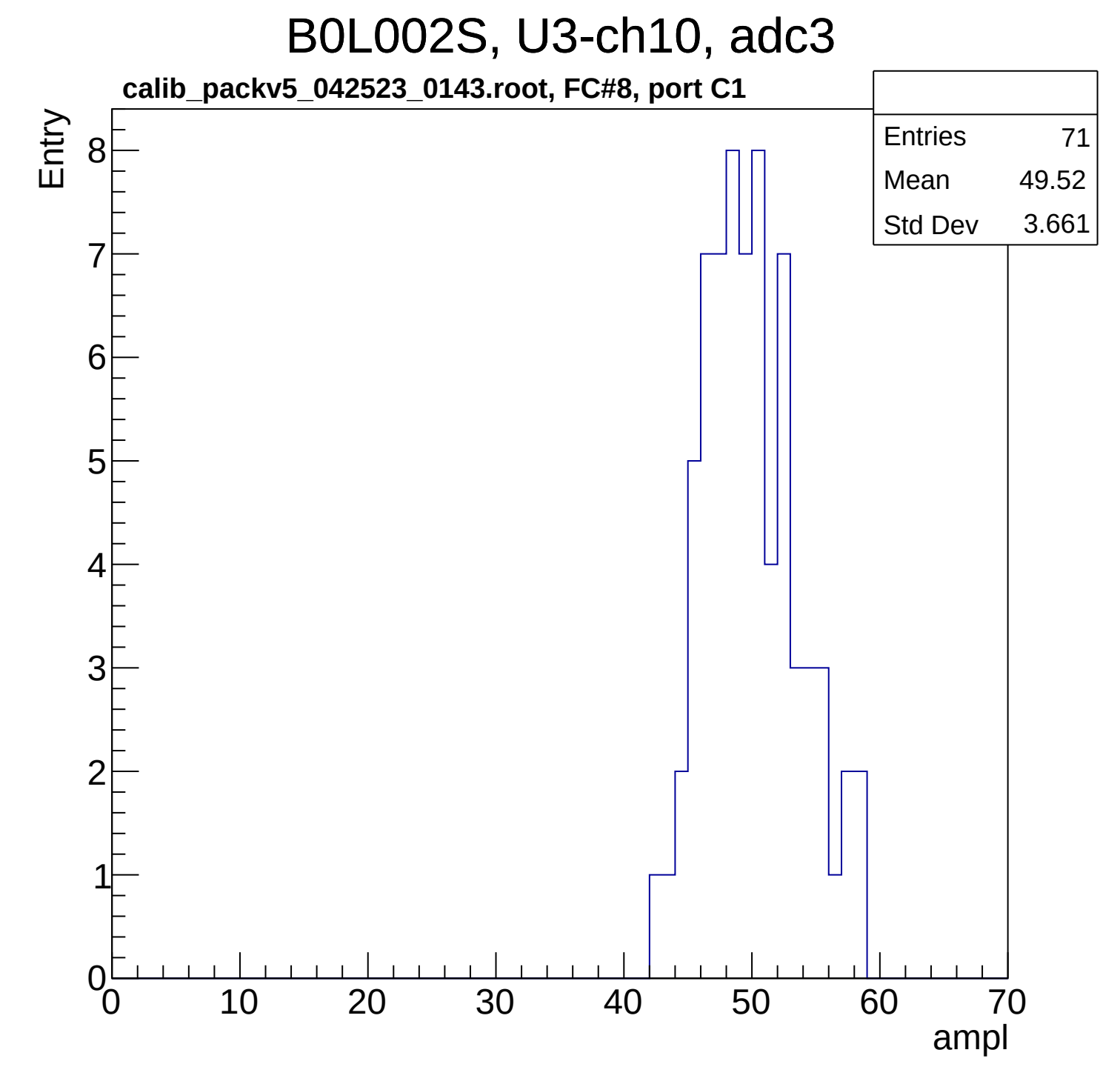
40
50
60
70


B0L002S, U3-ch10, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 49.52
Std Dev 3.661
Entry
ampl
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3
4
5
6
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0
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60
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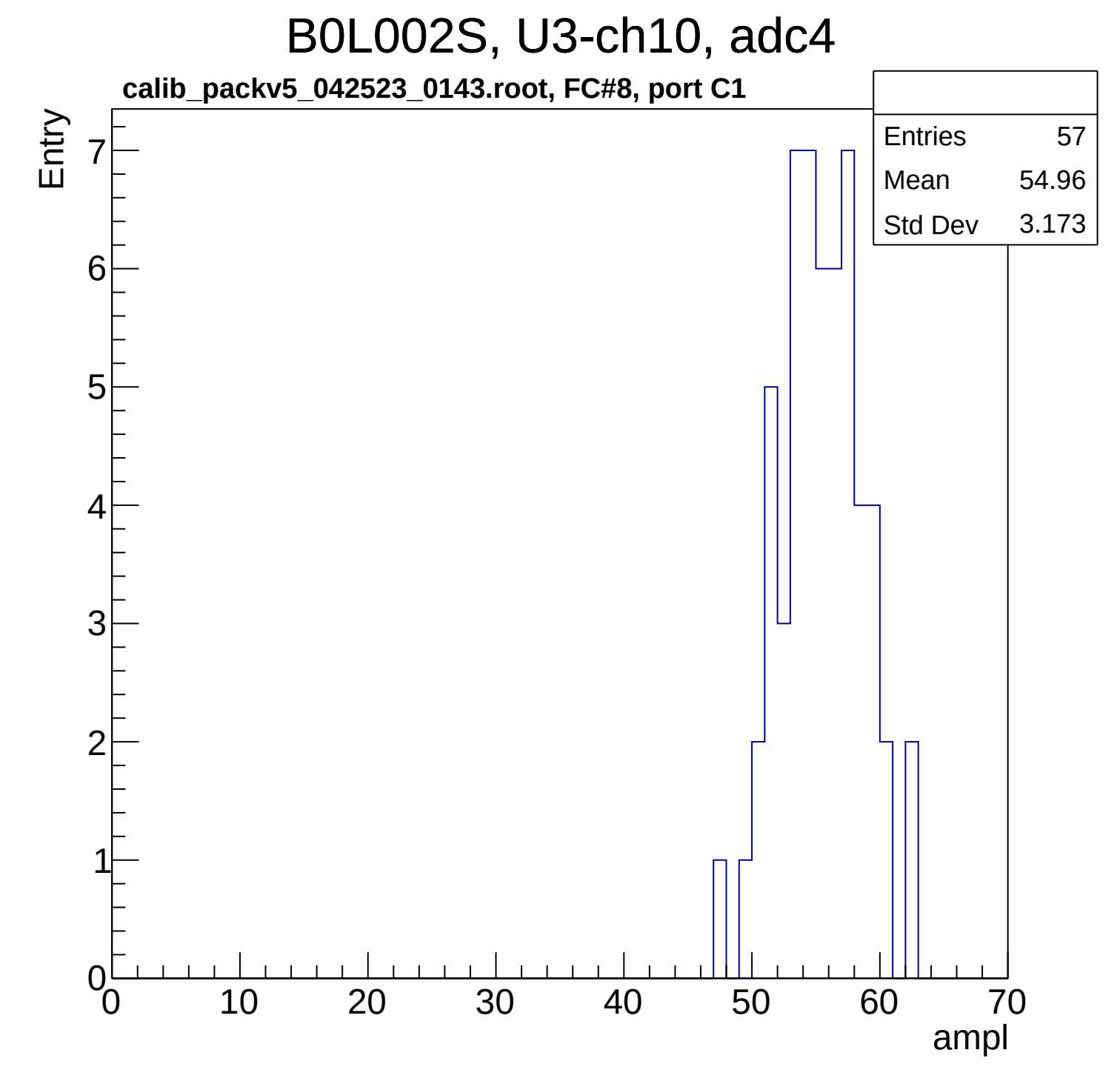
B0L002S, U3-ch10, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 54.96
Std Dev 3.173
Entry
ampl
0
1
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3
4
5
6
7
0
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40
50
60
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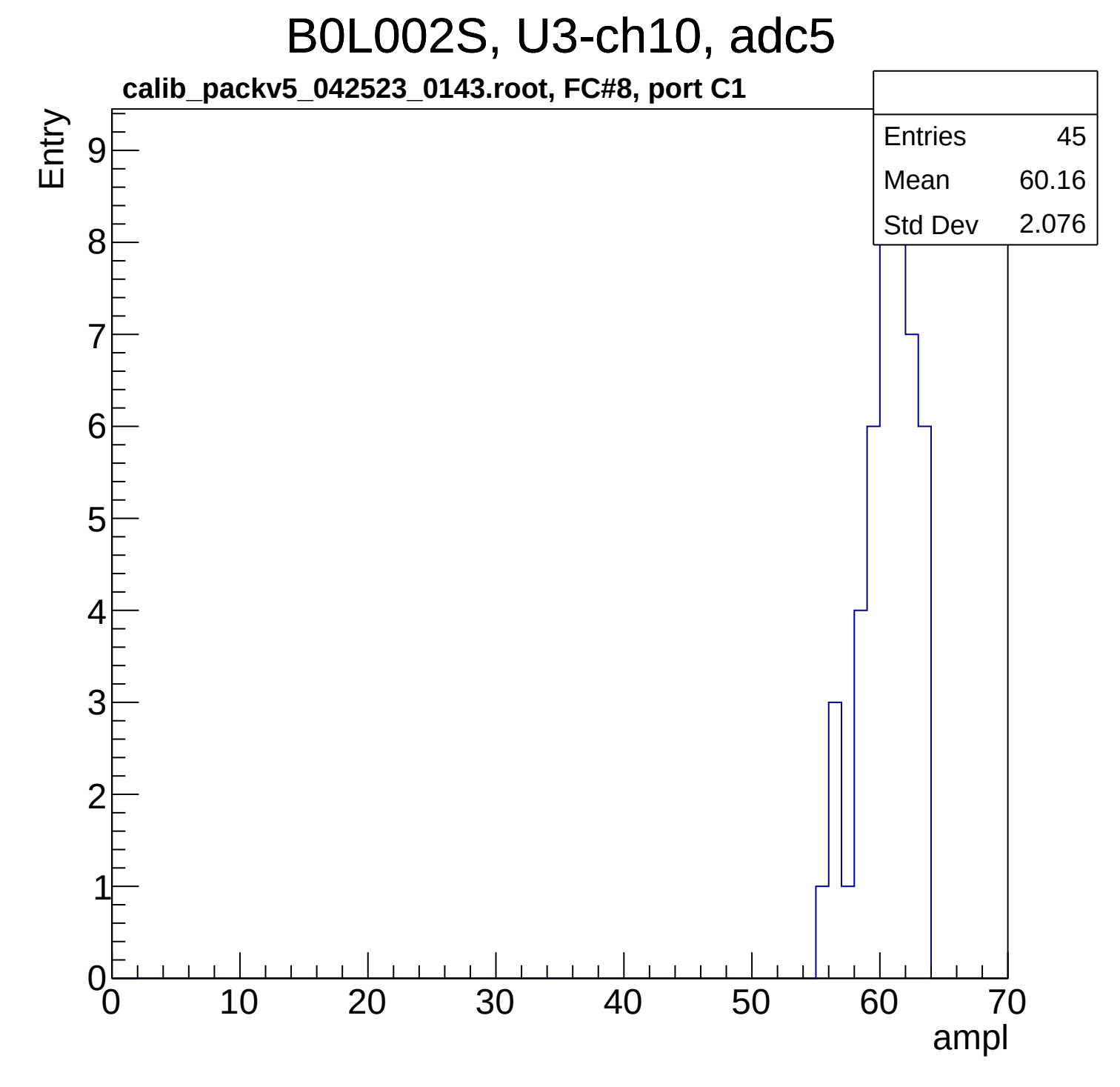
B0L002S, U3-ch10, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 60.16
Std Dev 2.076
Entry
ampl
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4
5
6
7
8
9
0
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60
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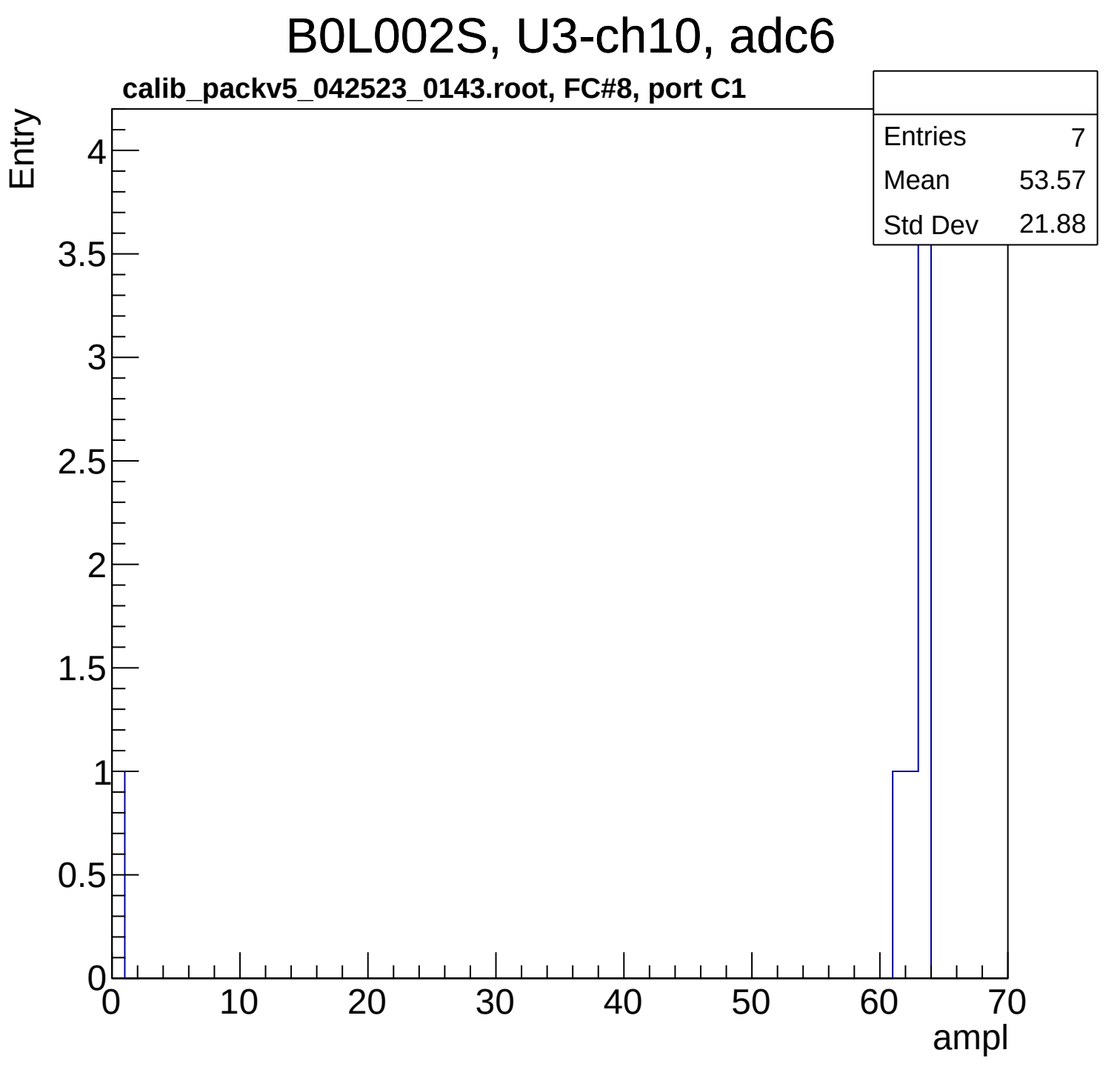
B0L002S, U3-ch10, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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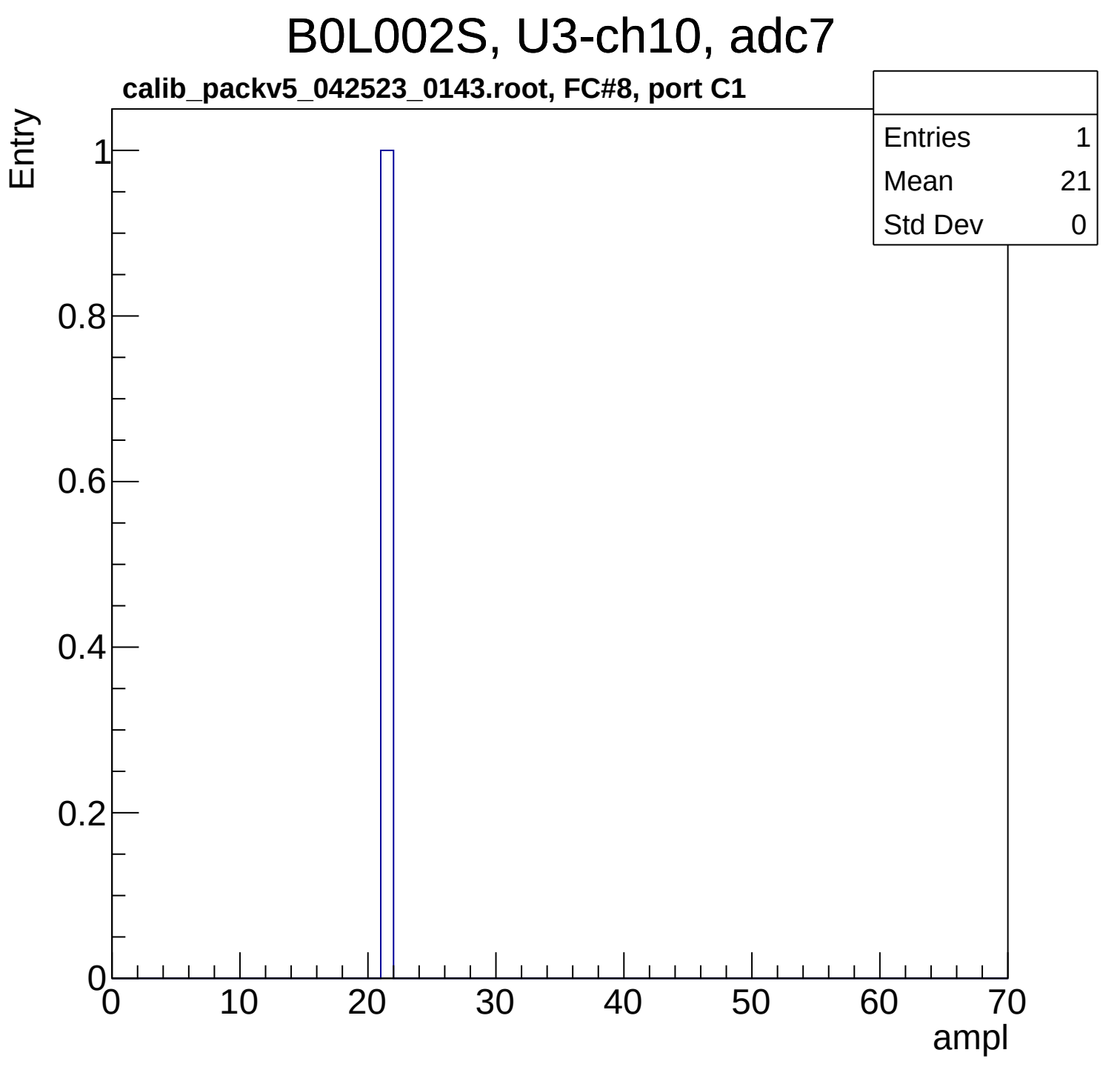
B0L002S, U3-ch10, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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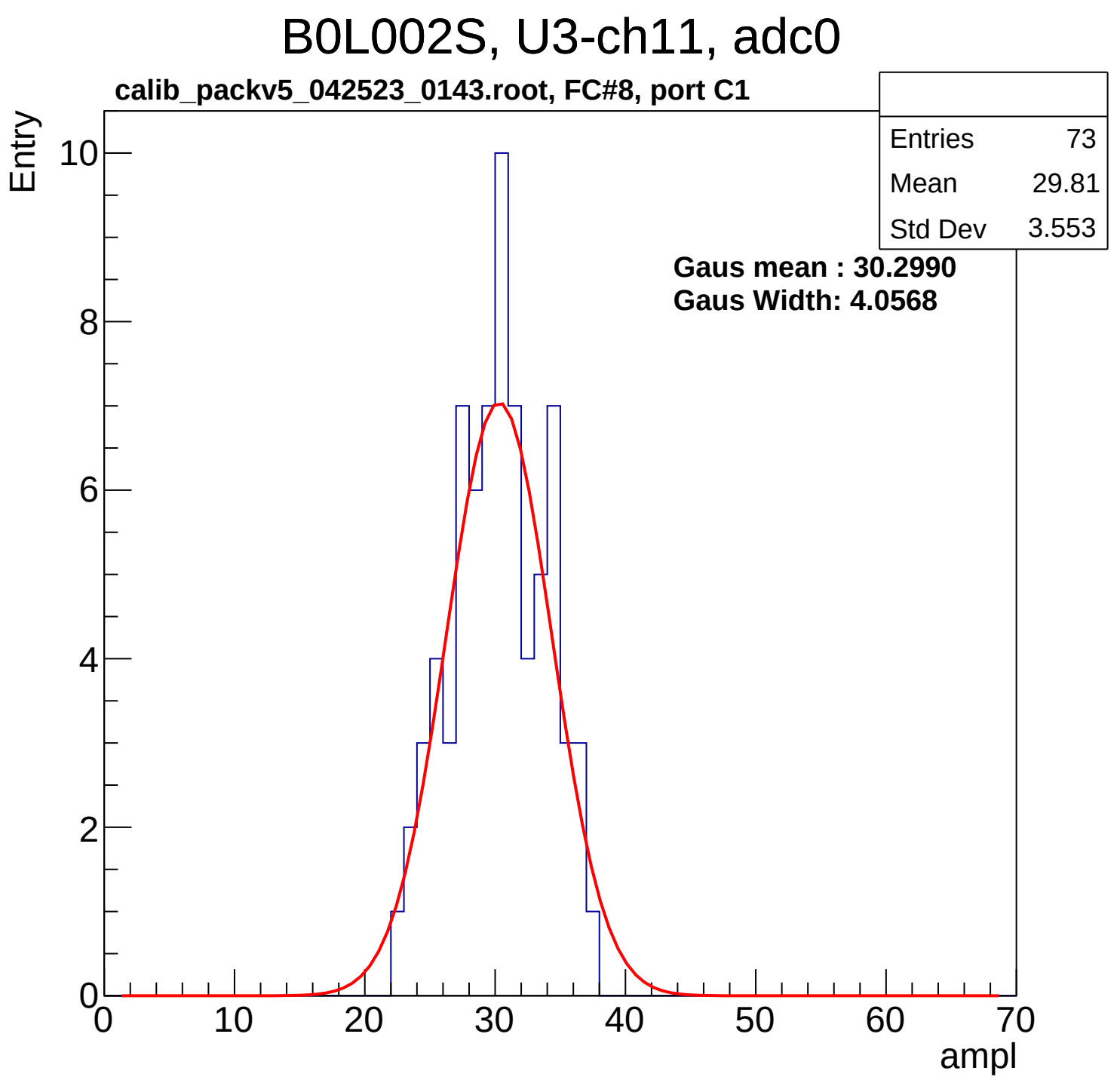
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch11, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 29.81
Std Dev 3.553
Gaus mean : 30.2990
Gaus Width: 4.0568
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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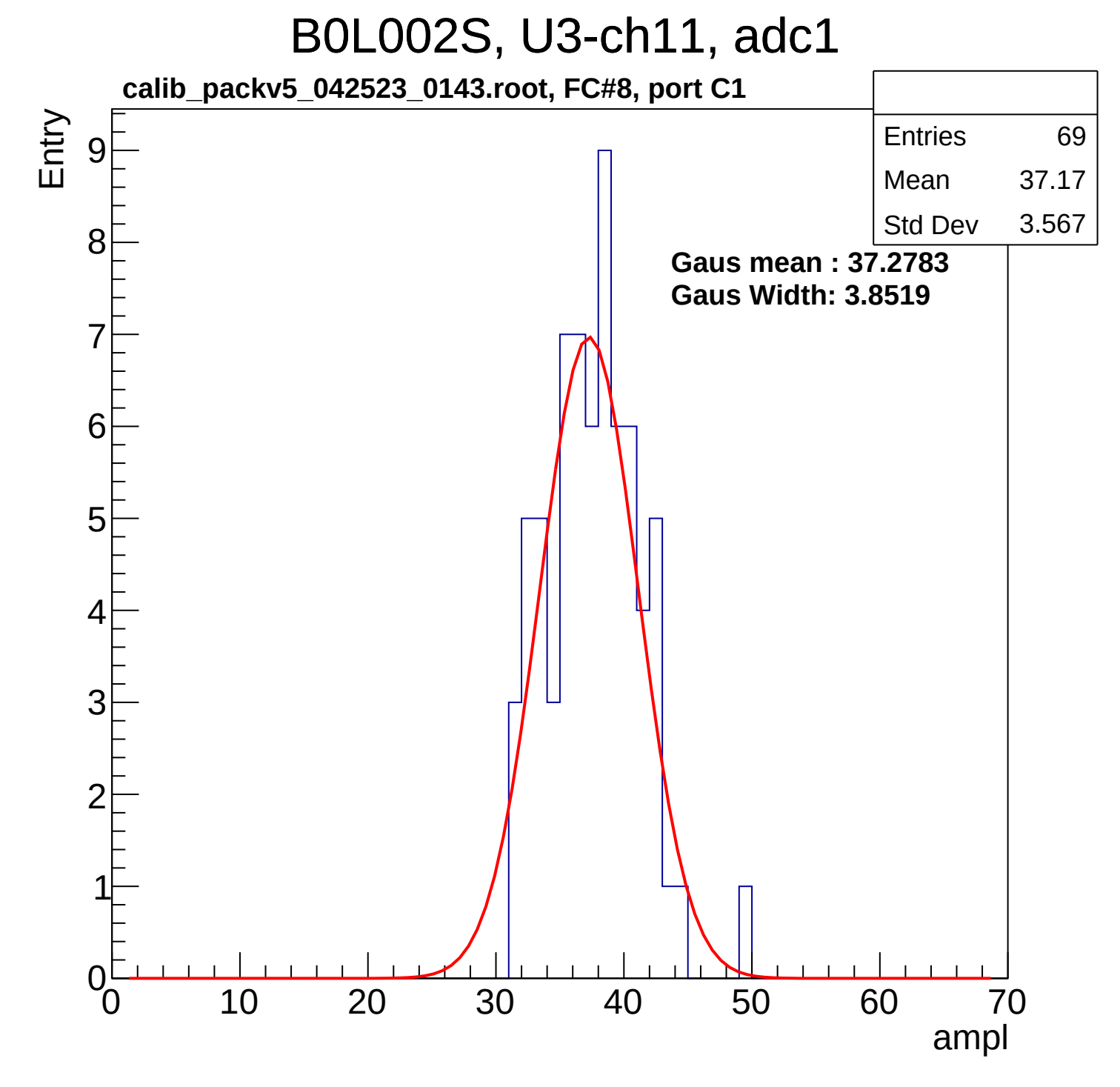
B0L002S, U3-ch11, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.17
Std Dev 3.567
Gaus mean : 37.2783
Gaus Width: 3.8519
Entry
ampl
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2
3
4
5
6
7
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0
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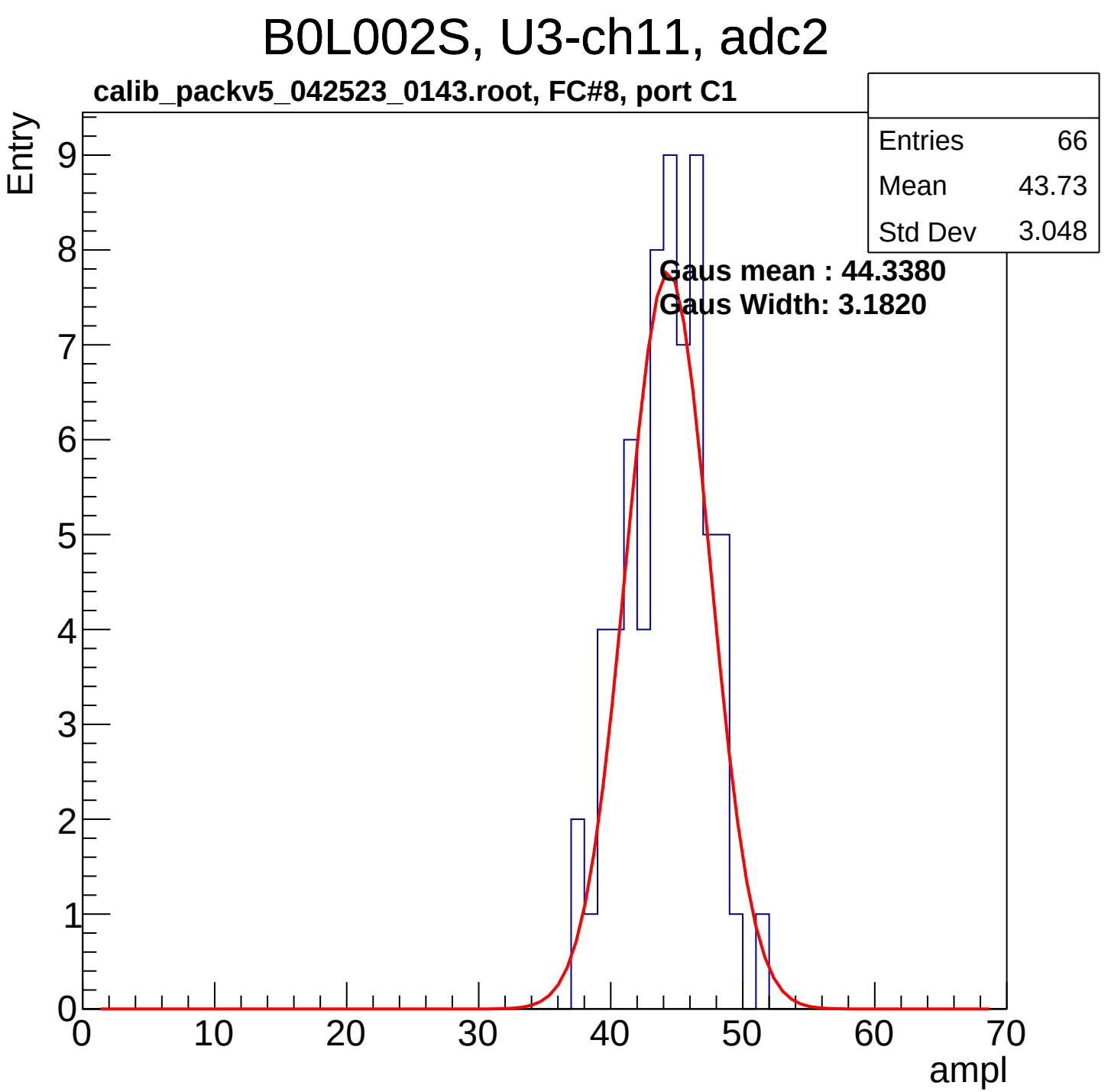
B0L002S, U3-ch11, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 43.73
Std Dev 3.048
Gaus mean : 44.3380
Gaus Width: 3.1820
Entry
ampl
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2
3
4
5
6
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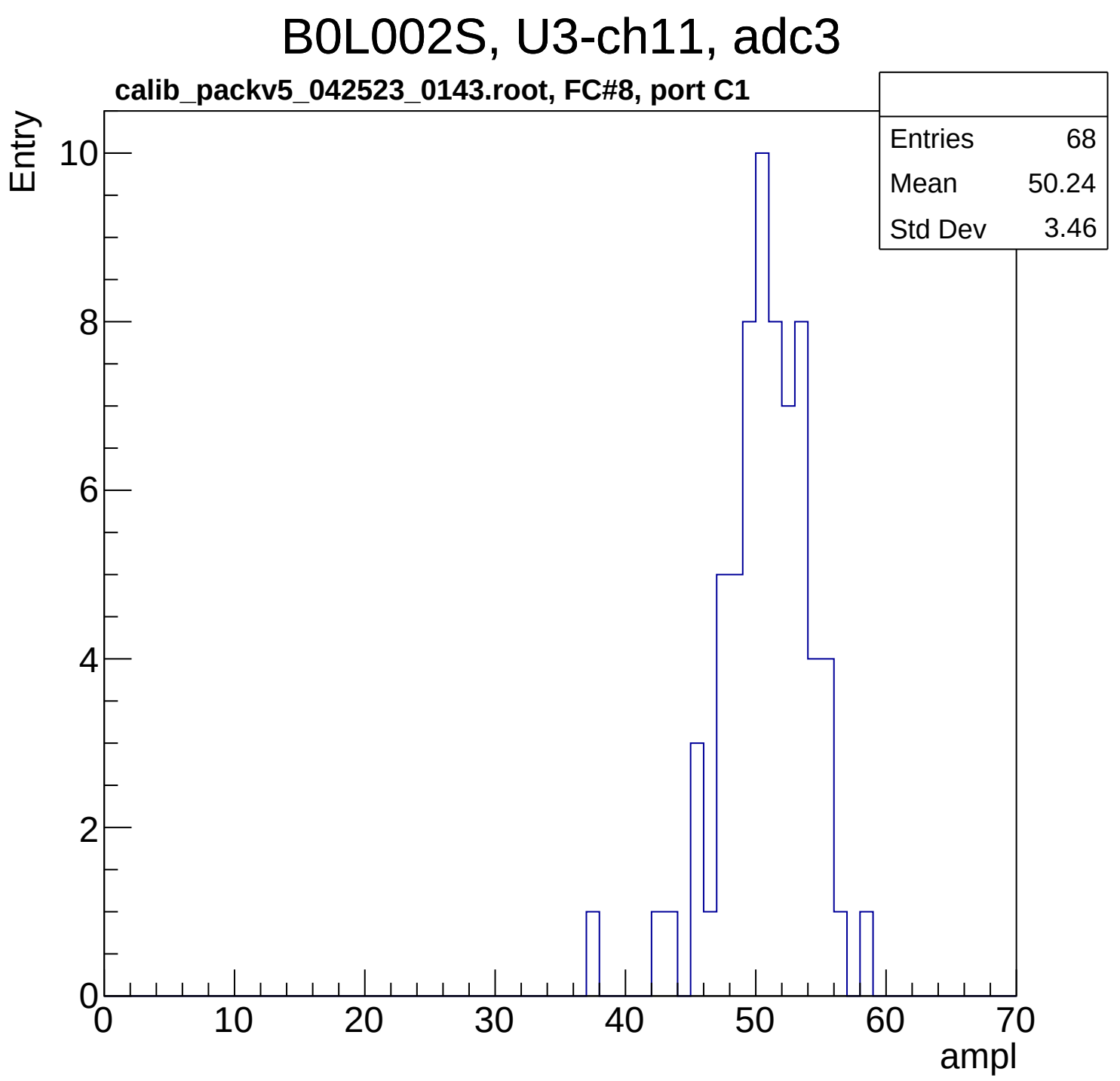
B0L002S, U3-ch11, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.24
Std Dev 3.46
Entry
ampl
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60
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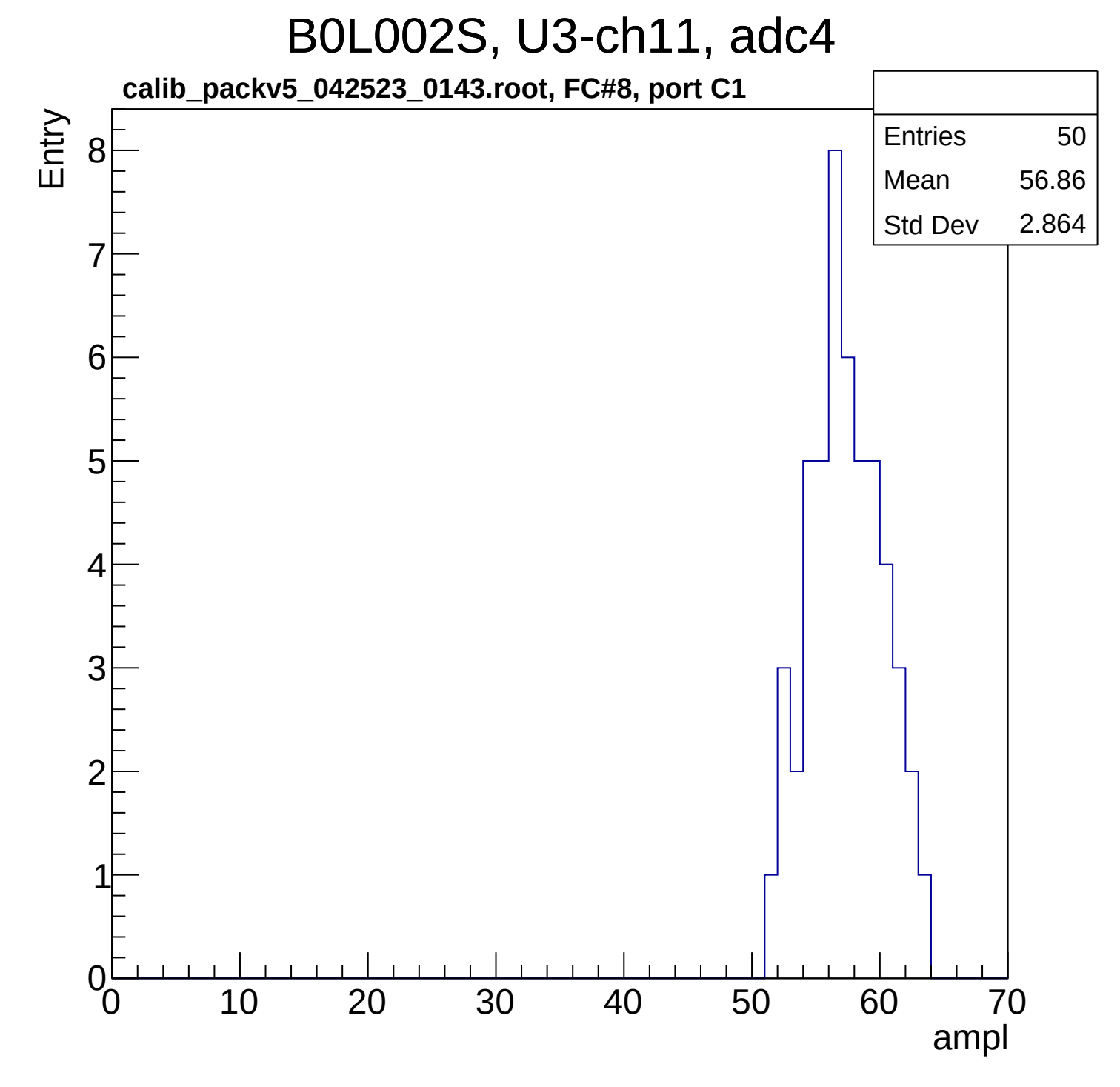
B0L002S, U3-ch11, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 56.86
Std Dev 2.864
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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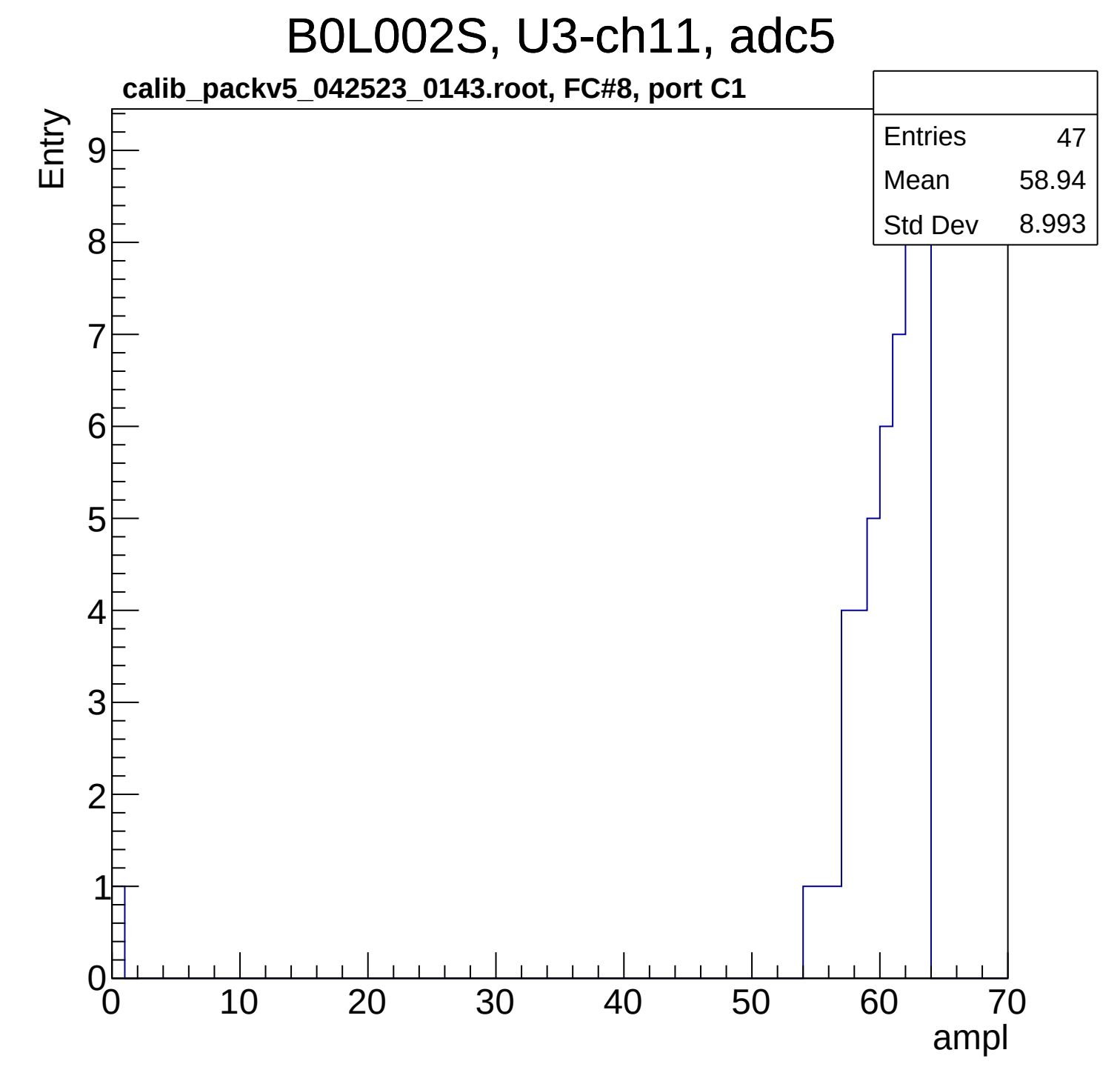
B0L002S, U3-ch11, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 58.94
Std Dev 8.993
Entry
ampl
0
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2
3
4
5
6
7
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0
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60
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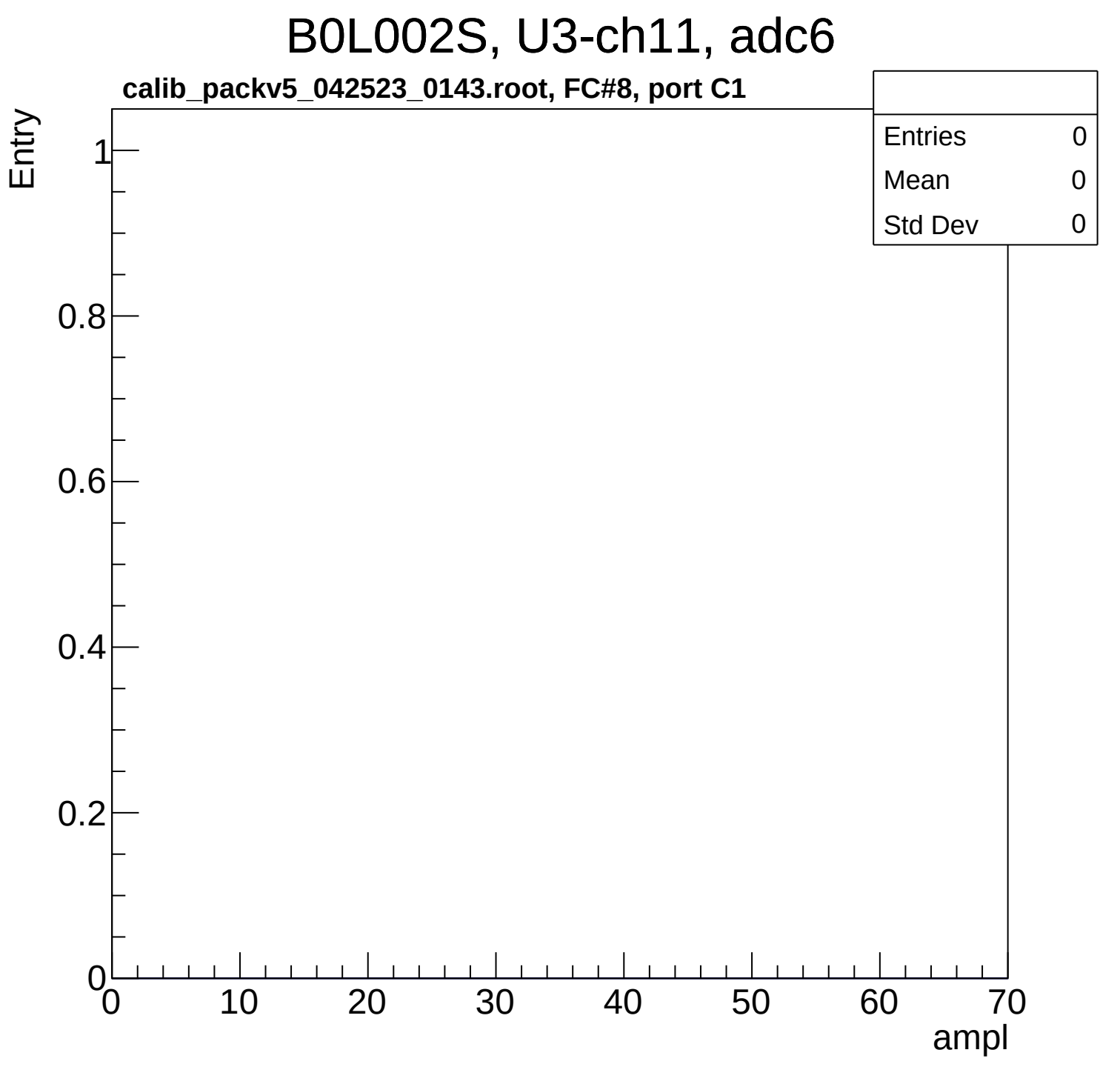
B0L002S, U3-ch11, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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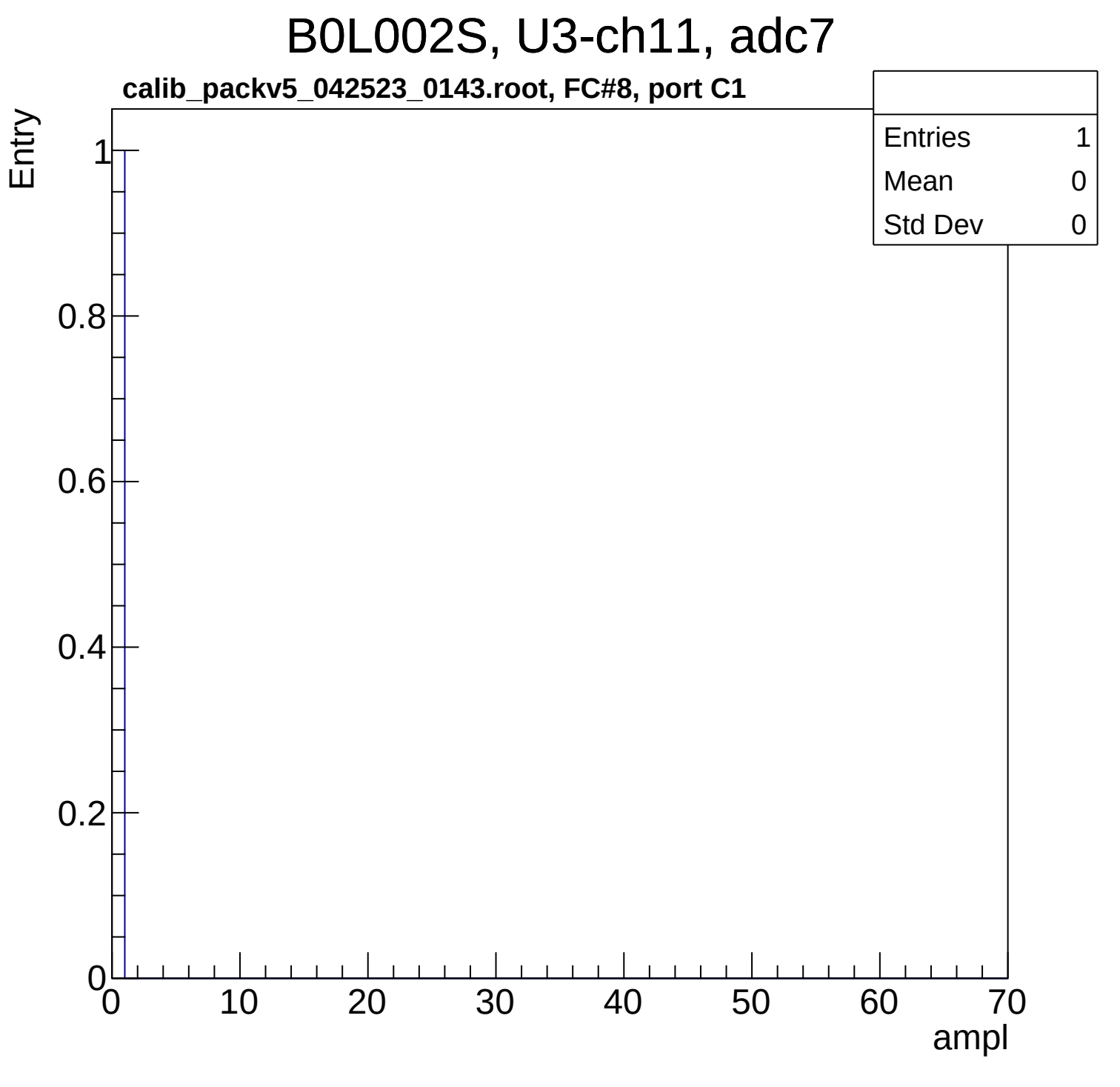
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch11, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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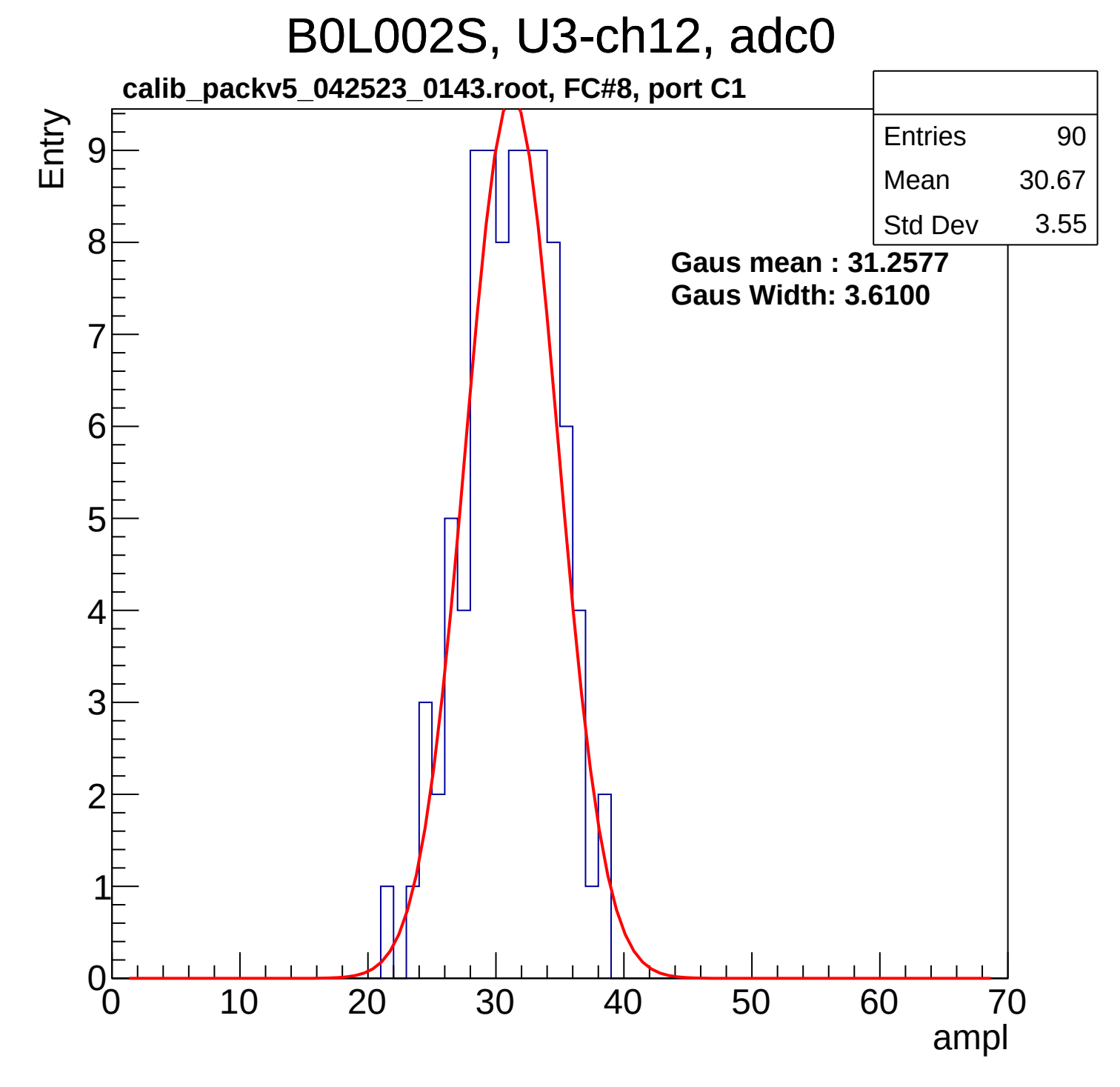
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch12, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 90
Mean 30.67
Std Dev 3.55
Gaus mean : 31.2577
Gaus Width: 3.6100
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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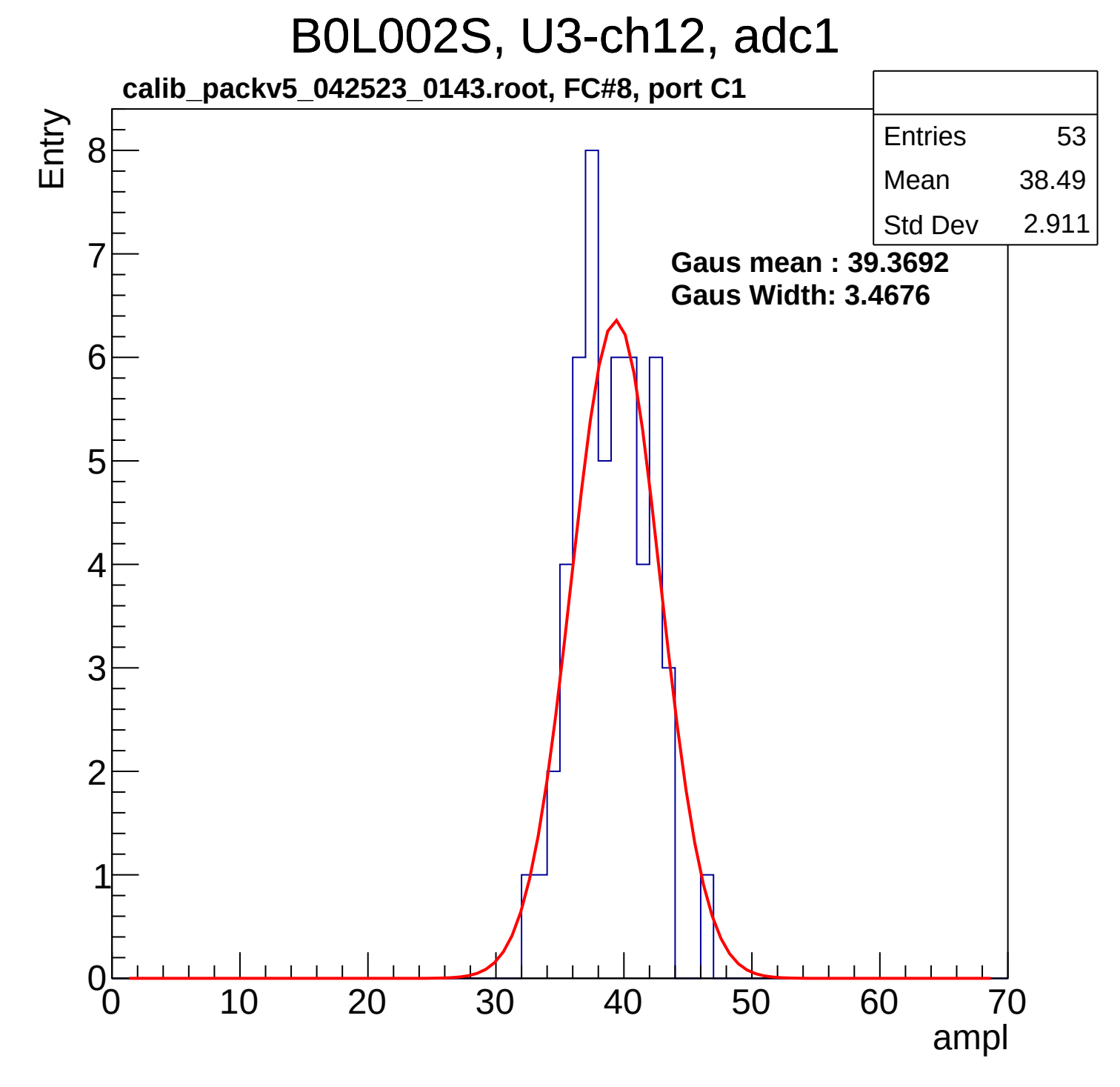
B0L002S, U3-ch12, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 38.49
Std Dev 2.911
Gaus mean : 39.3692
Gaus Width: 3.4676
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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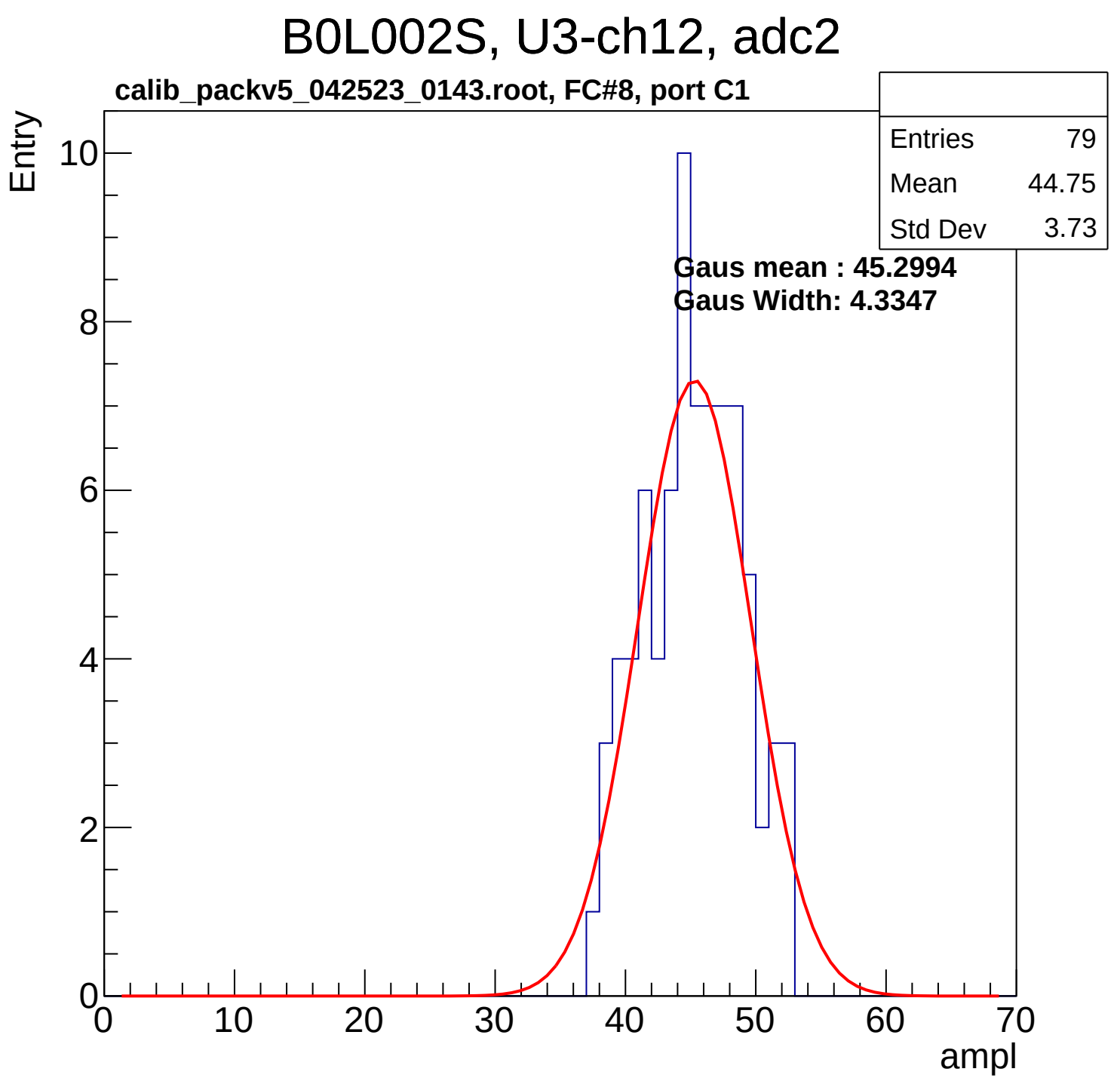
B0L002S, U3-ch12, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 44.75
Std Dev 3.73
Gaus mean : 45.2994
Gaus Width: 4.3347
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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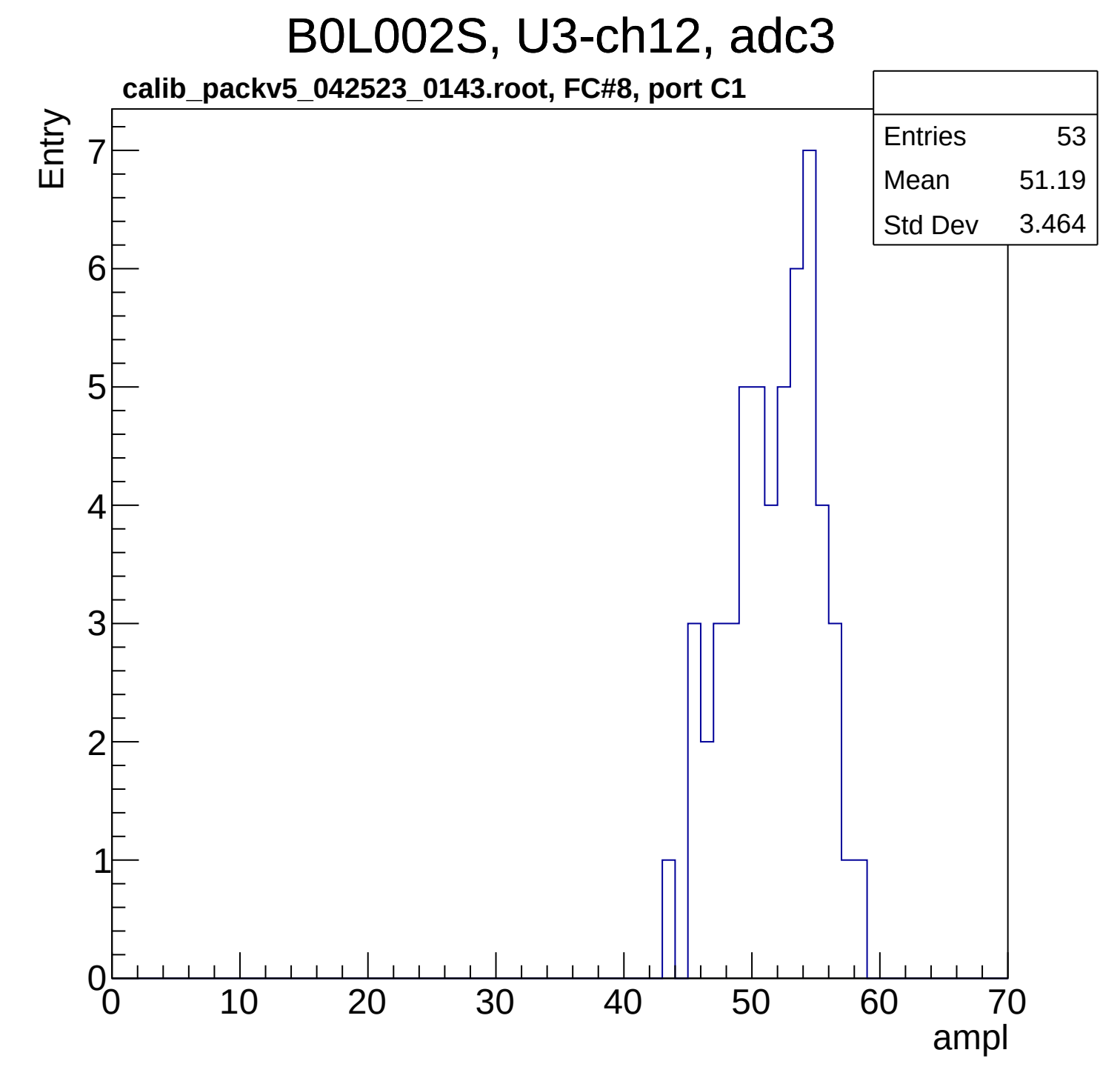
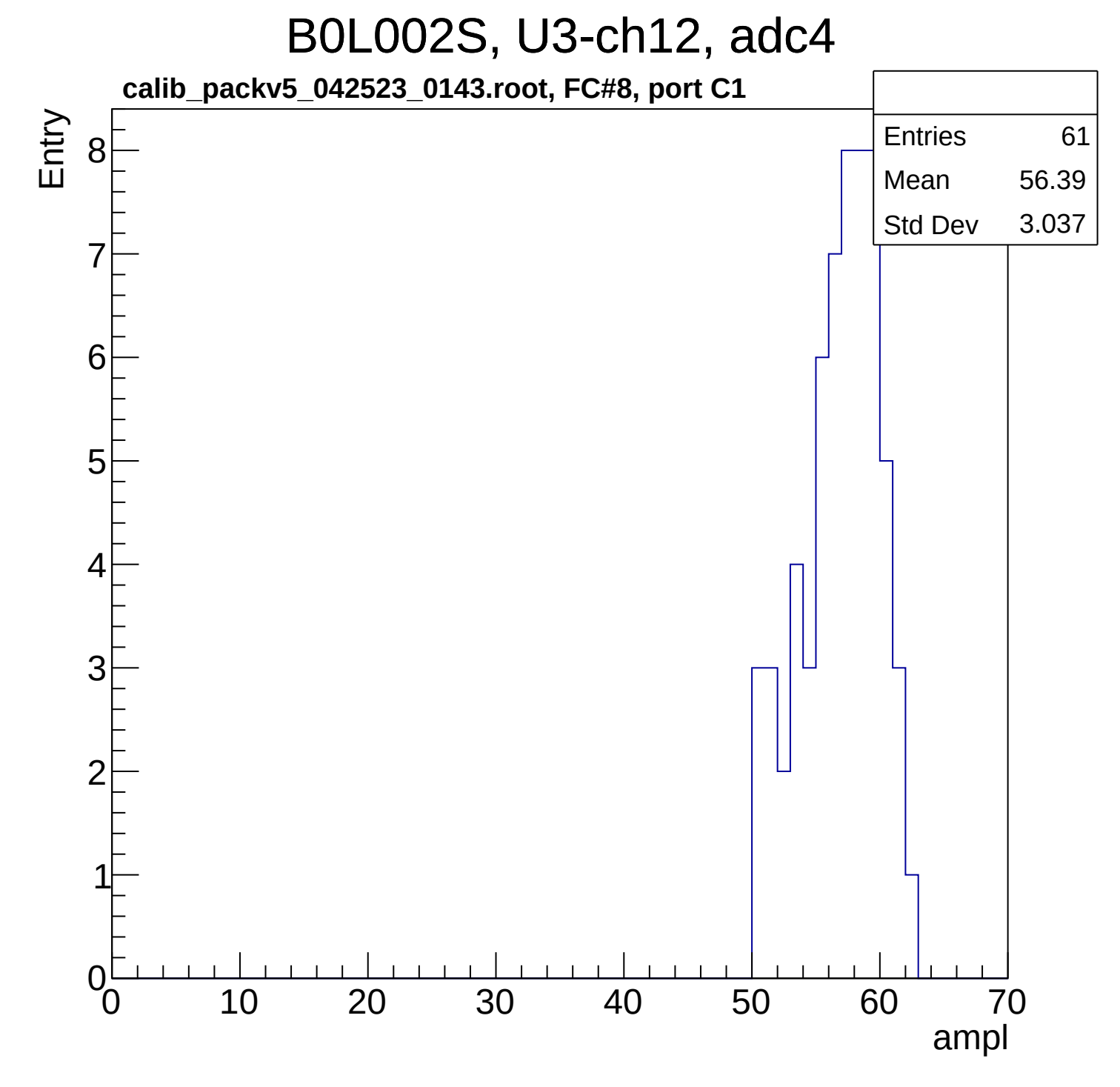

B0L002S, U3-ch12, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 51.19
Std Dev 3.464
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch12, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.39
Std Dev 3.037
Entry
ampl
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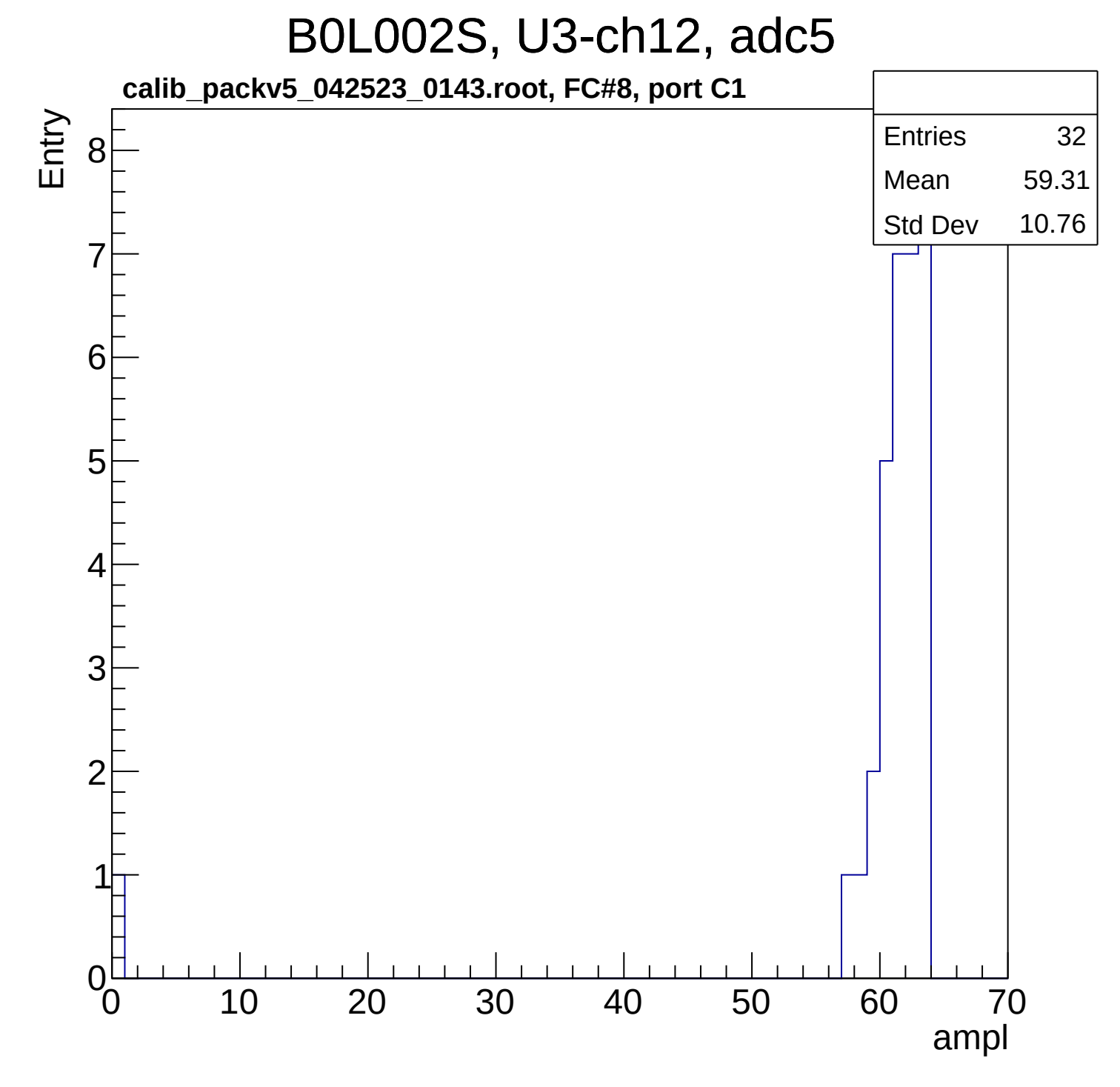
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch12, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.31
Std Dev 10.76
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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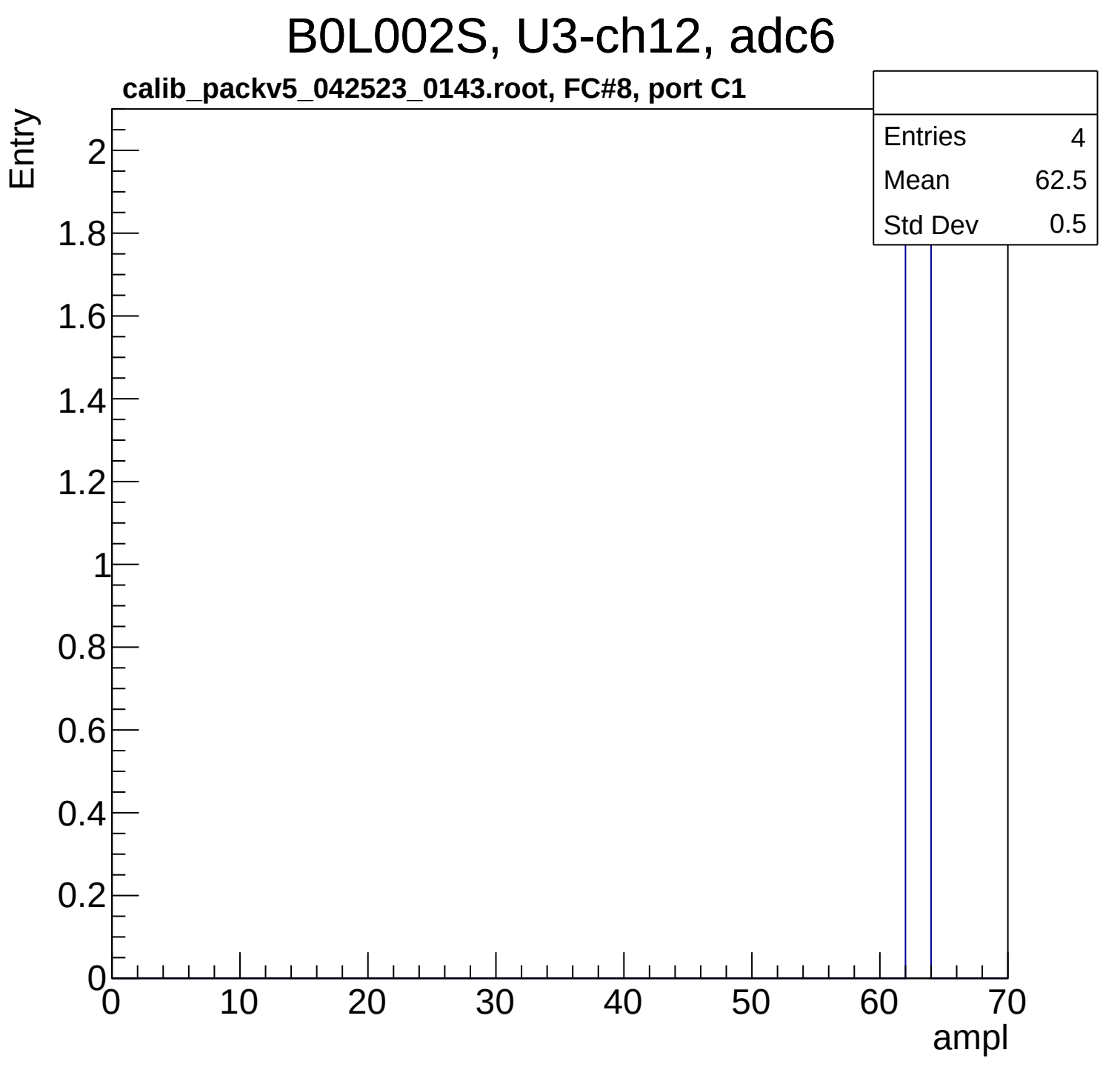
B0L002S, U3-ch12, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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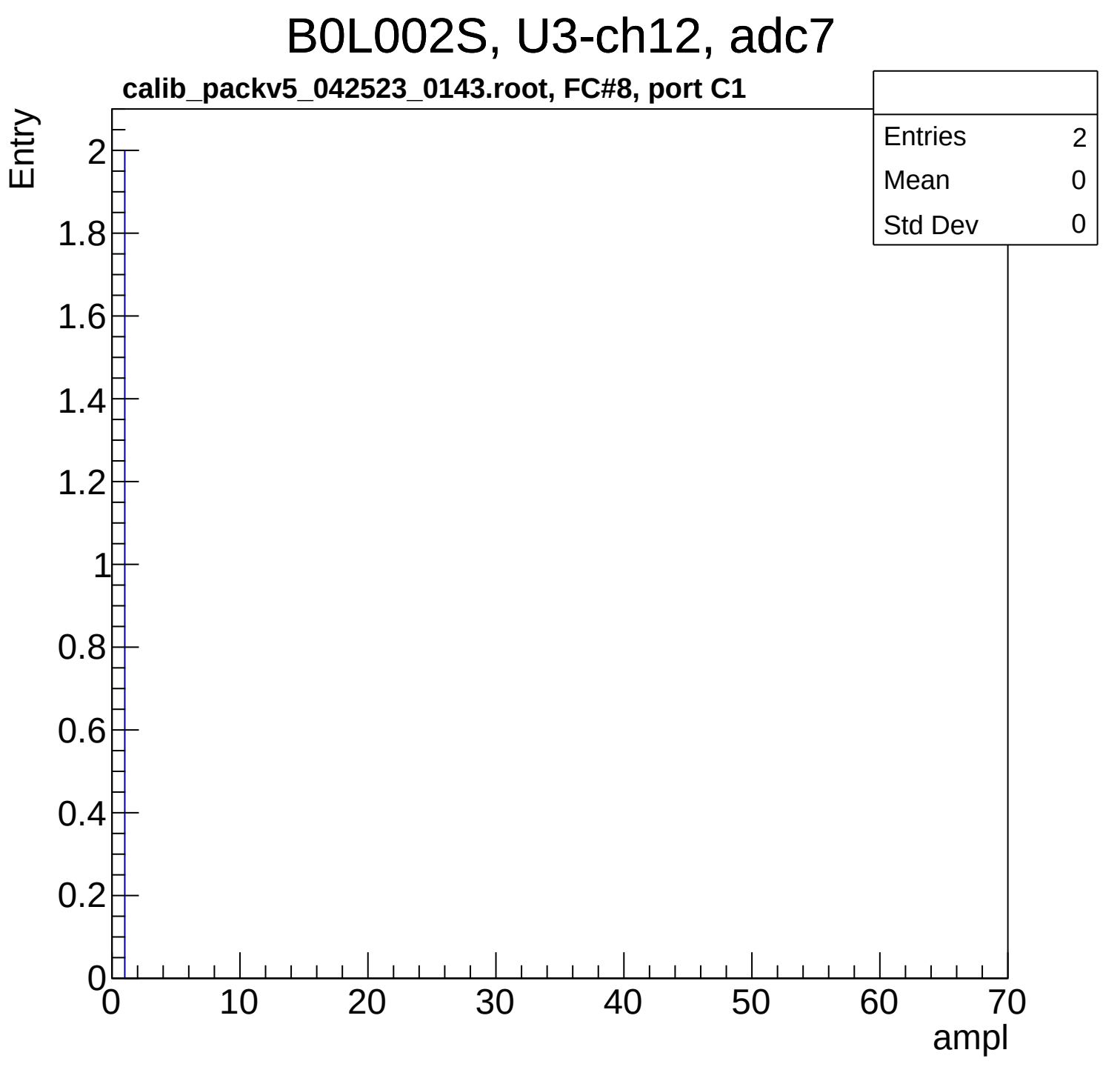
B0L002S, U3-ch12, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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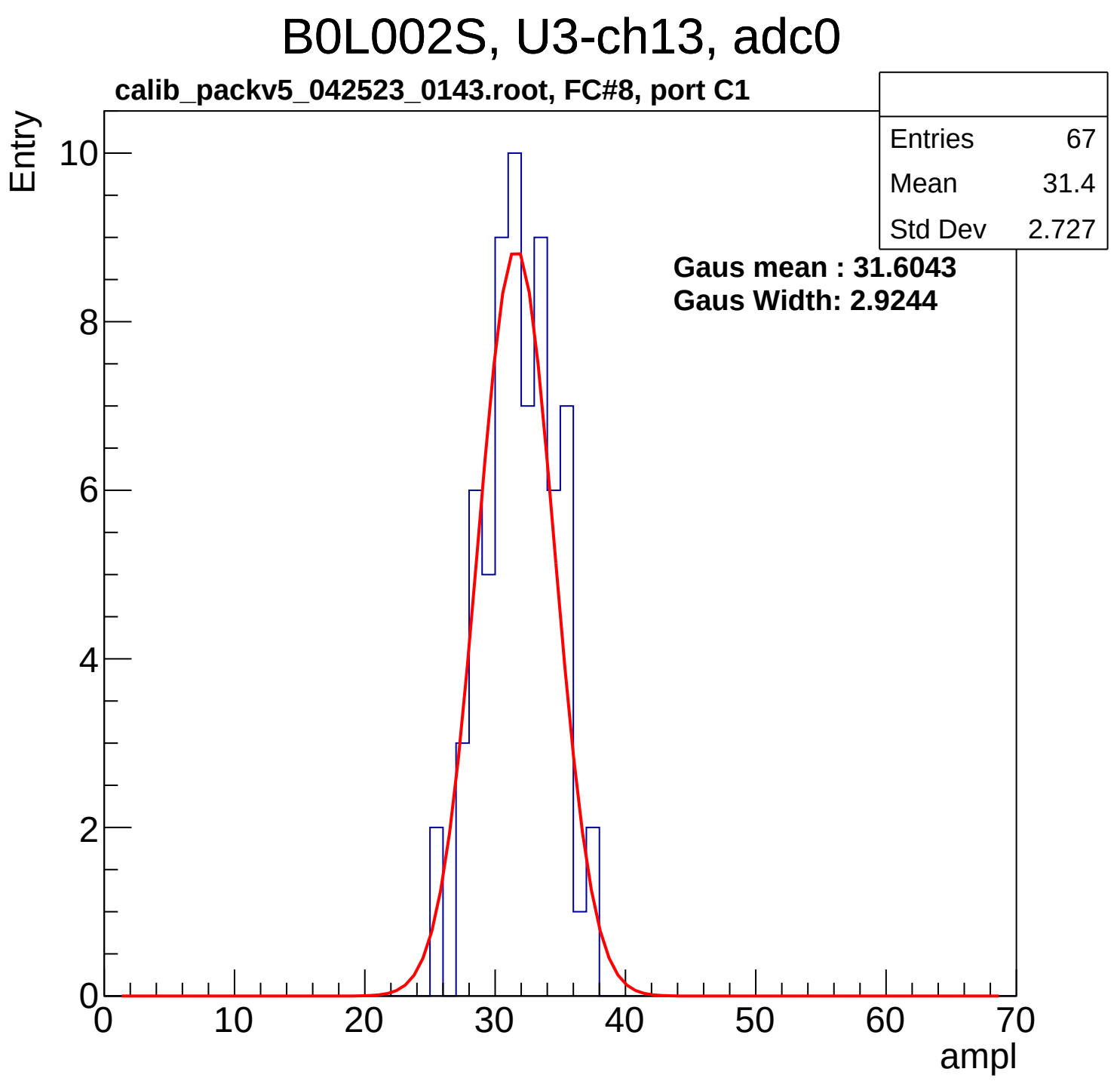
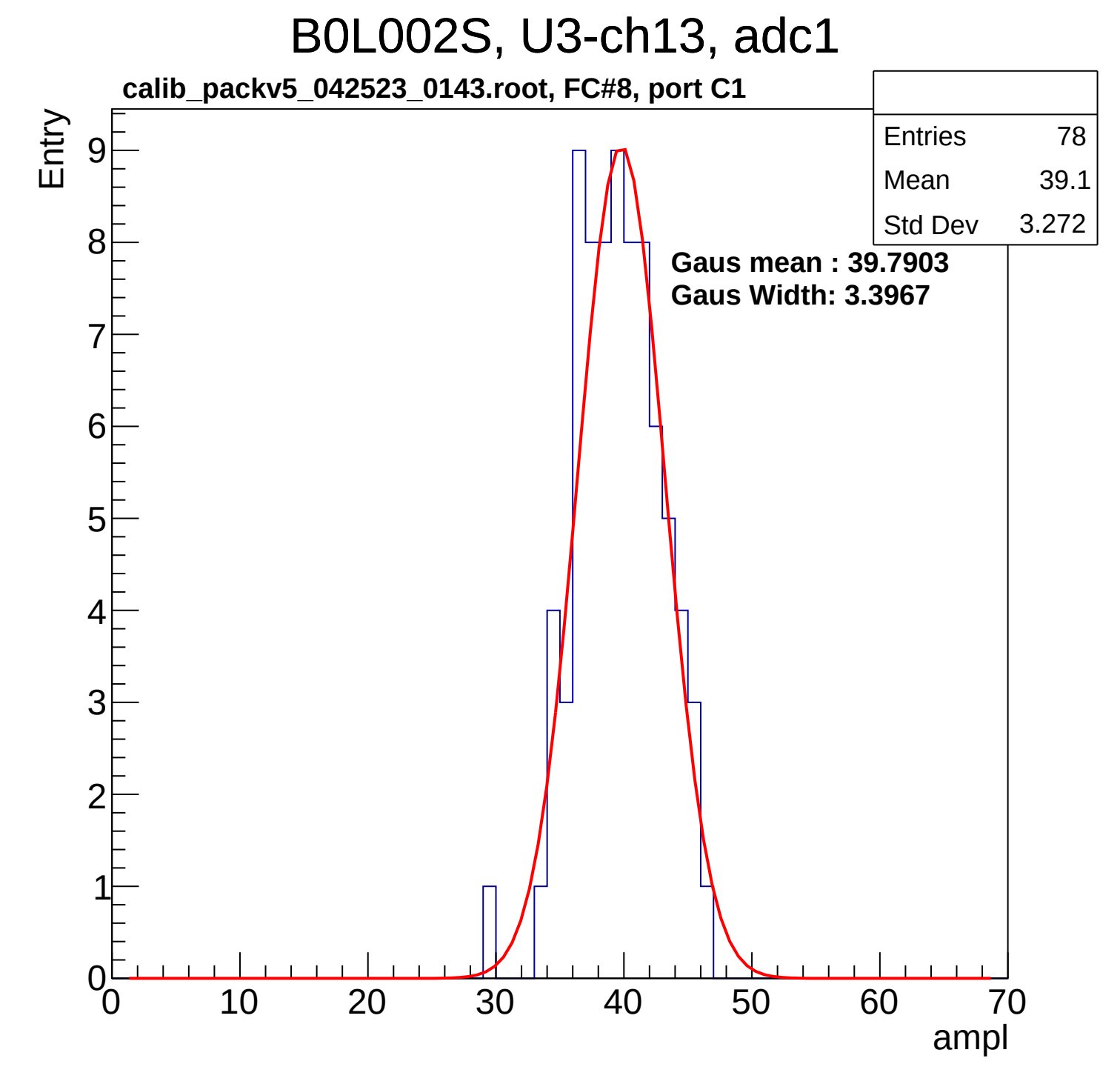

B0L002S, U3-ch13, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.4
Std Dev 2.727
Gaus mean : 31.6043
Gaus Width: 2.9244
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch13, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 39.1
Std Dev 3.272
Gaus mean : 39.7903
Gaus Width: 3.3967
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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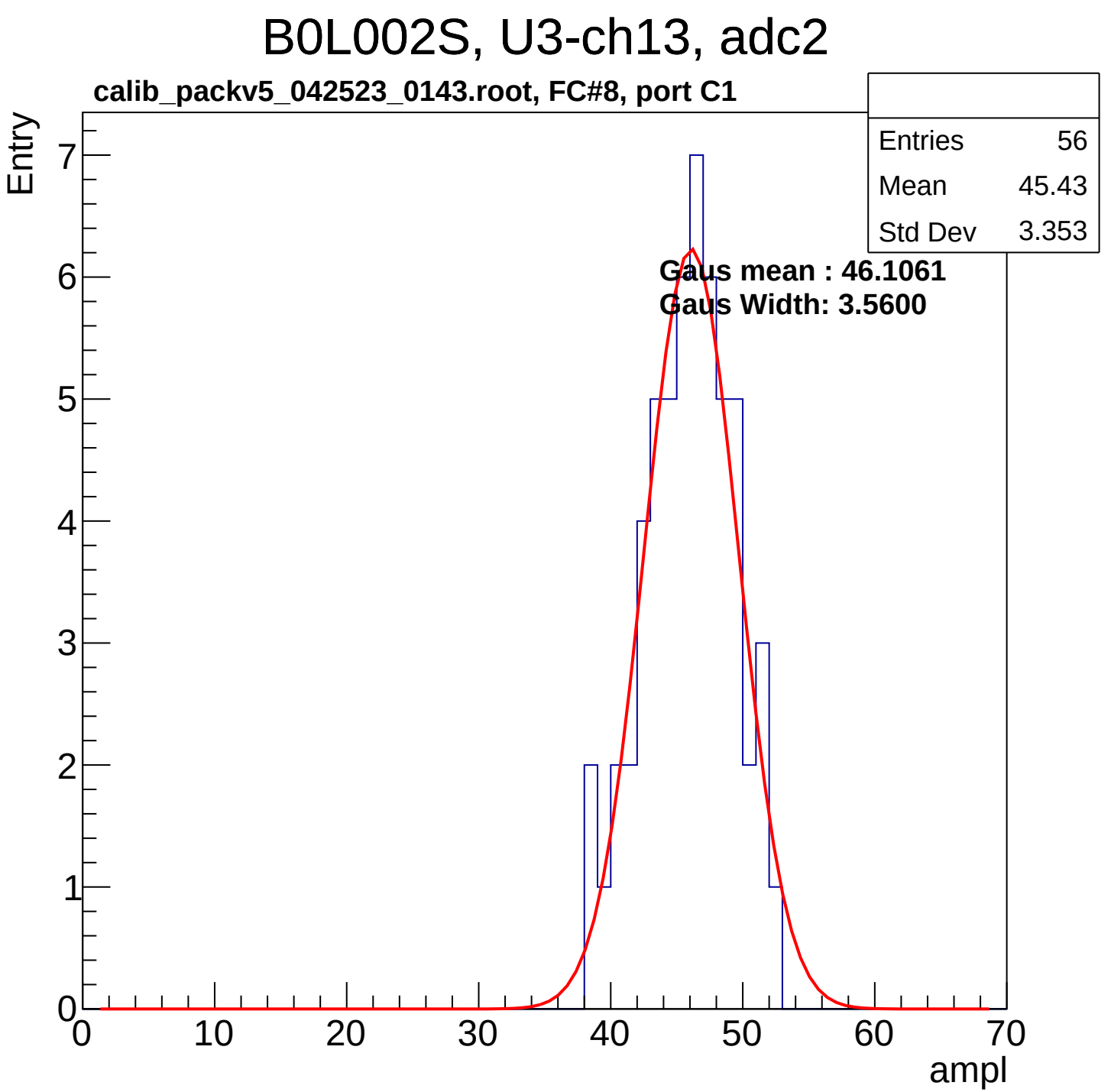
B0L002S, U3-ch13, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 45.43
Std Dev 3.353
Gaus mean : 46.1061
Gaus Width: 3.5600
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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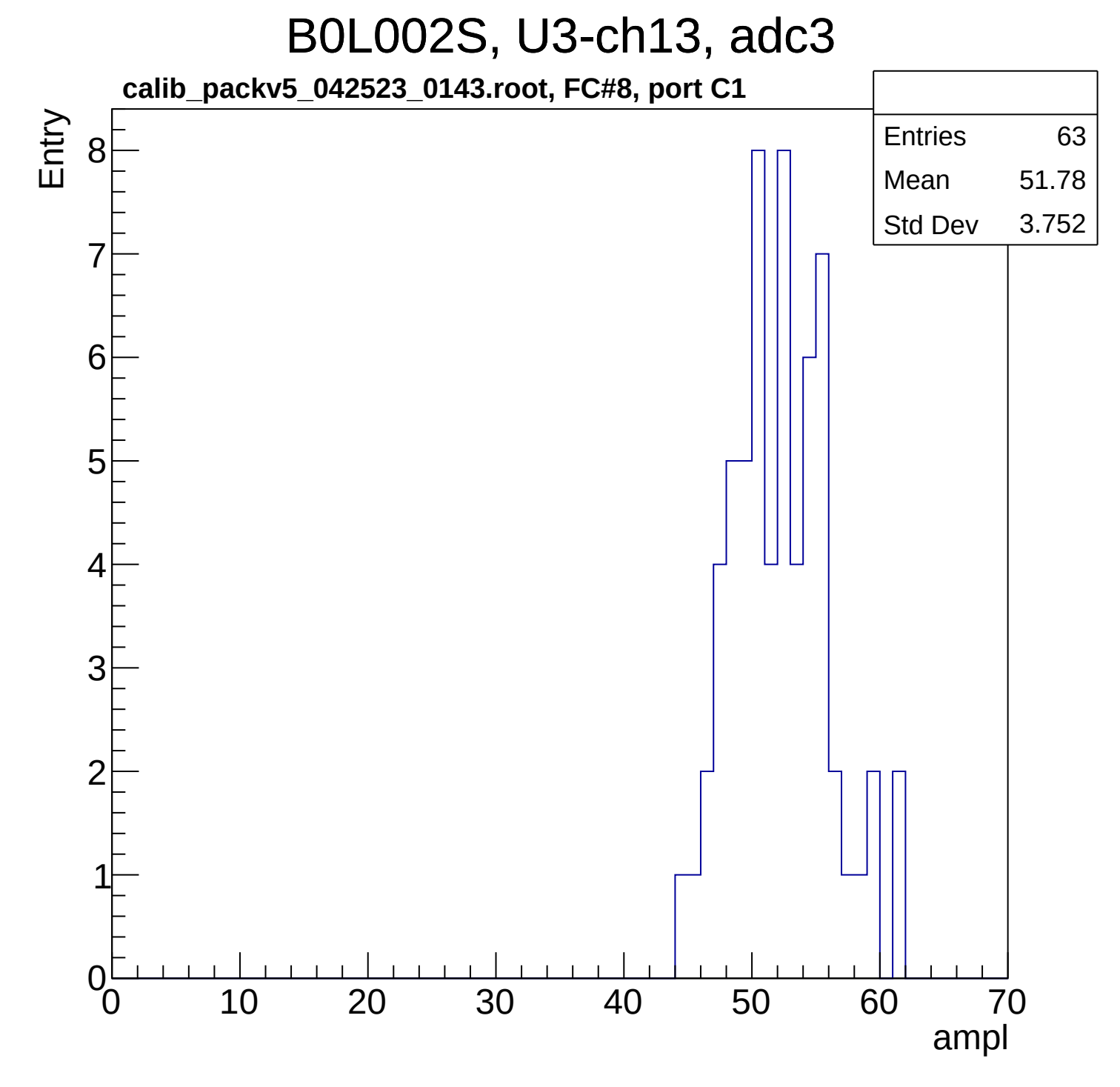
B0L002S, U3-ch13, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.78
Std Dev 3.752
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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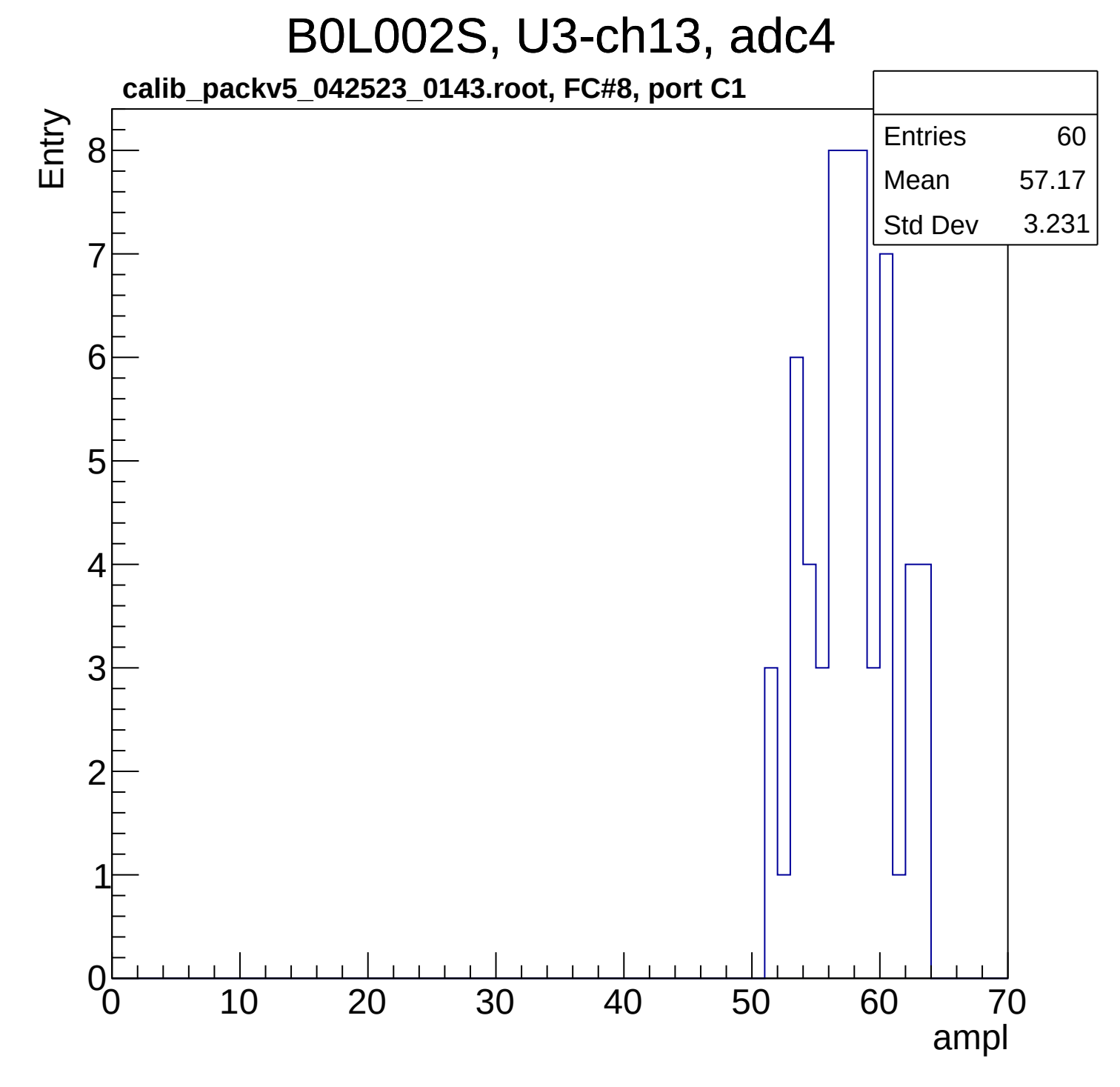
B0L002S, U3-ch13, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.17
Std Dev 3.231
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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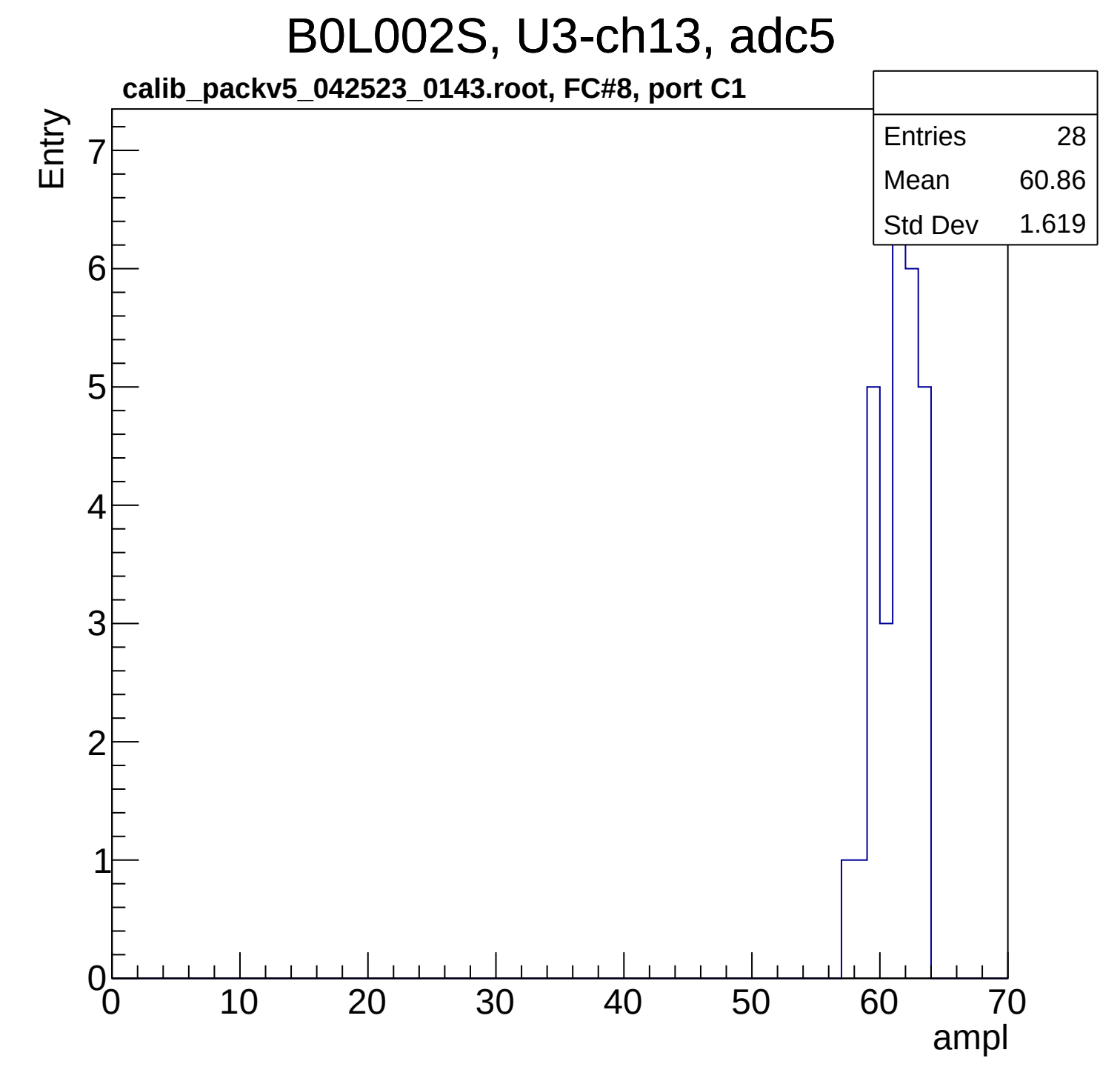
B0L002S, U3-ch13, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 60.86
Std Dev 1.619
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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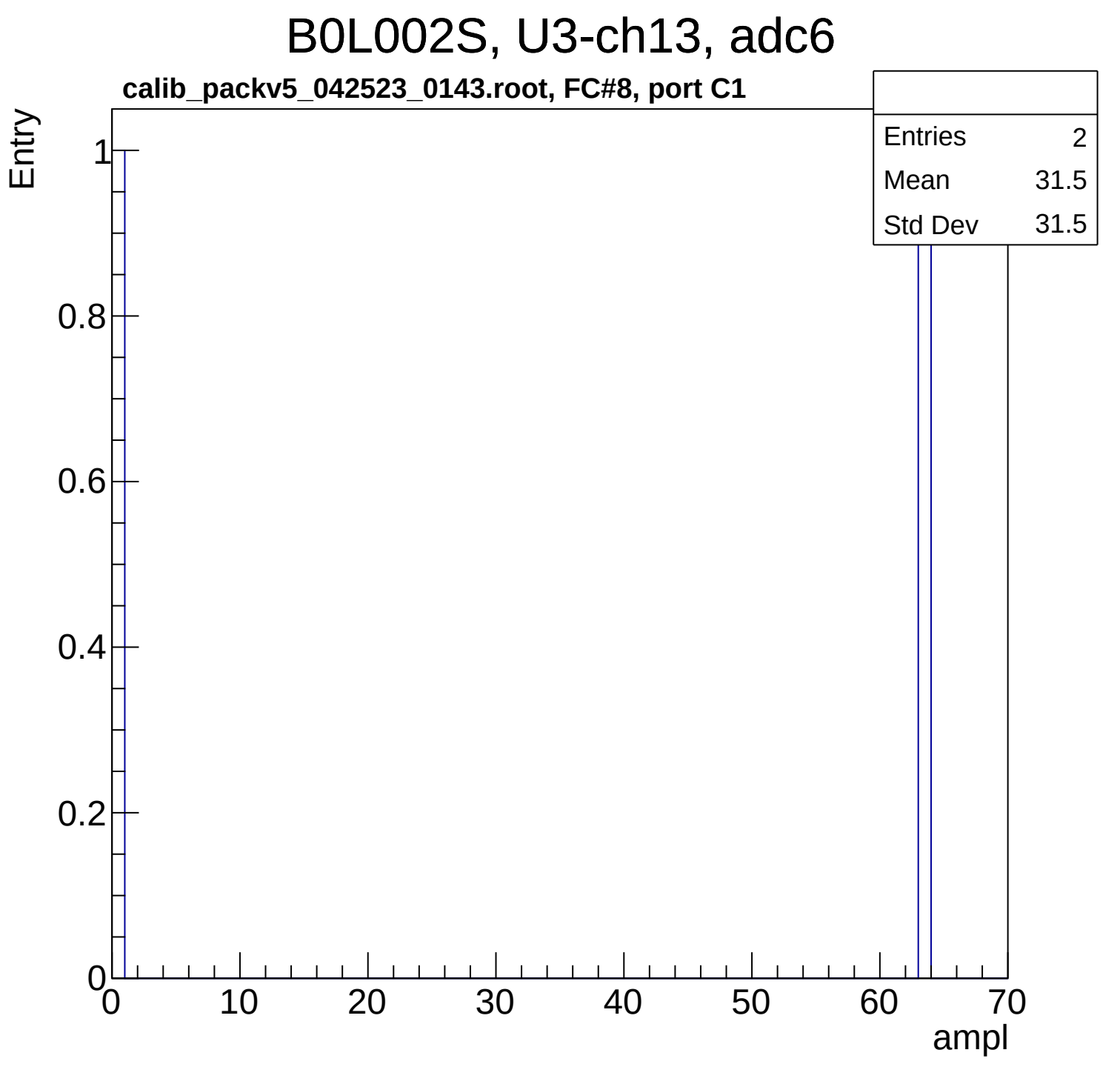
B0L002S, U3-ch13, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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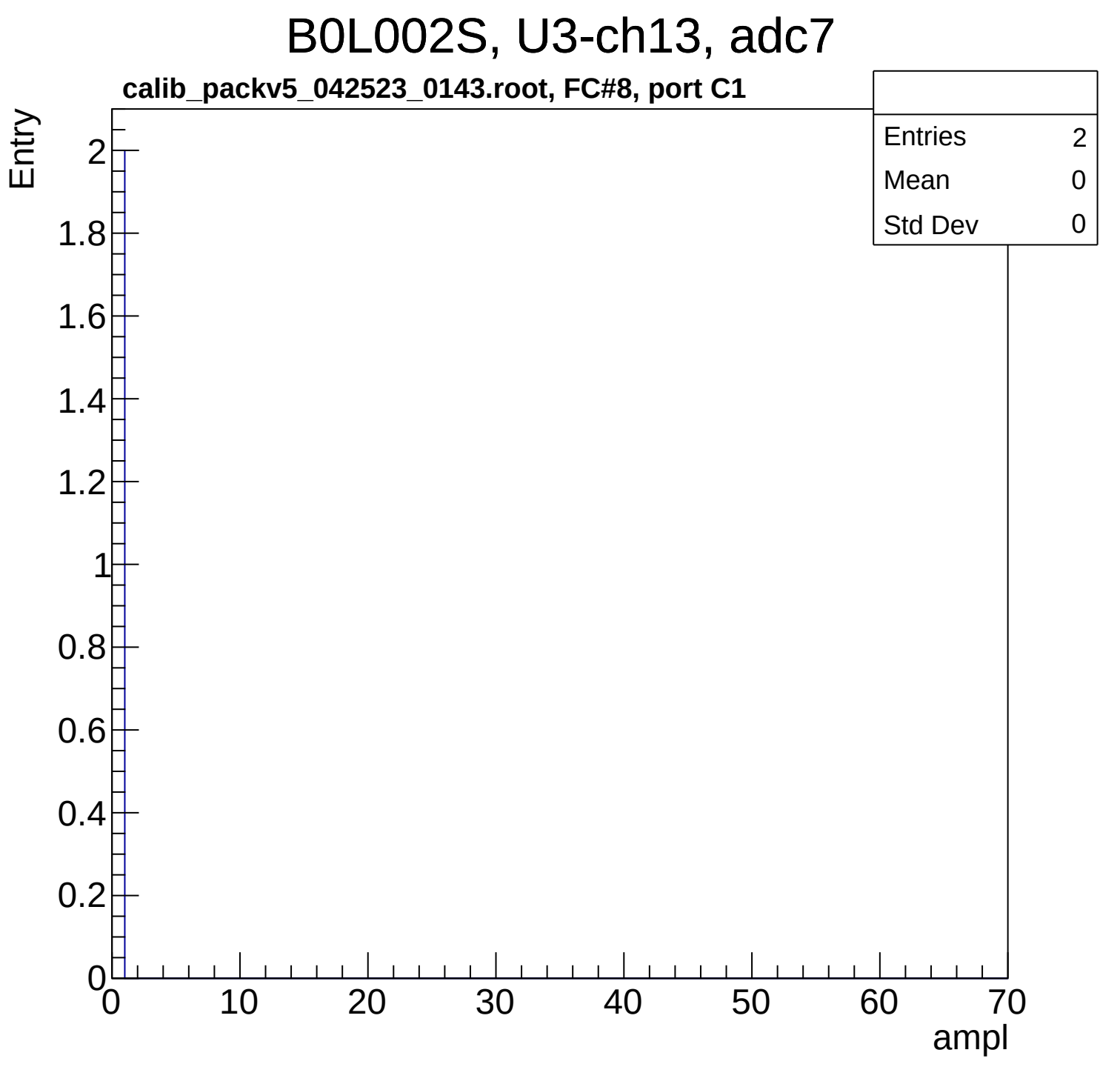
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch13, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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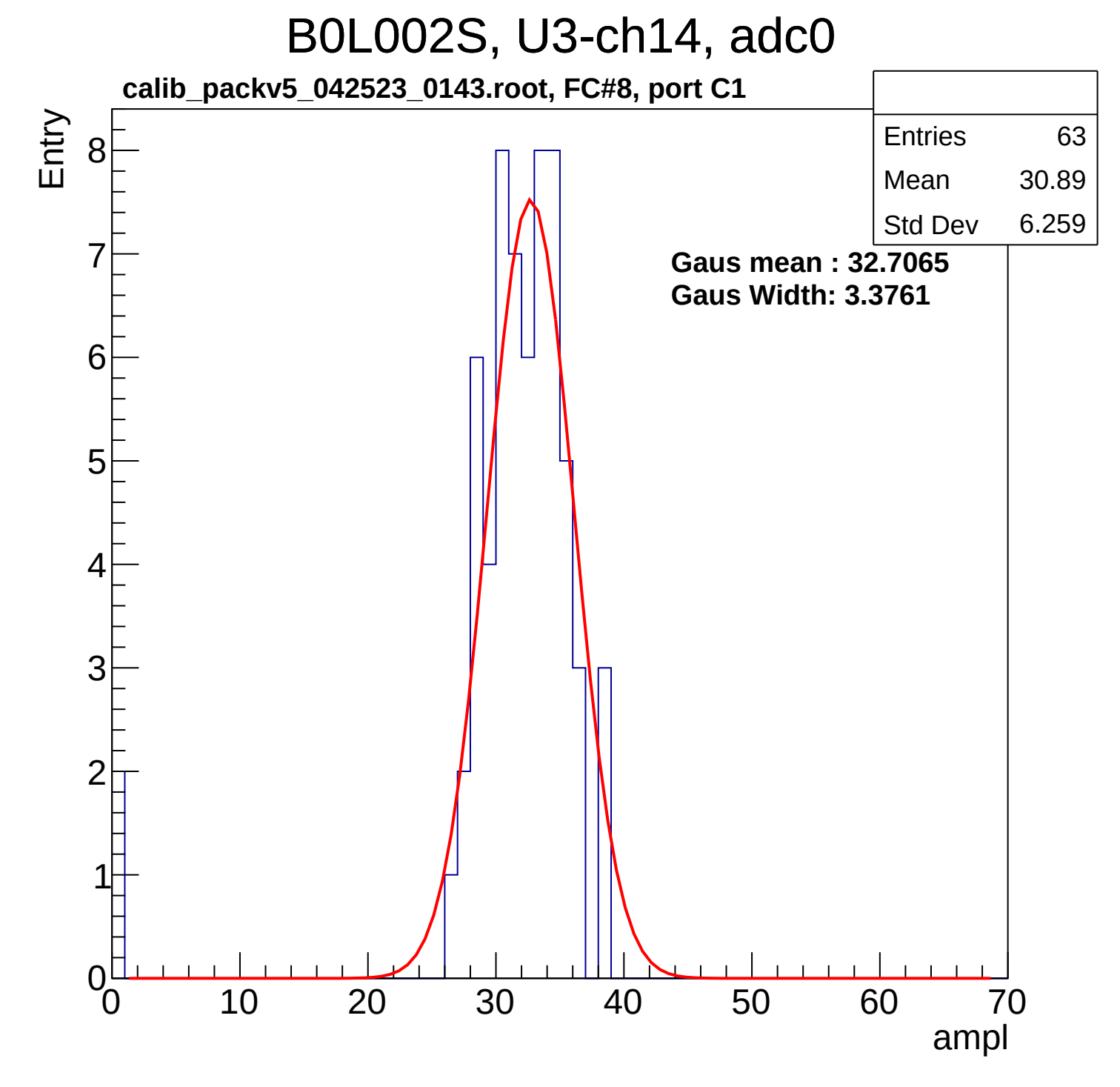
40
50
60
70


B0L002S, U3-ch14, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.89
Std Dev 6.259
Gaus mean : 32.7065
Gaus Width: 3.3761
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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50
60
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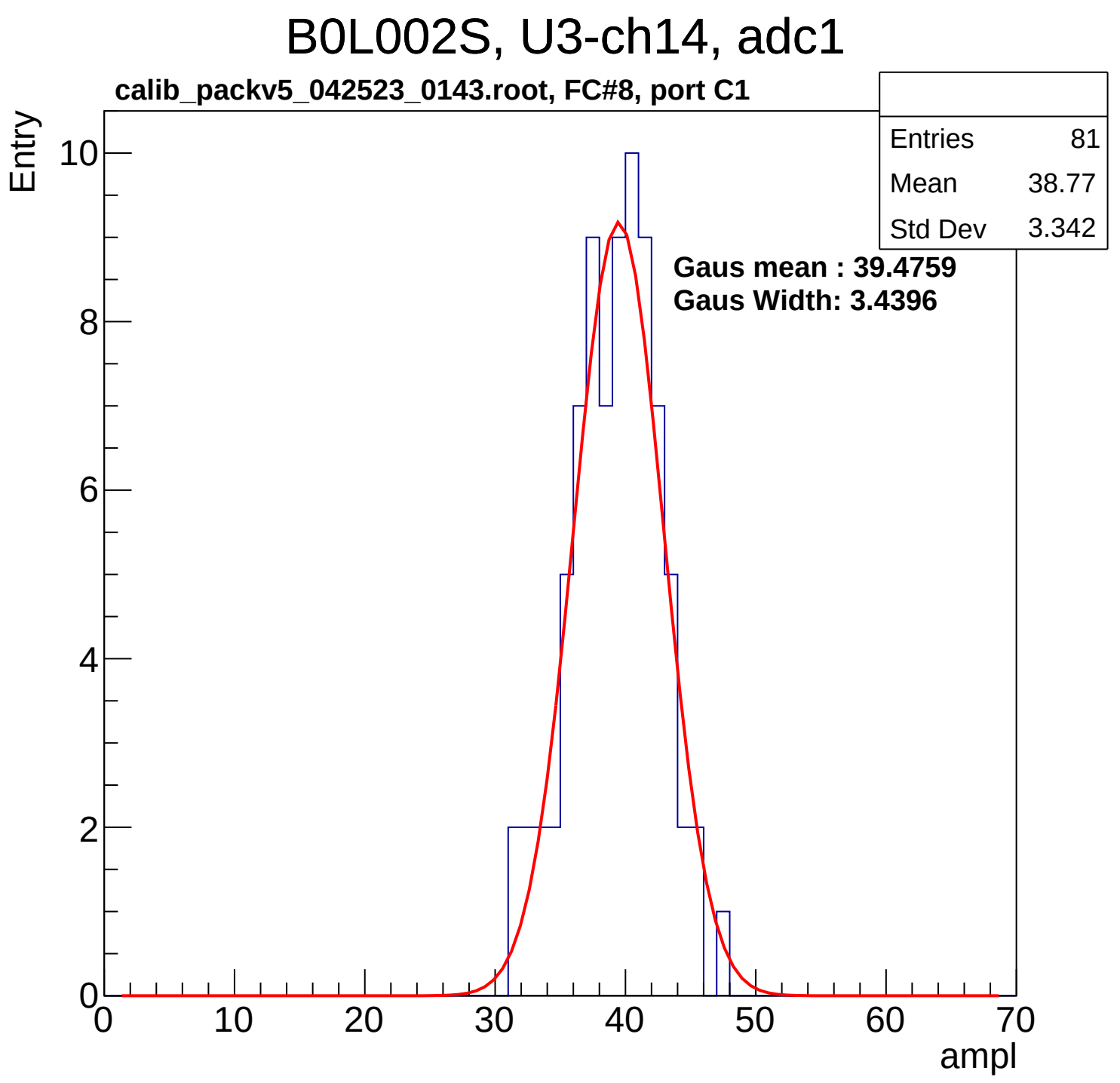
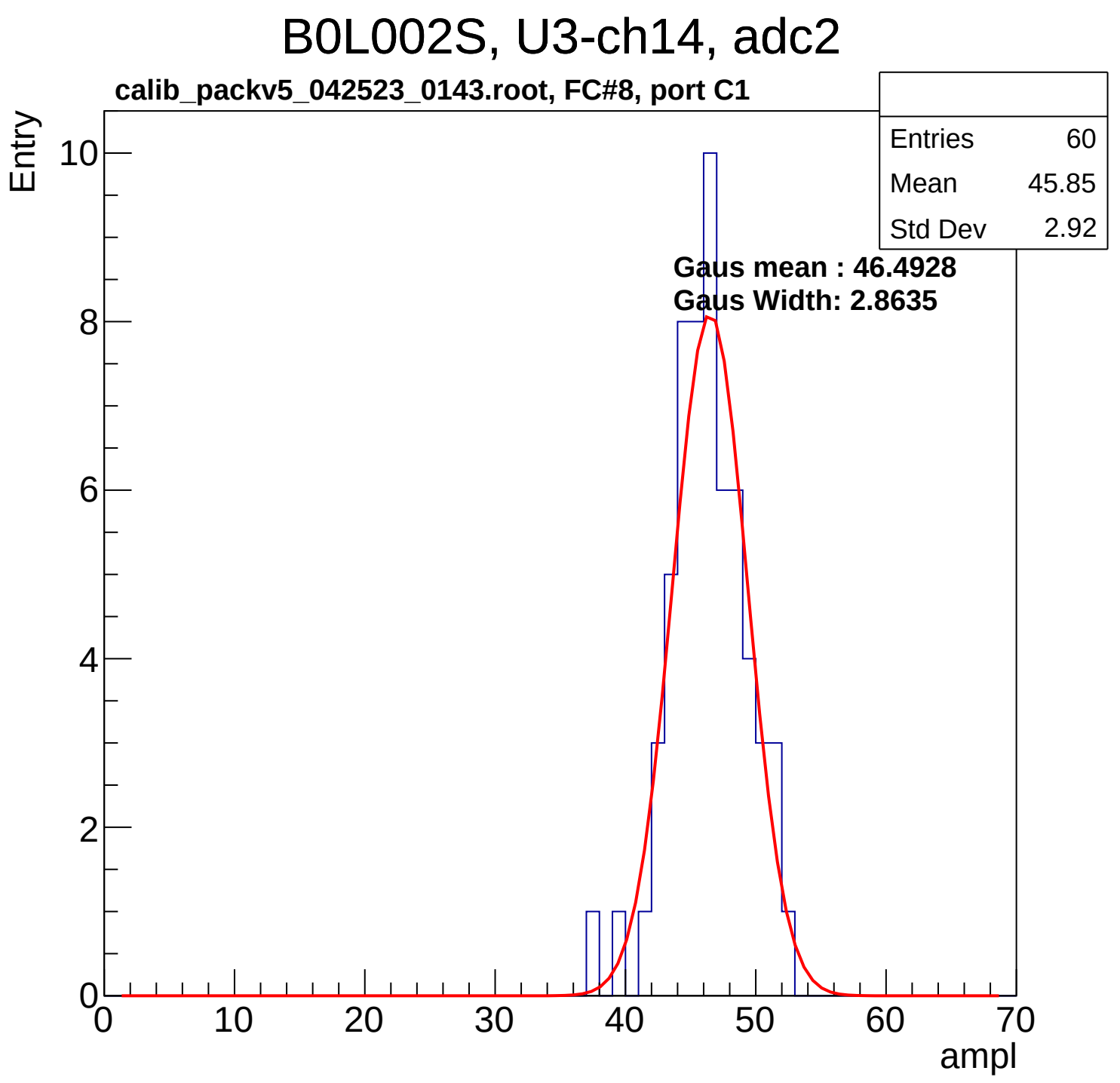

B0L002S, U3-ch14, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 38.77
Std Dev 3.342
Gaus mean : 39.4759
Gaus Width: 3.4396
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch14, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.85
Std Dev 2.92
Gaus mean : 46.4928
Gaus Width: 2.8635
Entry
ampl
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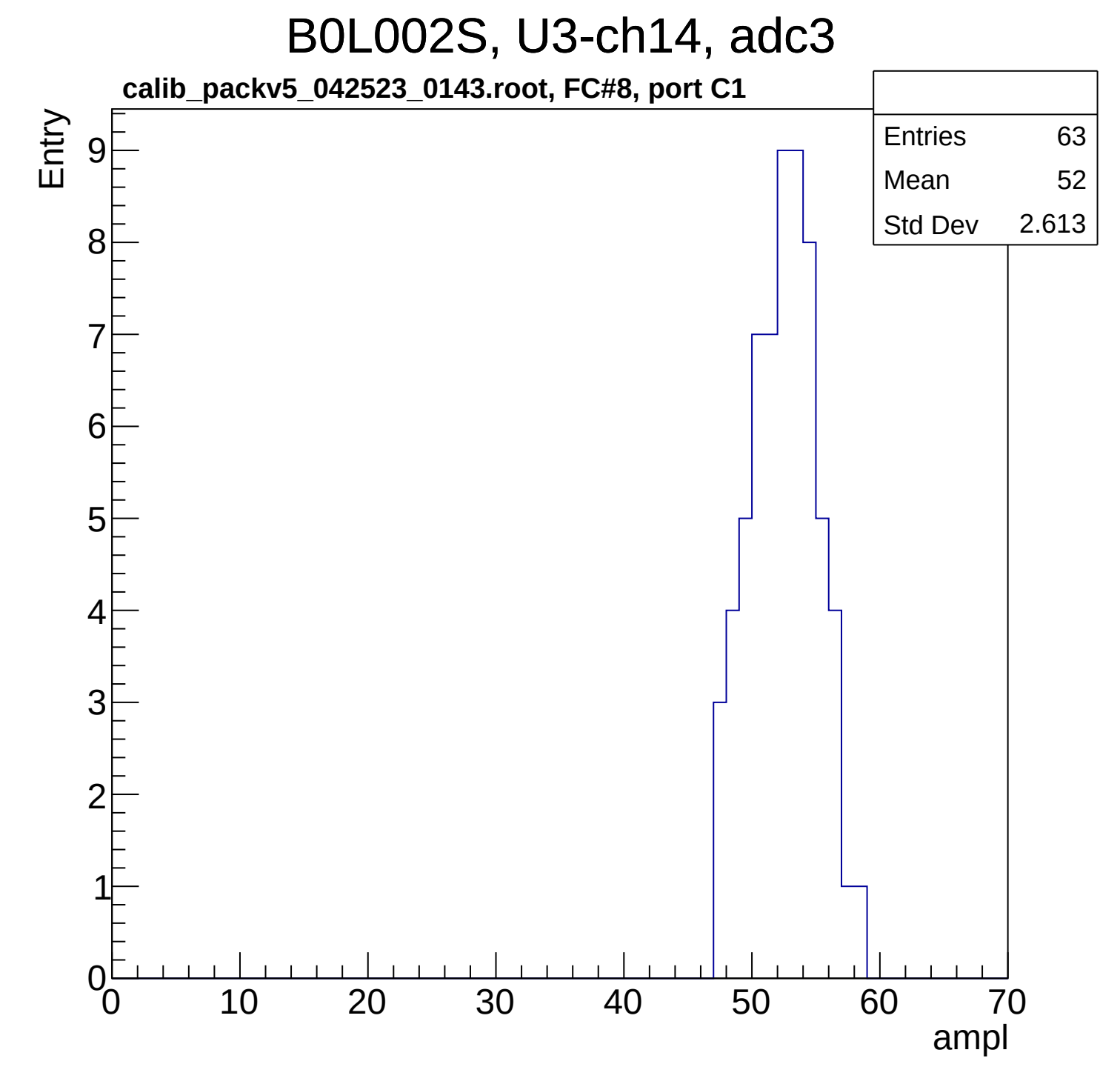
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch14, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 52
Std Dev 2.613
Entry
ampl
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6
7
8
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60
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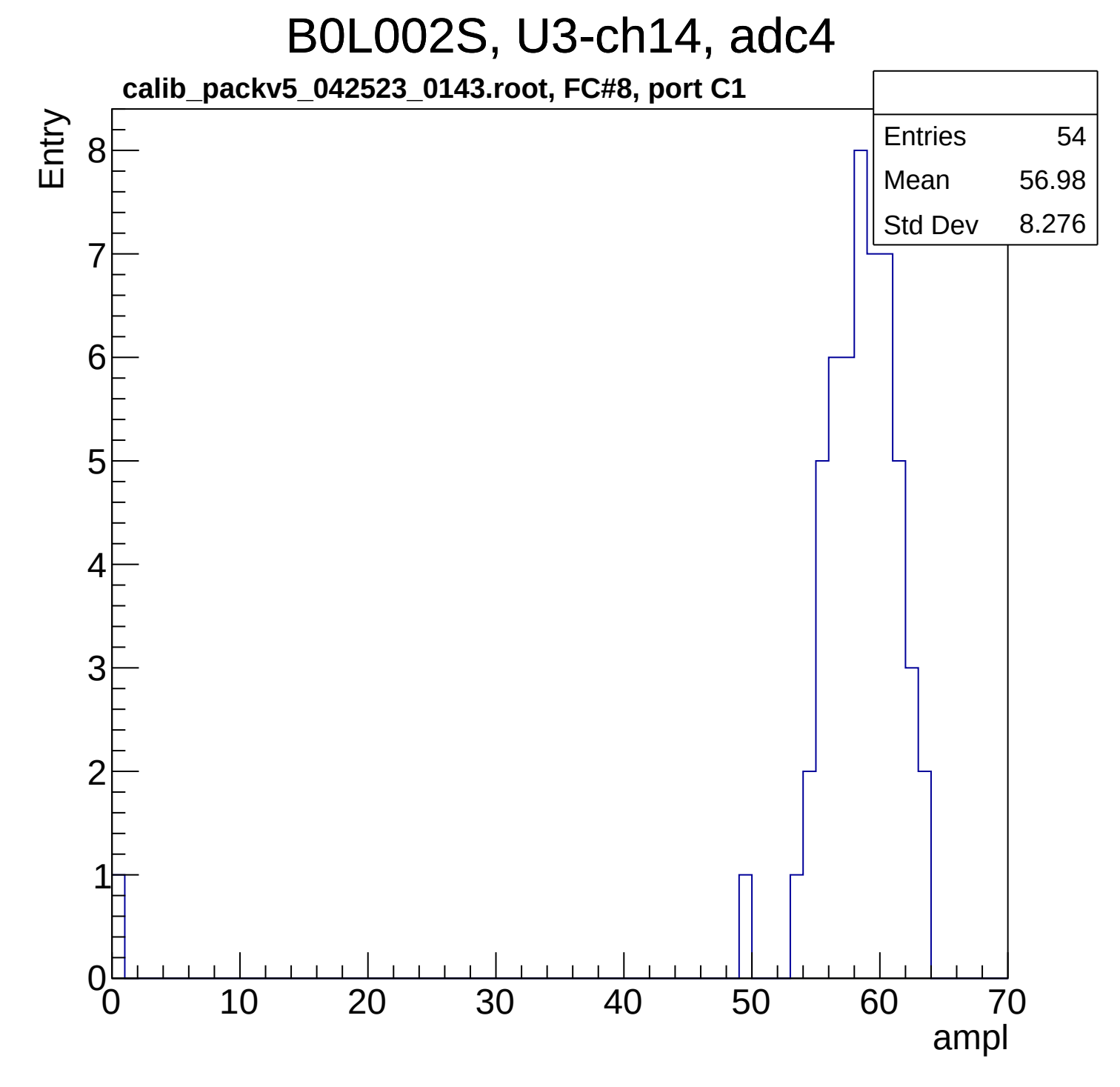
B0L002S, U3-ch14, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 56.98
Std Dev 8.276
Entry
ampl
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0
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60
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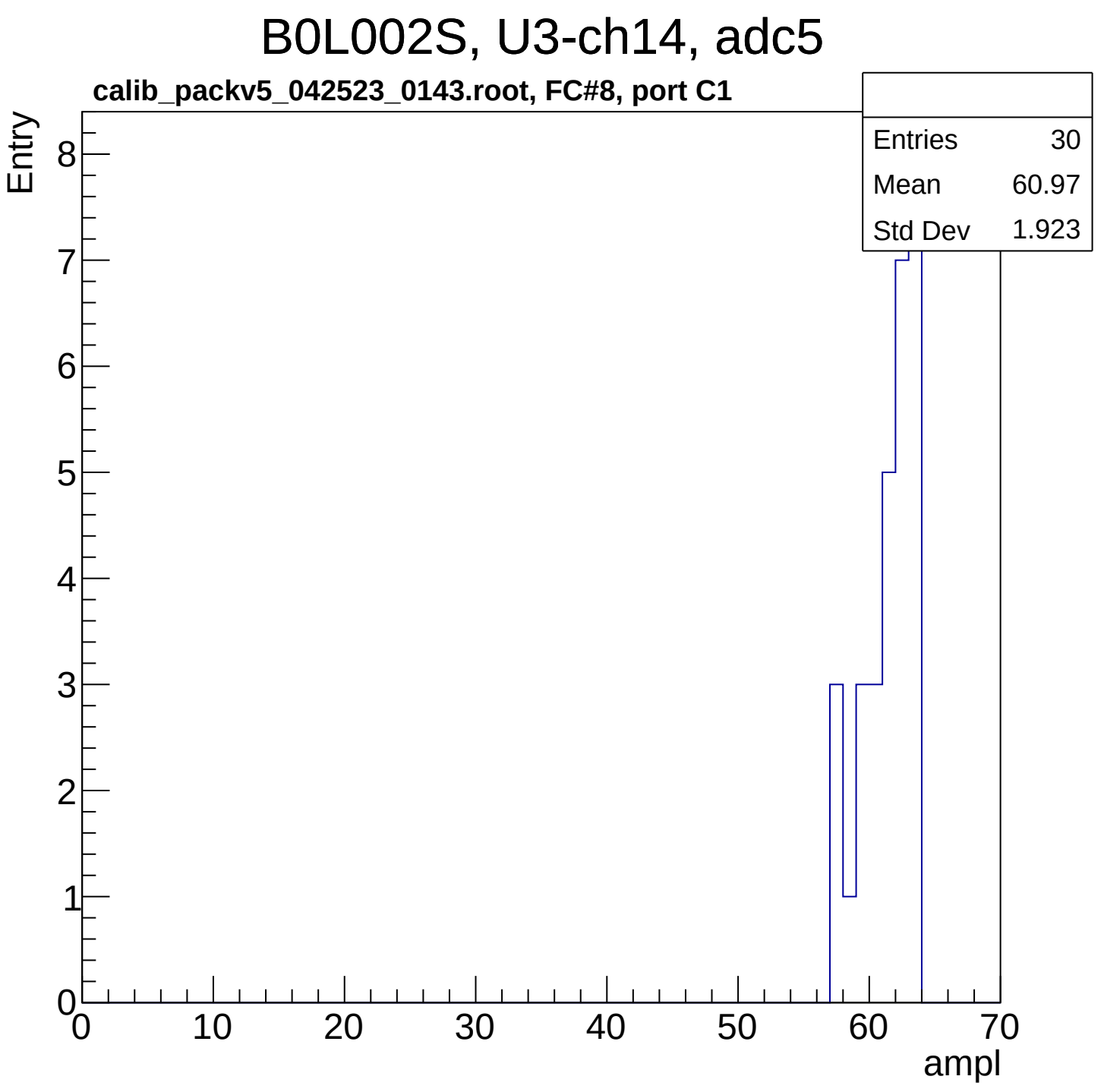
B0L002S, U3-ch14, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 60.97
Std Dev 1.923
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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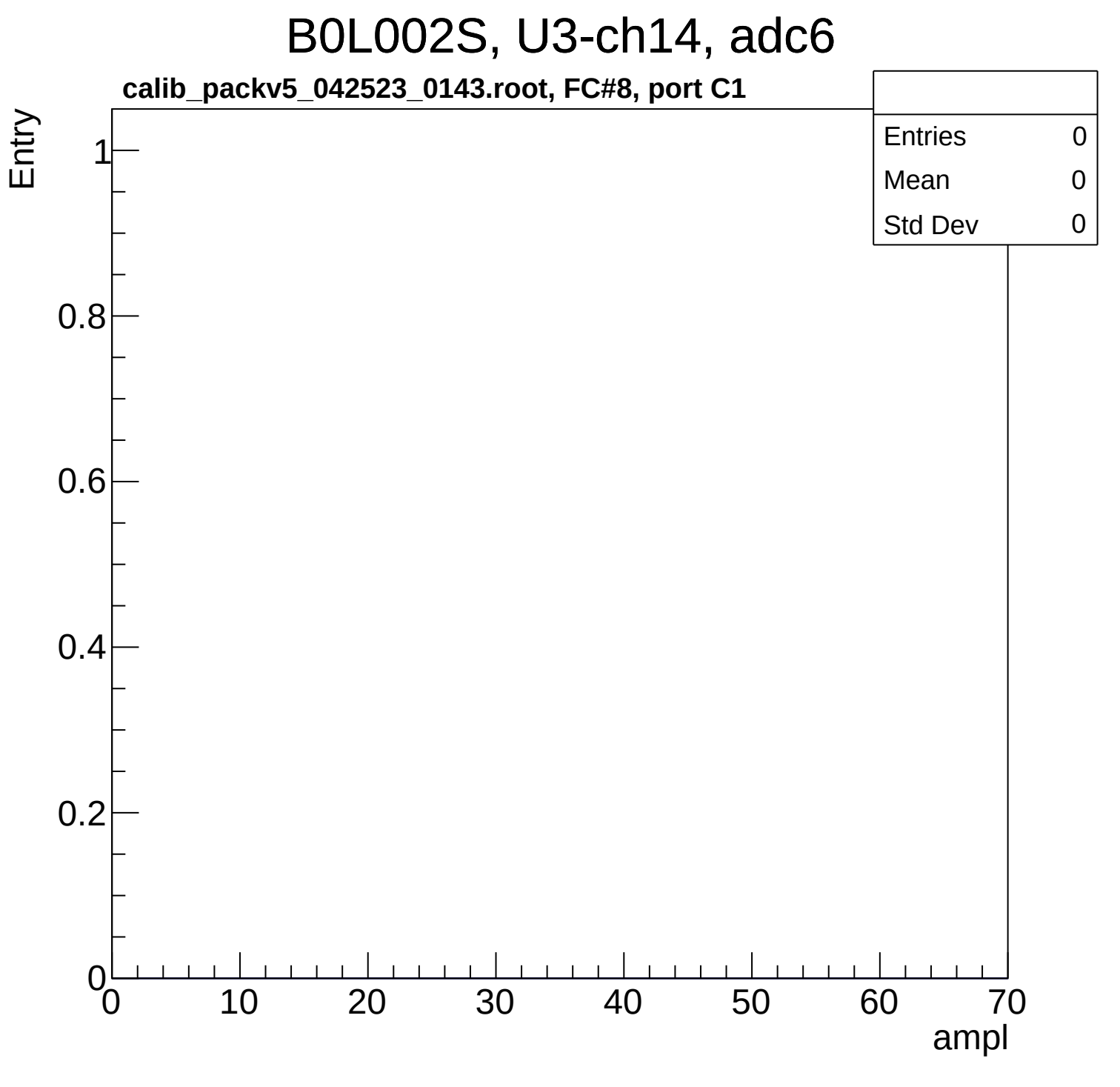
B0L002S, U3-ch14, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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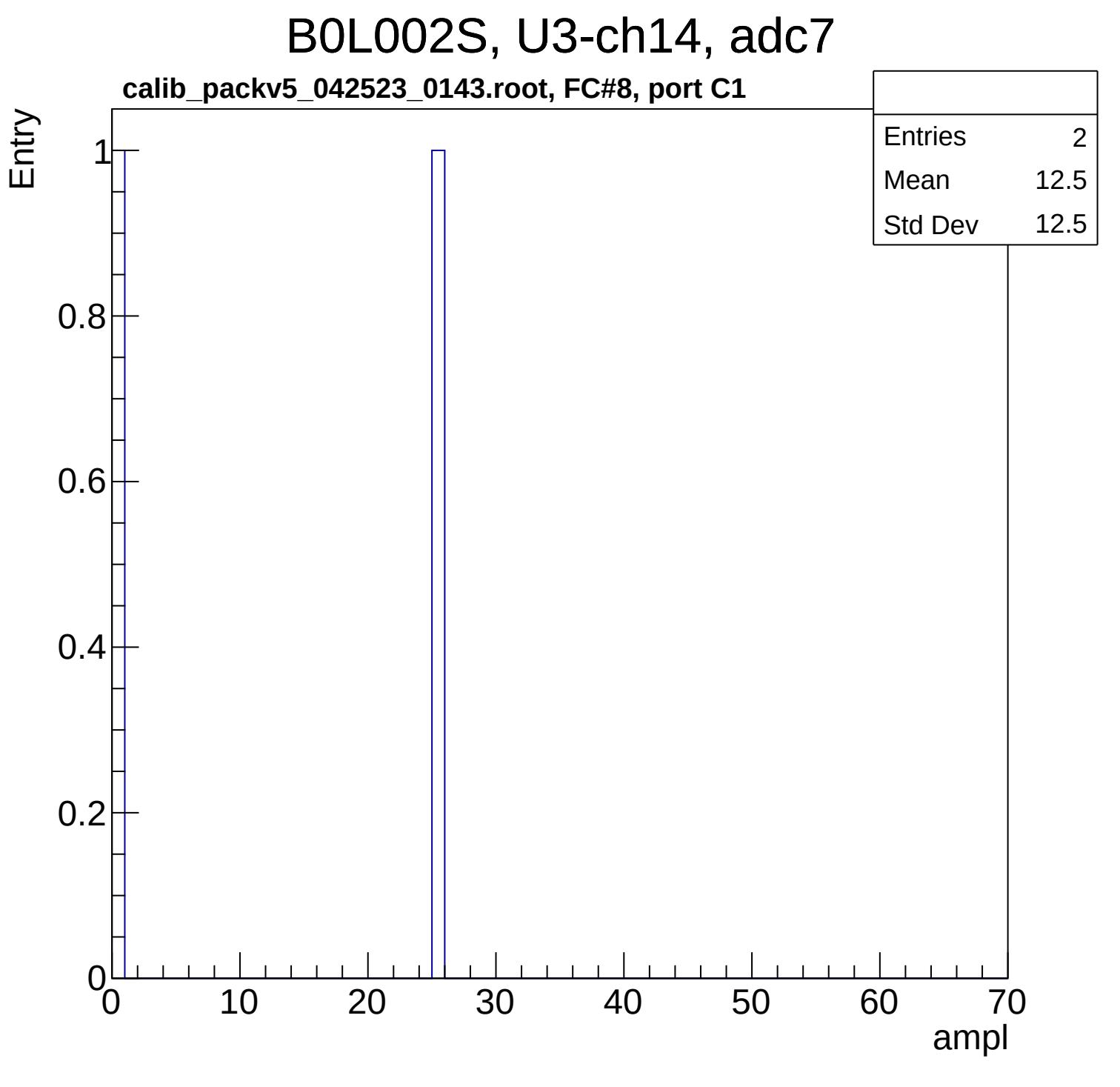
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch14, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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0.6
0.4
0.2
0
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10
20
30
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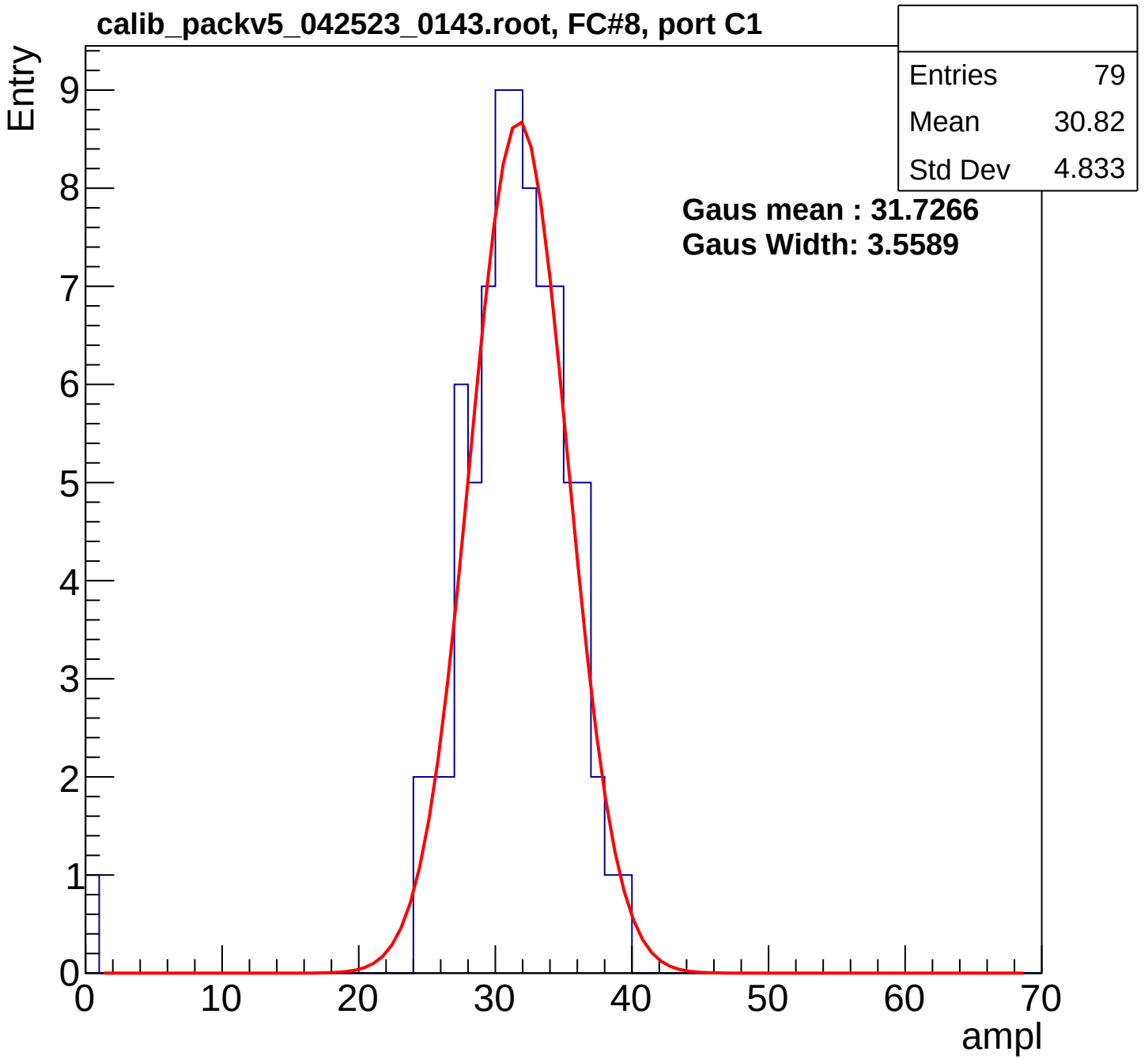
50
60
70
ampl


B0L002S, U3-ch15, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 30.82
Std Dev 4.833
Gaus mean : 31.7266
Gaus Width: 3.5589
Entry
ampl
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2
3
4
5
6
7
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0
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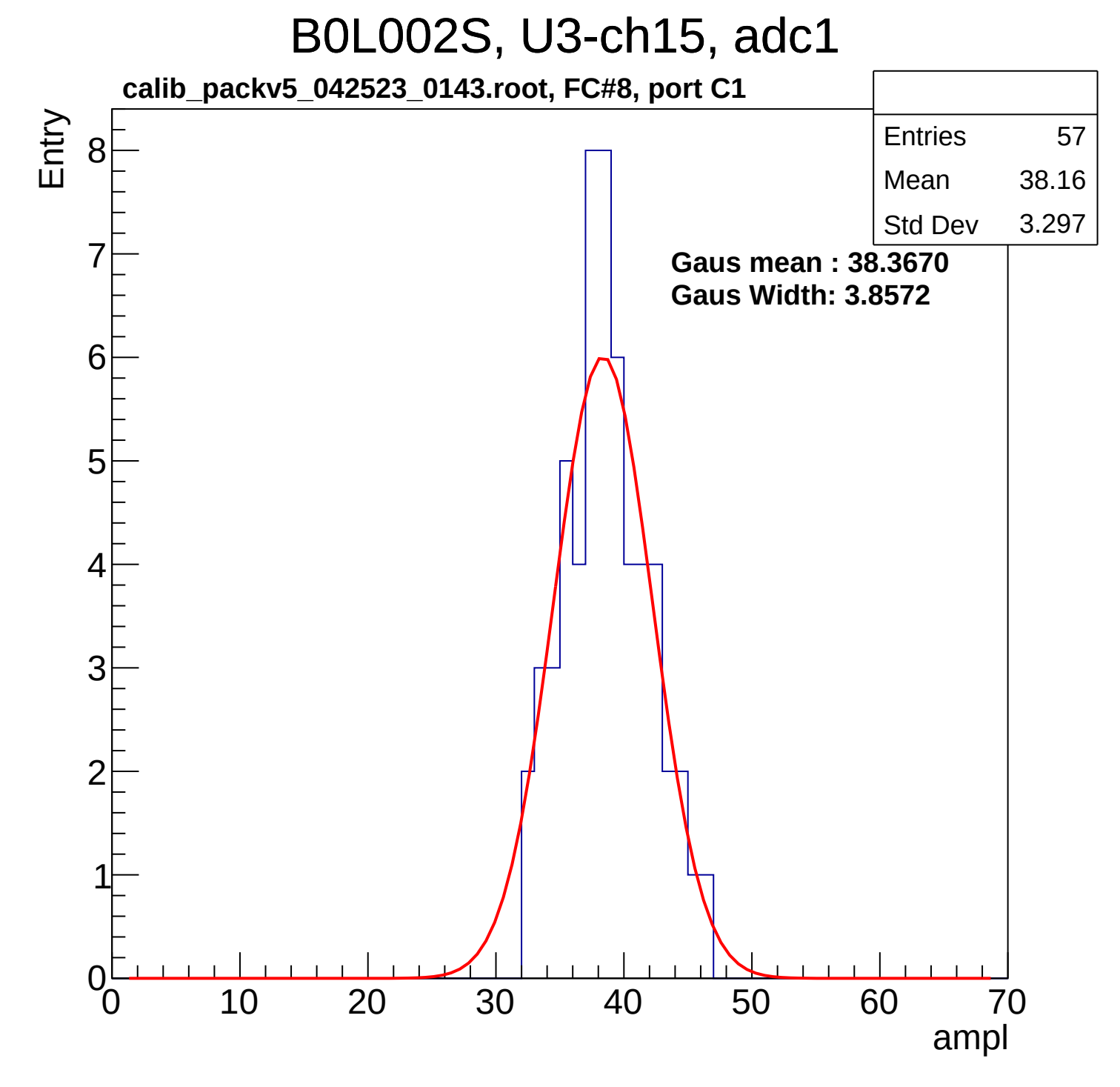
B0L002S, U3-ch15, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 38.16
Std Dev 3.297
Gaus mean : 38.3670
Gaus Width: 3.8572
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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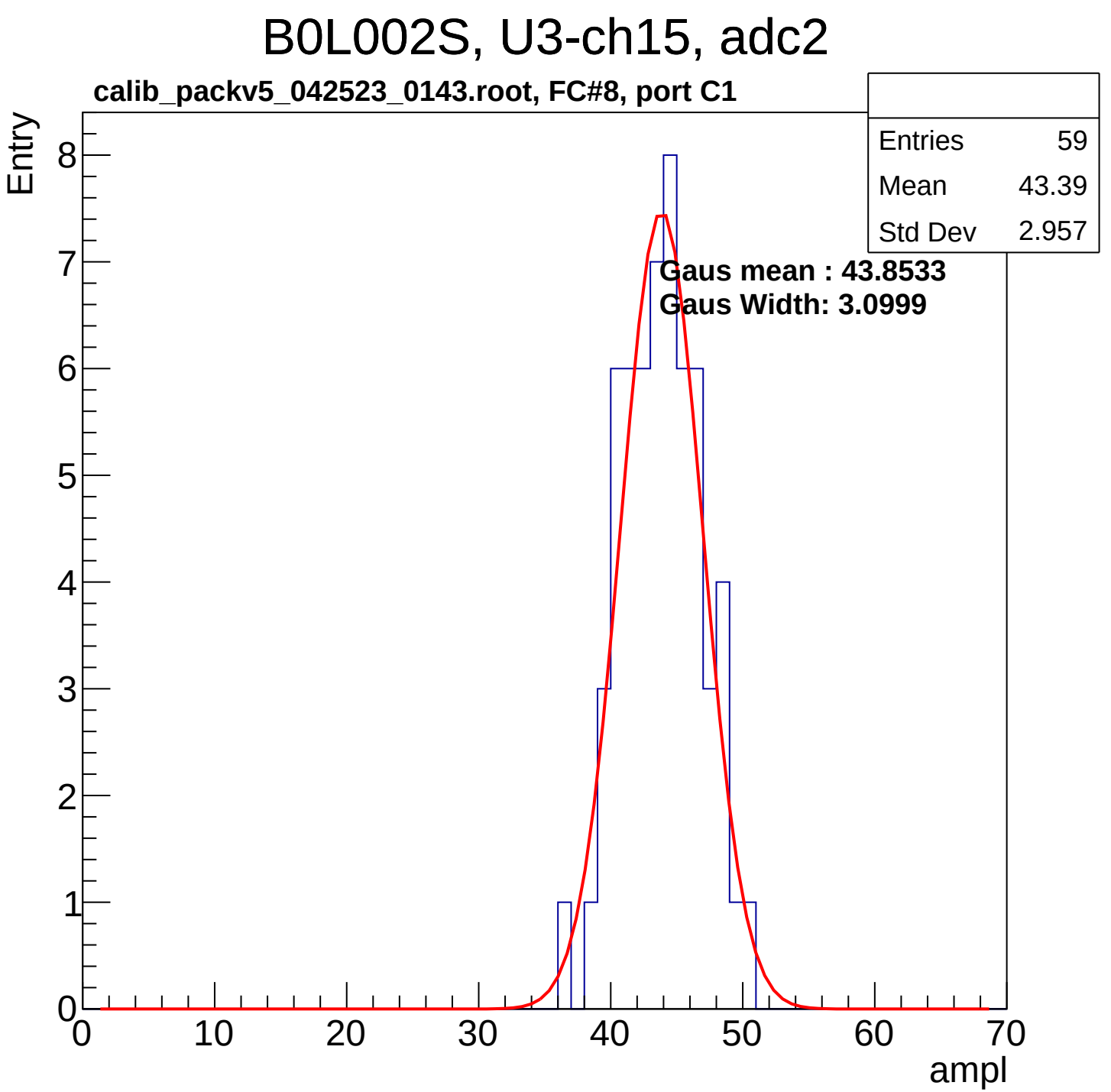
B0L002S, U3-ch15, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.39
Std Dev 2.957
Gaus mean : 43.8533
Gaus Width: 3.0999
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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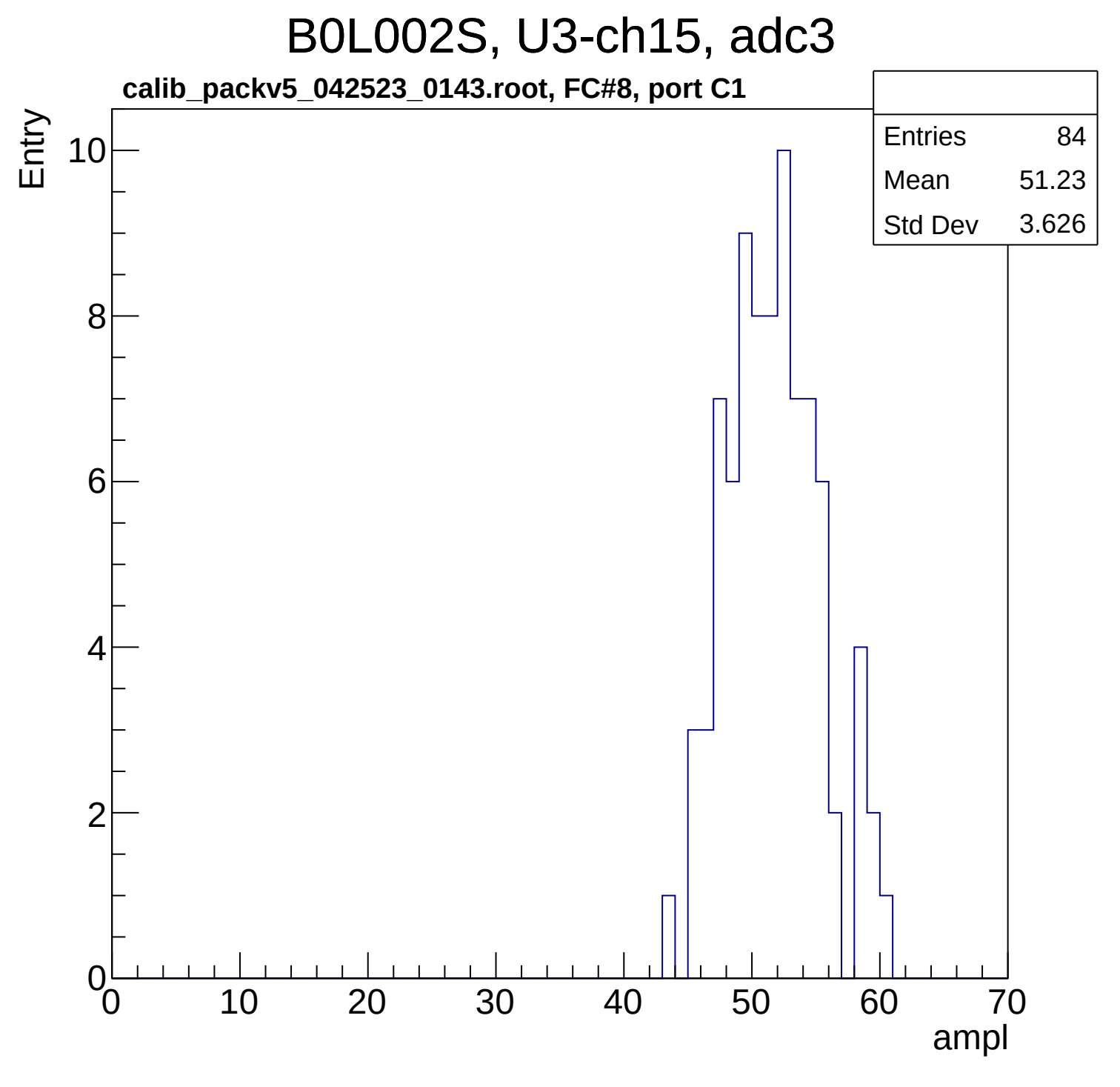
B0L002S, U3-ch15, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 51.23
Std Dev 3.626
Entry
ampl
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2
4
6
8
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0
10
20
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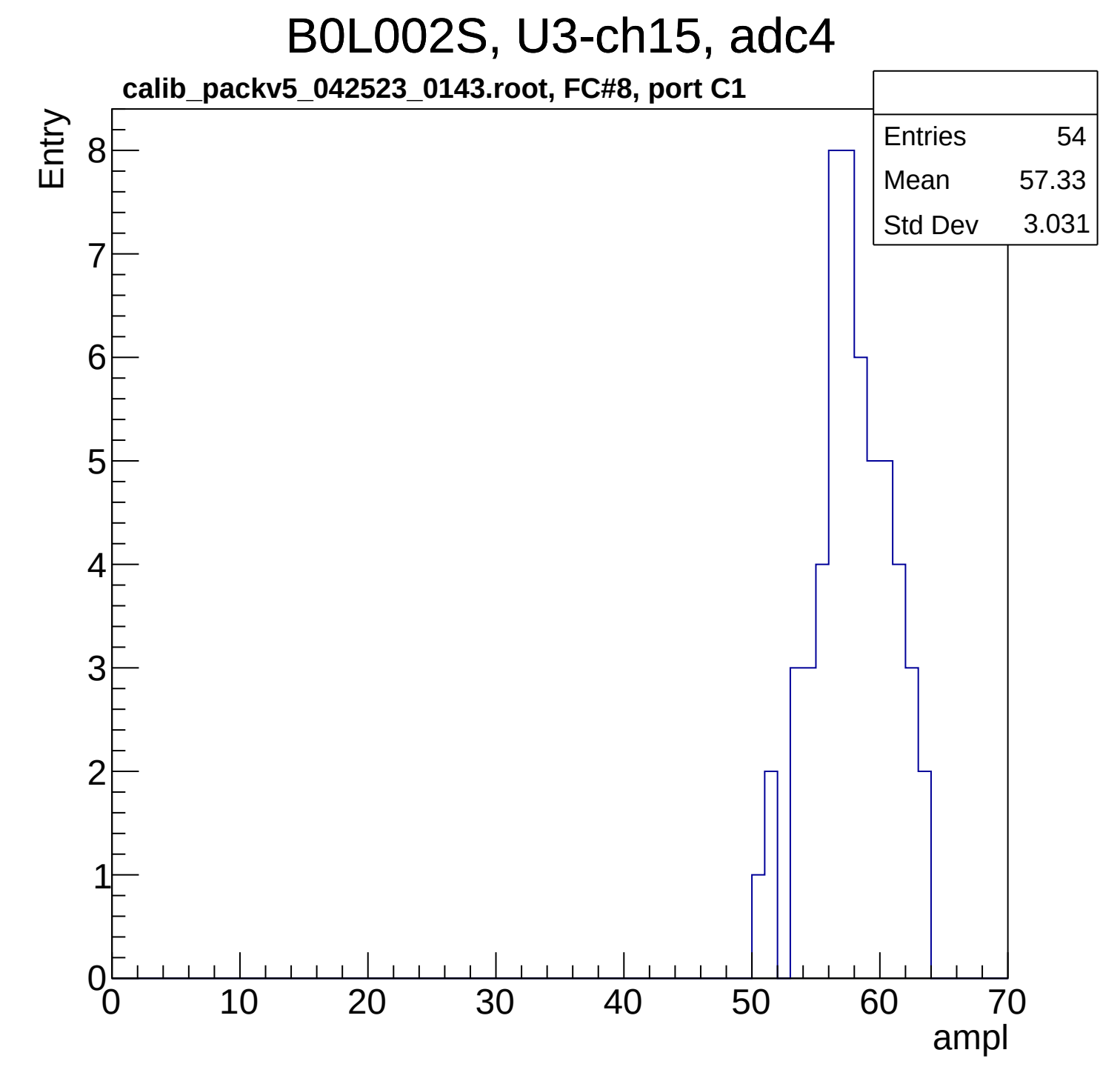
40
50
60
70


B0L002S, U3-ch15, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.33
Std Dev 3.031
Entry
ampl
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1
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4
5
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60
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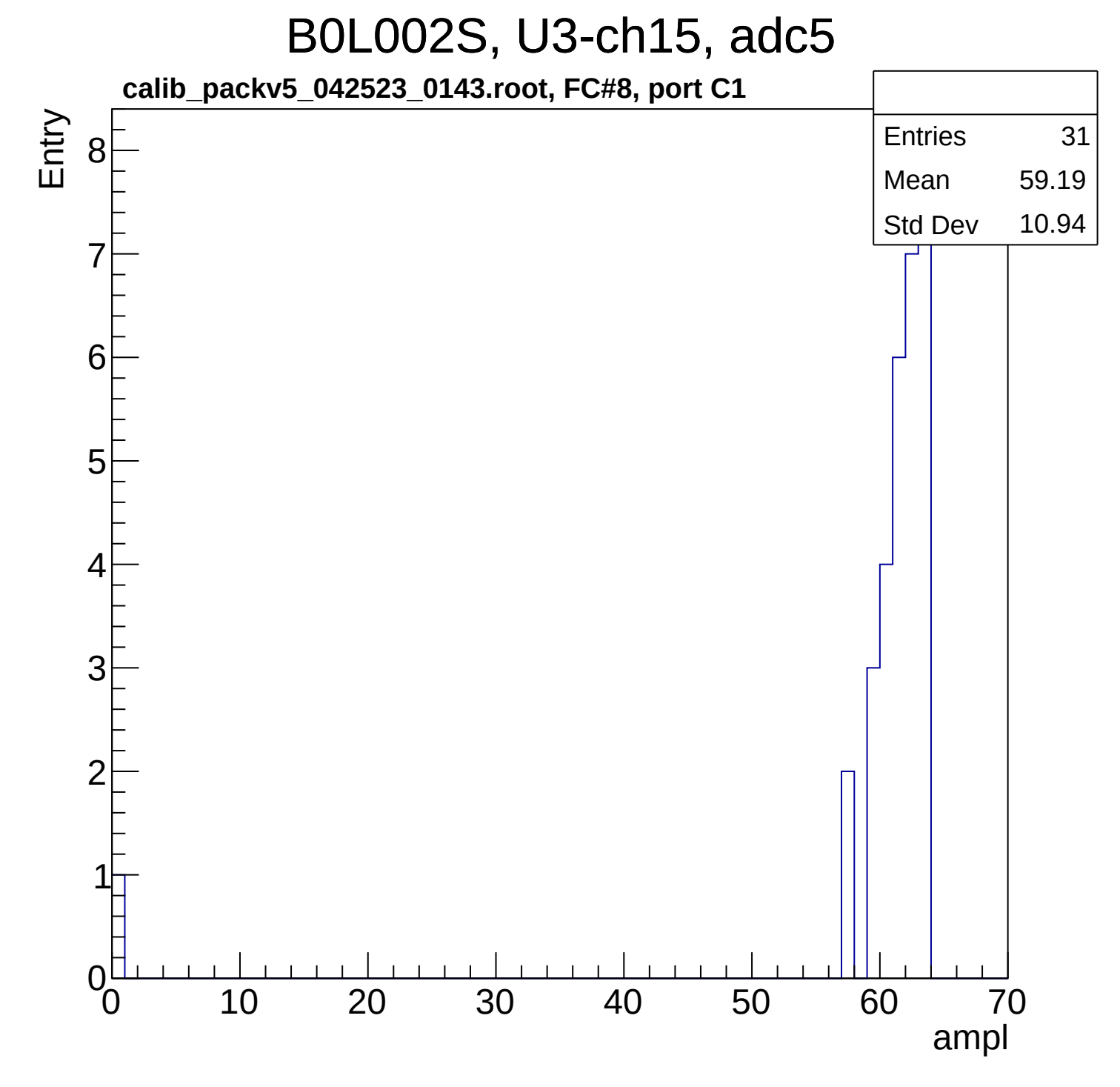
B0L002S, U3-ch15, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.19
Std Dev 10.94
Entry
ampl
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1
2
3
4
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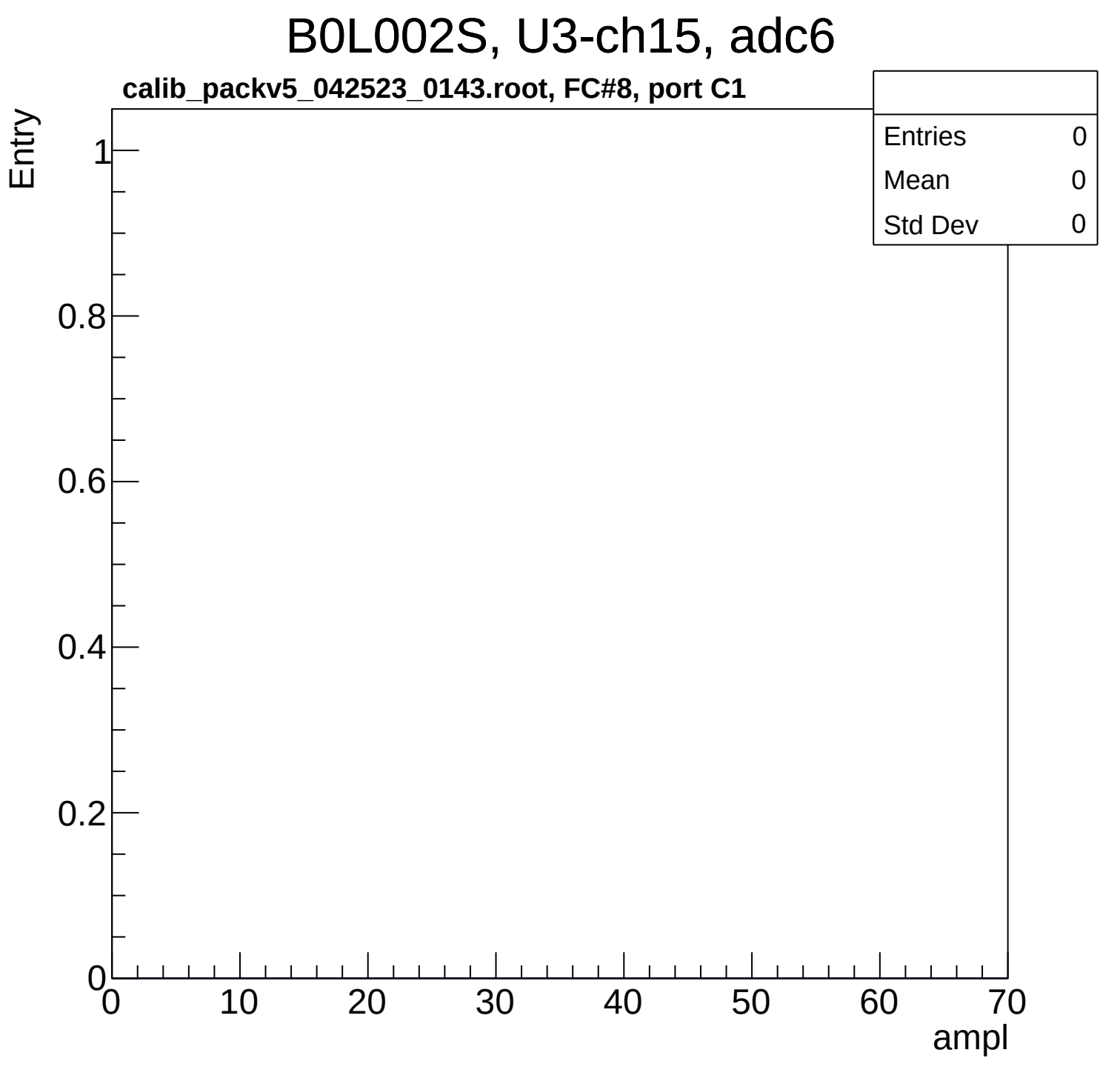
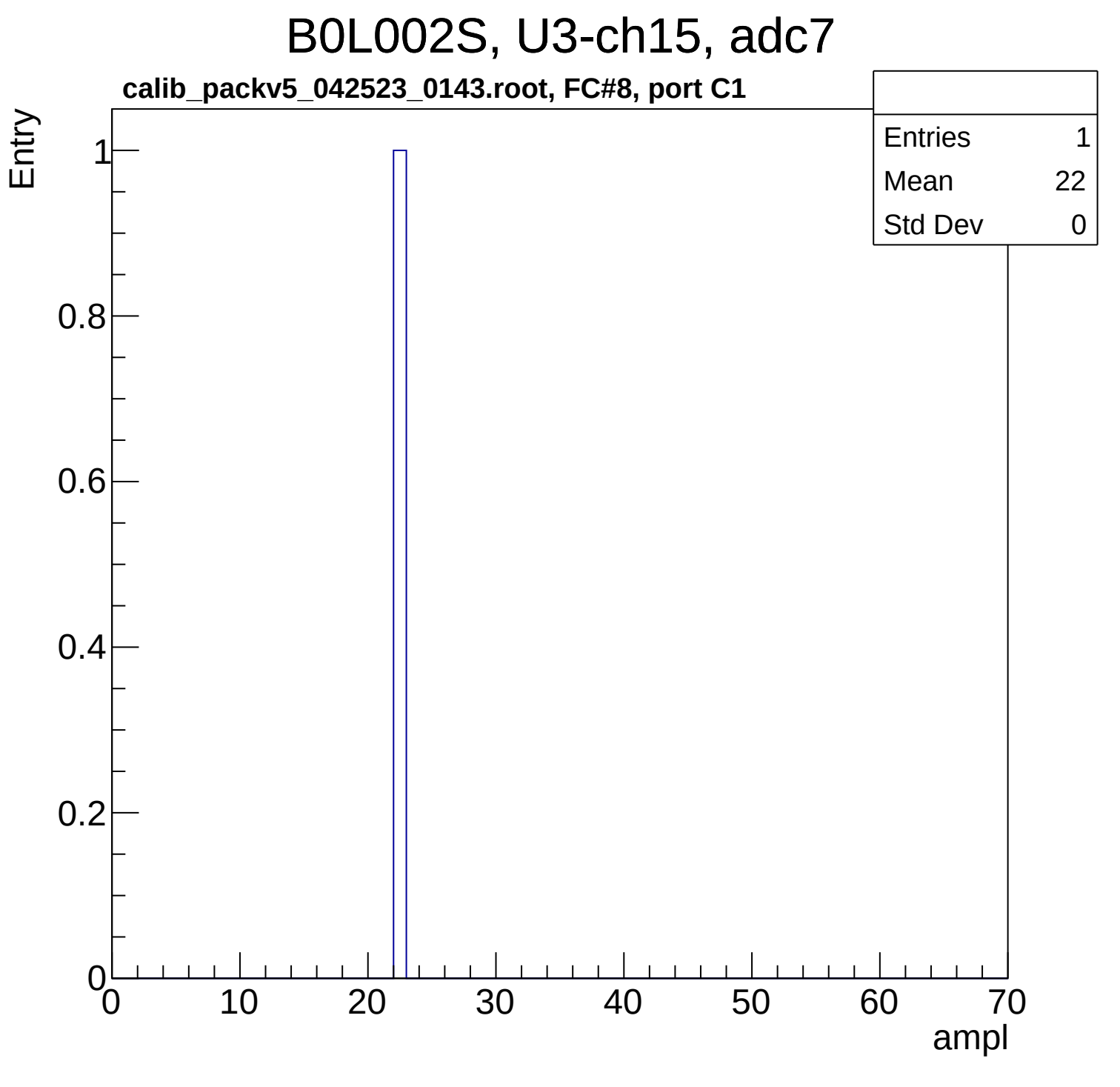

B0L002S, U3-ch15, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch15, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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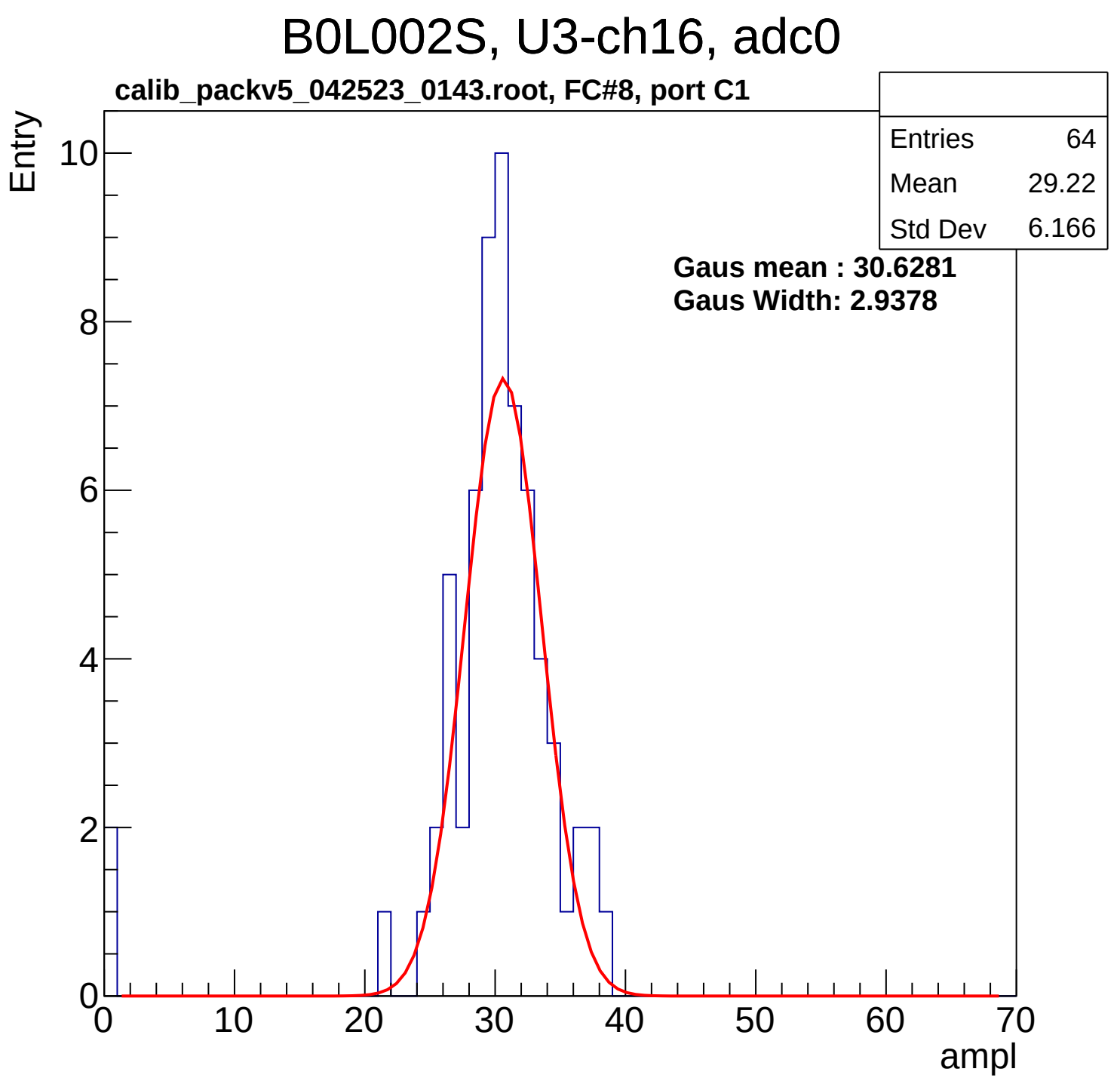
50
60
70
ampl


B0L002S, U3-ch16, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.22
Std Dev 6.166
Gaus mean : 30.6281
Gaus Width: 2.9378
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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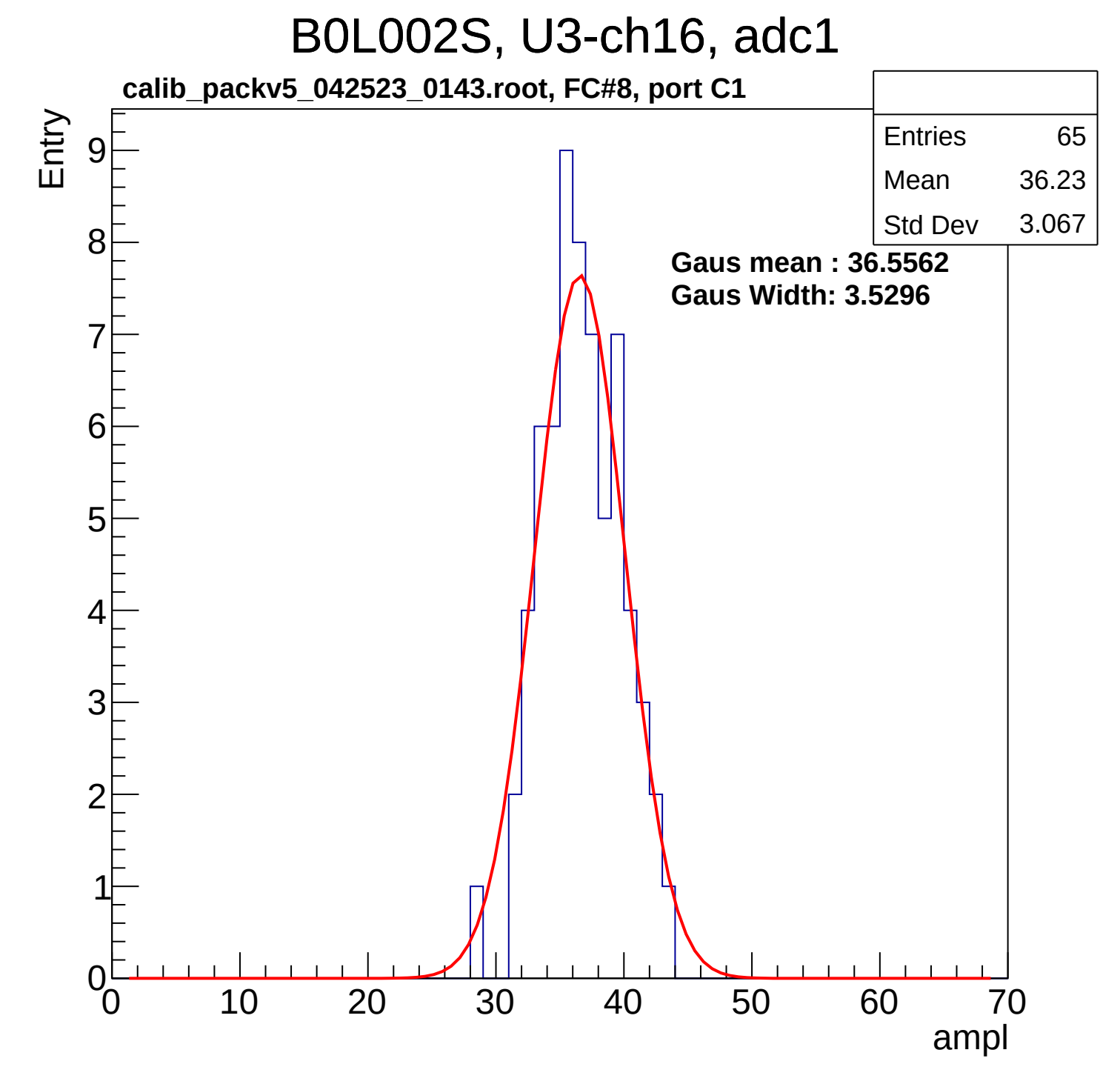
B0L002S, U3-ch16, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.23
Std Dev 3.067
Gaus mean : 36.5562
Gaus Width: 3.5296
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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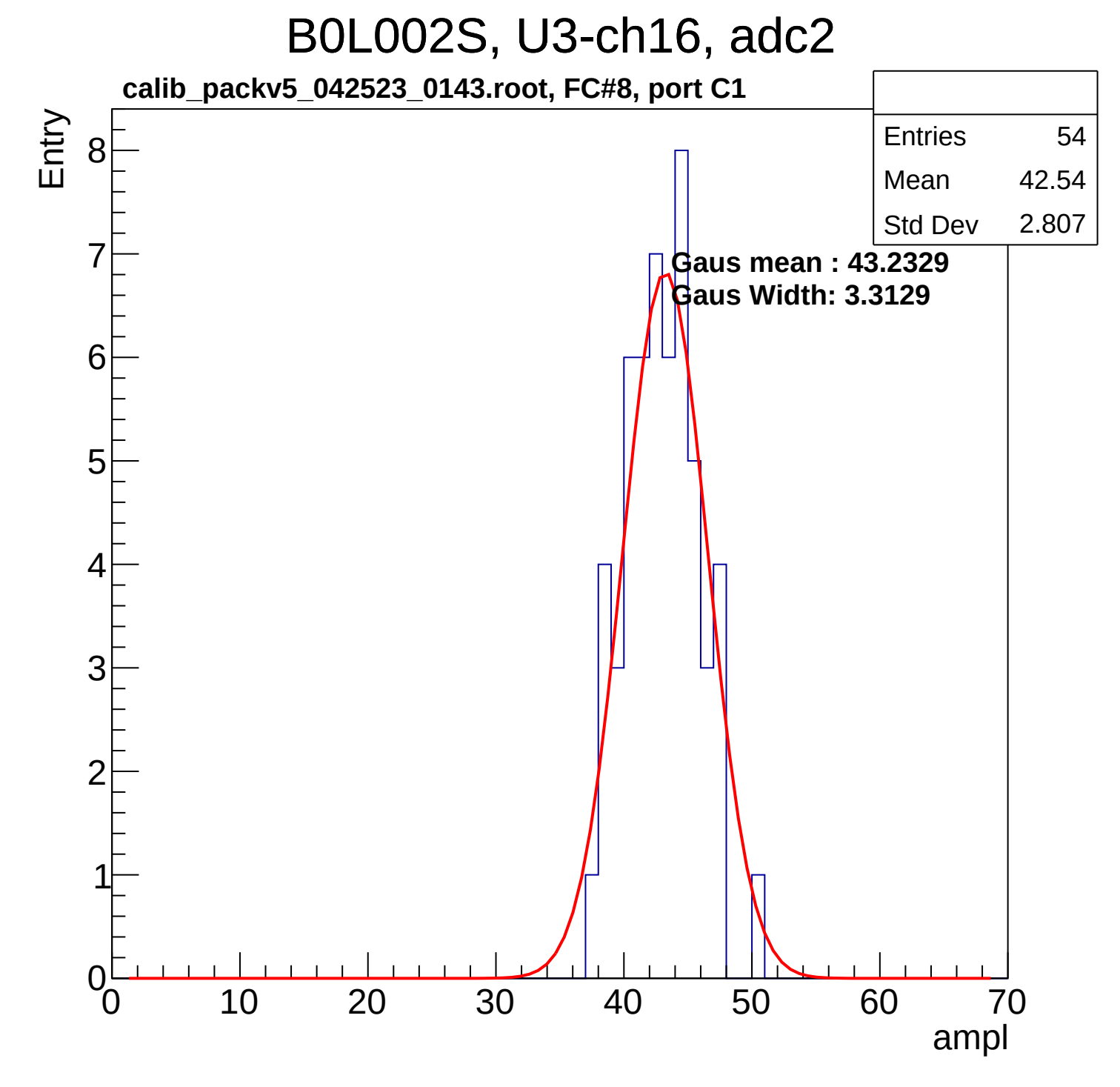
B0L002S, U3-ch16, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 42.54
Std Dev 2.807
Gaus mean : 43.2329
Gaus Width: 3.3129
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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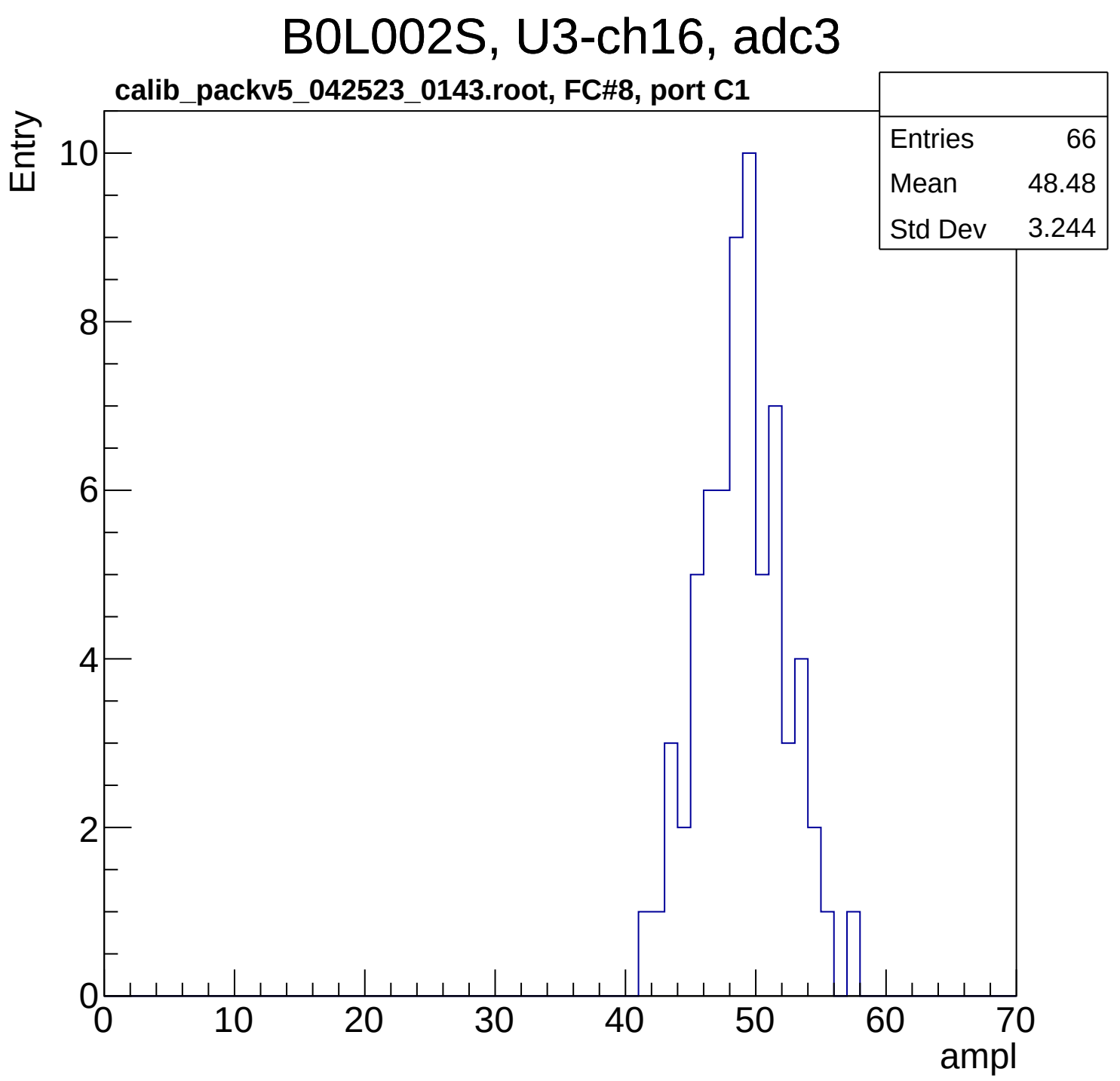
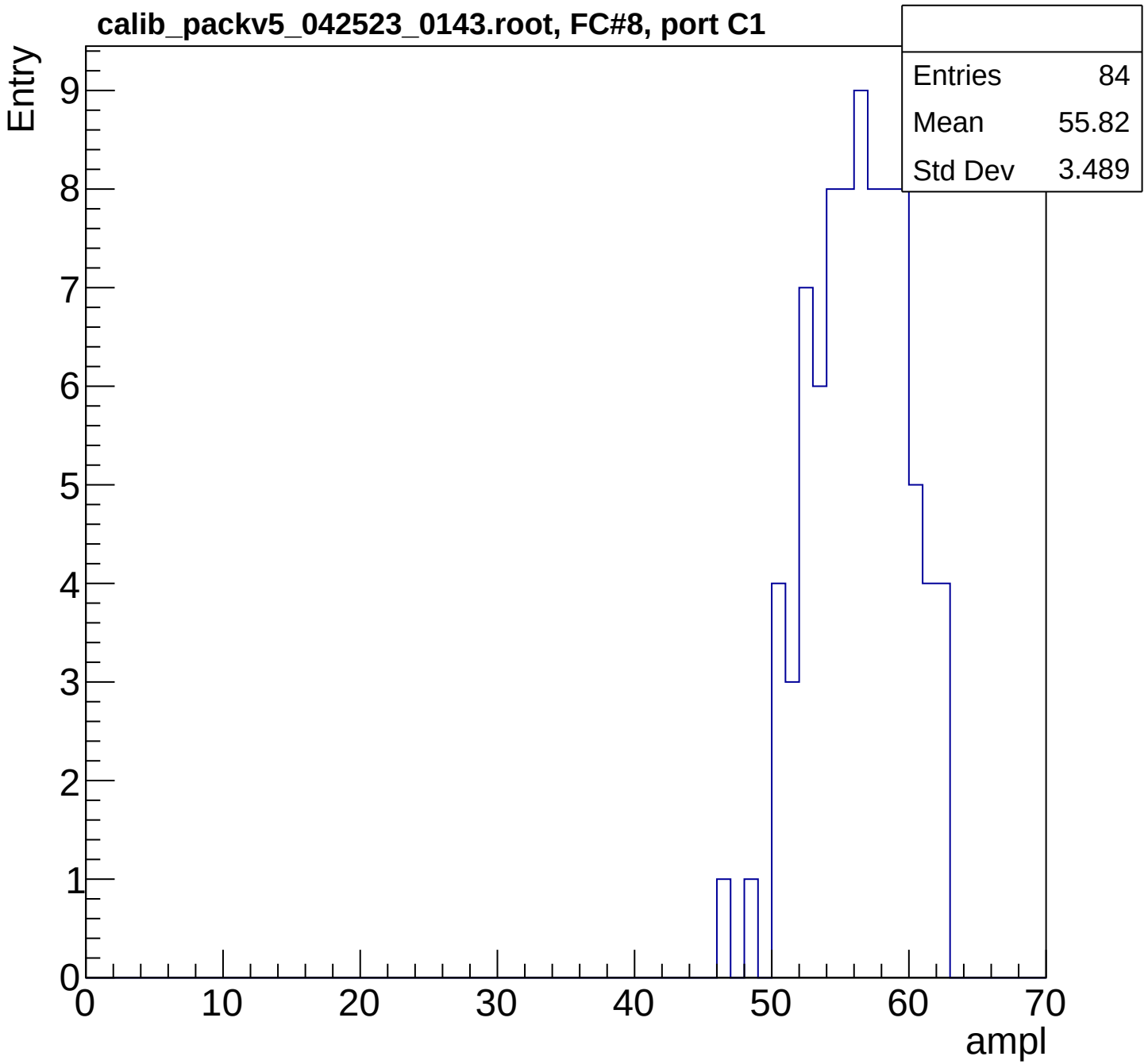

B0L002S, U3-ch16, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 48.48
Std Dev 3.244
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch16, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 55.82
Std Dev 3.489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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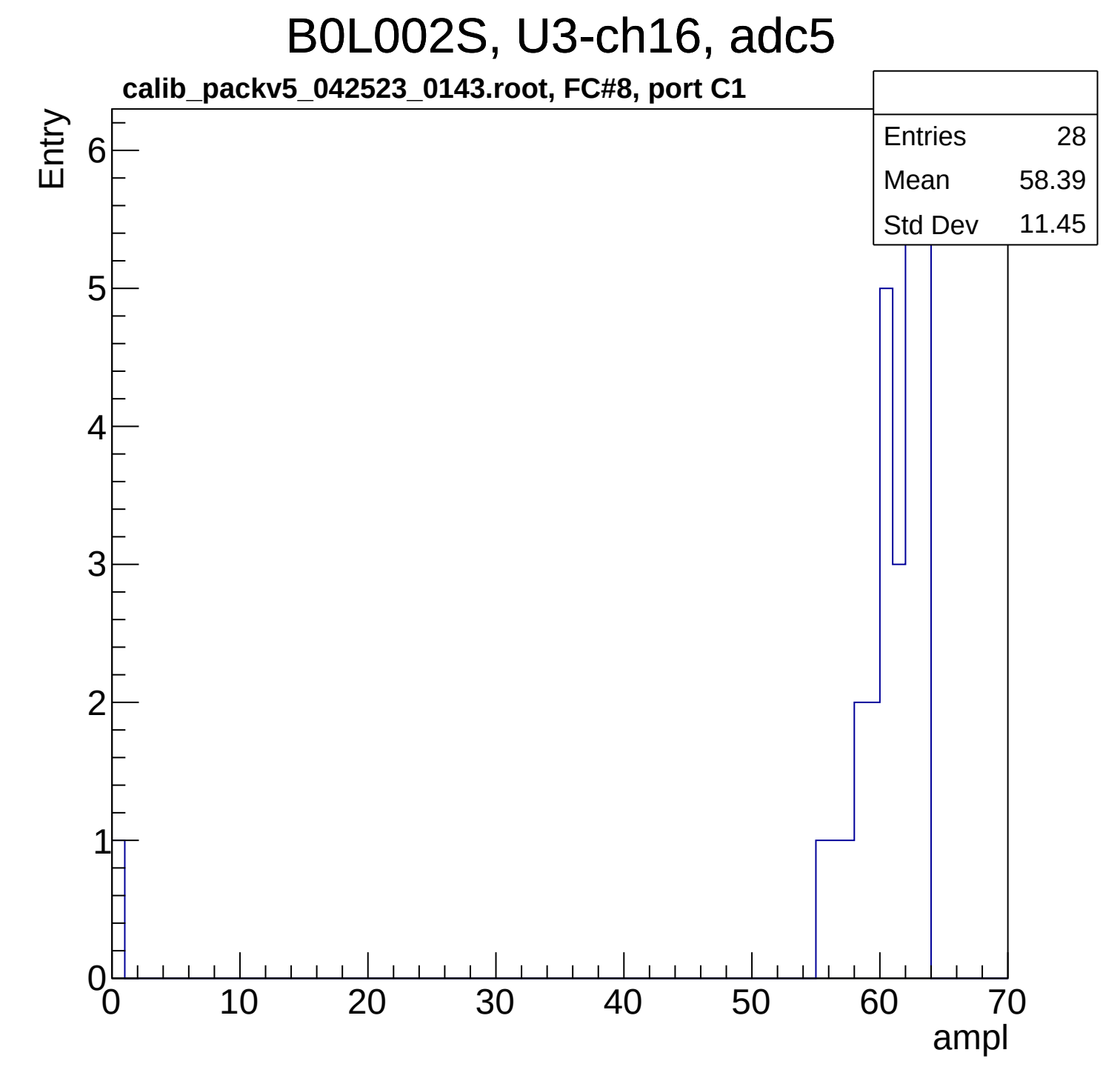
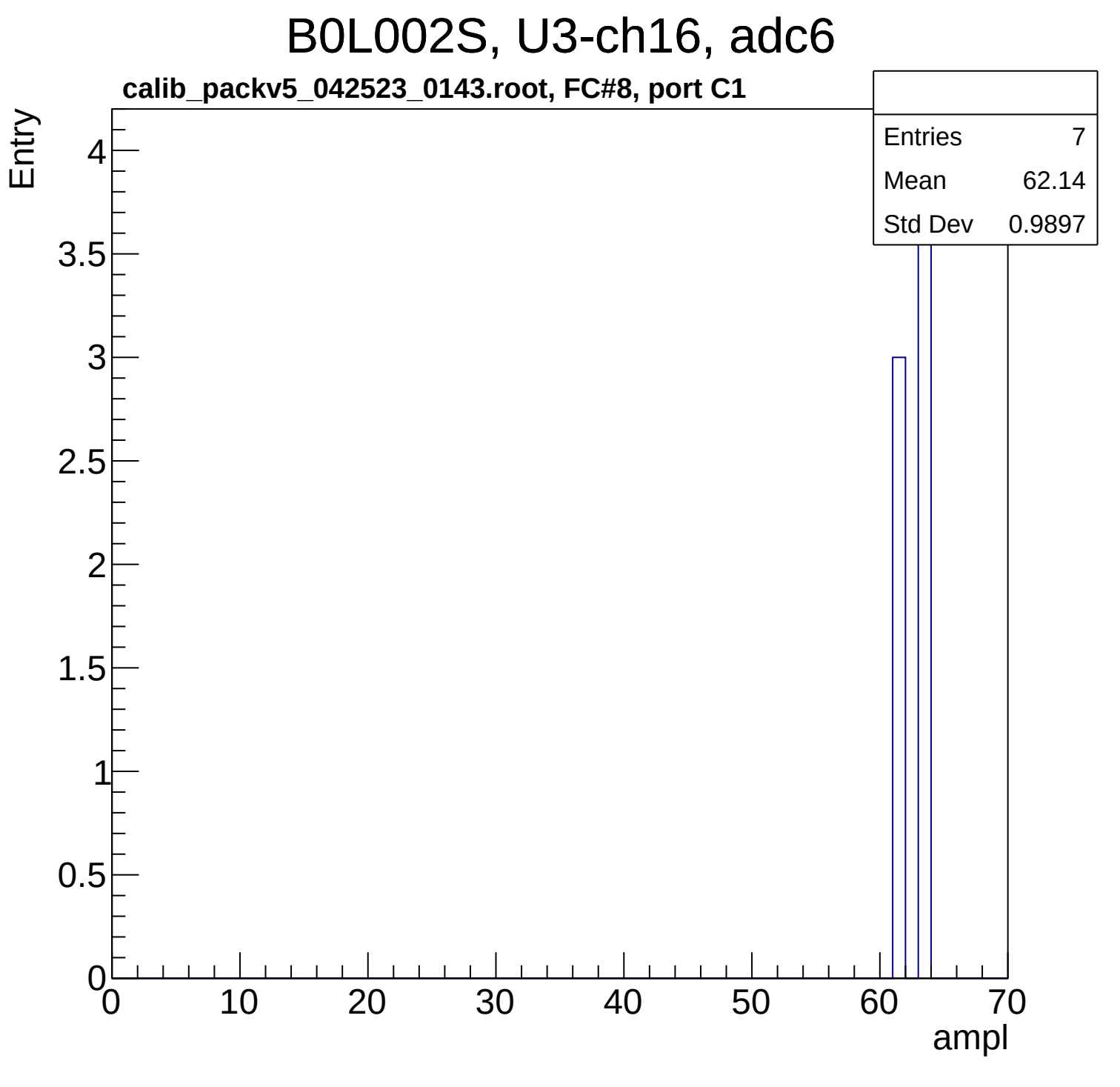
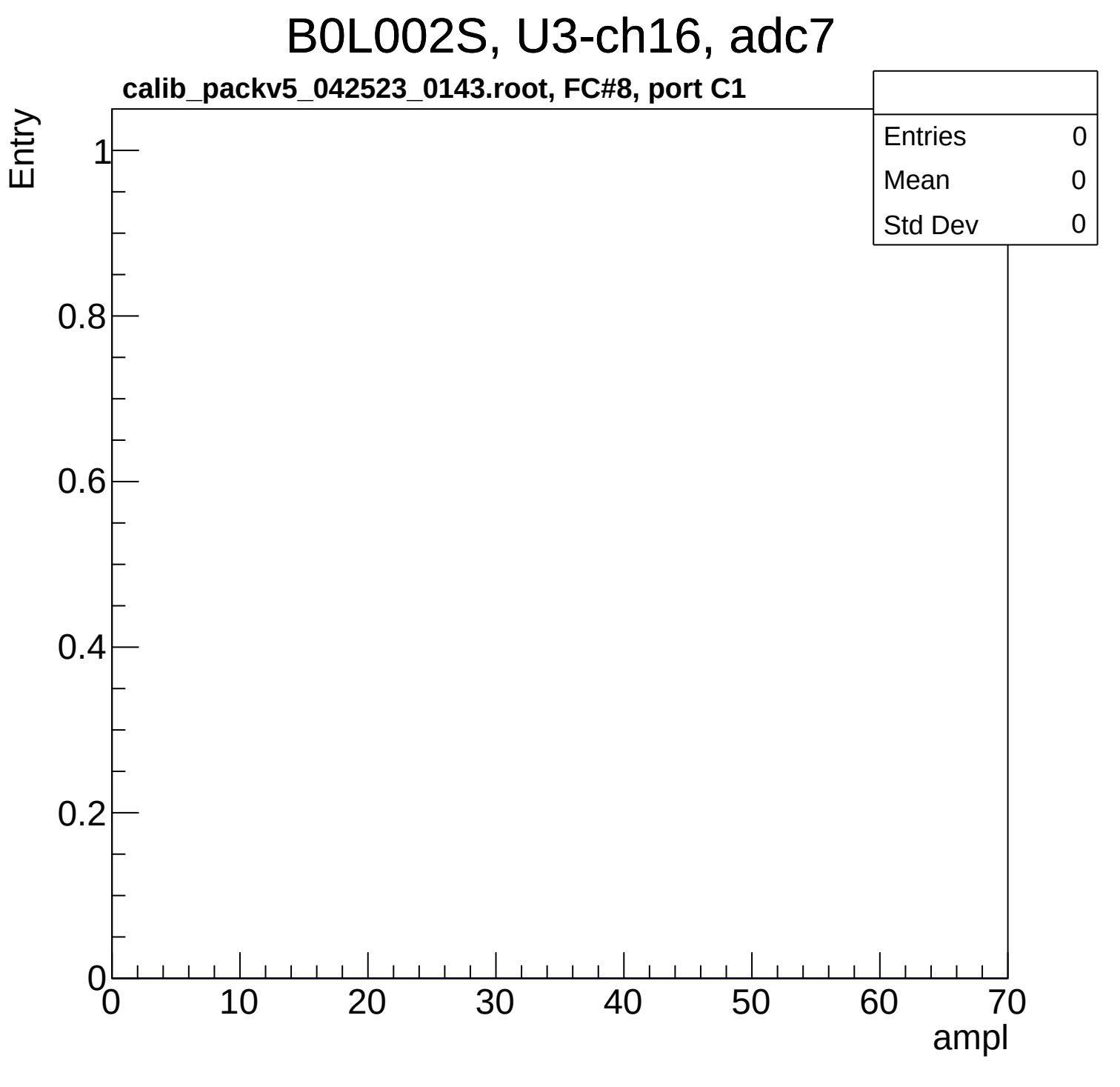
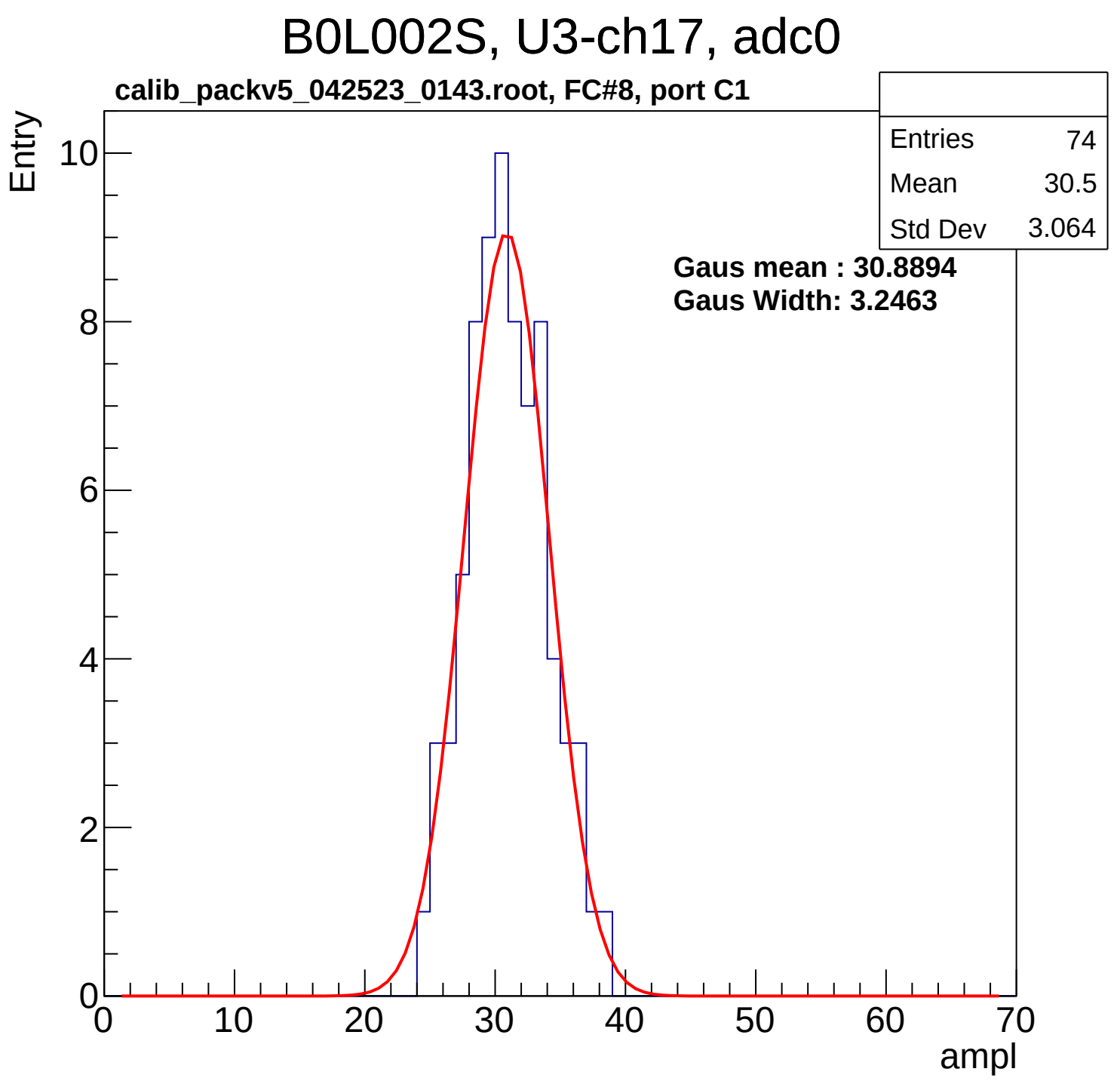
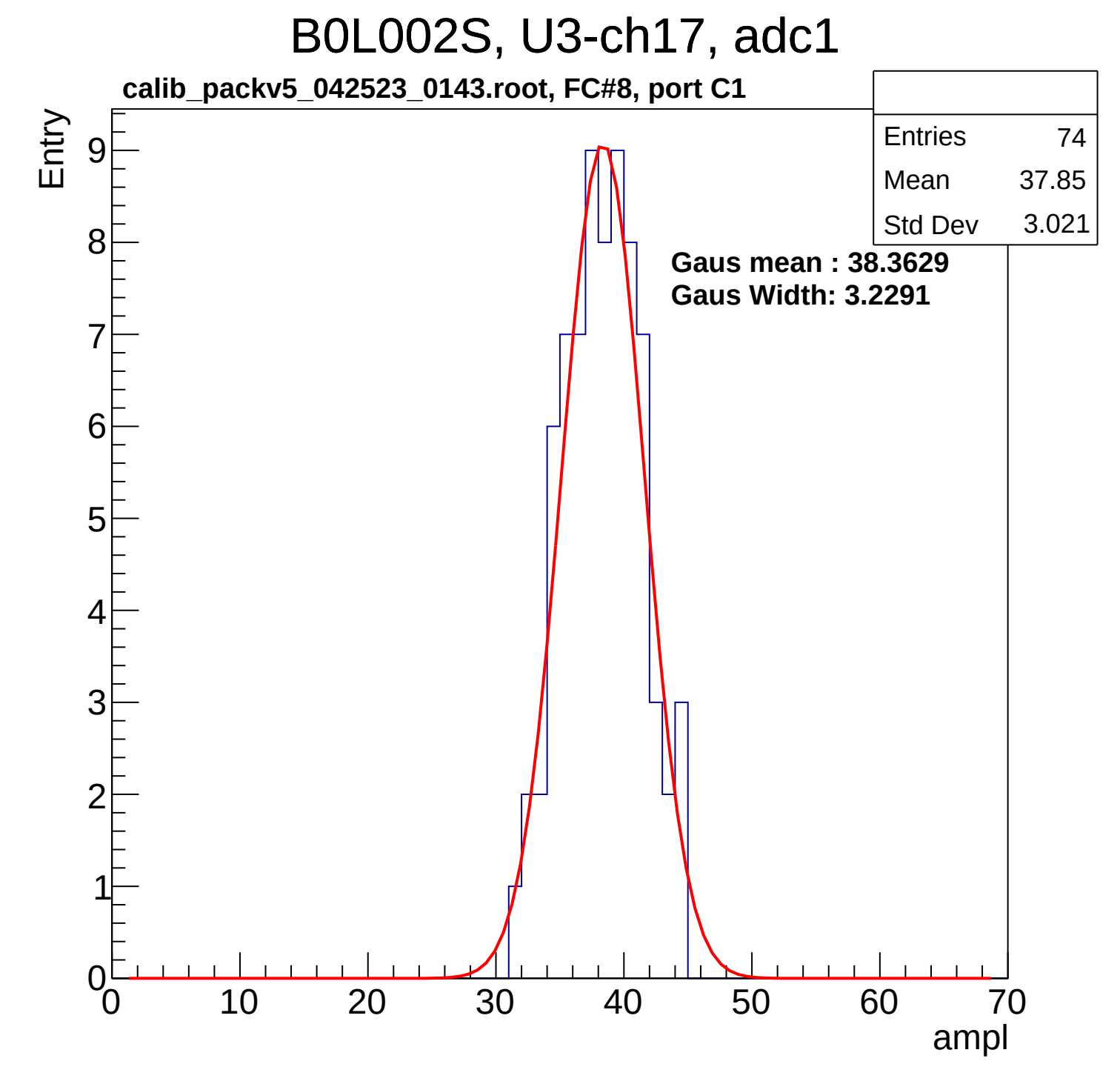
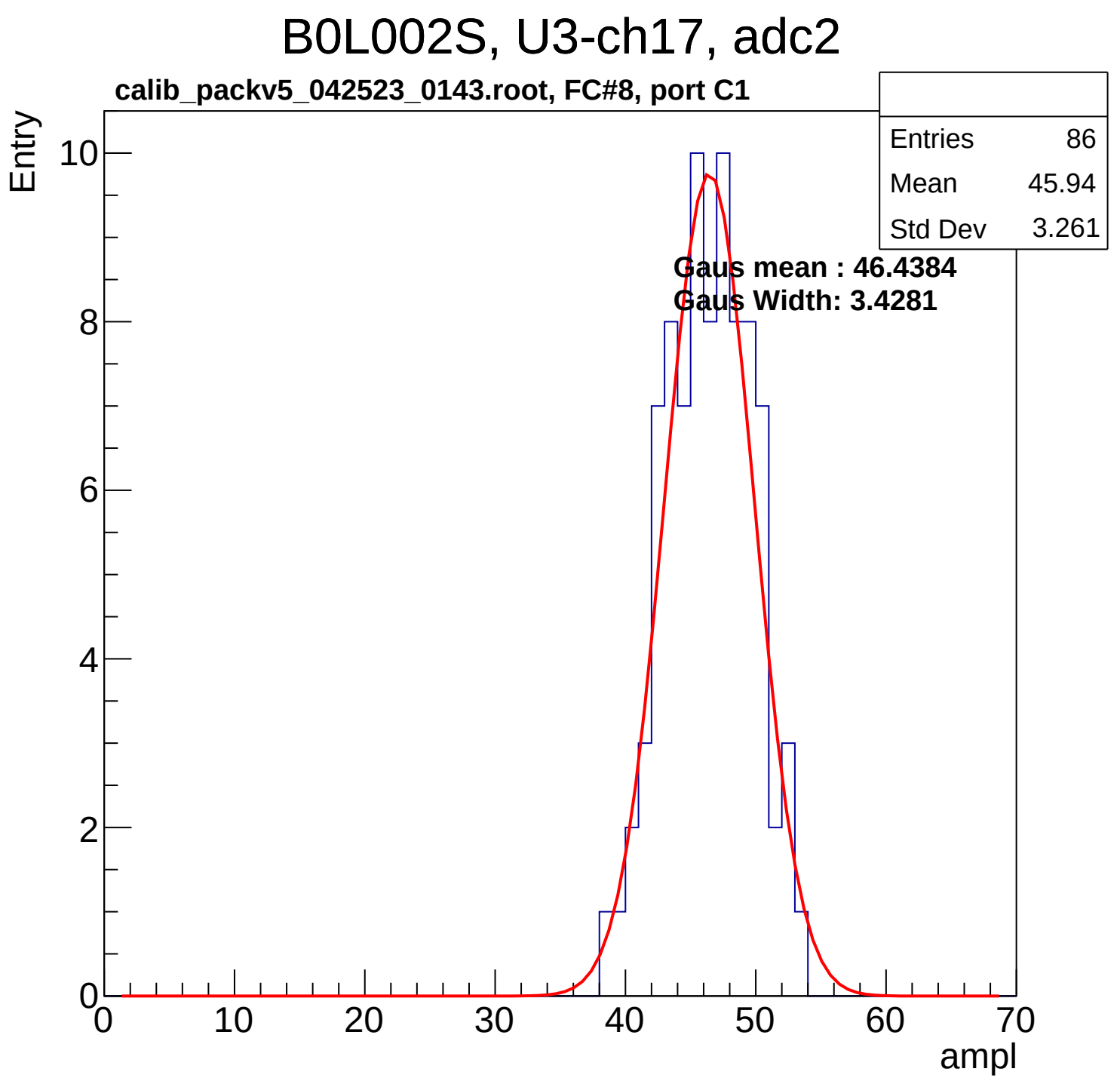

B0L002S, U3-ch16, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.39
Std Dev 11.45
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

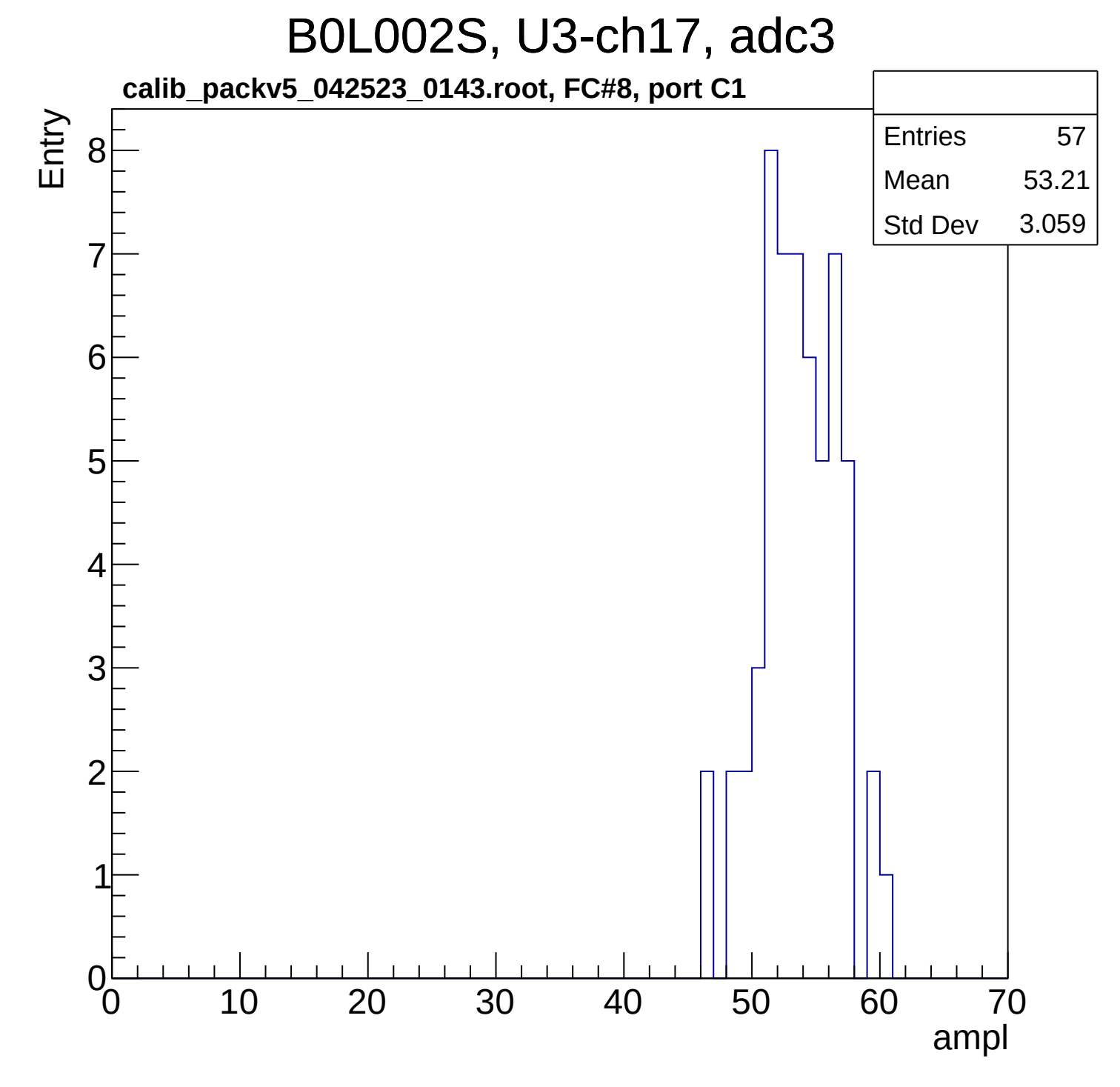

B0L002S, U3-ch16, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

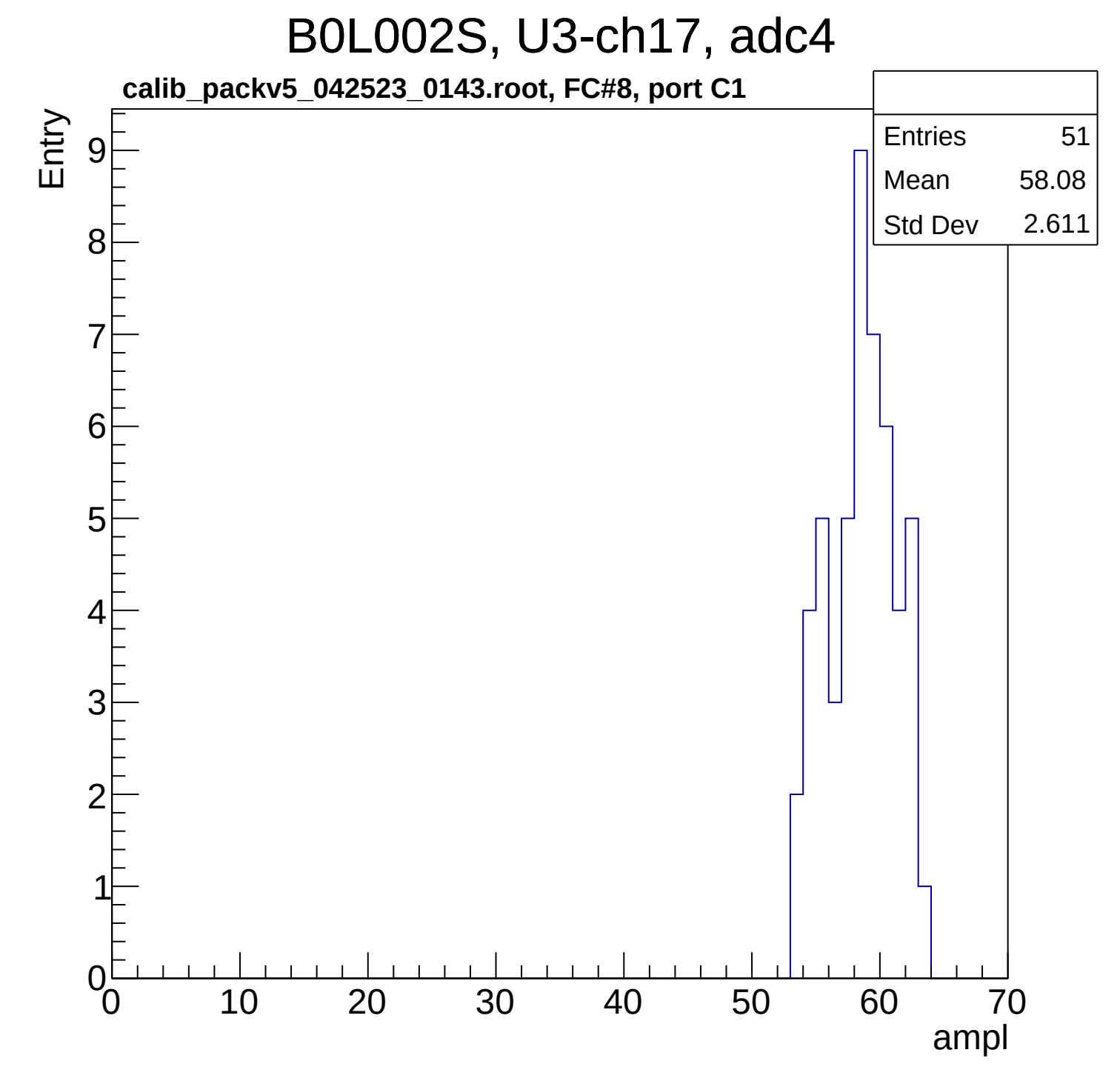

B0L002S, U3-ch16, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

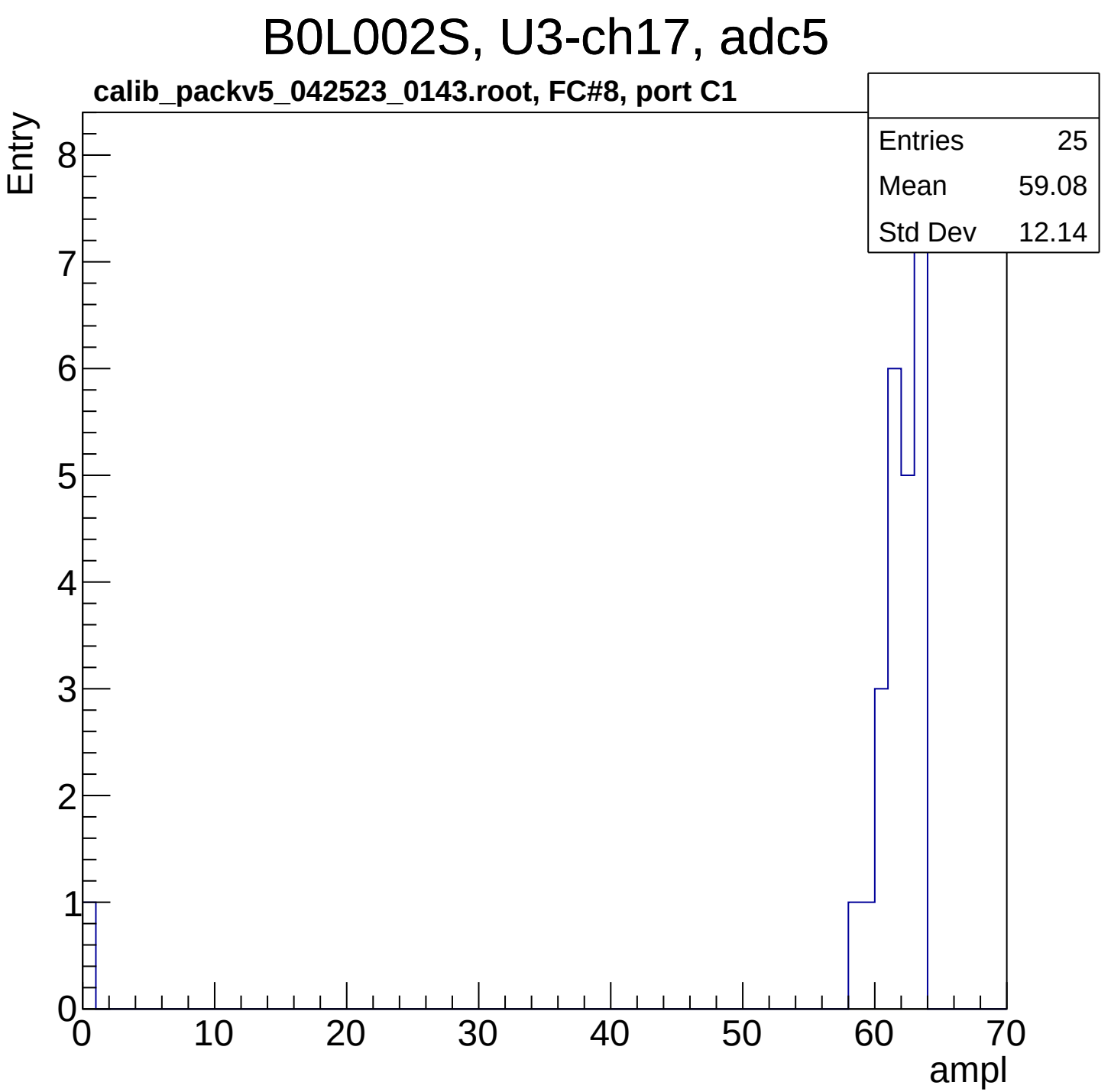

B0L002S, U3-ch17, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 30.5
Std Dev 3.064
Gaus mean : 30.8894
Gaus Width: 3.2463
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch17, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 37.85
Std Dev 3.021
Gaus mean : 38.3629
Gaus Width: 3.2291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch17, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 45.94
Std Dev 3.261
Gaus mean : 46.4384
Gaus Width: 3.4281
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch17, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 53.21
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch17, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 58.08
Std Dev 2.611
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch17, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 59.08
Std Dev 12.14
Entry
0
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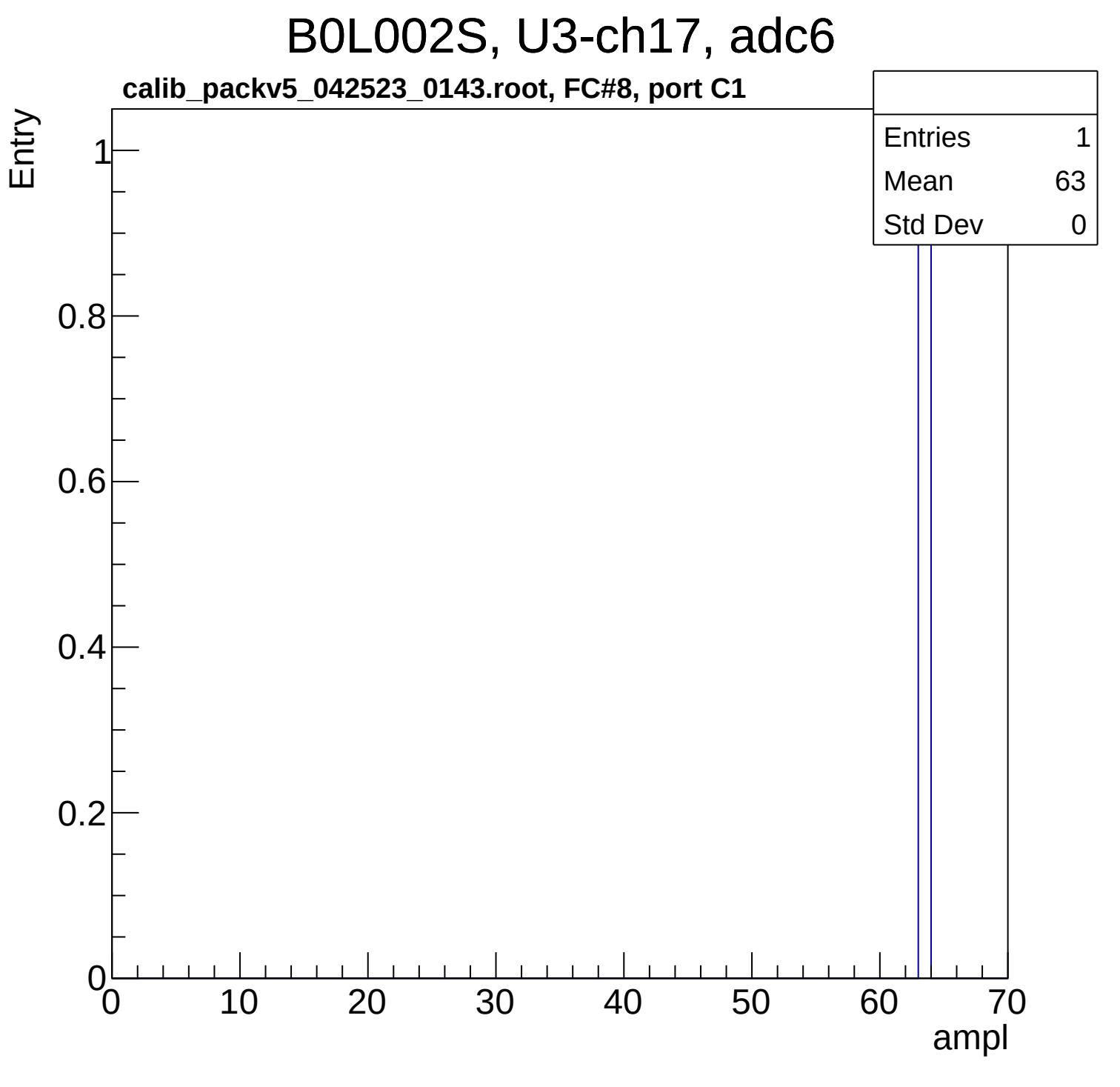
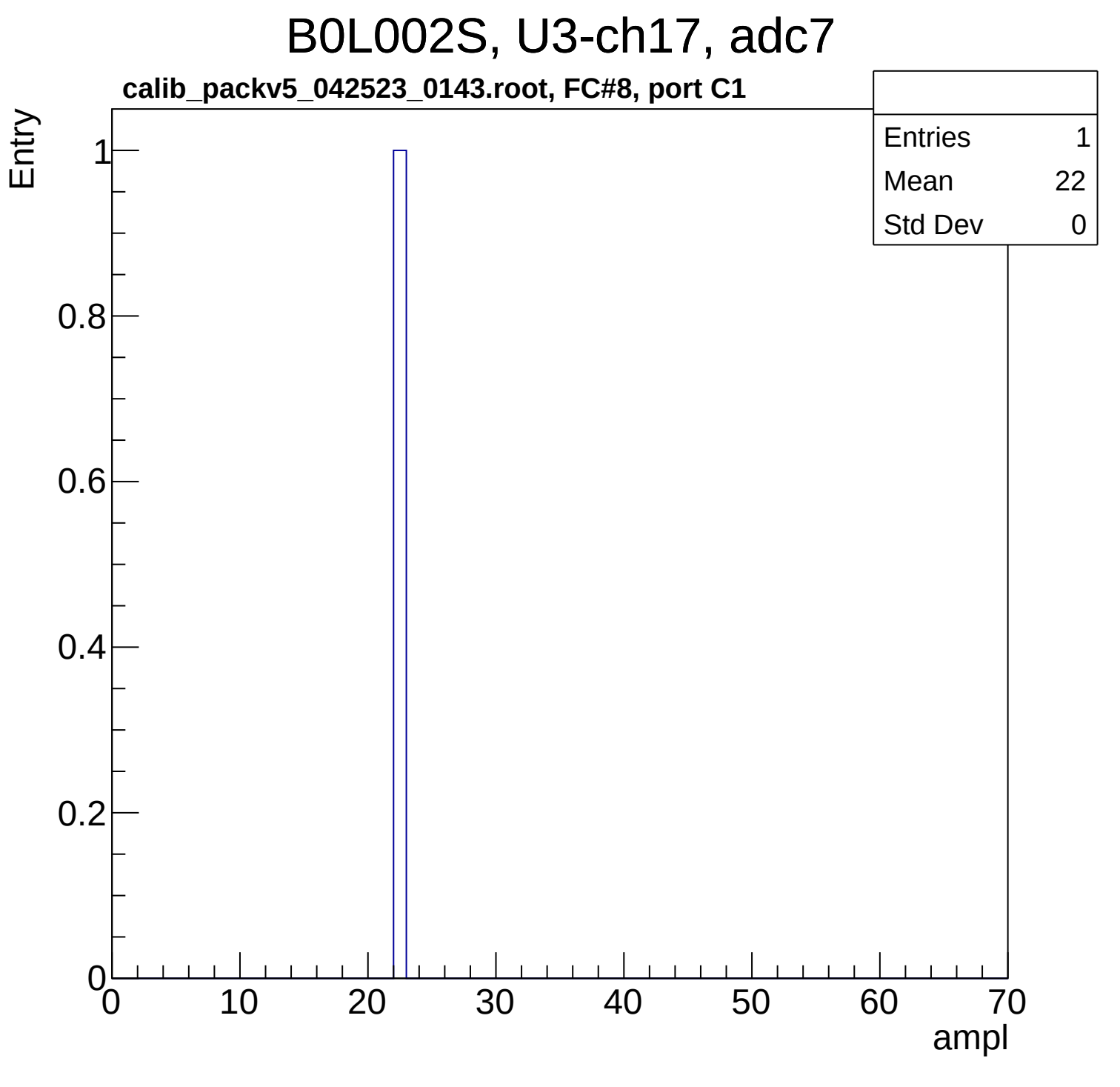
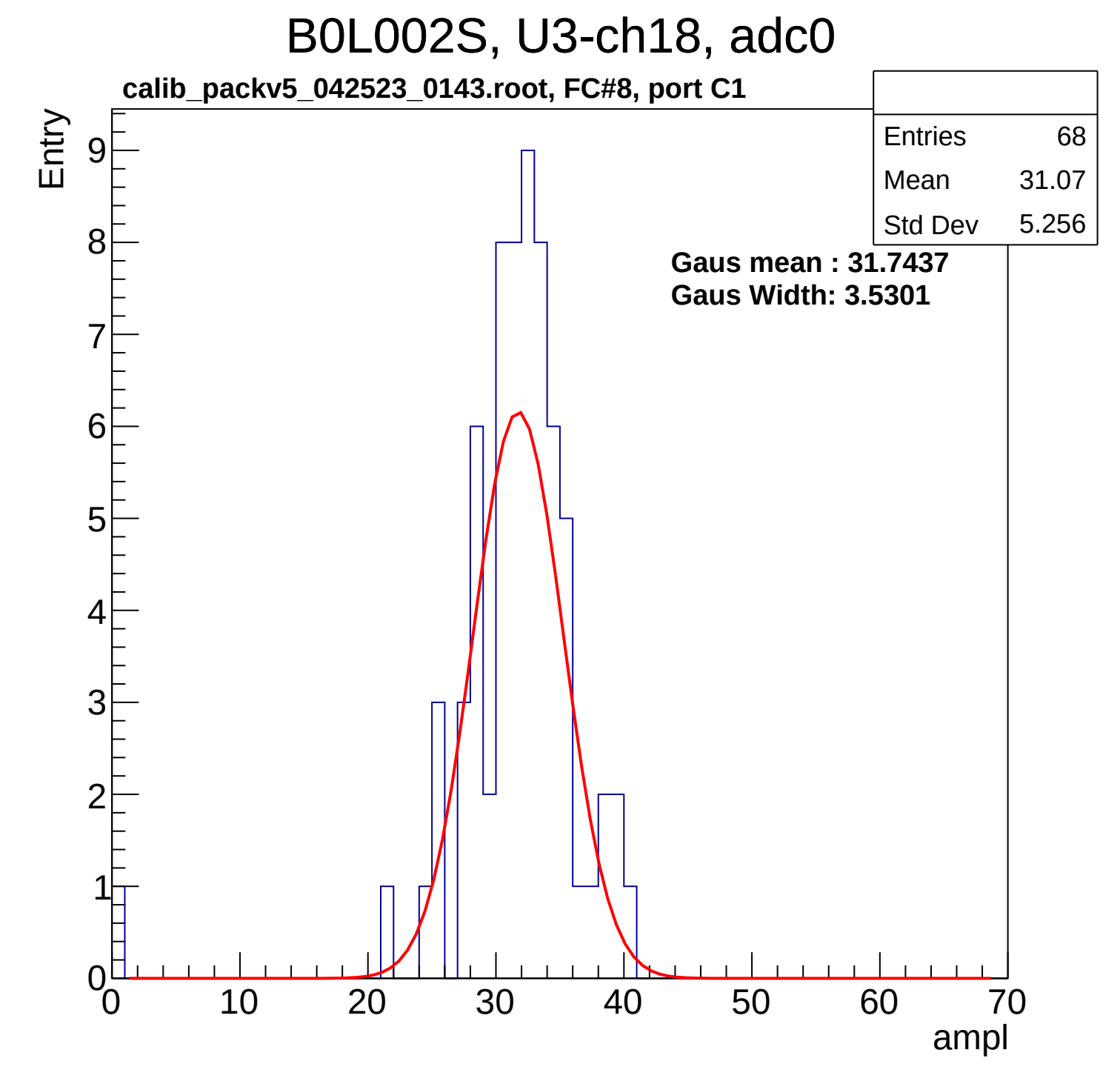
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch17, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch17, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch18, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31.07
Std Dev 5.256
Gaus mean : 31.7437
Gaus Width: 3.5301
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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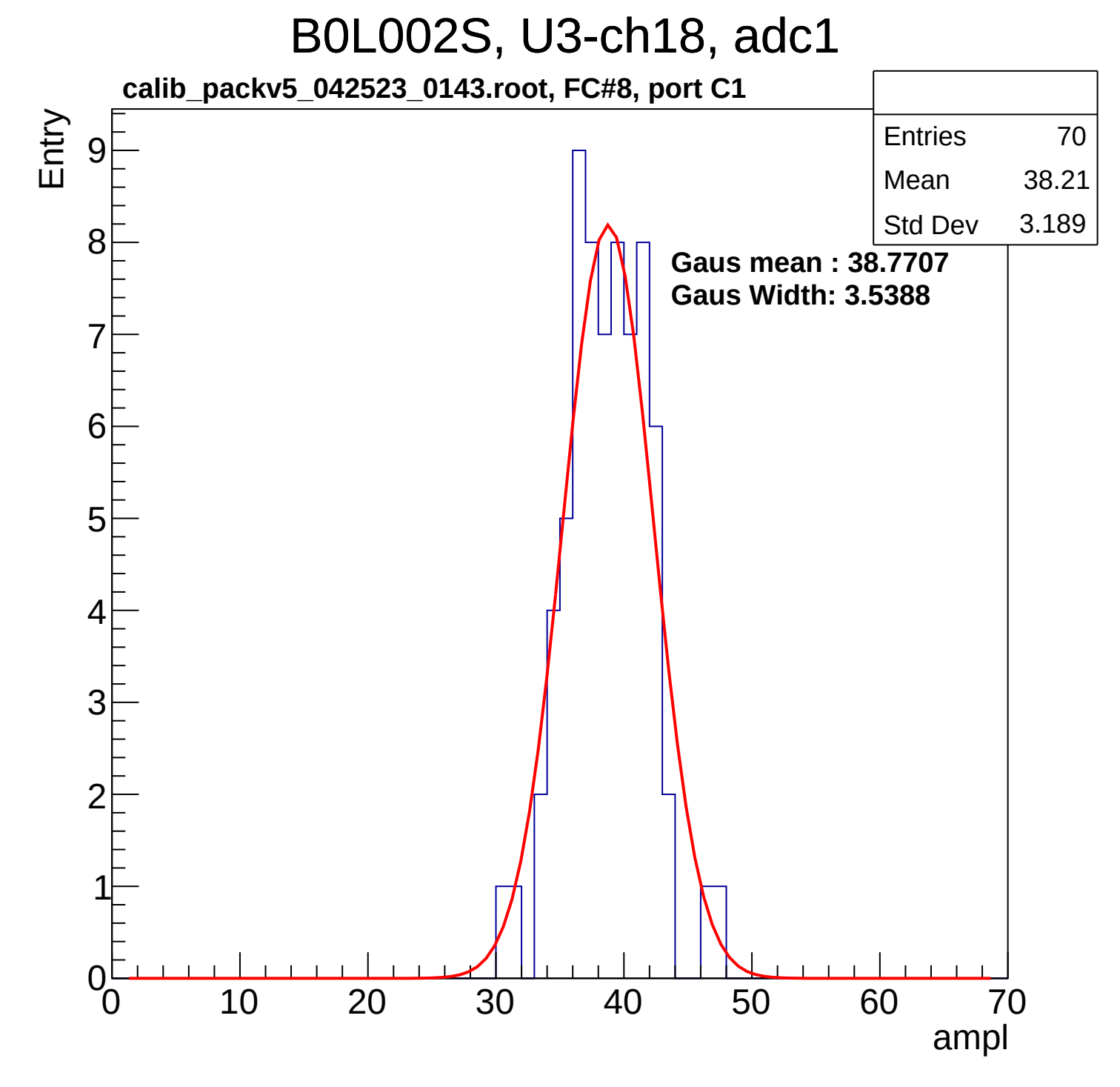
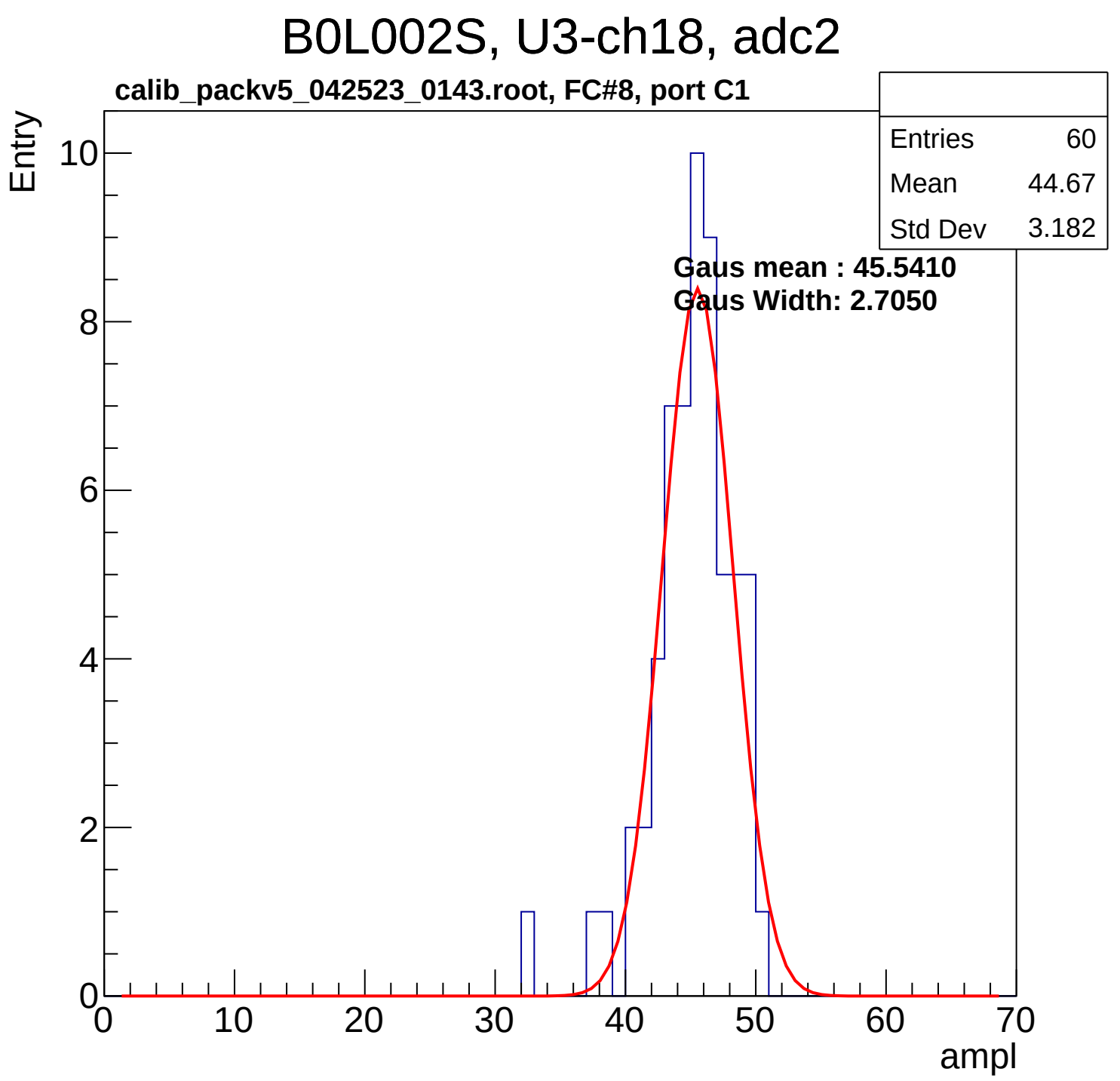
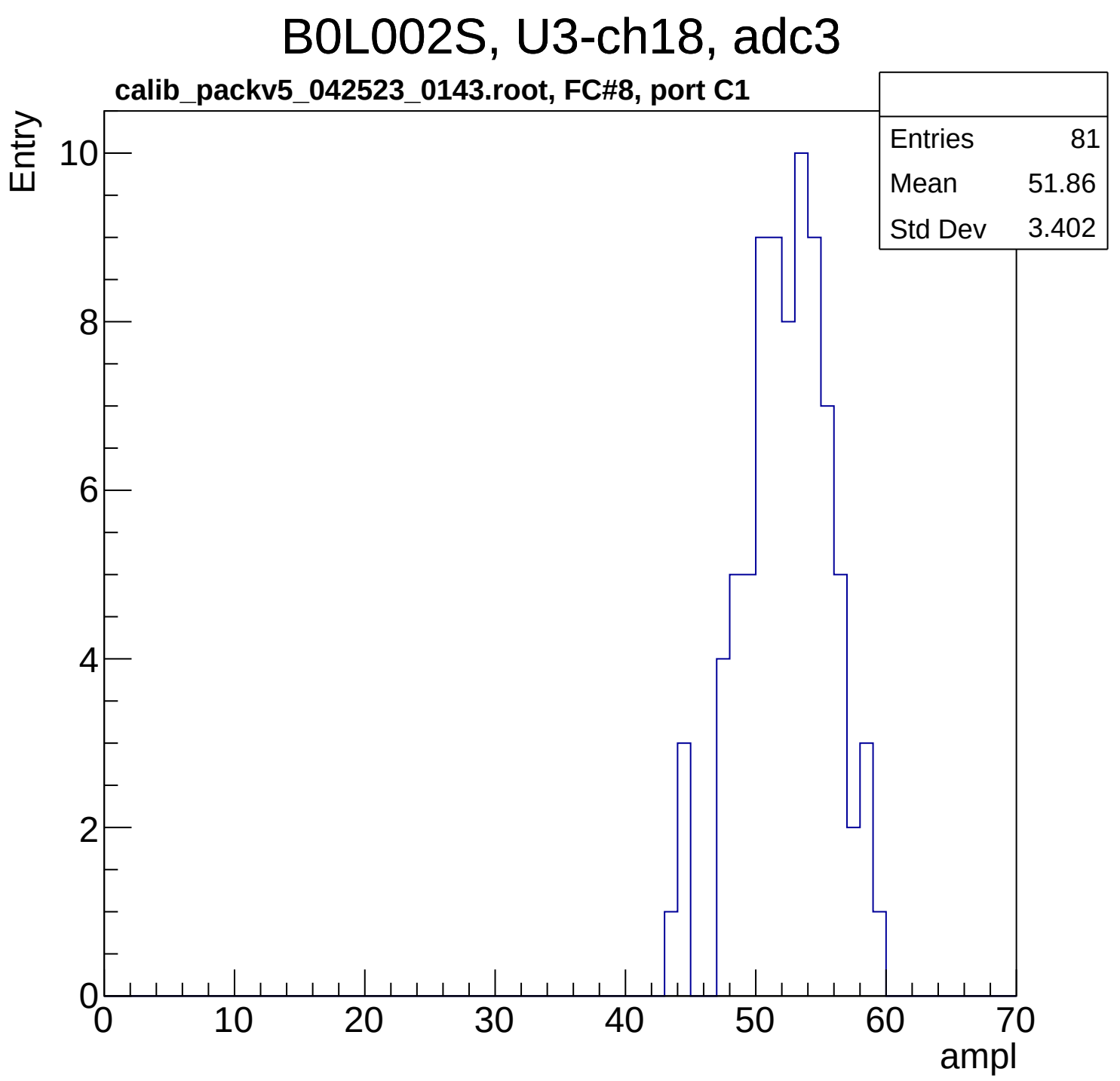
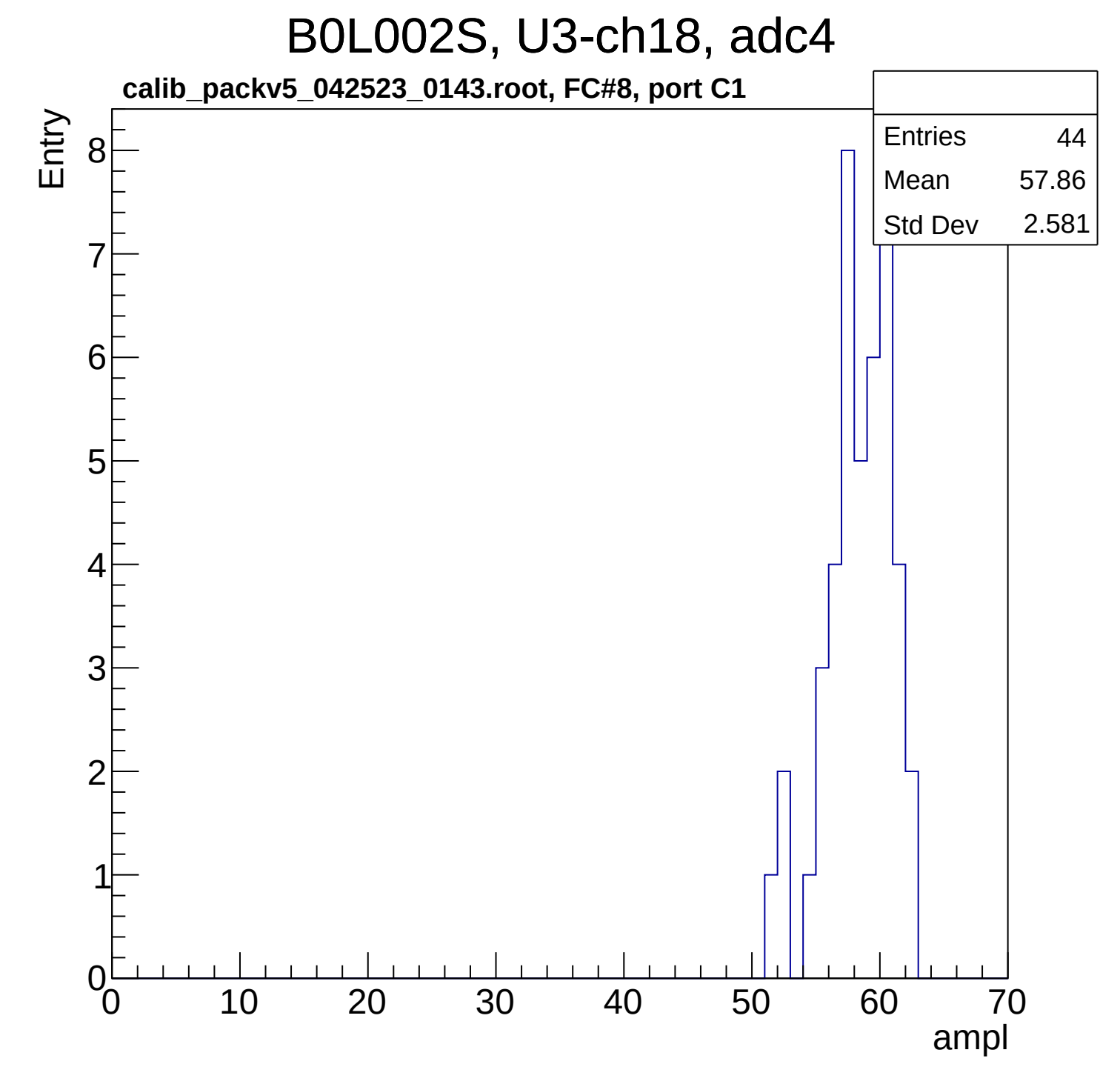

B0L002S, U3-ch18, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 38.21
Std Dev 3.189
Gaus mean : 38.7707
Gaus Width: 3.5388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch18, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 44.67
Std Dev 3.182
Gaus mean : 45.5410
Gaus Width: 2.7050
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch18, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 51.86
Std Dev 3.402
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch18, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 57.86
Std Dev 2.581
Entry
ampl
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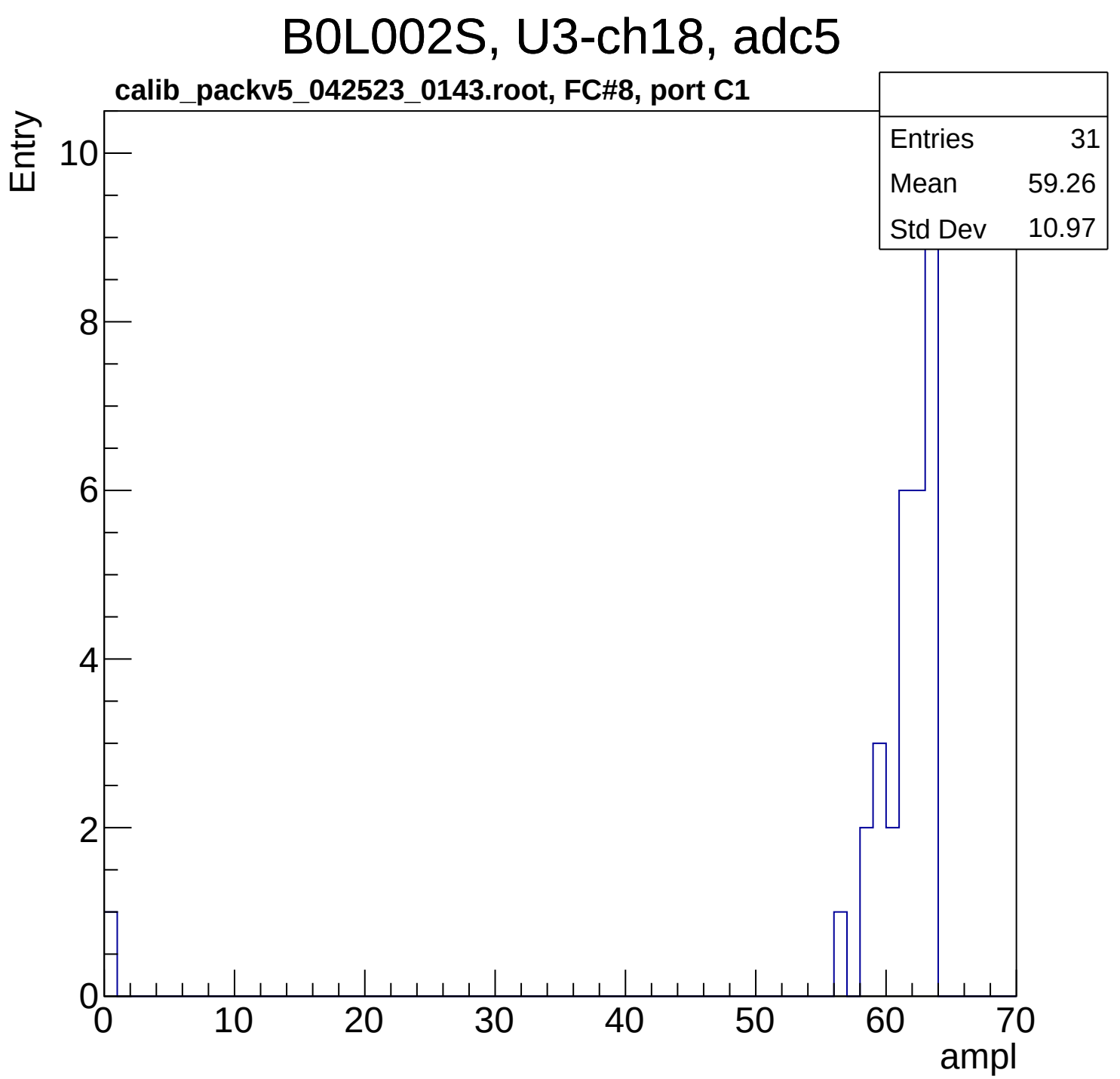
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch18, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.26
Std Dev 10.97
Entry
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8
10
0
10
20
30
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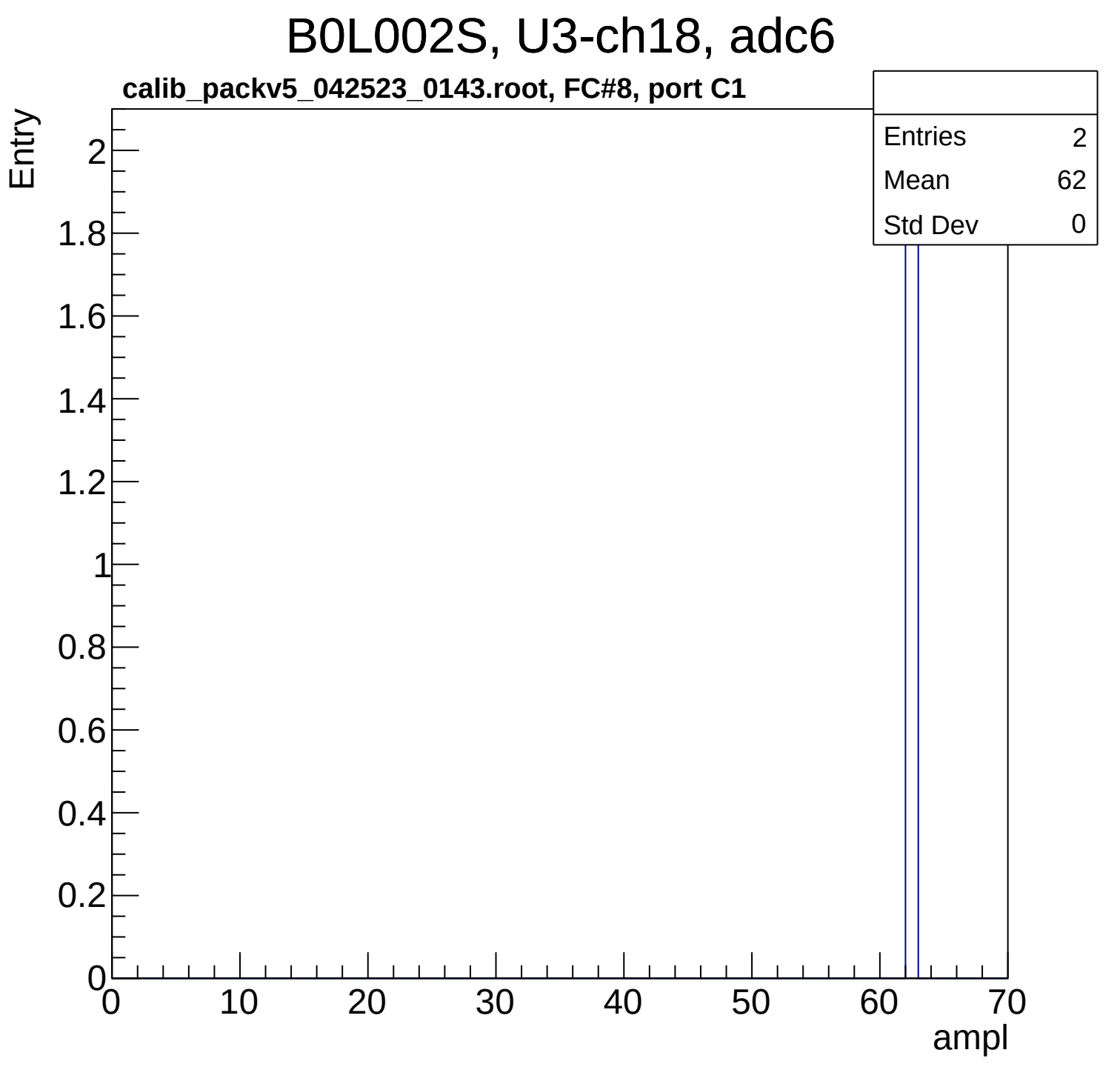
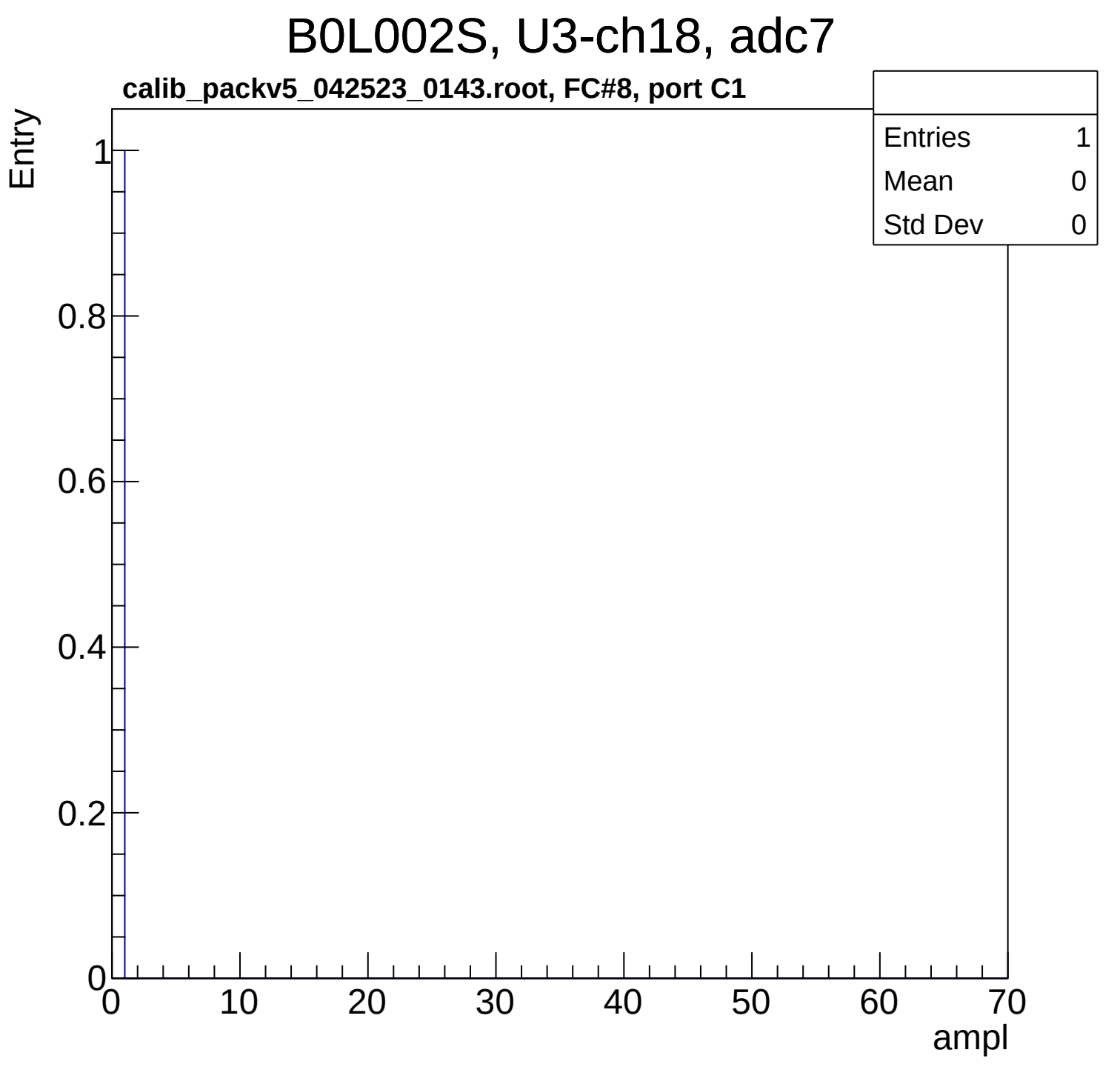
50
60
70
ampl


B0L002S, U3-ch18, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch18, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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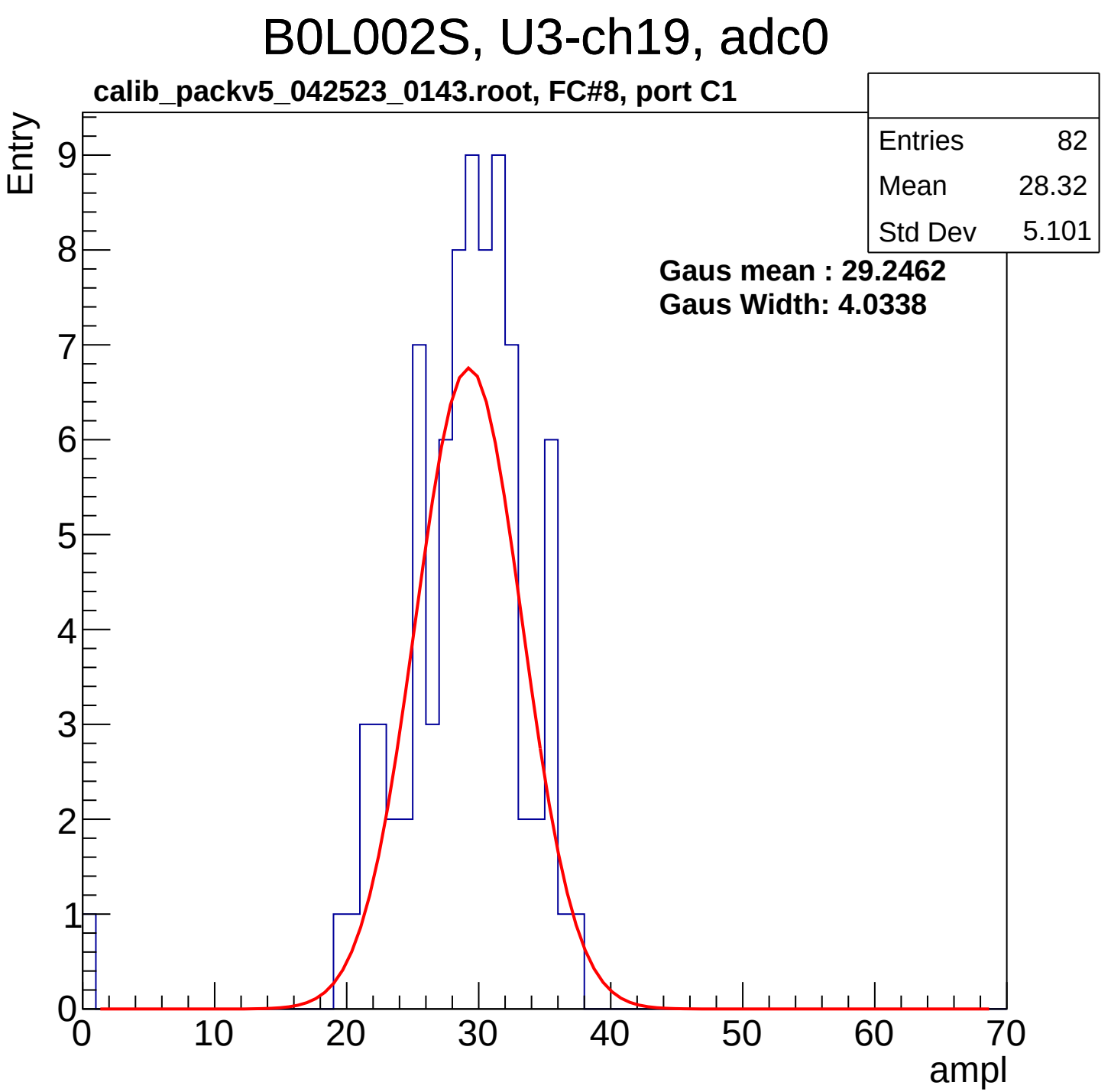
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch19, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 28.32
Std Dev 5.101
Gaus mean : 29.2462
Gaus Width: 4.0338
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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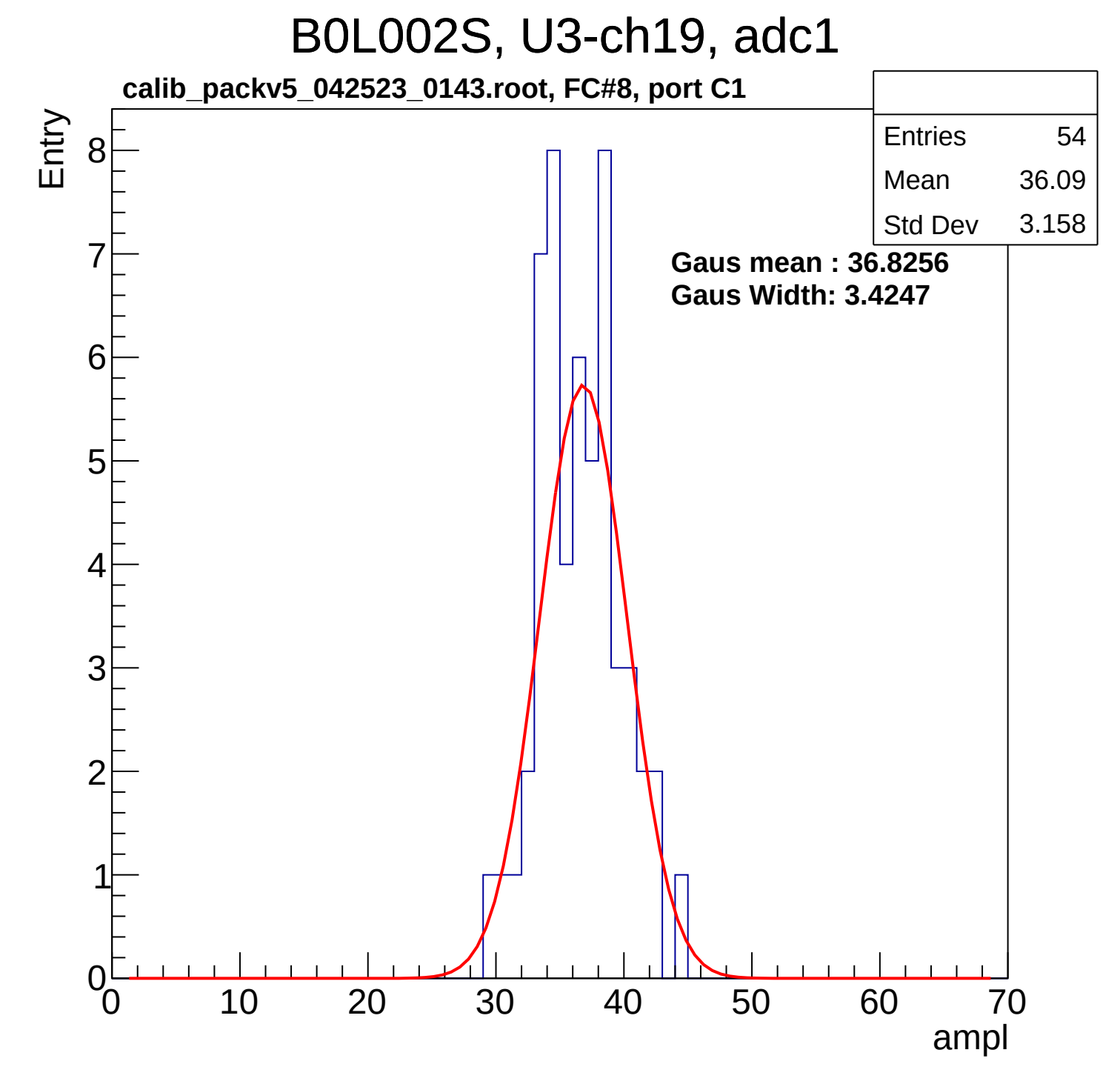
B0L002S, U3-ch19, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 36.09
Std Dev 3.158
Gaus mean : 36.8256
Gaus Width: 3.4247
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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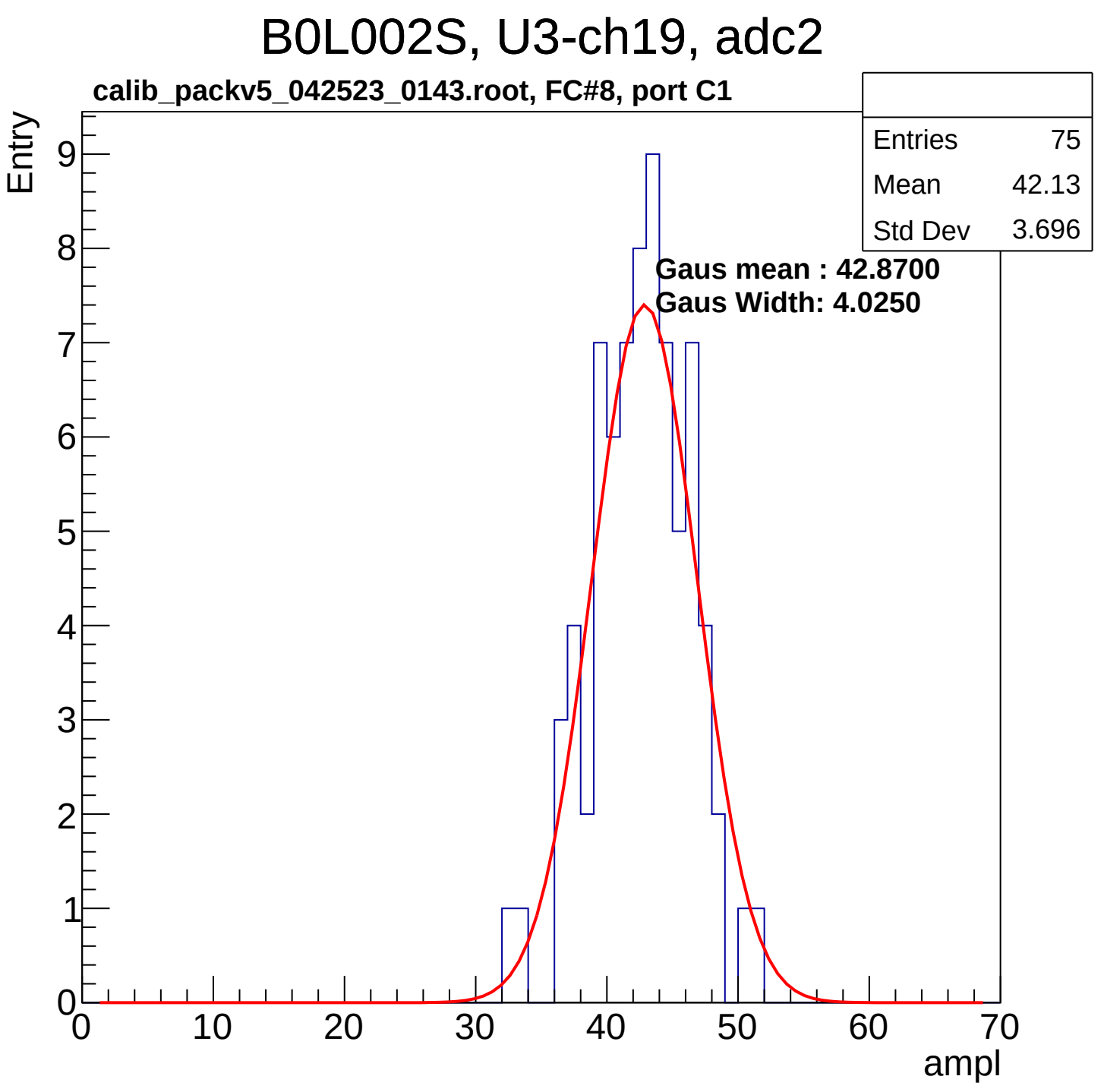
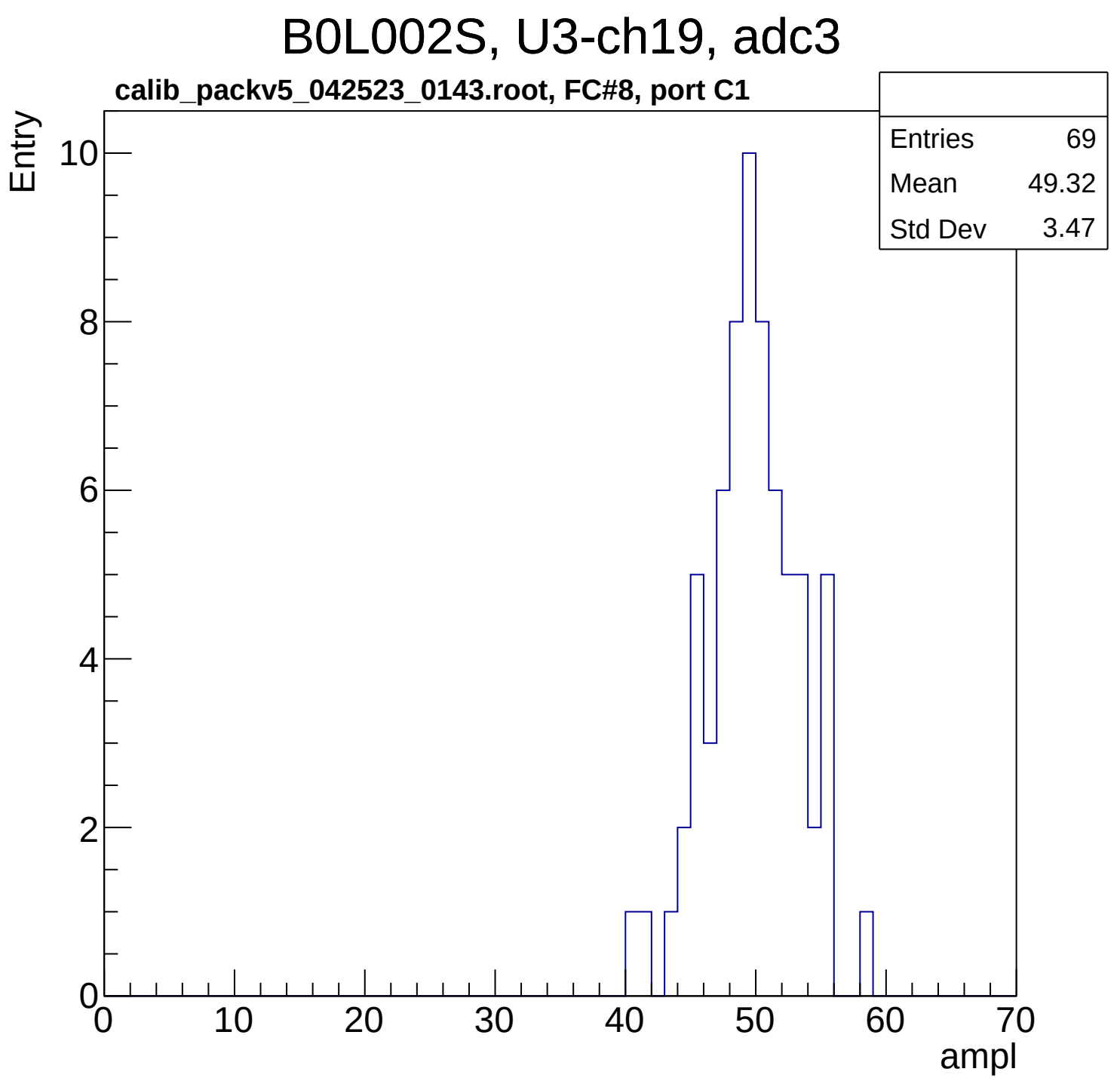
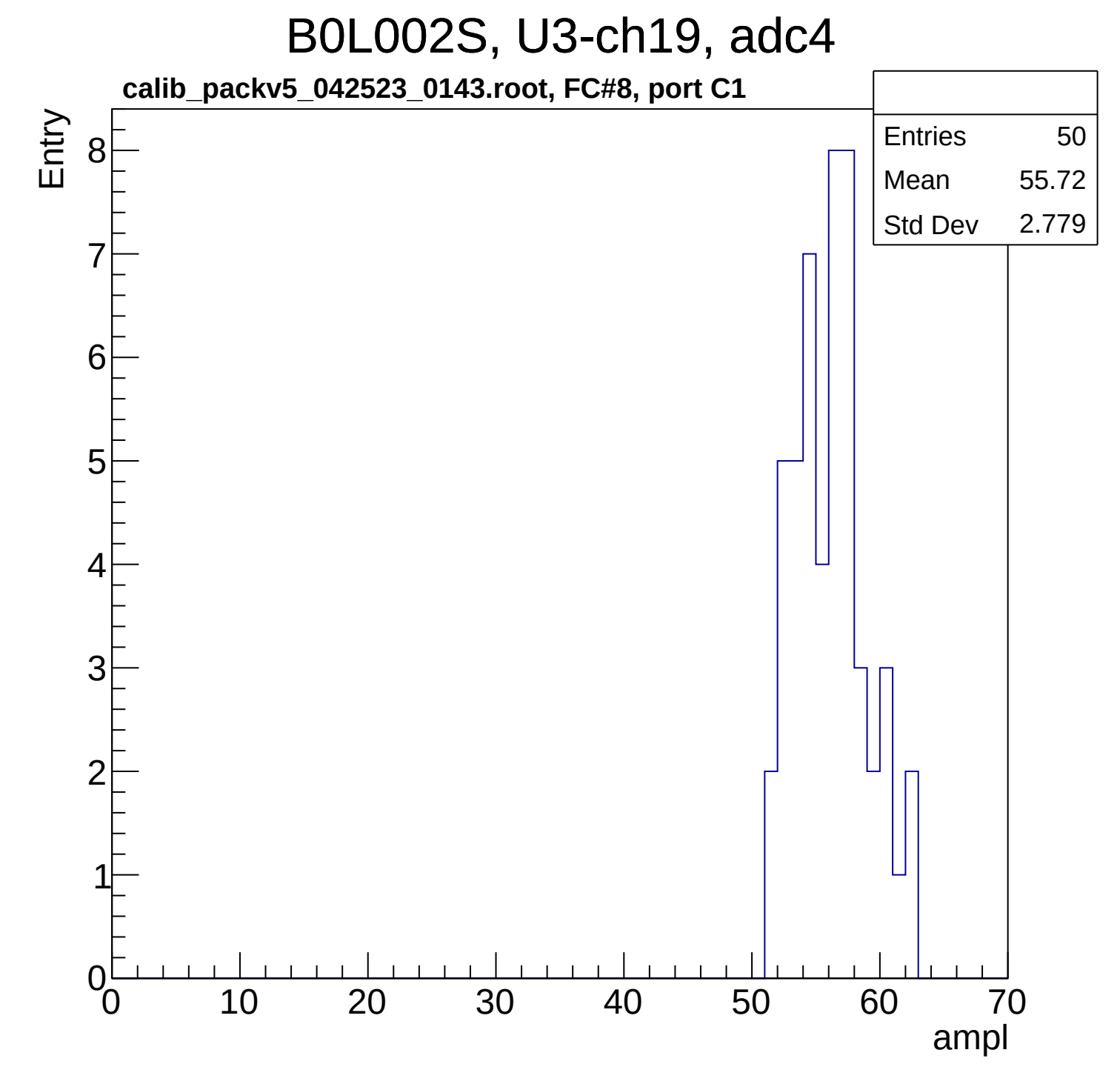
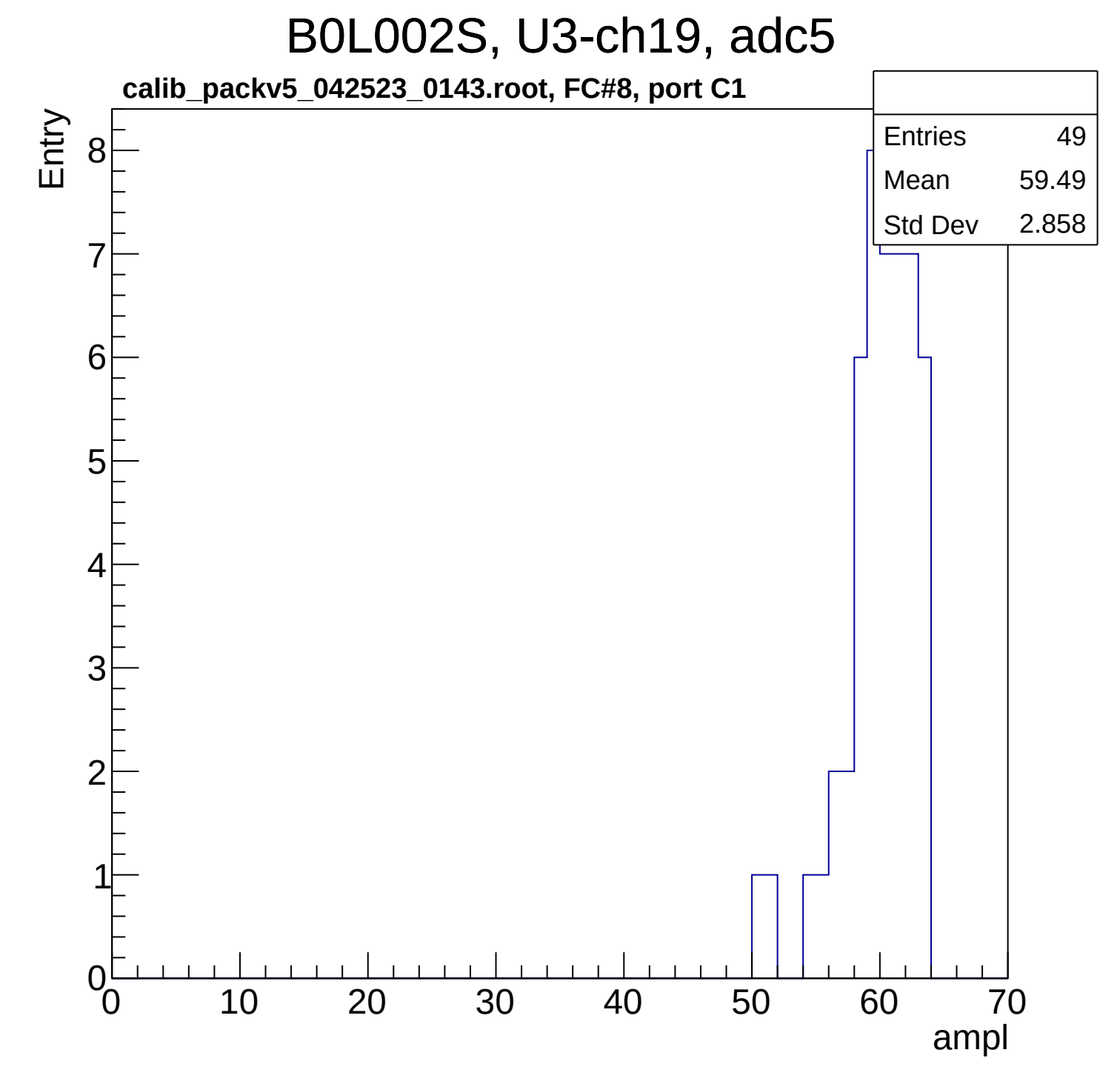
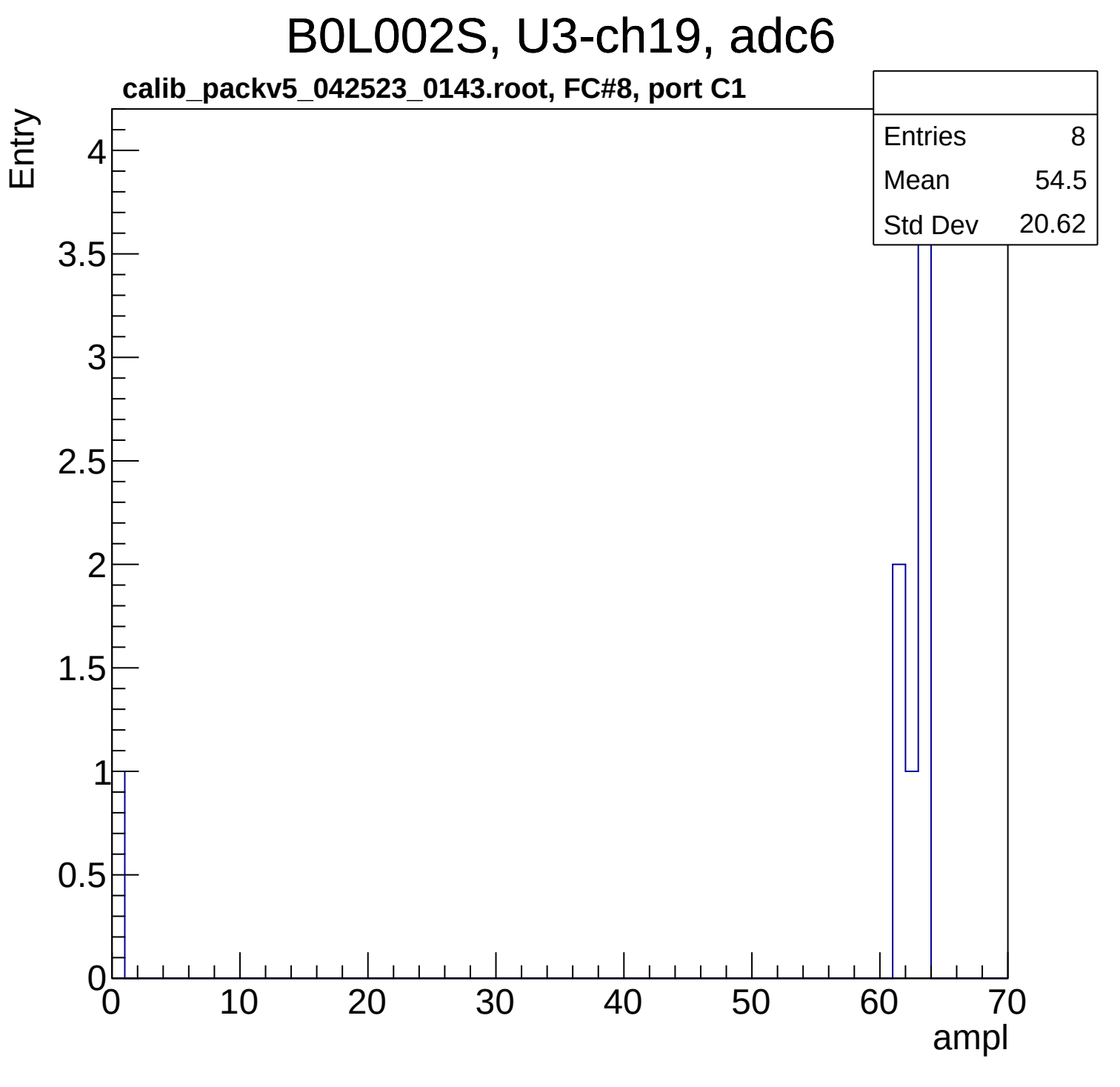
40
50
60
70

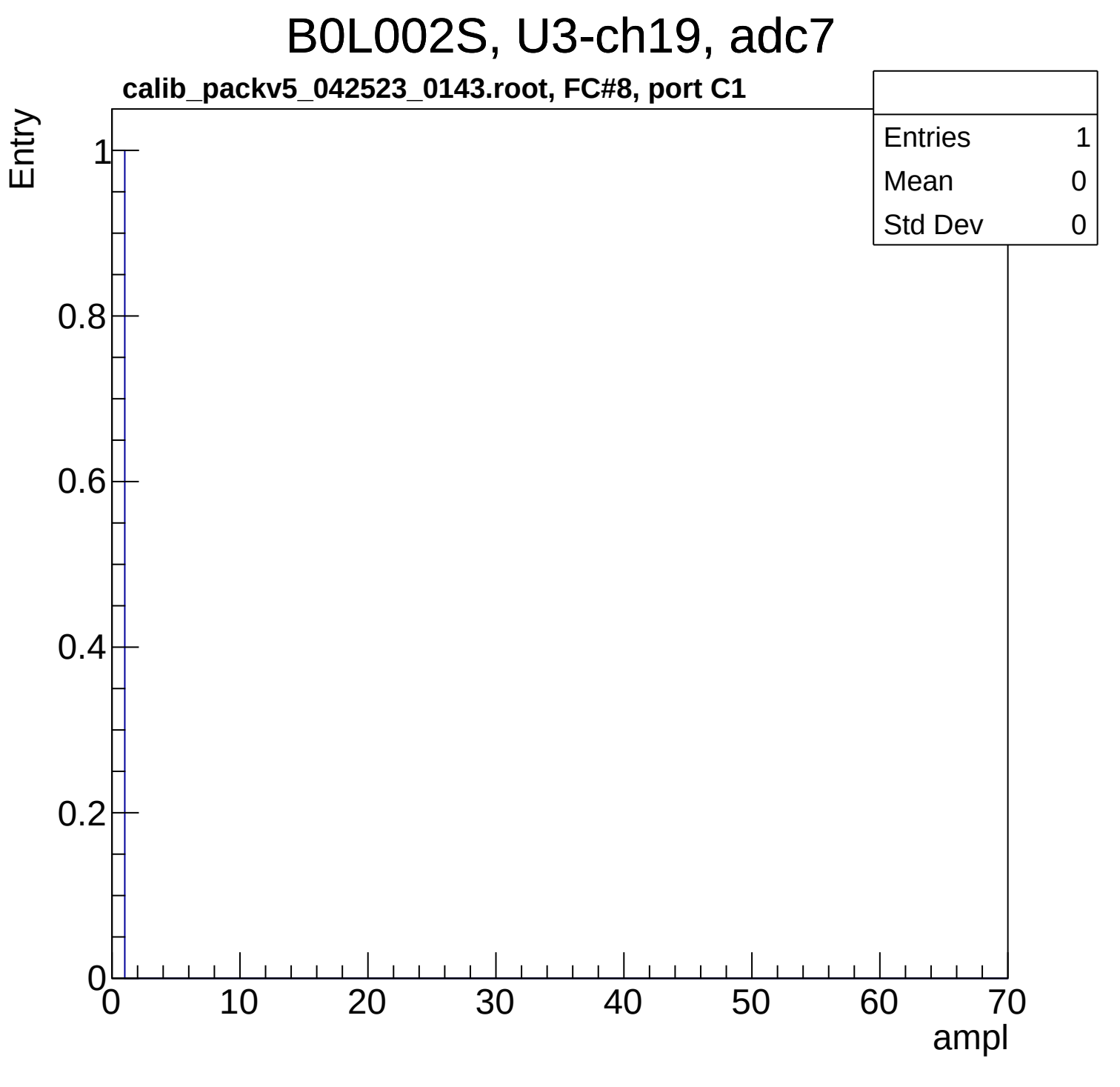

B0L002S, U3-ch19, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 42.13
Std Dev 3.696
Gaus mean : 42.8700
Gaus Width: 4.0250
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

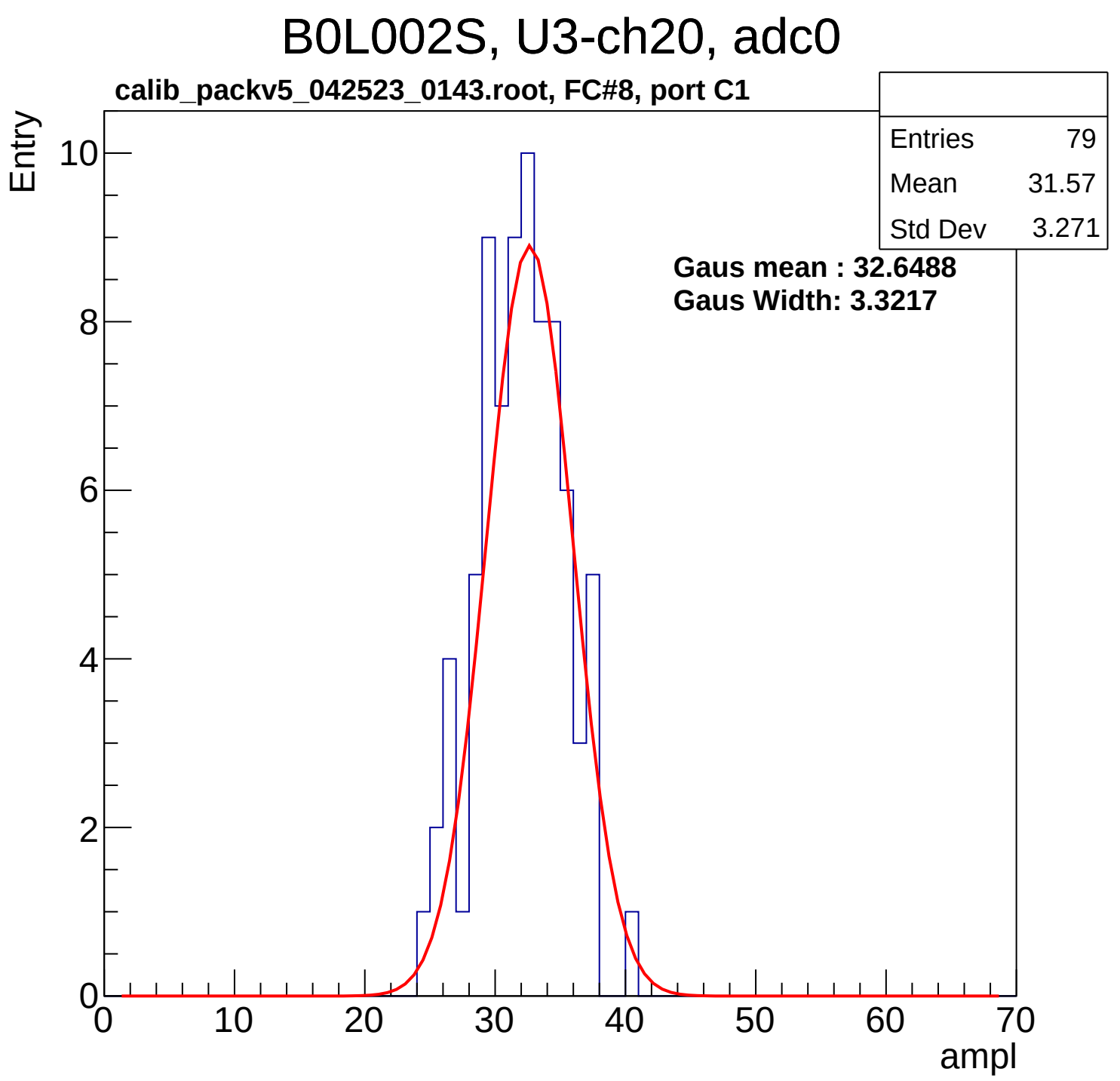

B0L002S, U3-ch19, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 49.32
Std Dev 3.47
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

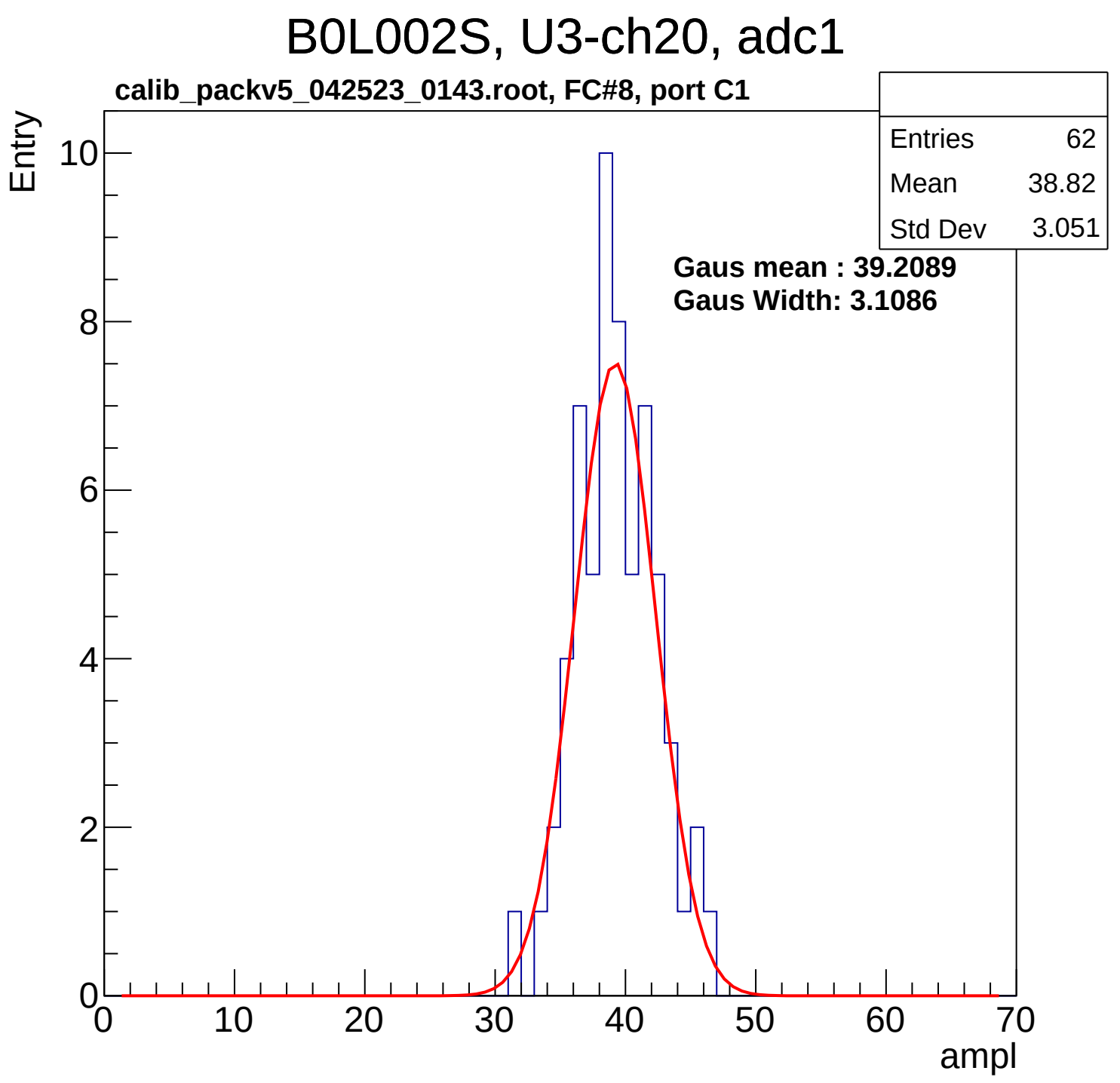

B0L002S, U3-ch19, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 55.72
Std Dev 2.779
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

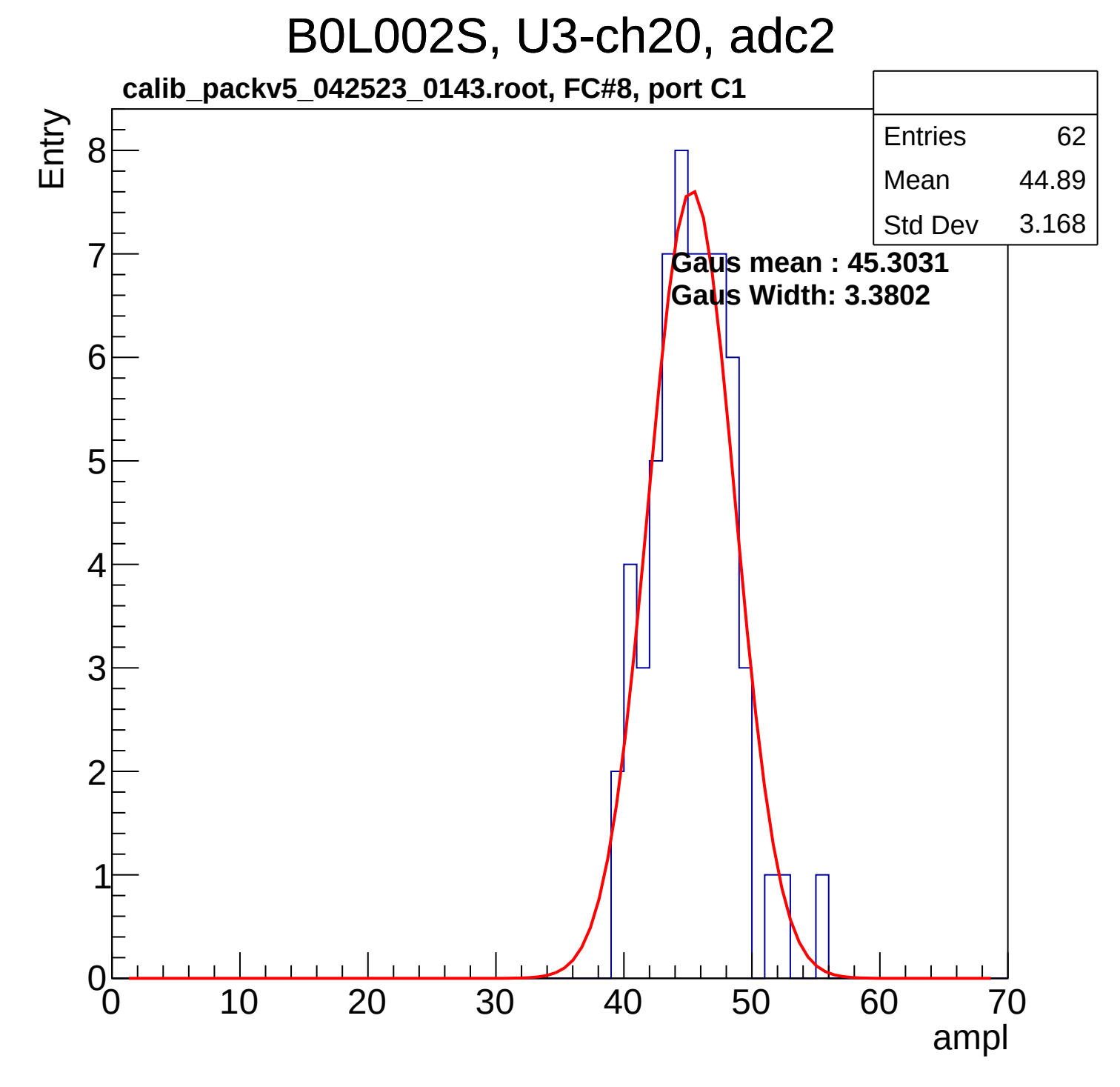

B0L002S, U3-ch19, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 59.49
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

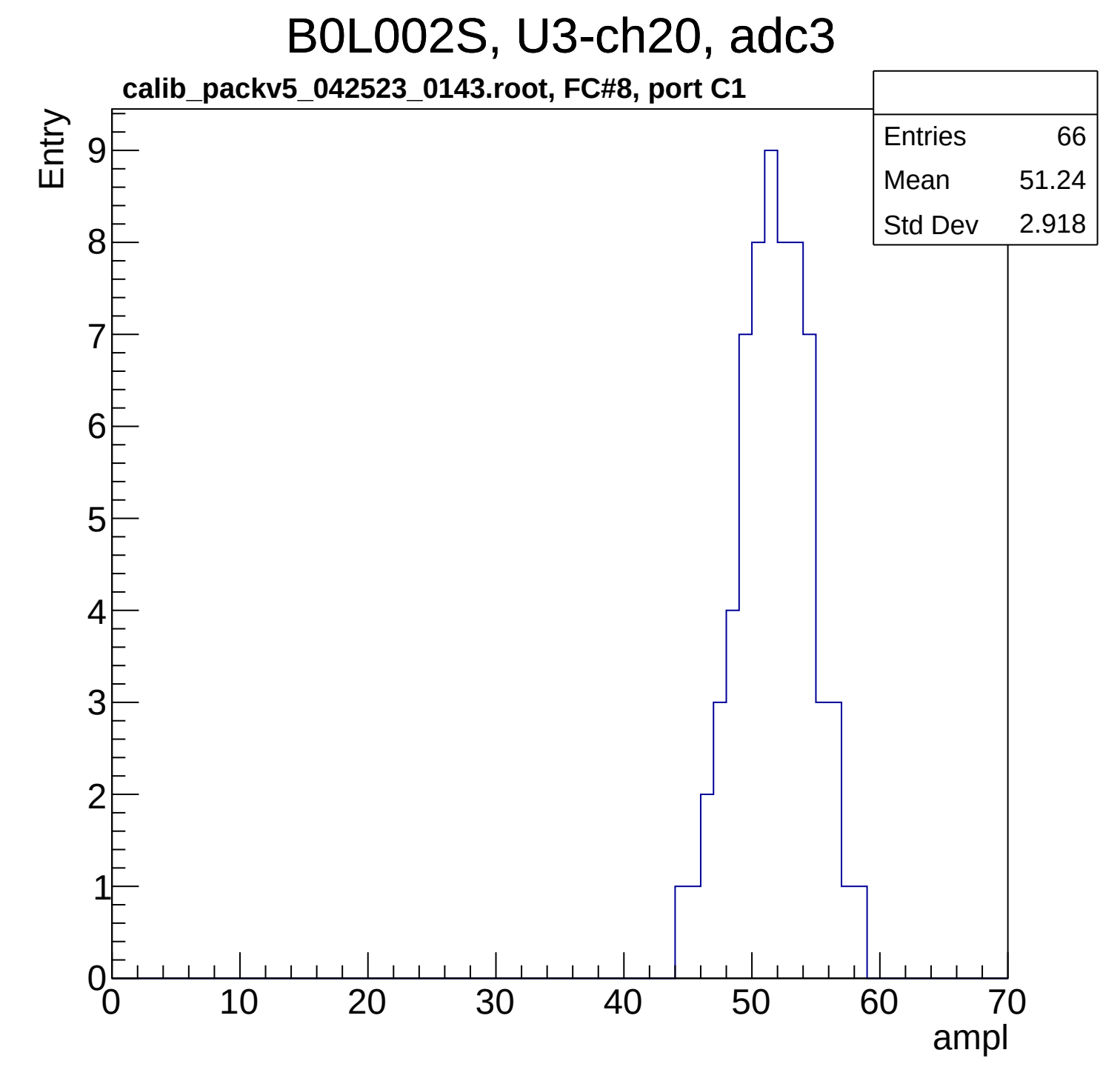

B0L002S, U3-ch19, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 54.5
Std Dev 20.62
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

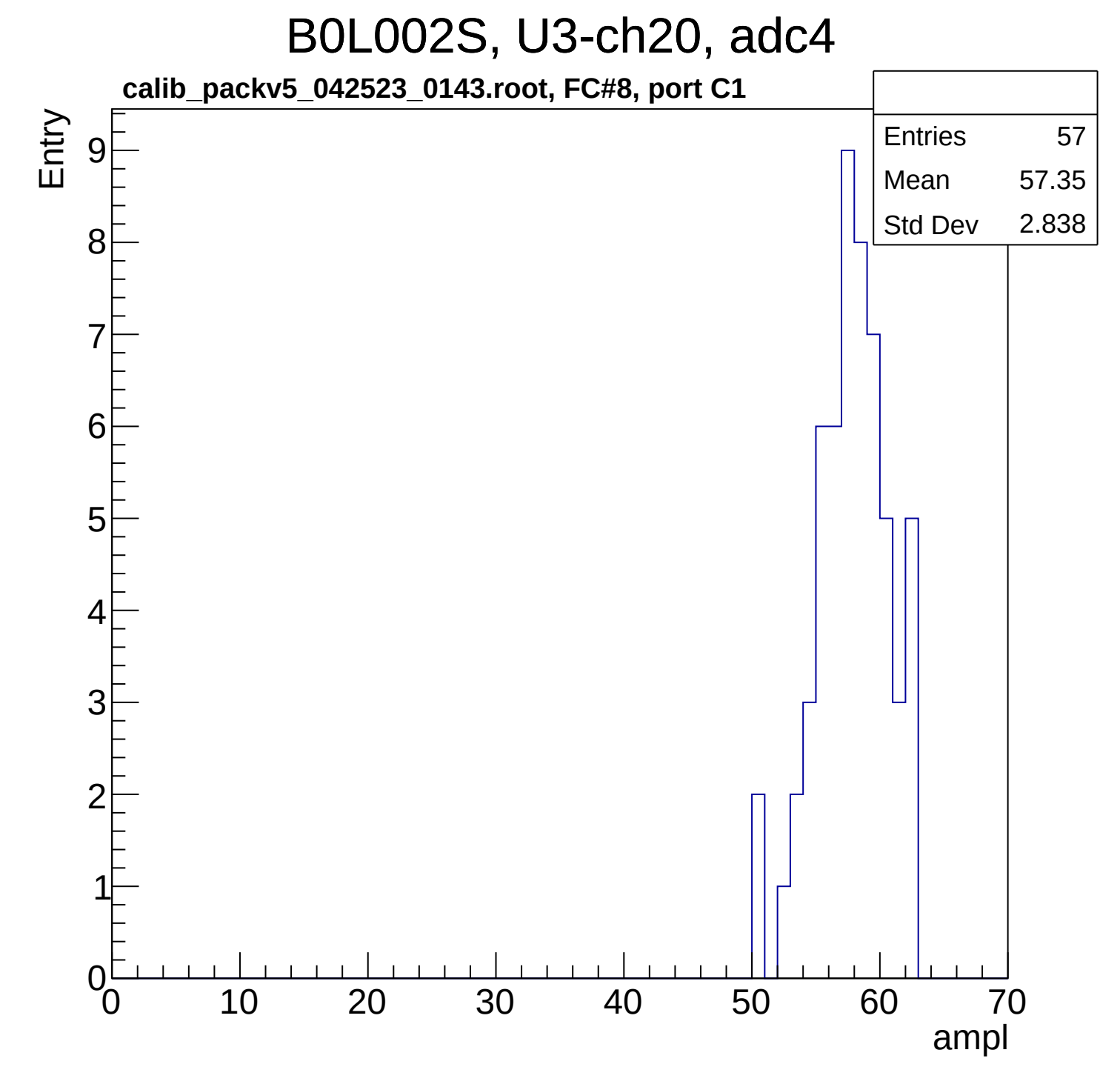

B0L002S, U3-ch19, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

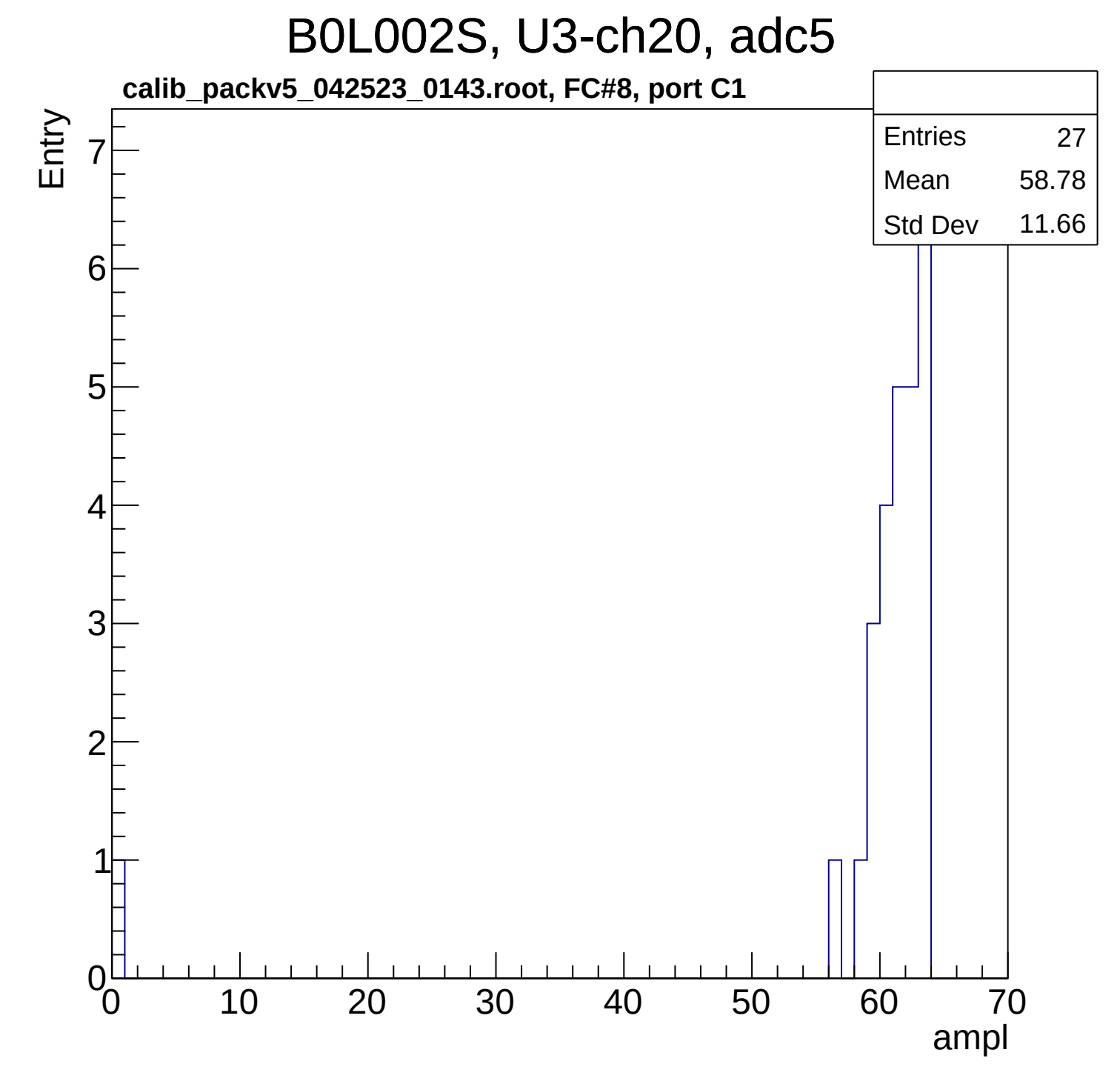

B0L002S, U3-ch20, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 31.57
Std Dev 3.271
Gaus mean : 32.6488
Gaus Width: 3.3217
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch20, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 38.82
Std Dev 3.051
Gaus mean : 39.2089
Gaus Width: 3.1086
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch20, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.89
Std Dev 3.168
Gaus mean : 45.3031
Gaus Width: 3.3802
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch20, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.24
Std Dev 2.918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch20, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.35
Std Dev 2.838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch20, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.78
Std Dev 11.66
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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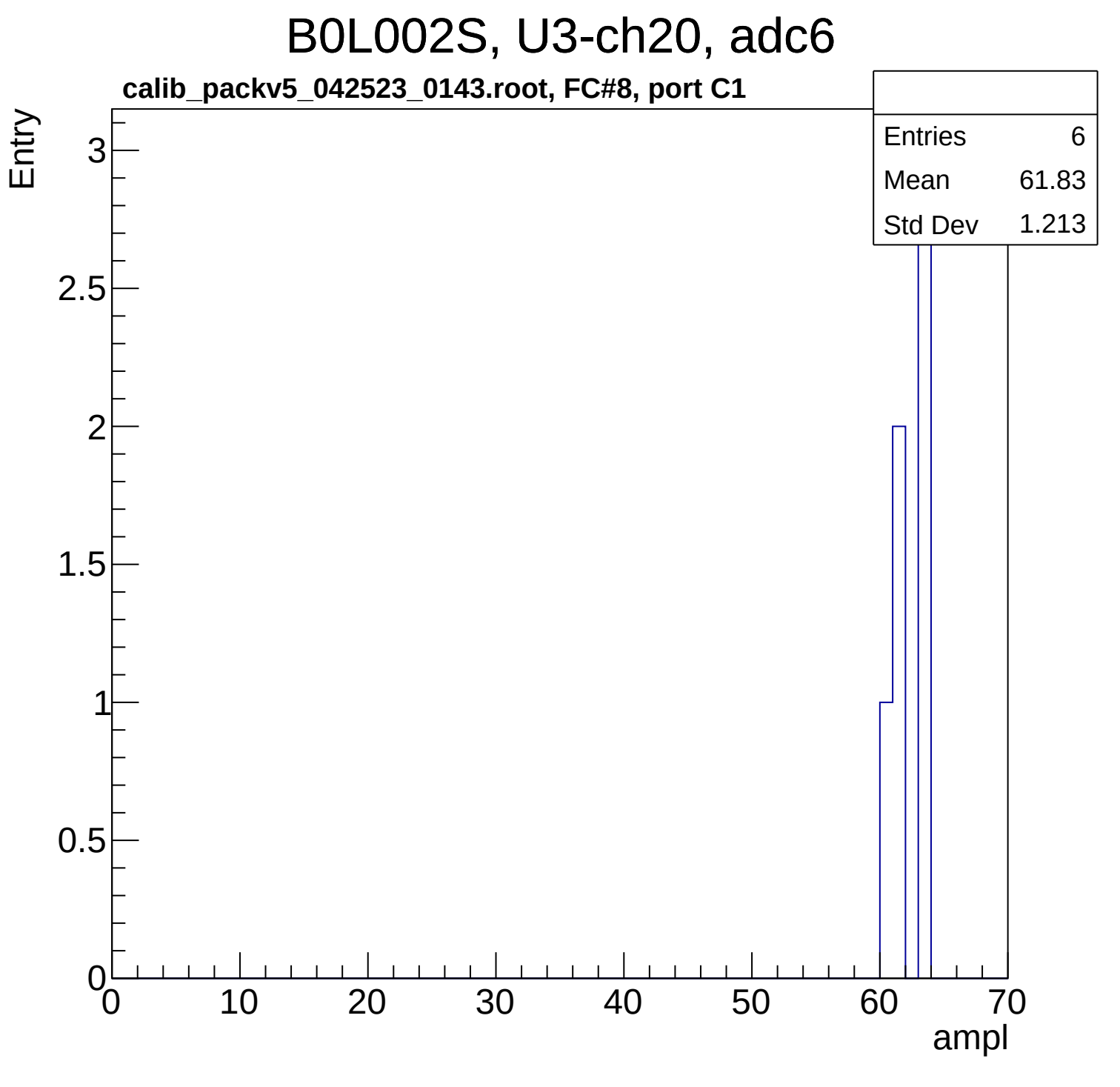
B0L002S, U3-ch20, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.83
Std Dev 1.213
Entry
ampl
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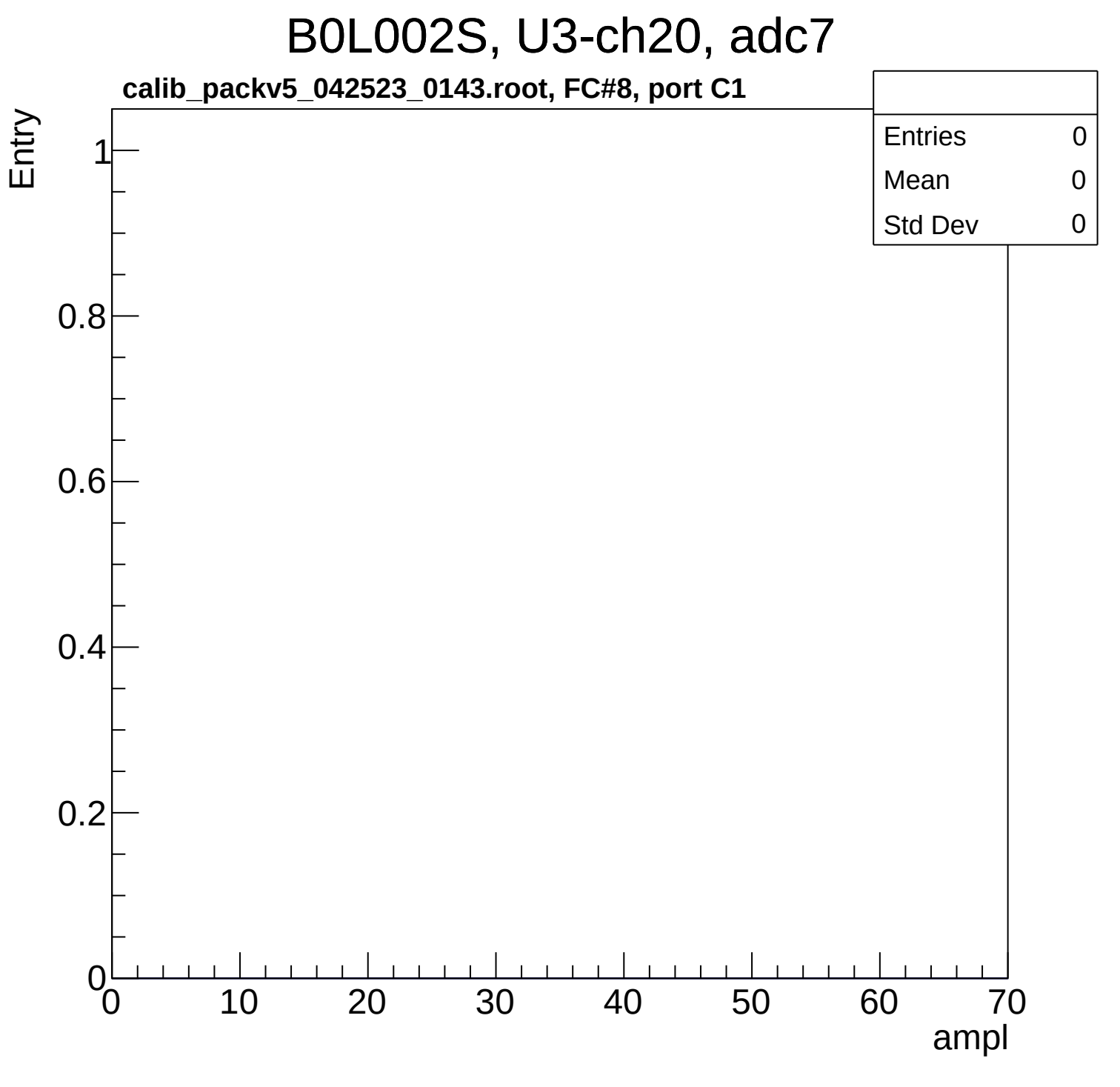
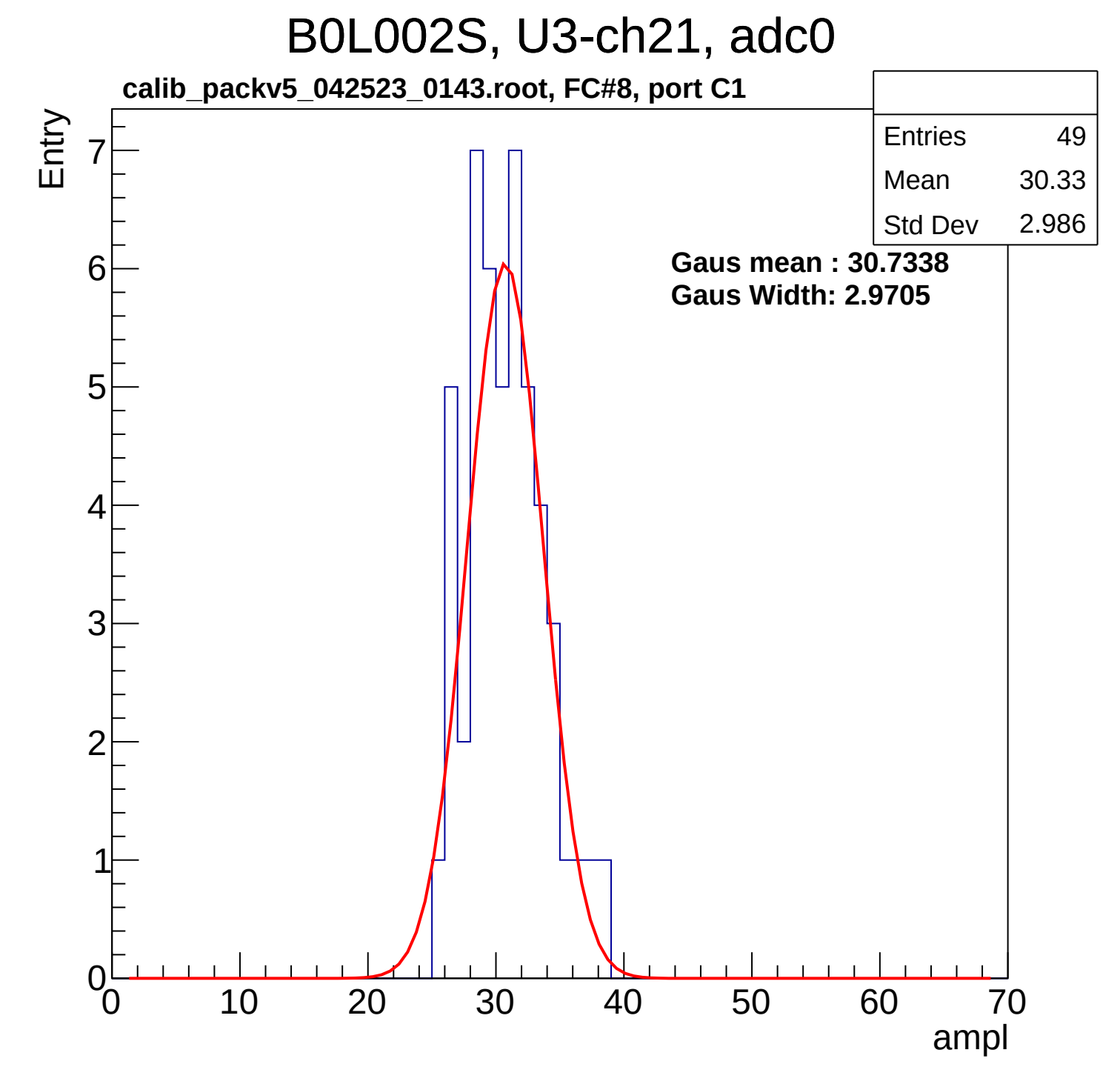
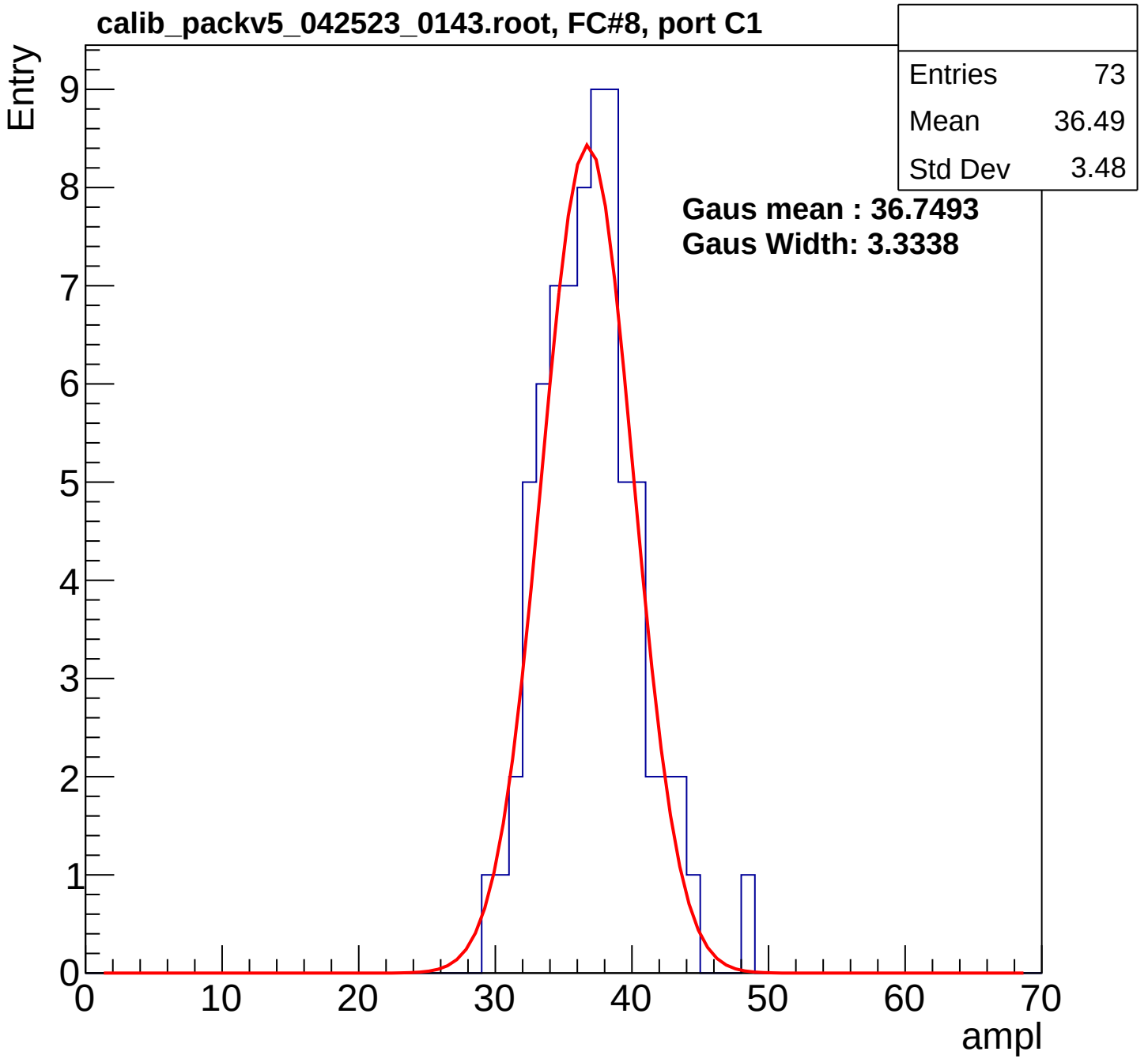
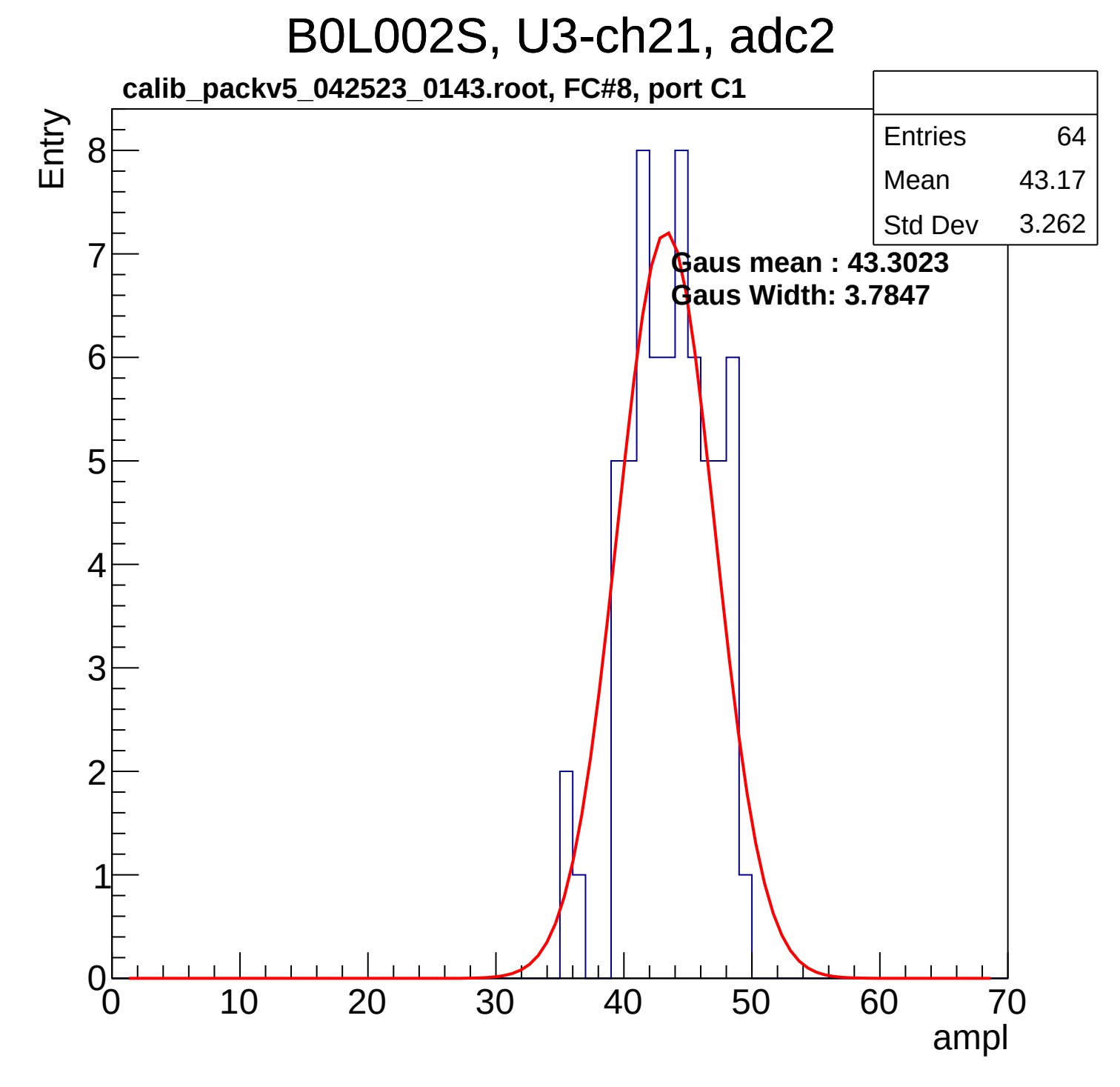
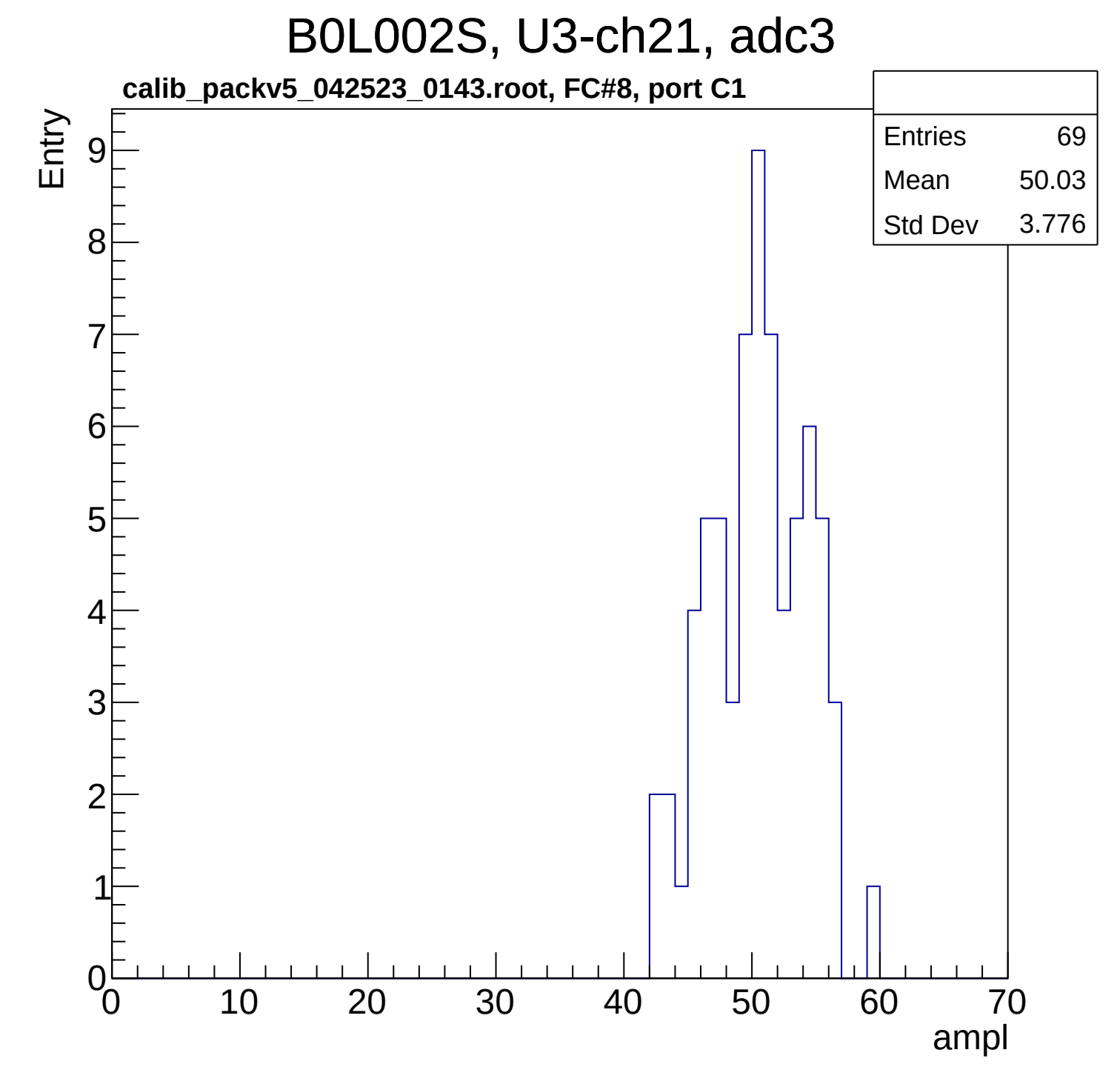
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

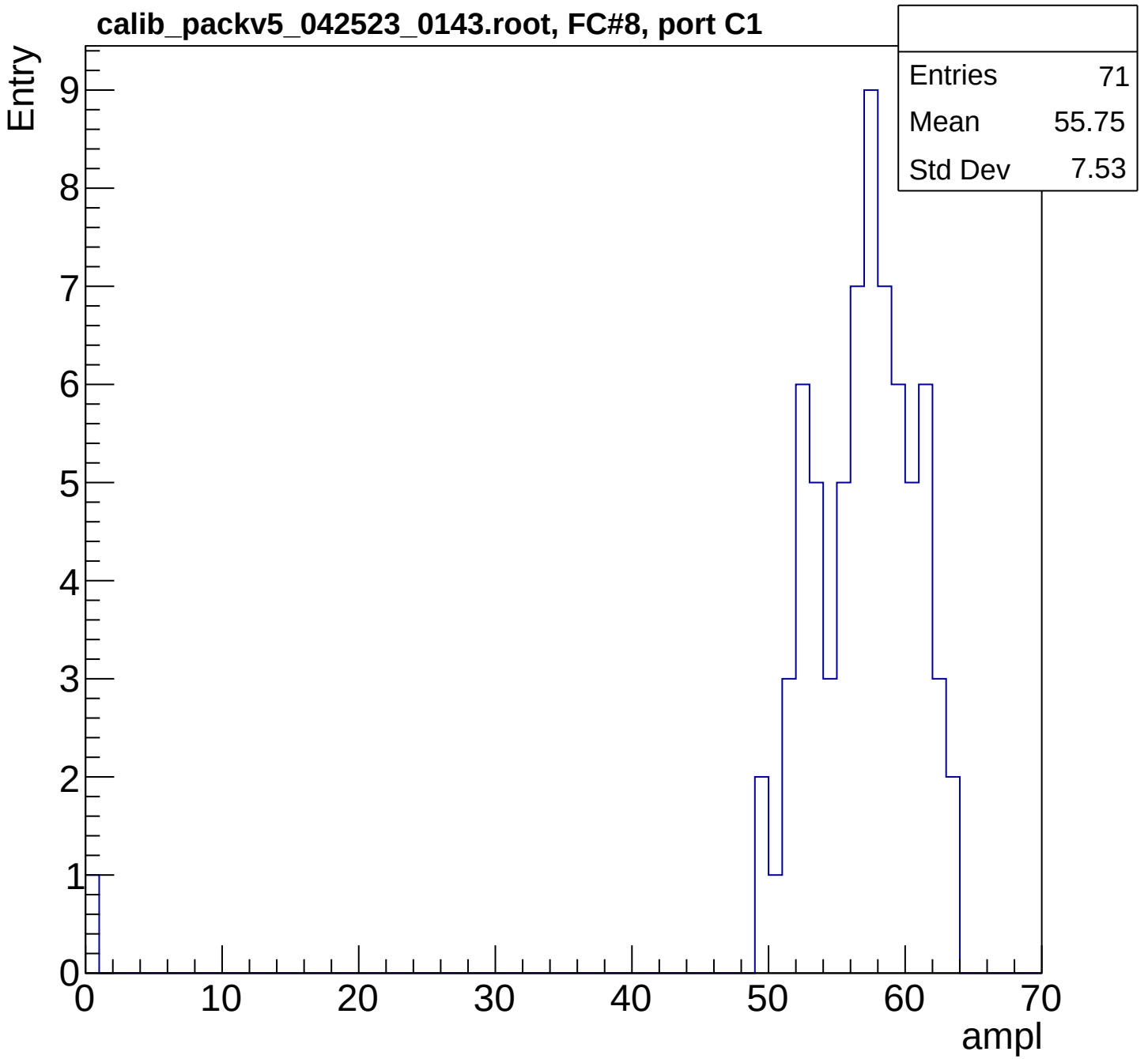

B0L002S, U3-ch20, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch21, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 30.33
Std Dev 2.986
Gaus mean : 30.7338
Gaus Width: 2.9705
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch21, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 36.49
Std Dev 3.48
Gaus mean : 36.7493
Gaus Width: 3.3338
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch21, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.17
Std Dev 3.262
Gaus mean : 43.3023
Gaus Width: 3.7847
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch21, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 50.03
Std Dev 3.776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch21, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 55.75
Std Dev 7.53
Entry
0
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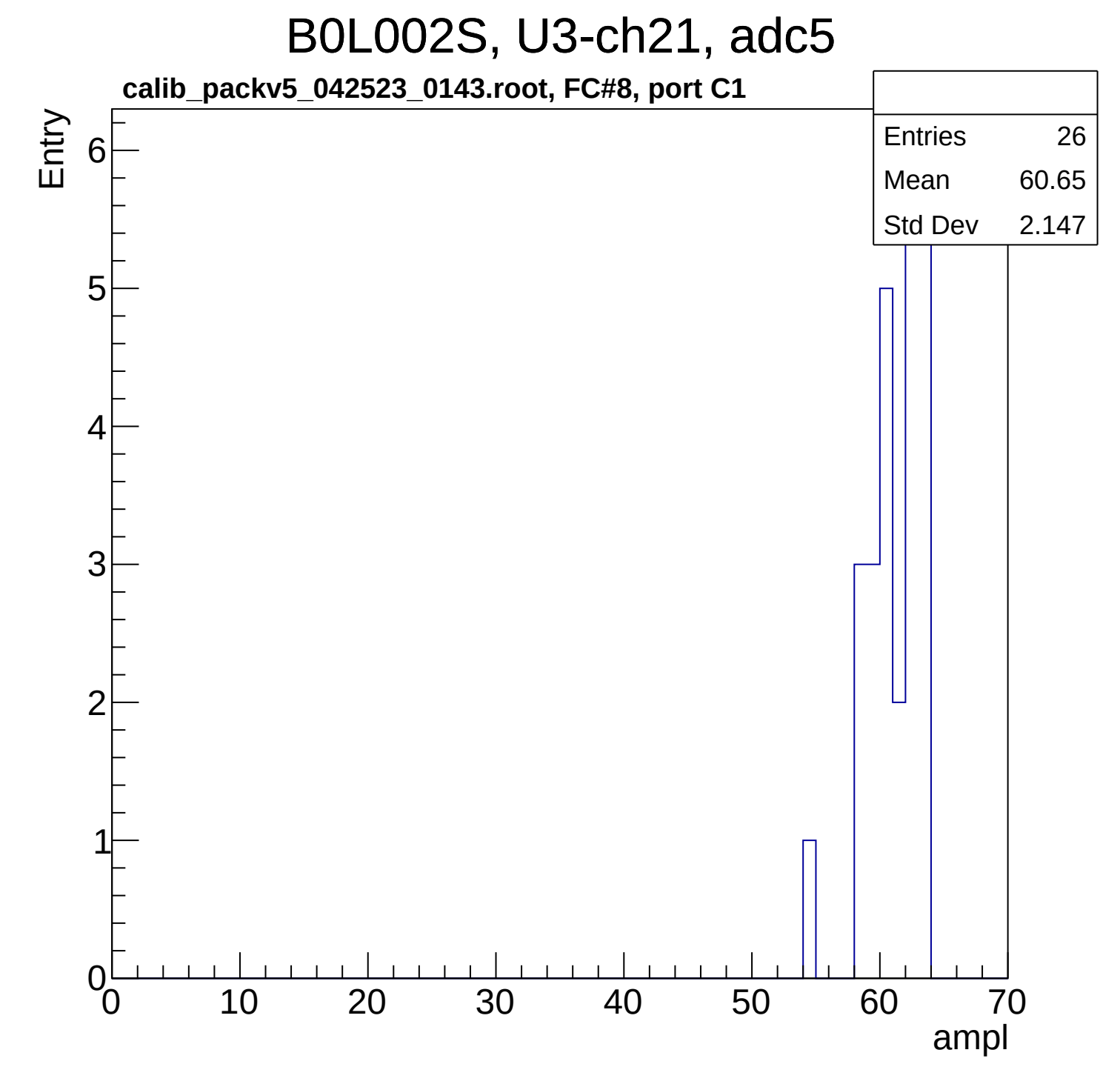
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch21, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 60.65
Std Dev 2.147
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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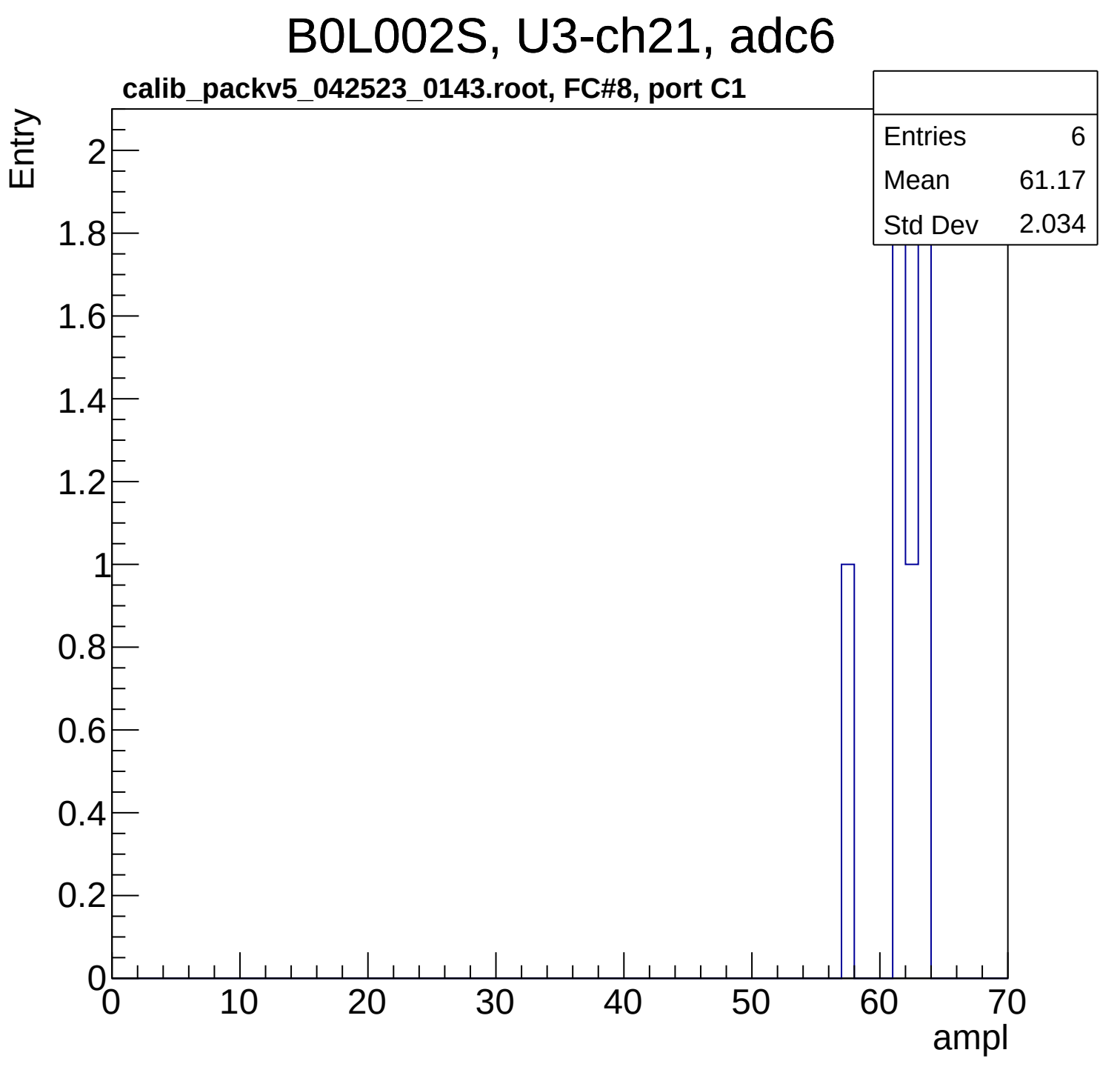
40
50
60
70


B0L002S, U3-ch21, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.17
Std Dev 2.034
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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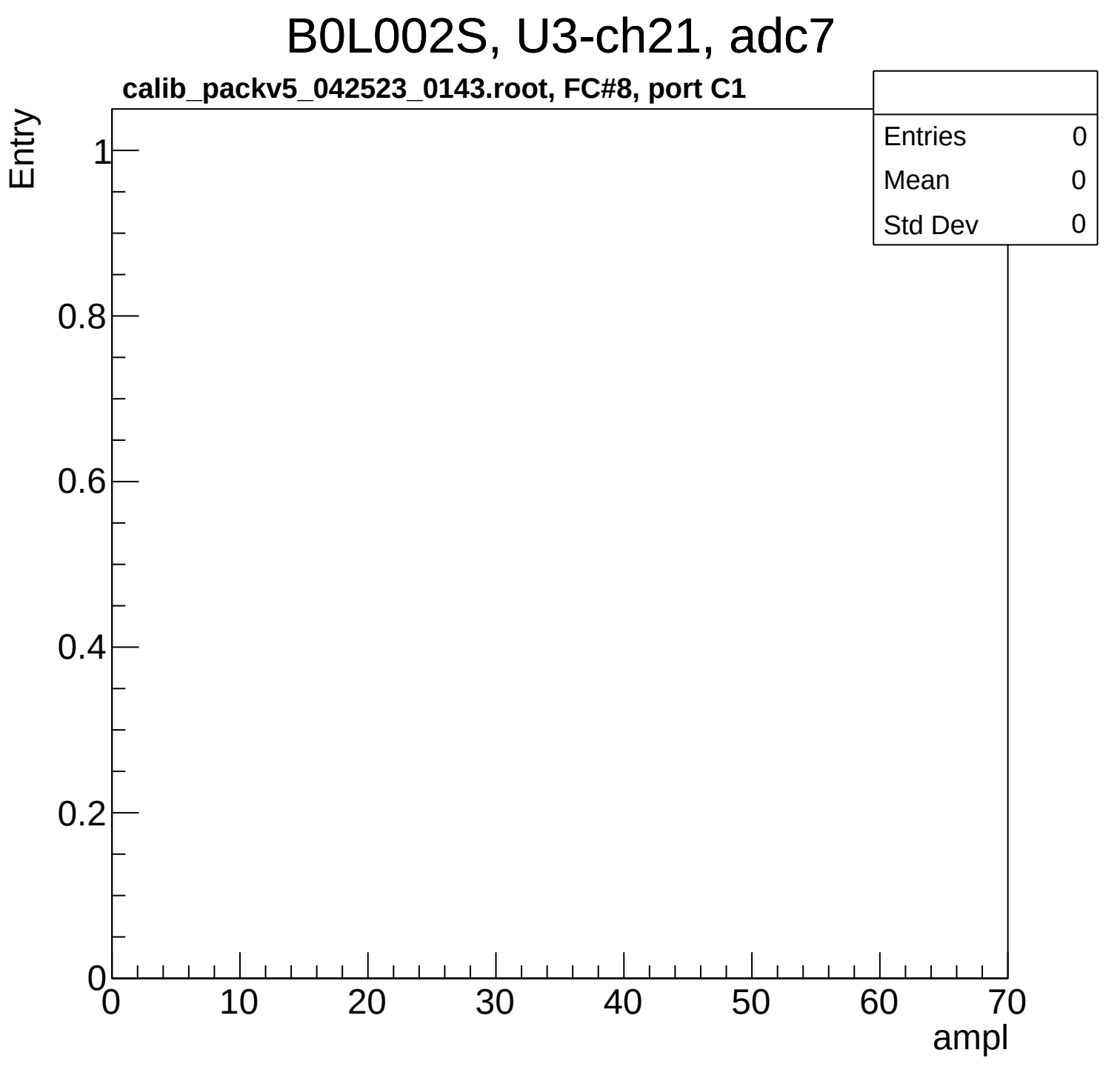
B0L002S, U3-ch21, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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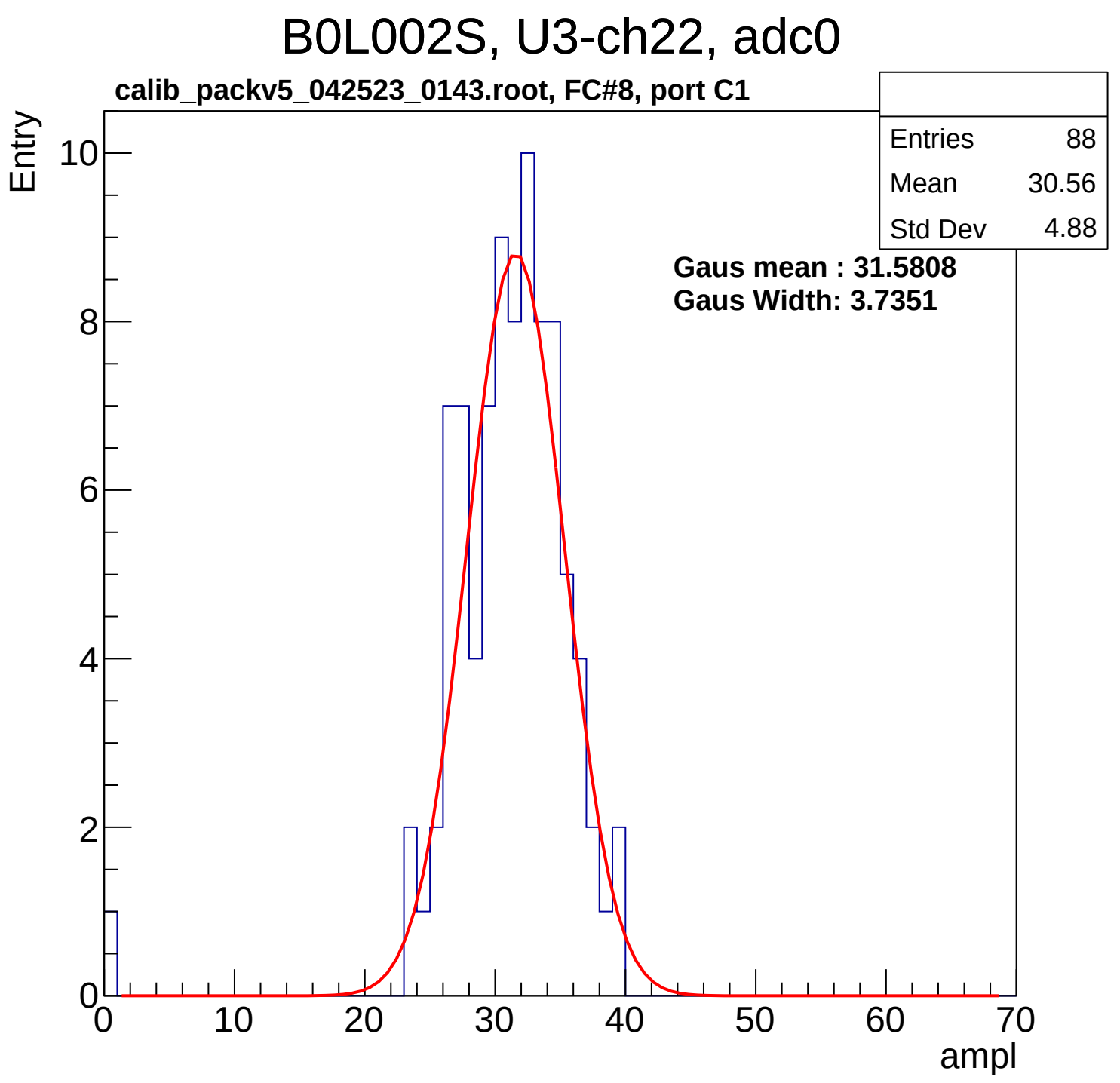
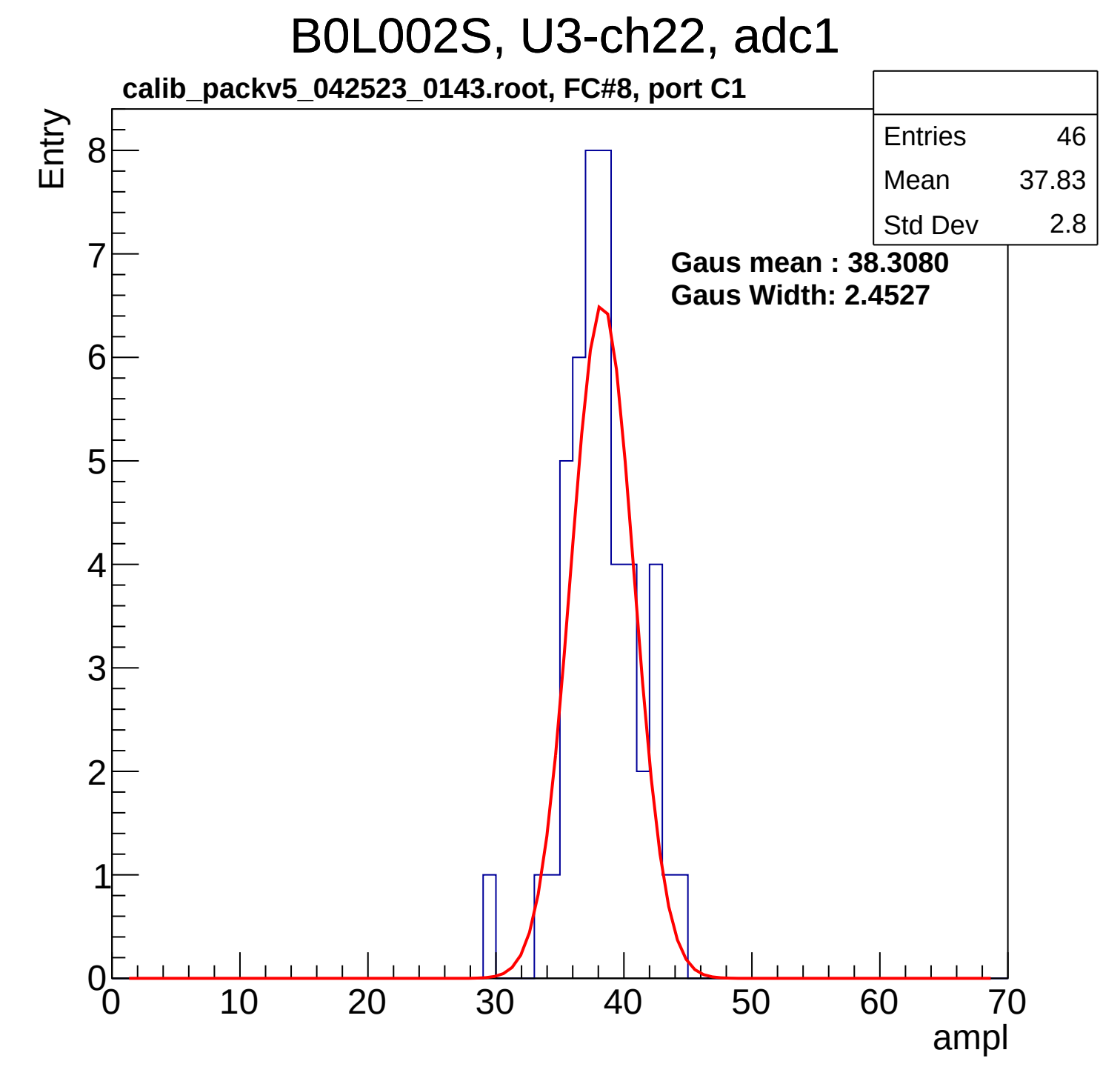
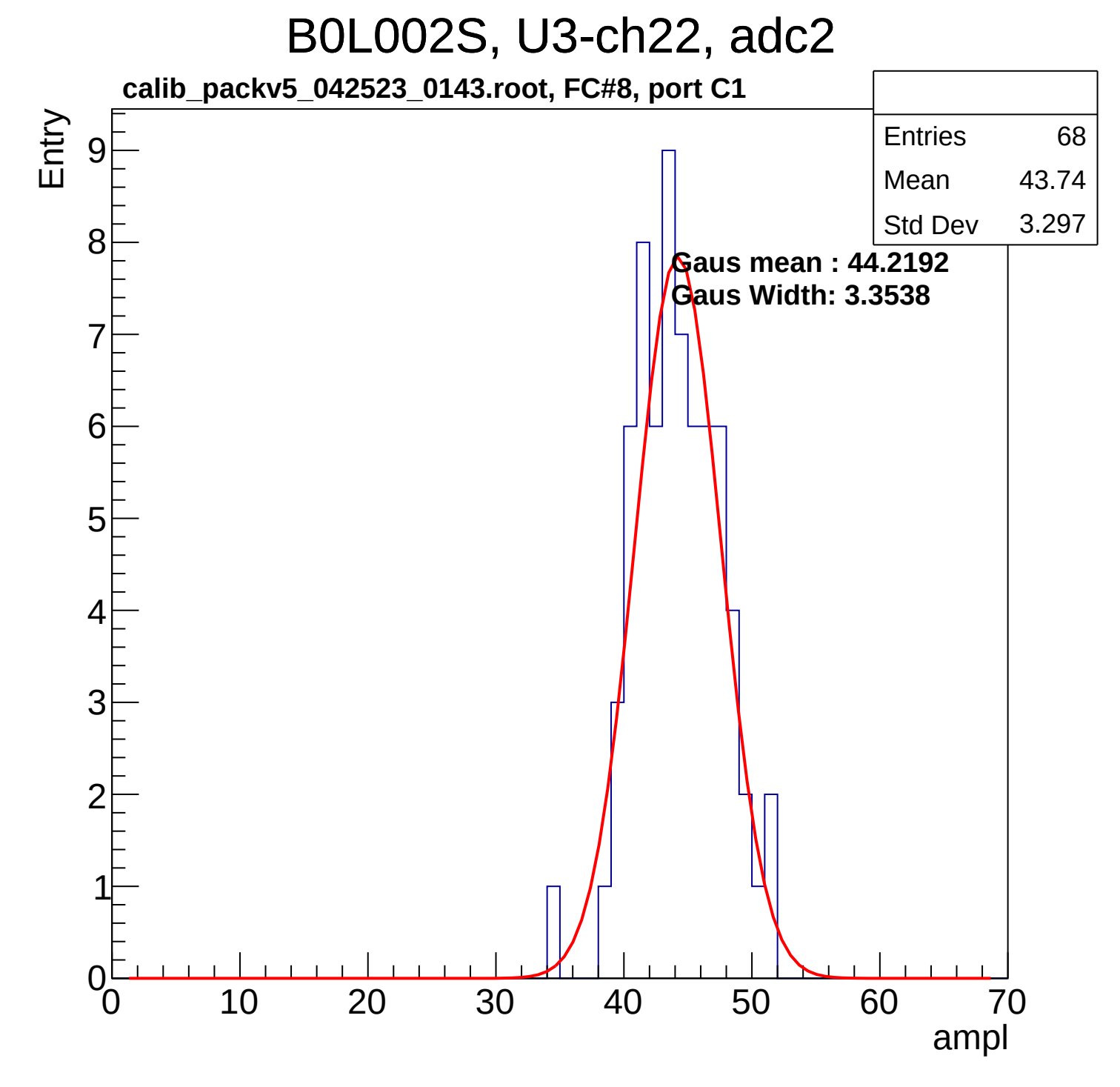
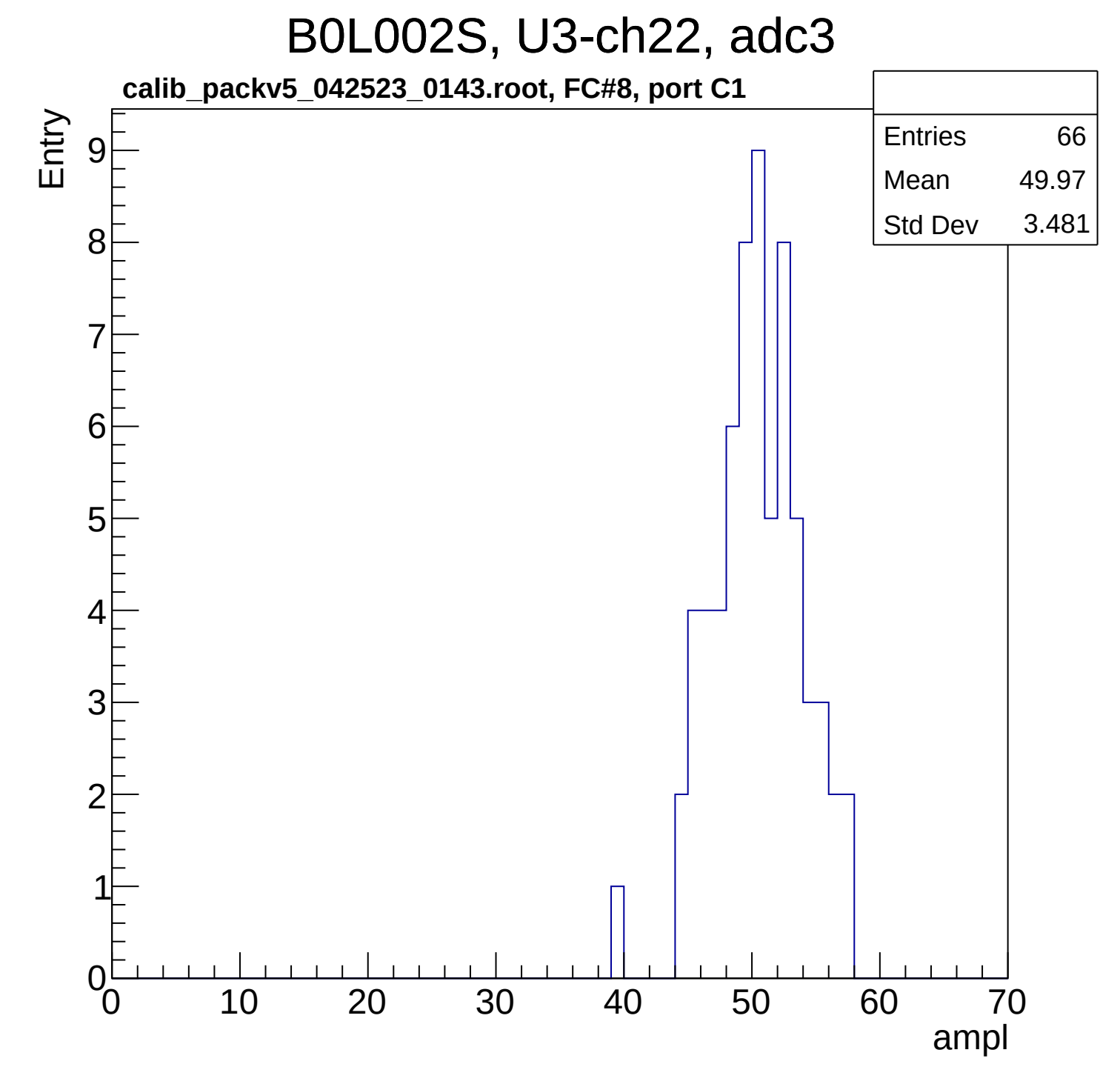
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch22, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 30.56
Std Dev 4.88
Gaus mean : 31.5808
Gaus Width: 3.7351
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch22, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 37.83
Std Dev 2.8
Gaus mean : 38.3080
Gaus Width: 2.4527
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch22, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.74
Std Dev 3.297
Gaus mean : 44.2192
Gaus Width: 3.3538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch22, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.97
Std Dev 3.481
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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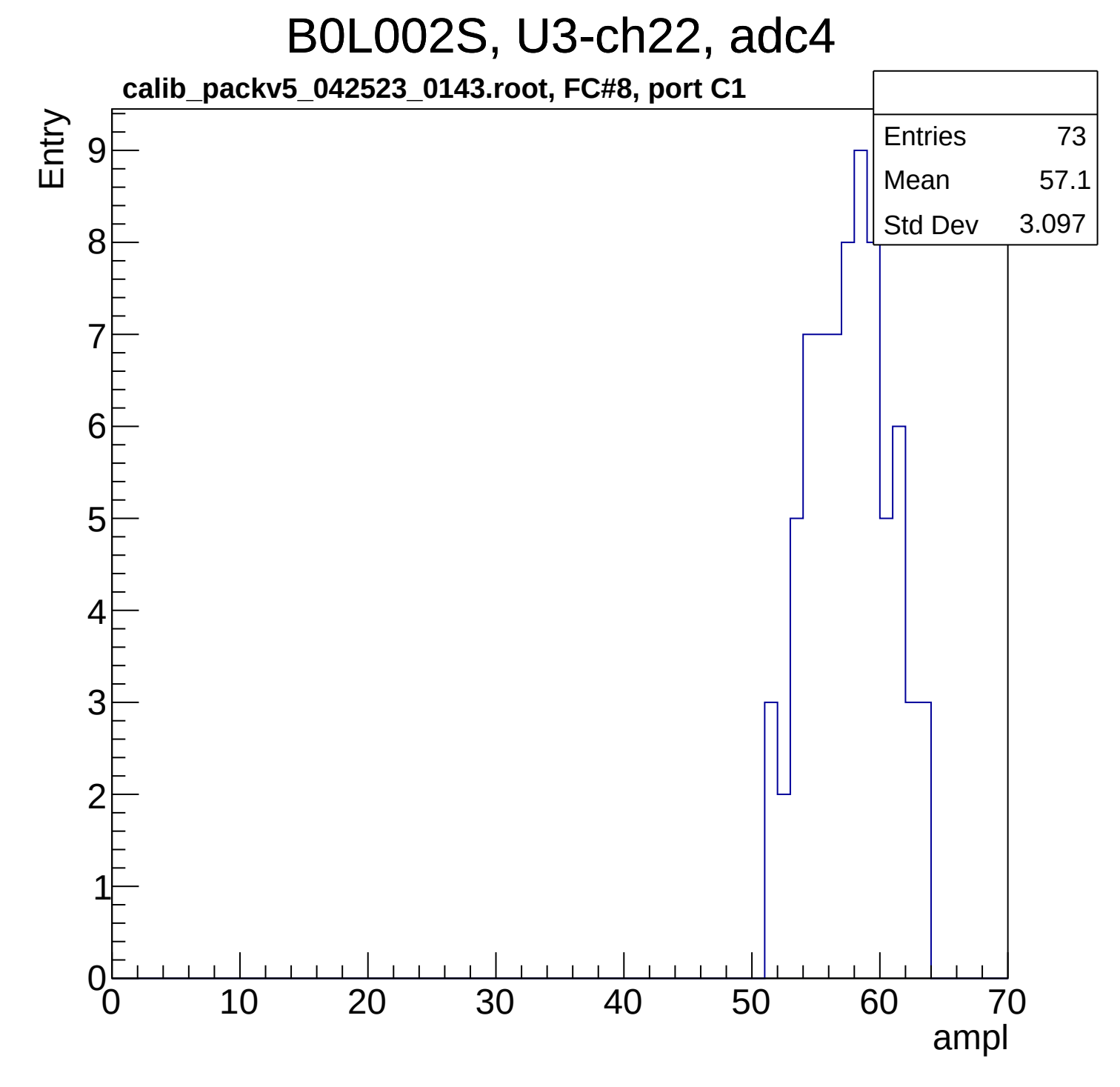
B0L002S, U3-ch22, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 57.1
Std Dev 3.097
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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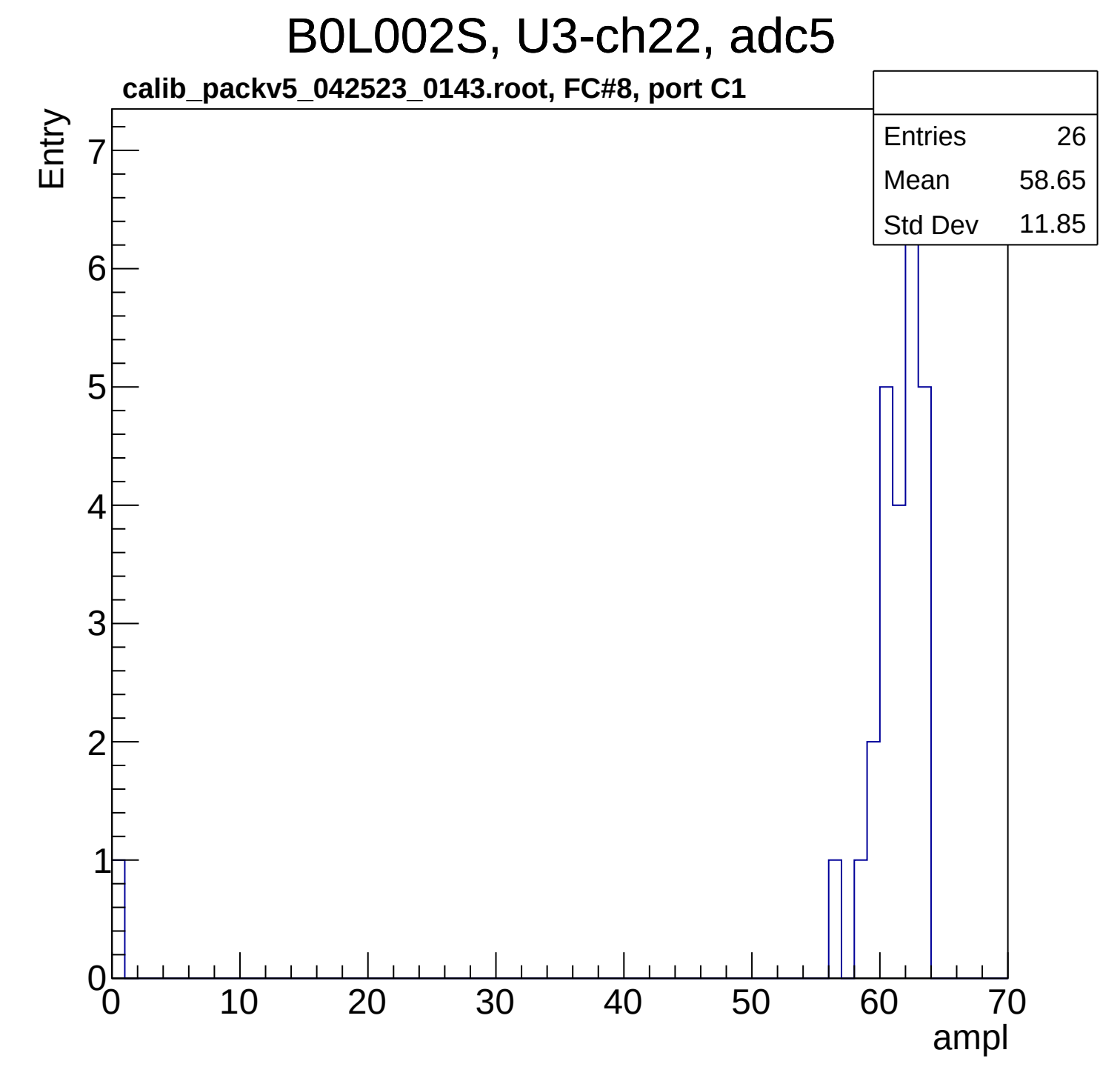
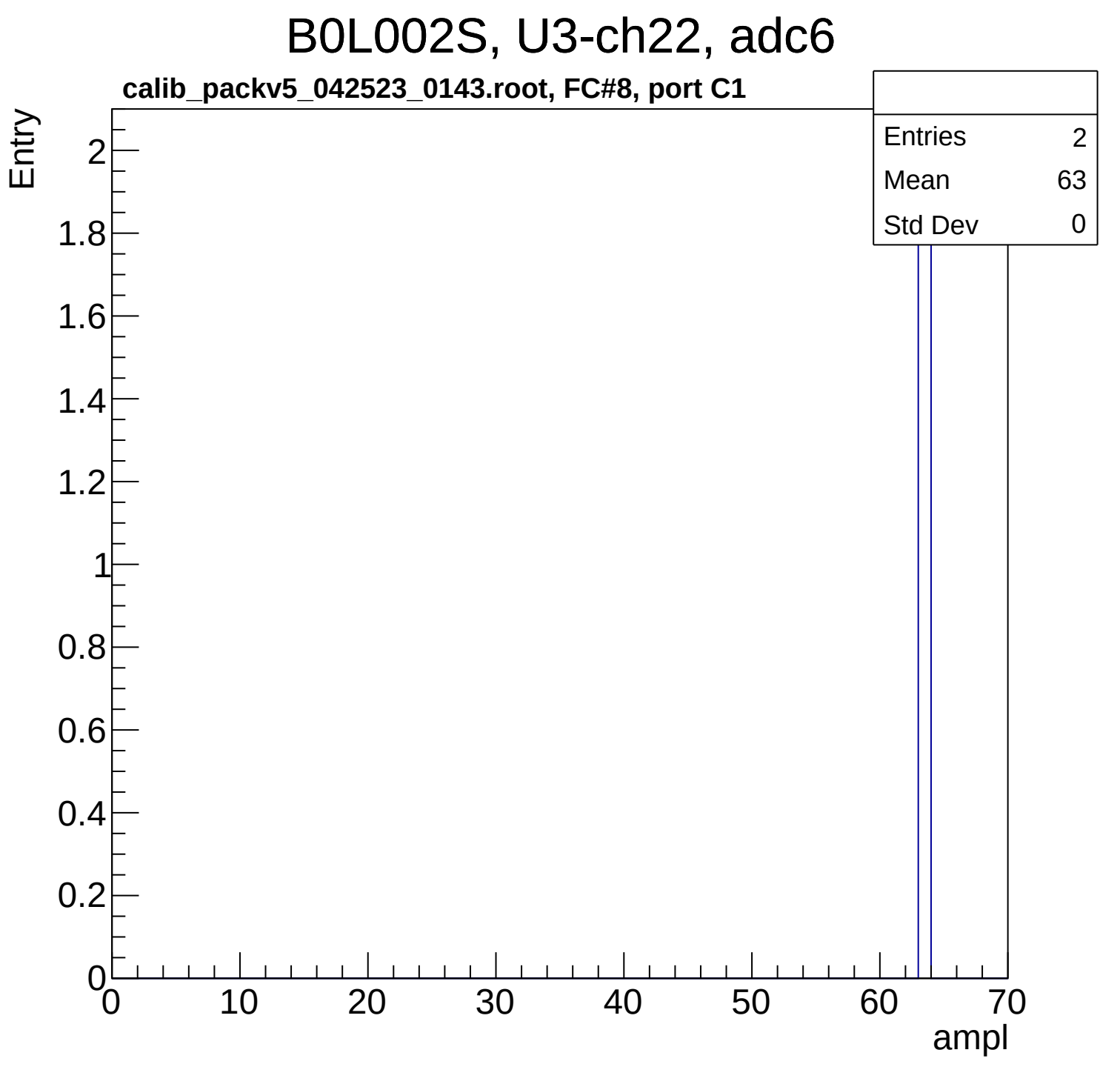
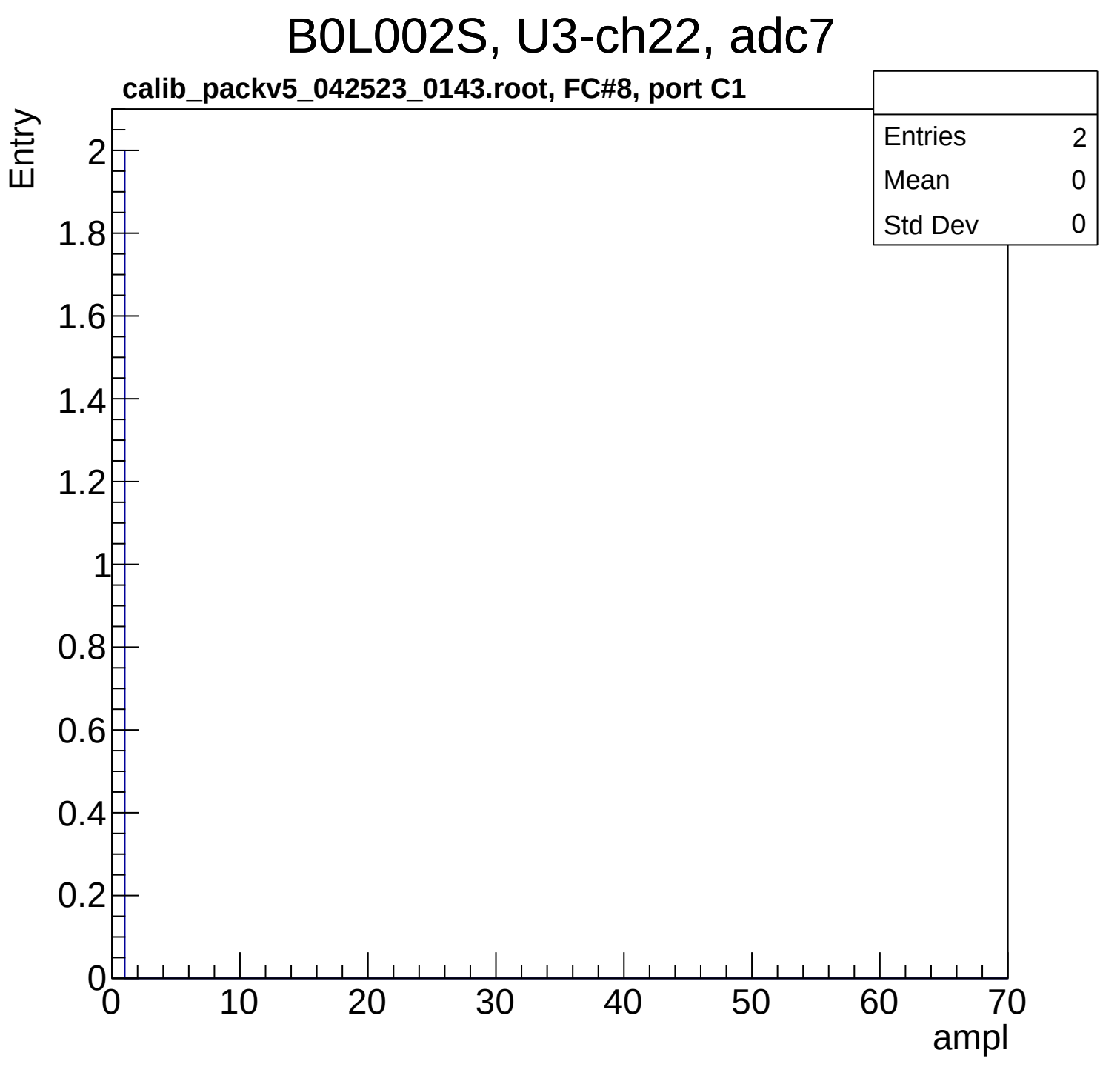

B0L002S, U3-ch22, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.65
Std Dev 11.85
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch22, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch22, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
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60
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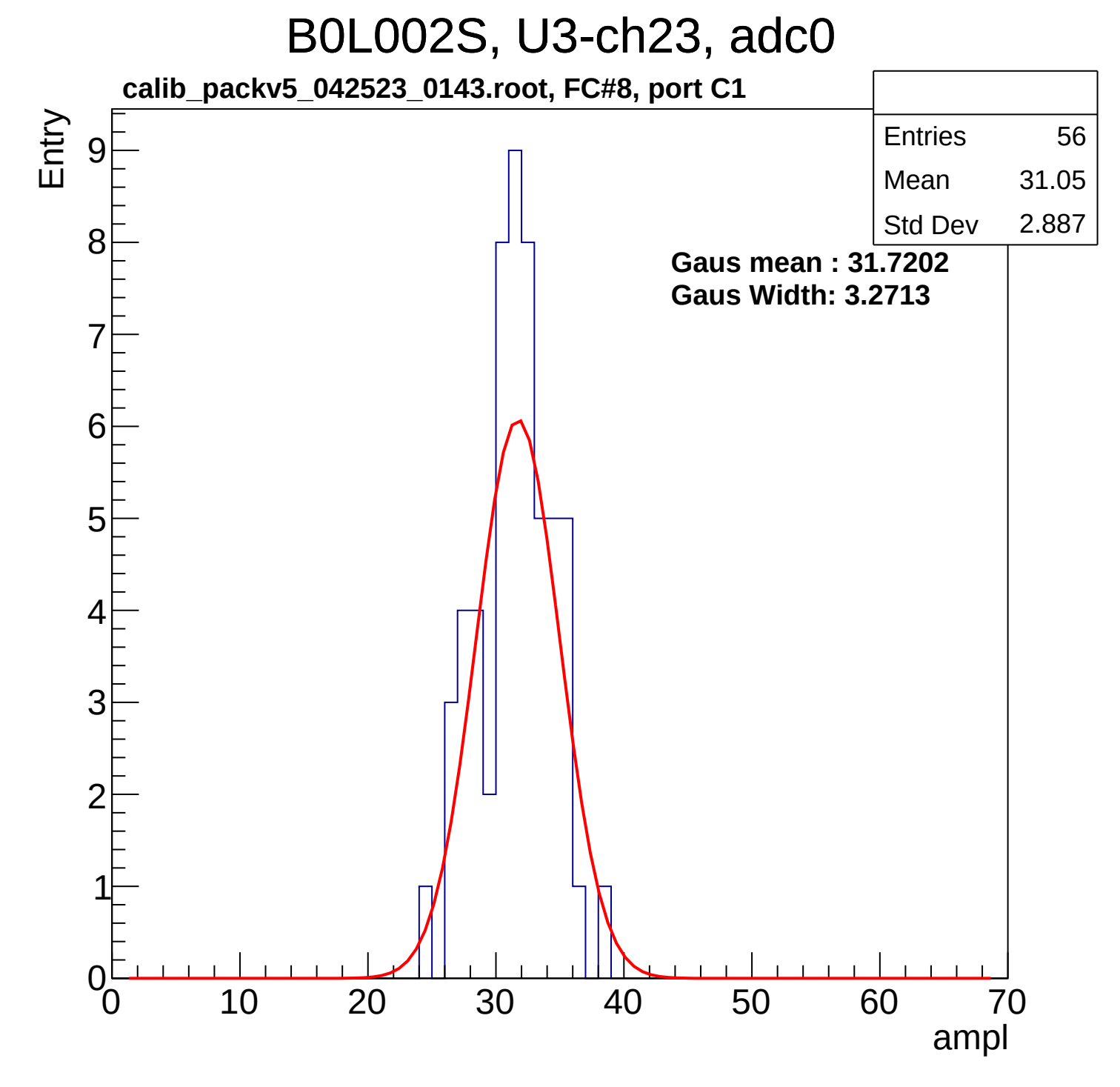
B0L002S, U3-ch23, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 31.05
Std Dev 2.887
Gaus mean : 31.7202
Gaus Width: 3.2713
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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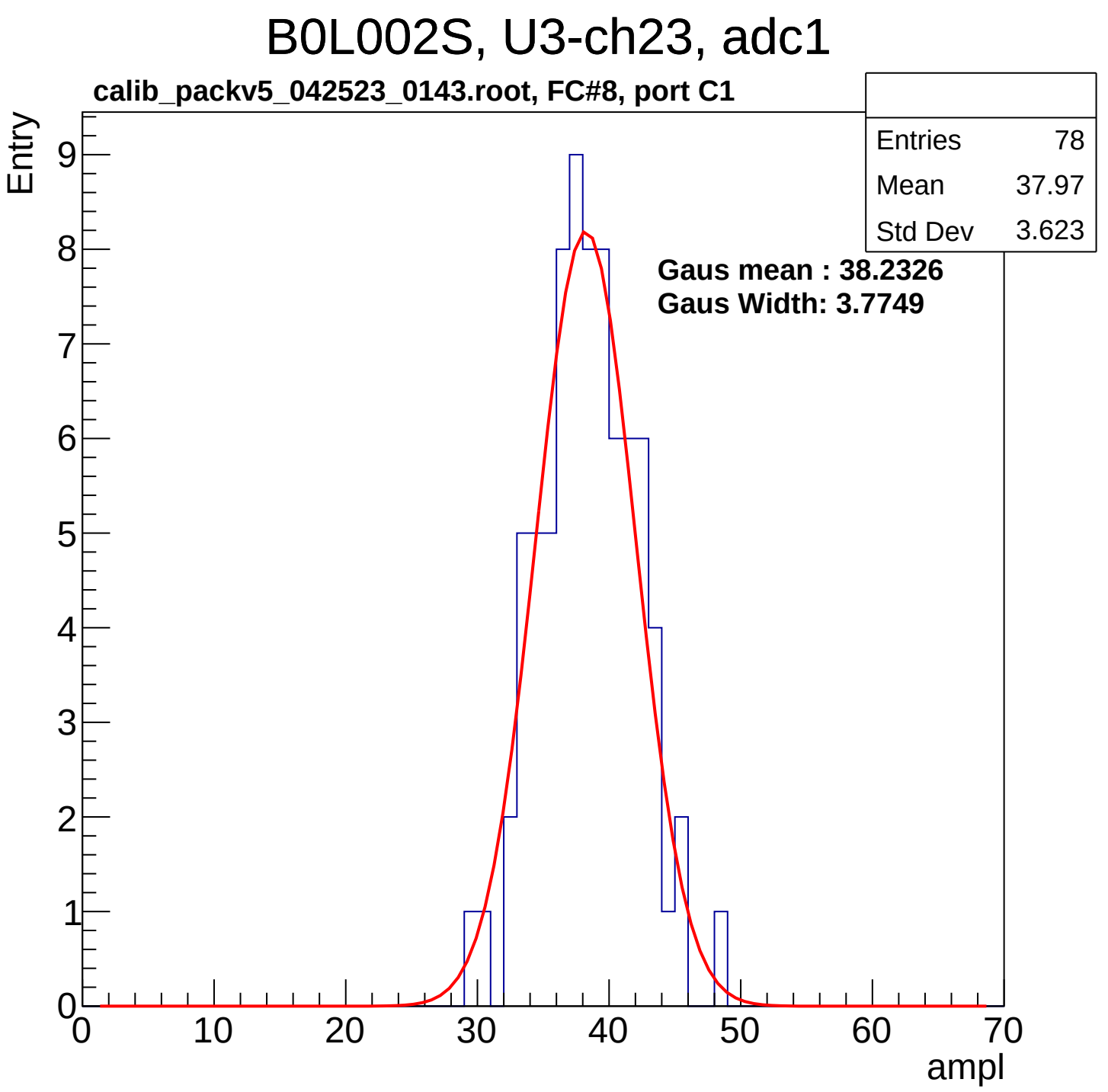
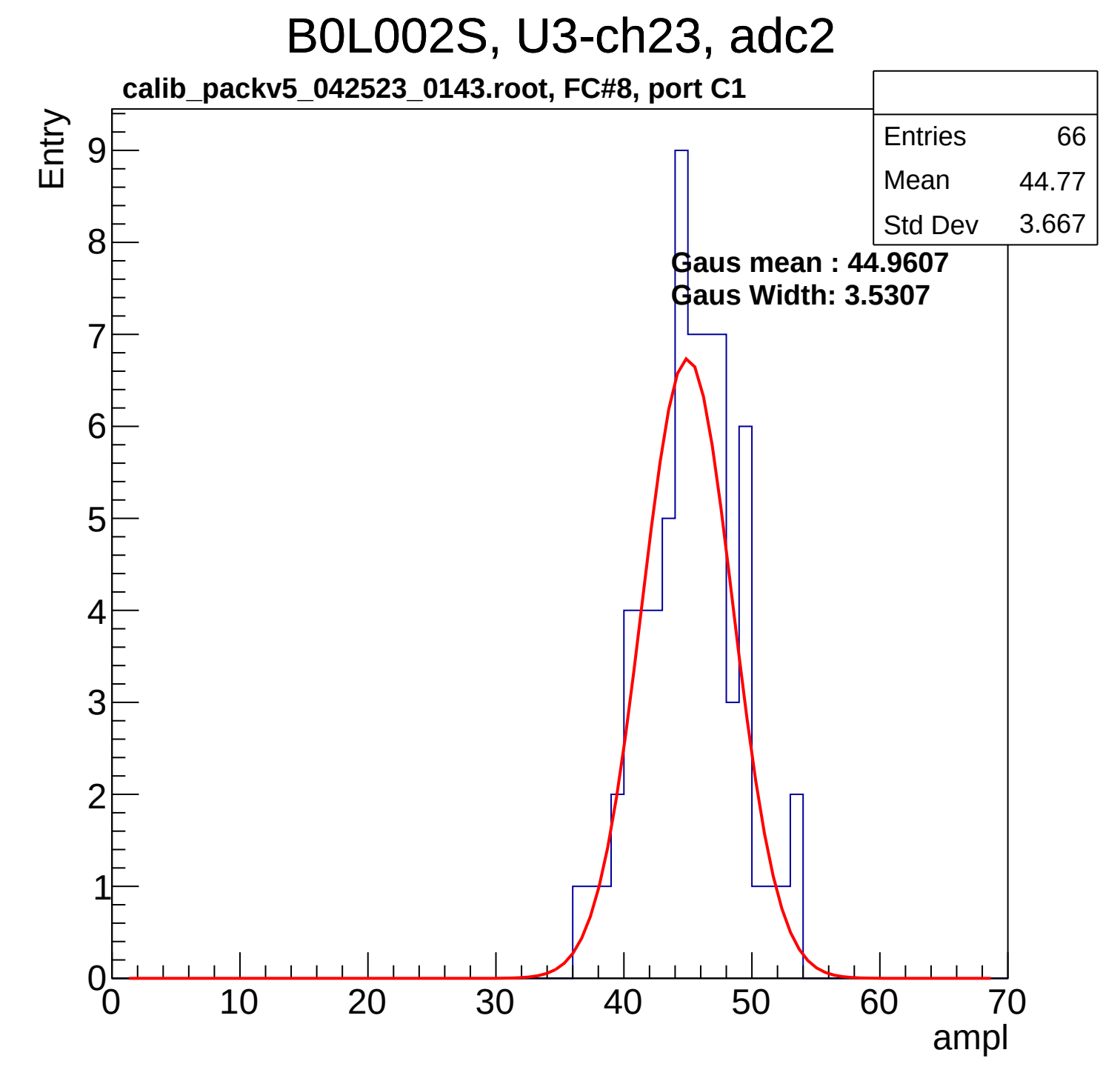

B0L002S, U3-ch23, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 37.97
Std Dev 3.623
Gaus mean : 38.2326
Gaus Width: 3.7749
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch23, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 44.77
Std Dev 3.667
Gaus mean : 44.9607
Gaus Width: 3.5307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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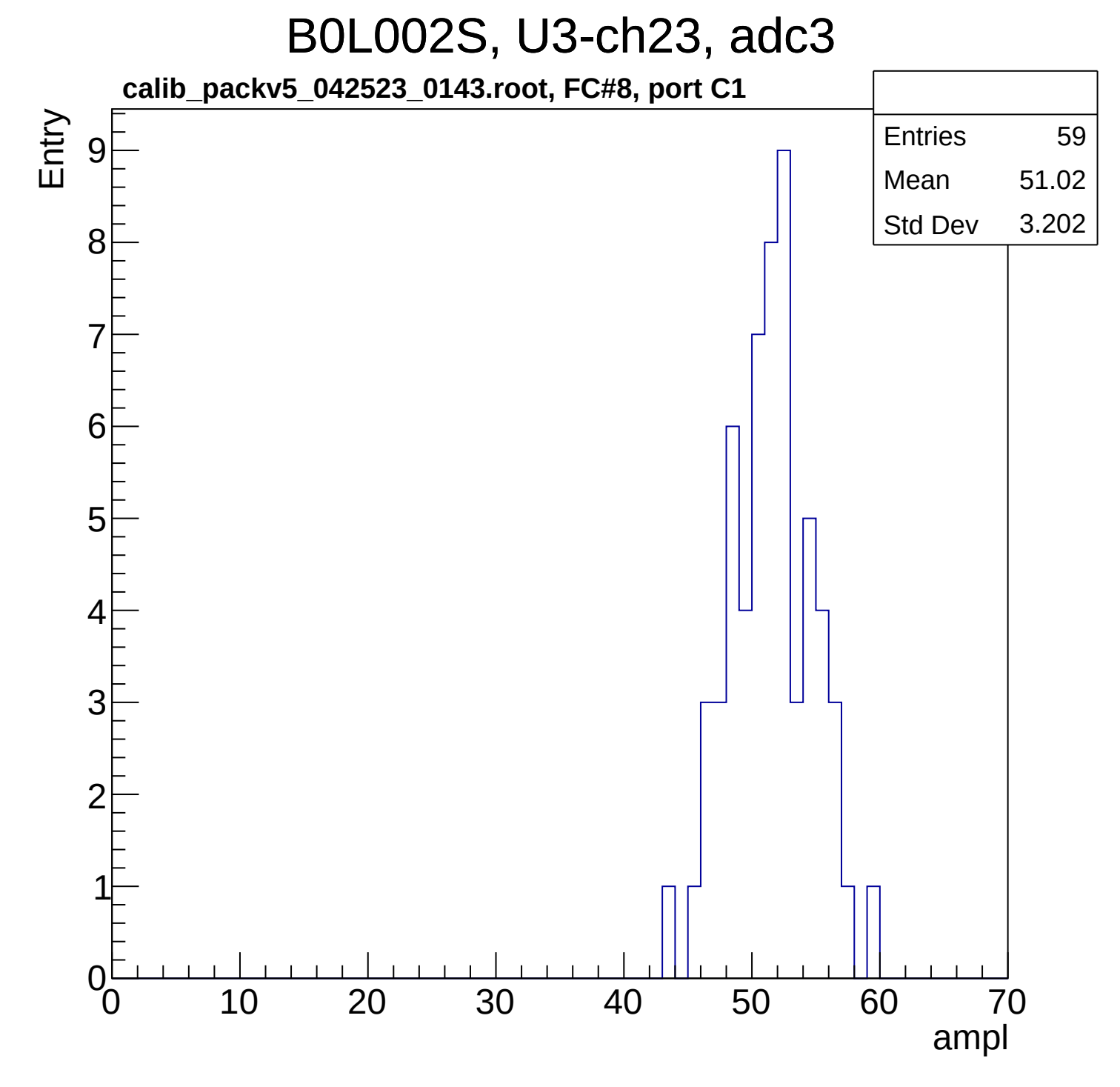
B0L002S, U3-ch23, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 51.02
Std Dev 3.202
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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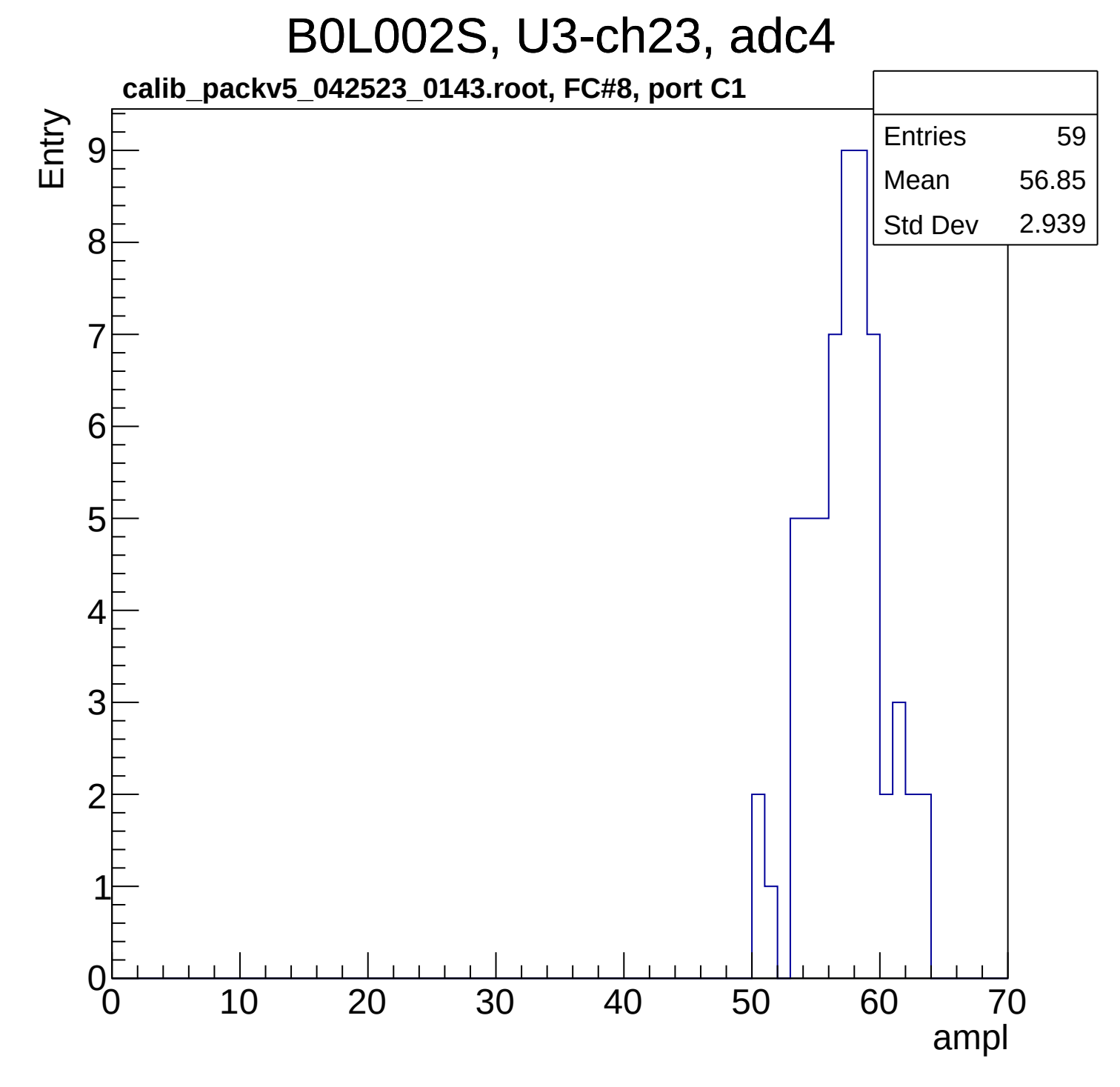
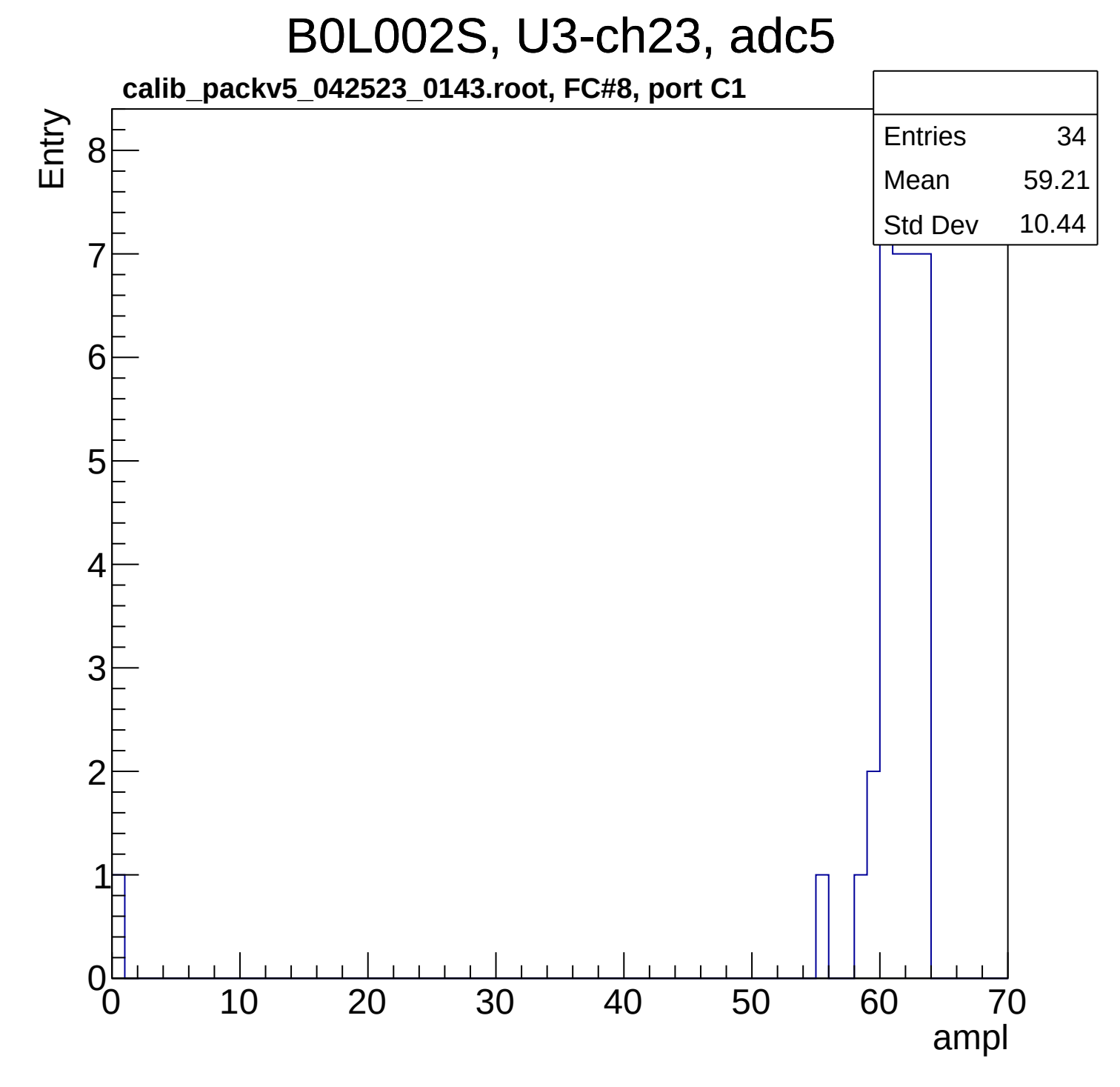

B0L002S, U3-ch23, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.85
Std Dev 2.939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch23, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.21
Std Dev 10.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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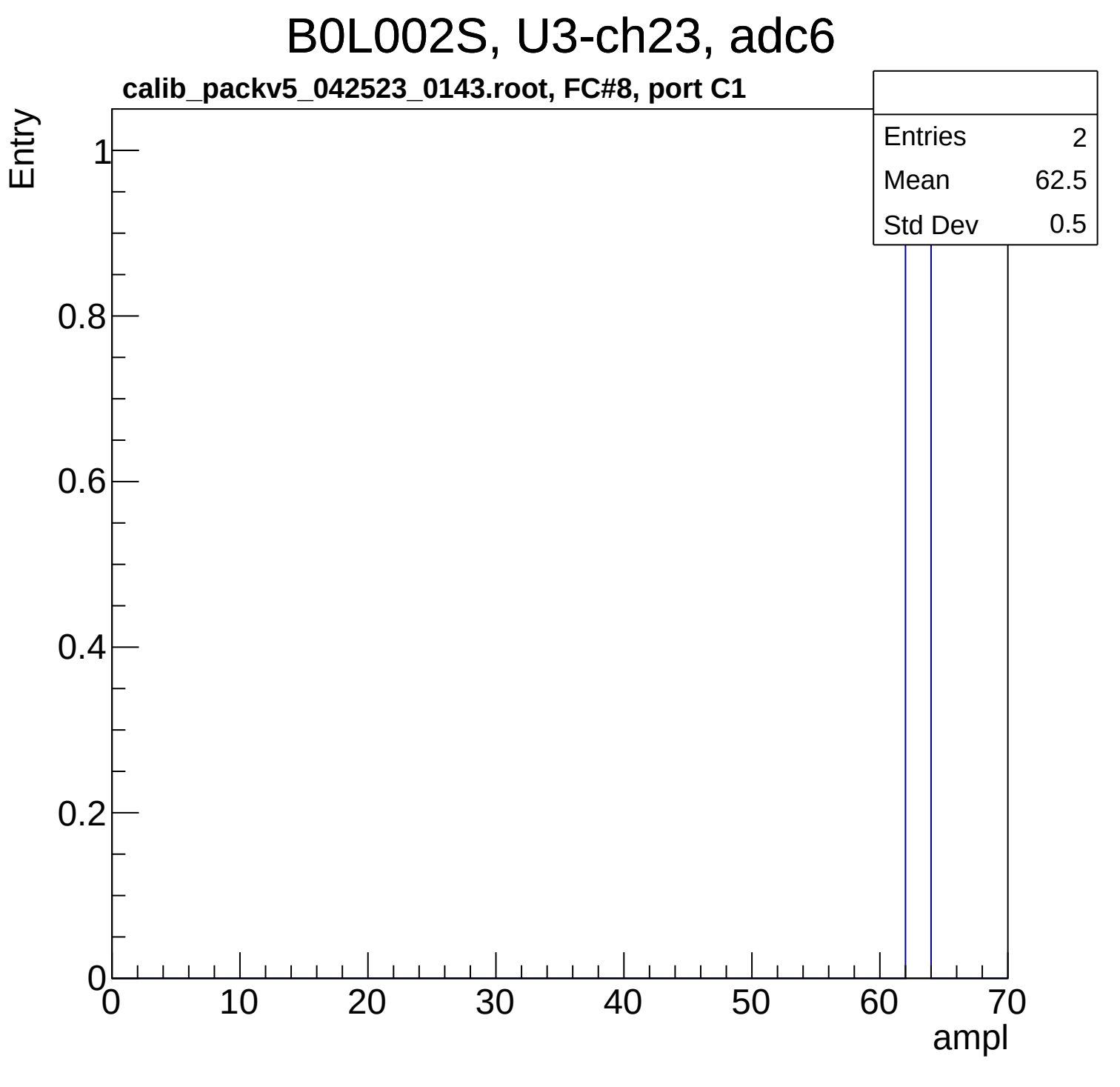
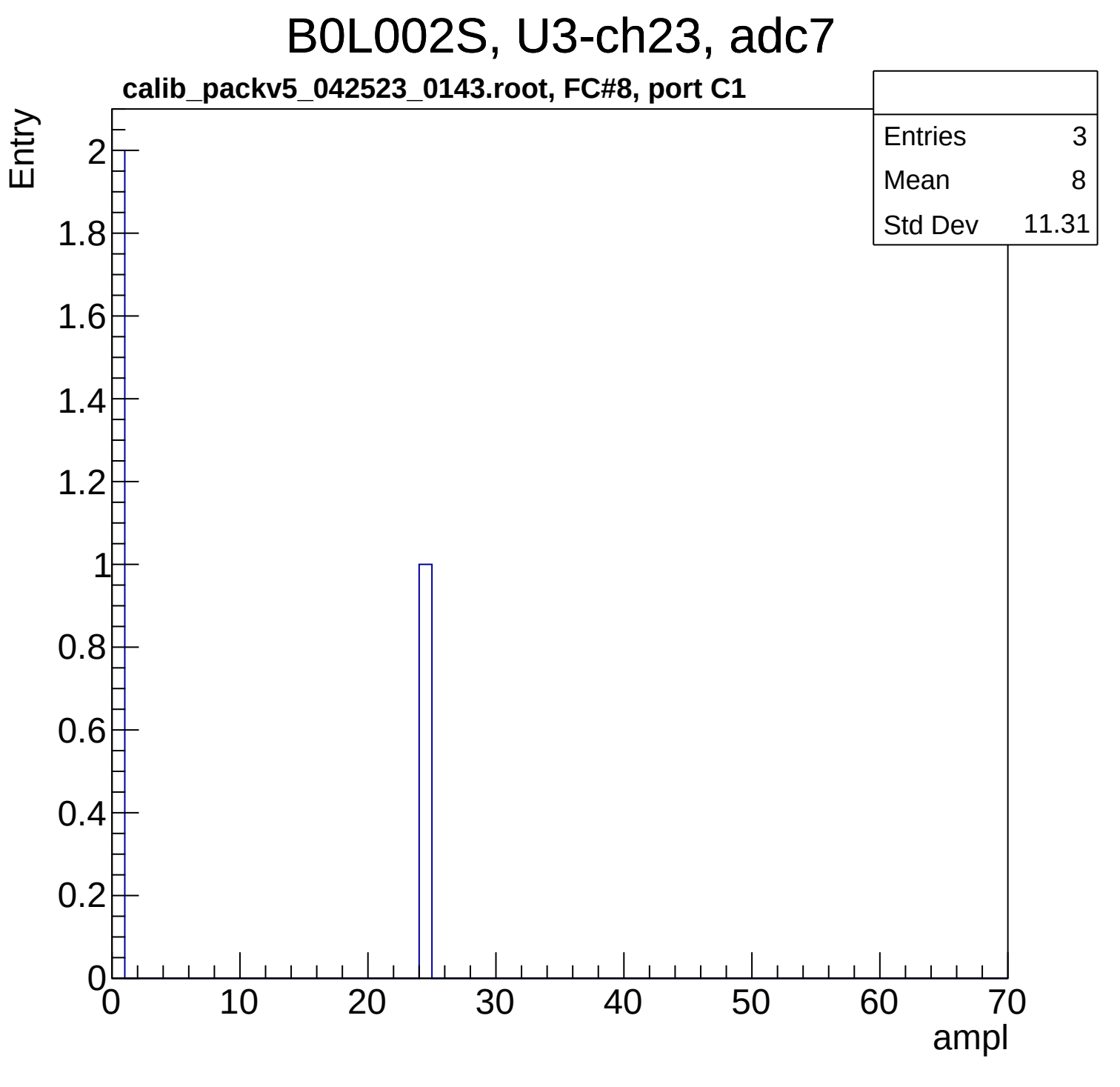
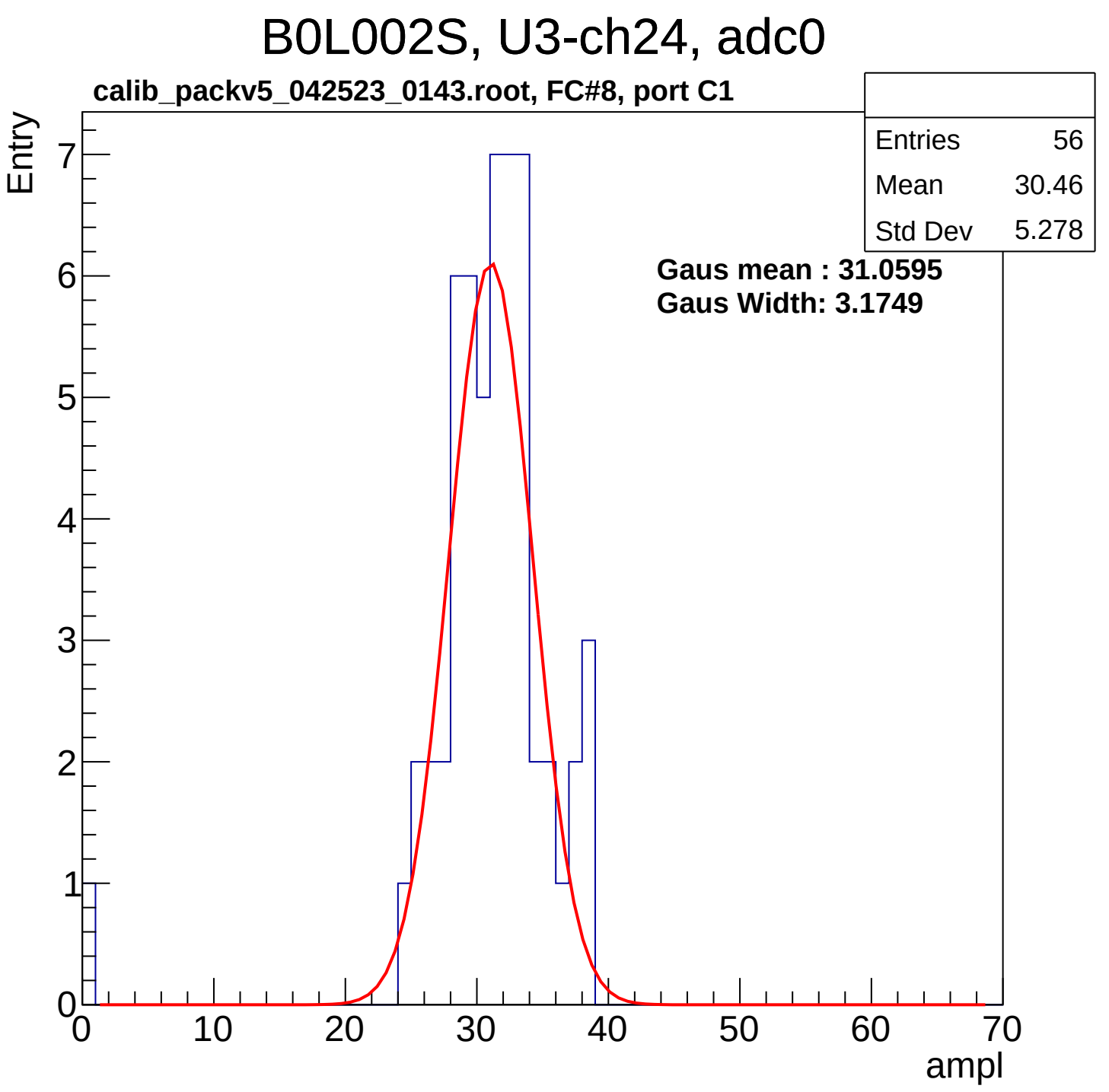
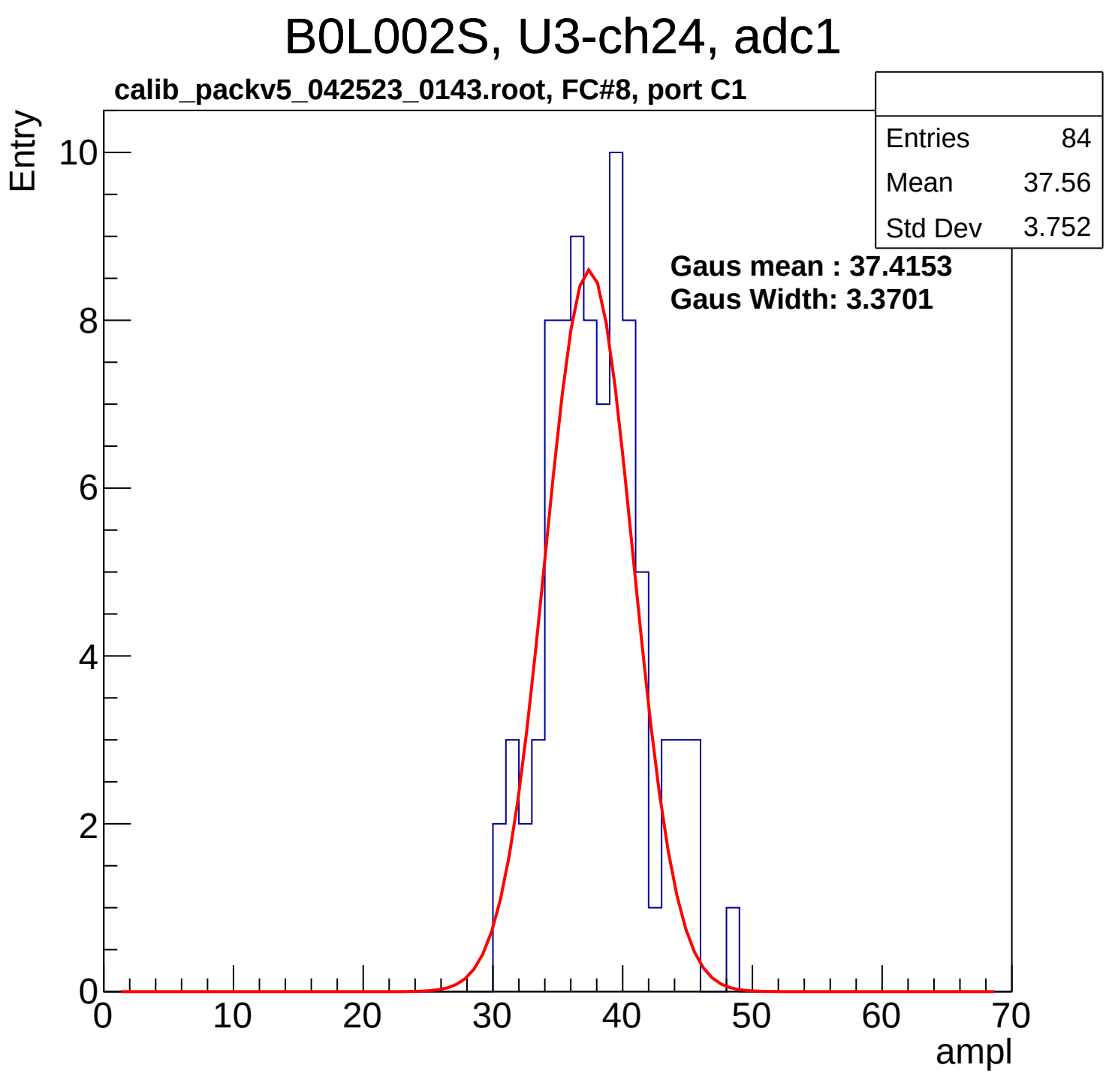
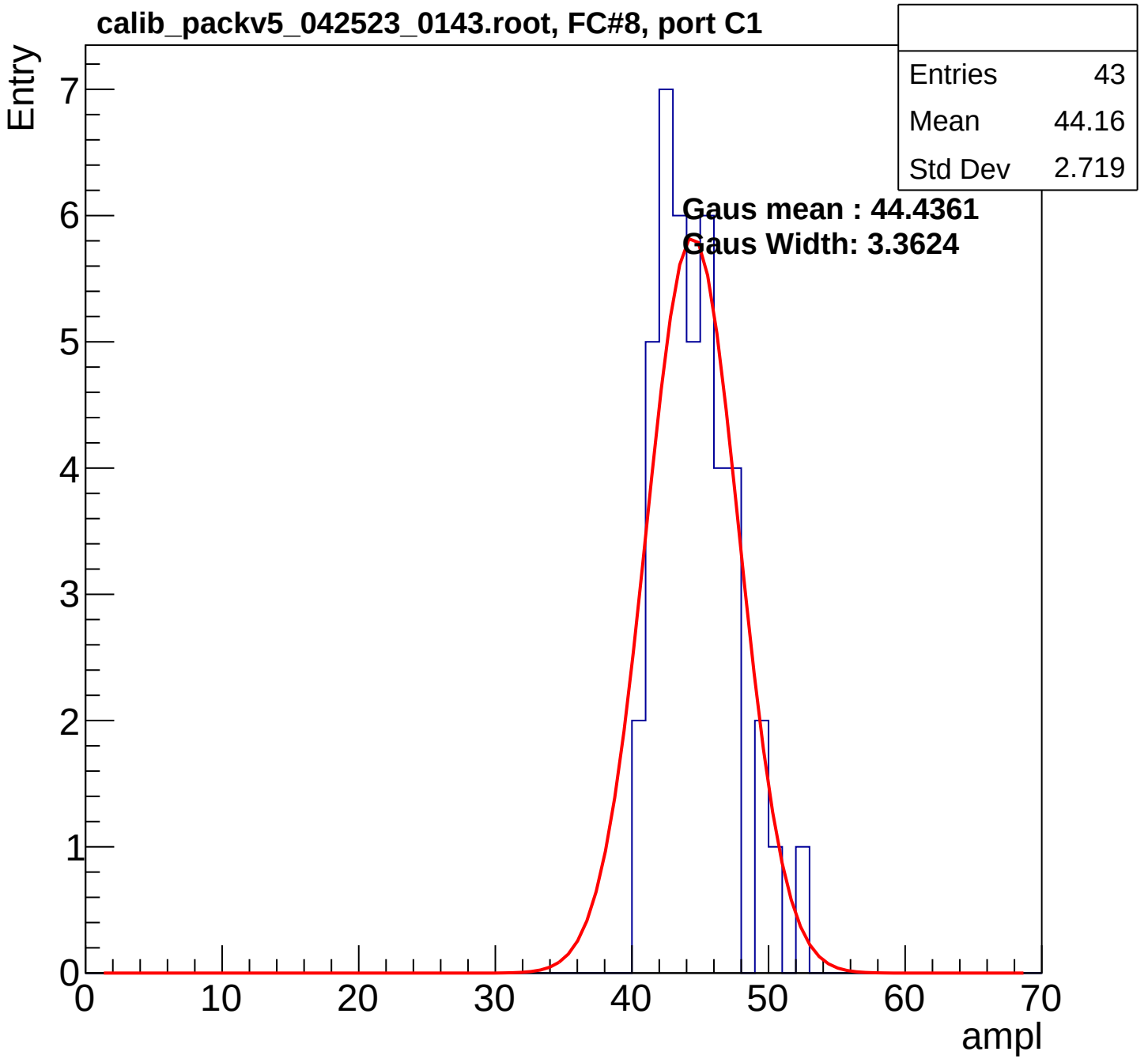
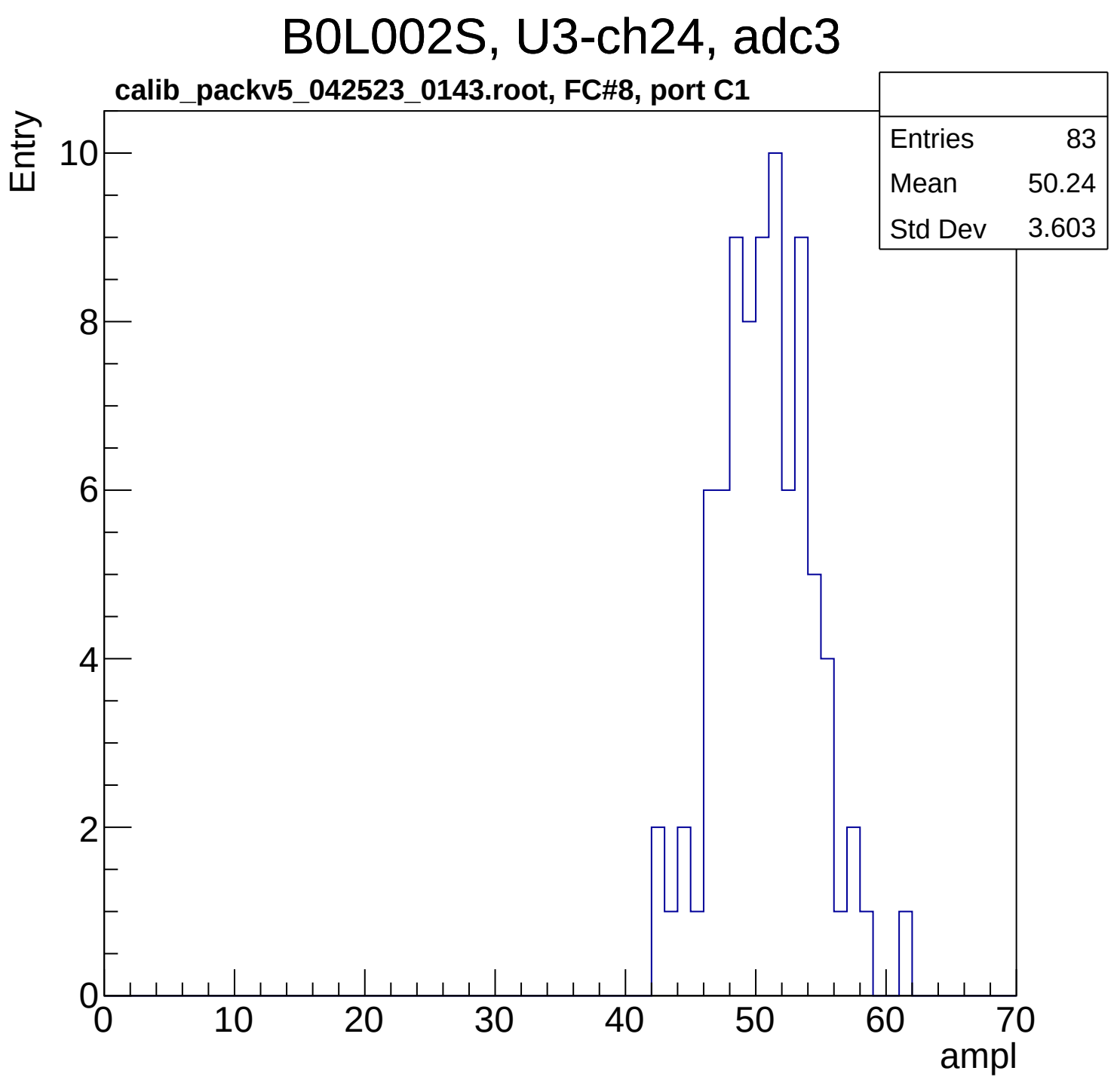

B0L002S, U3-ch23, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

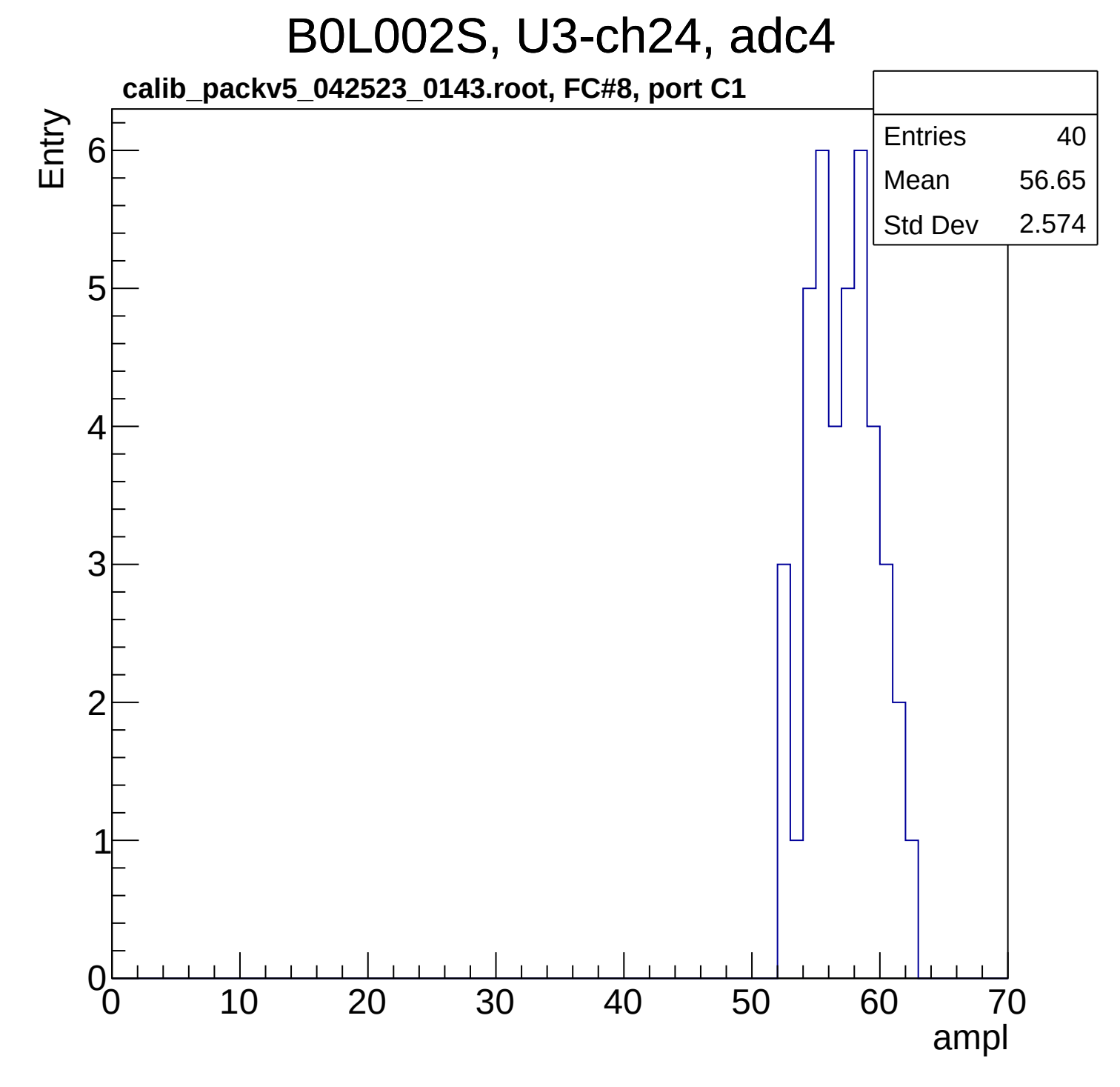

B0L002S, U3-ch23, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

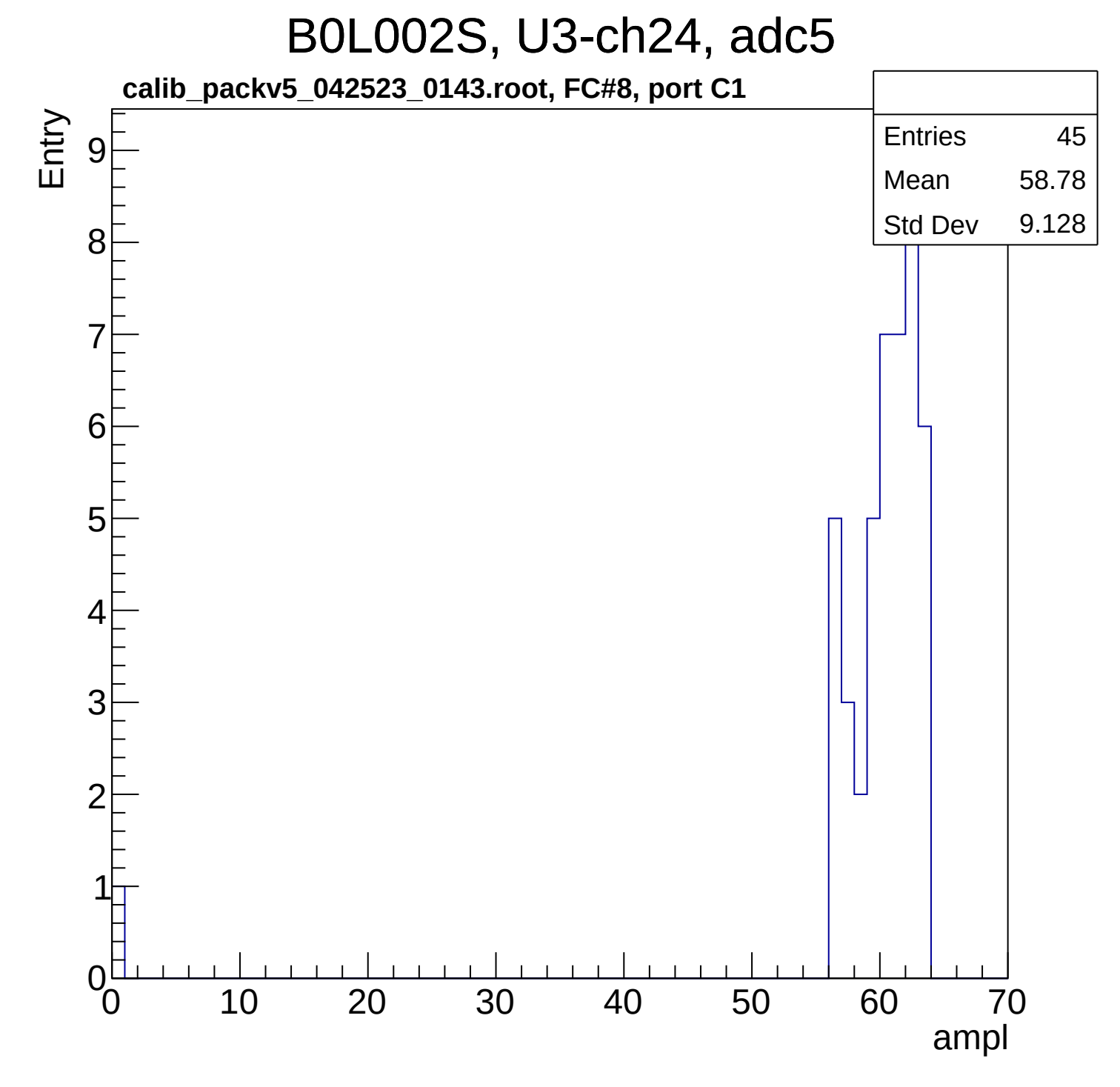

B0L002S, U3-ch24, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 30.46
Std Dev 5.278
Gaus mean : 31.0595
Gaus Width: 3.1749
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

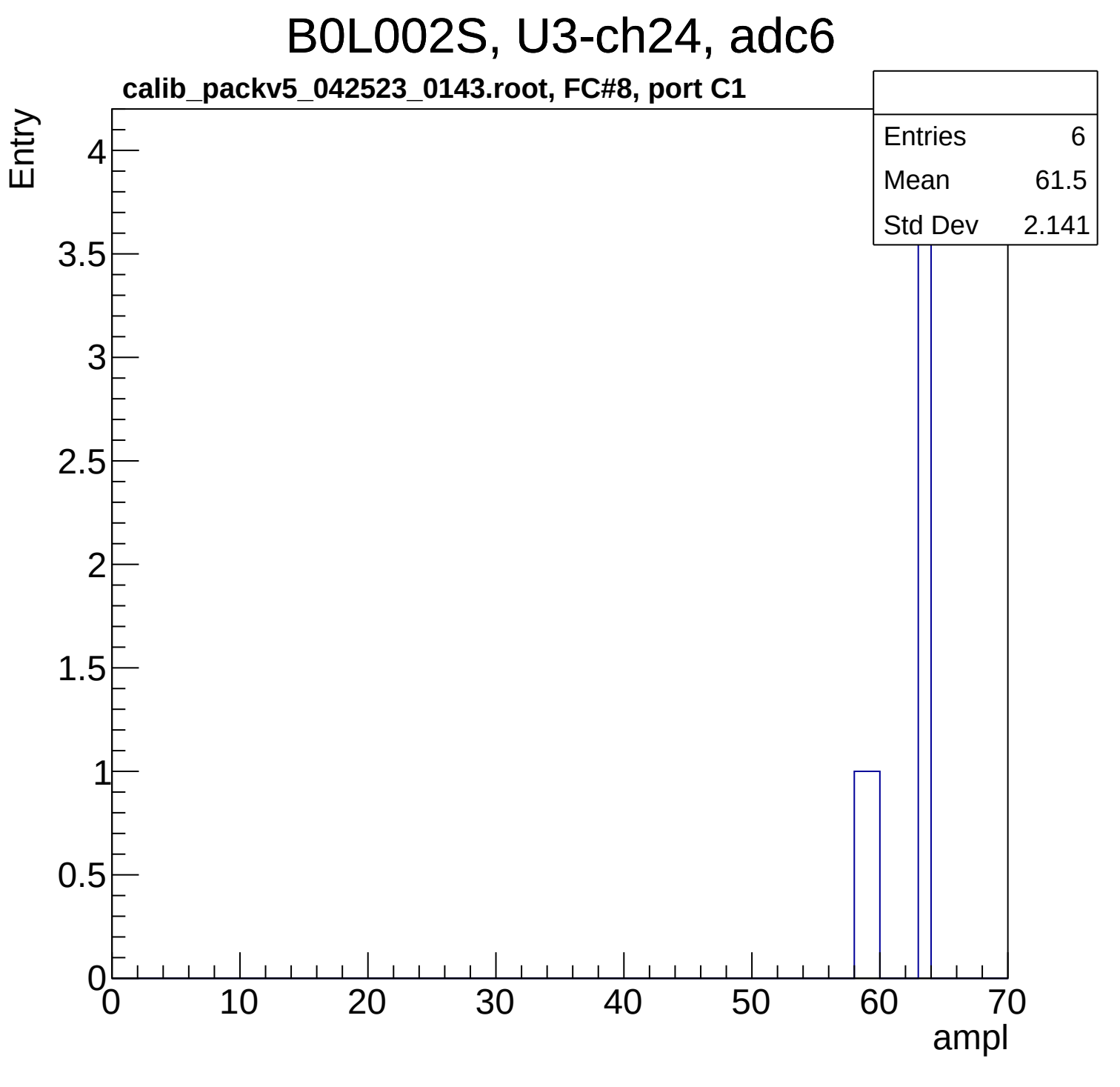

B0L002S, U3-ch24, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 37.56
Std Dev 3.752
Gaus mean : 37.4153
Gaus Width: 3.3701
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

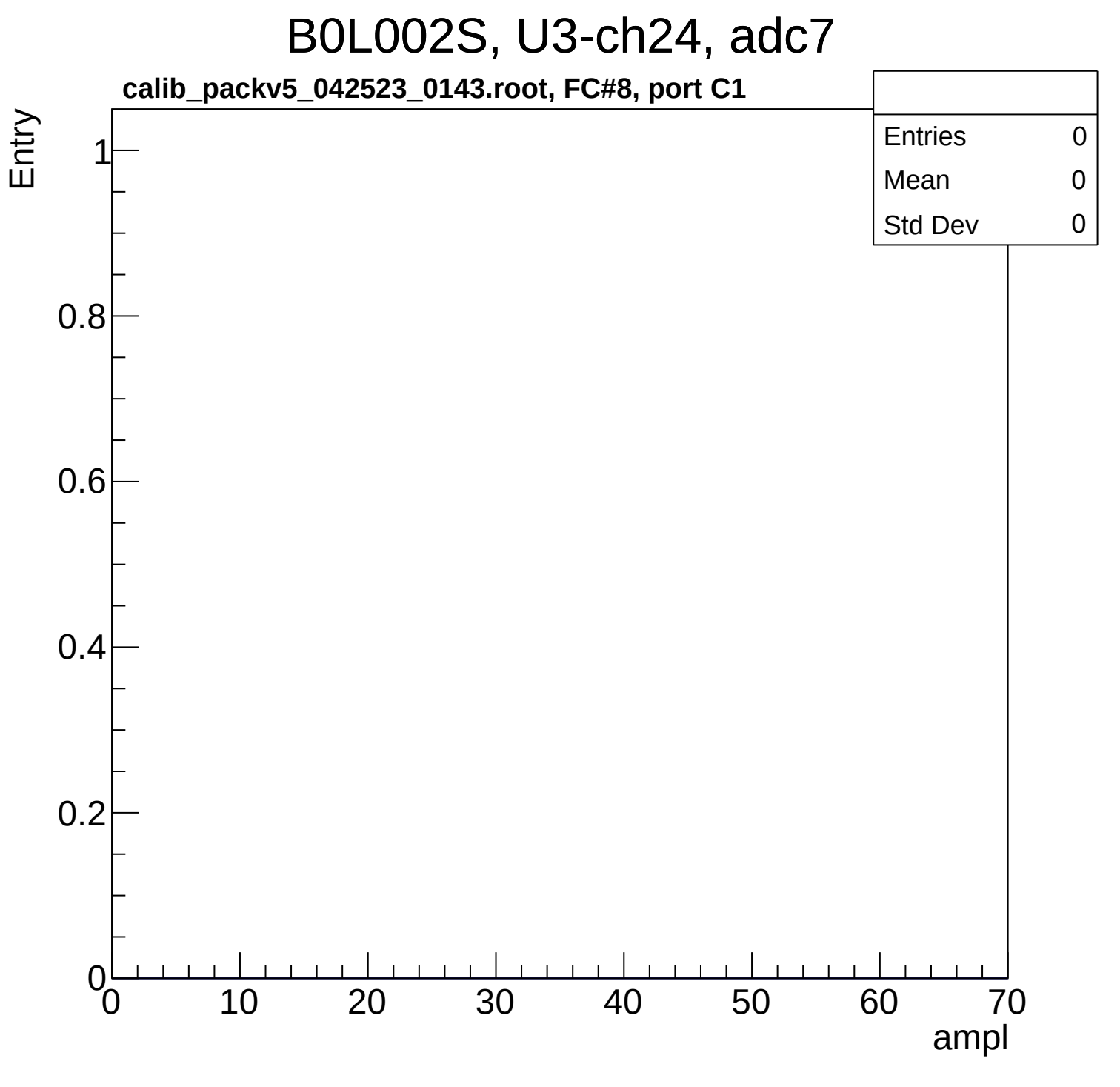

B0L002S, U3-ch24, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 44.16
Std Dev 2.719
Gaus mean : 44.4361
Gaus Width: 3.3624
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

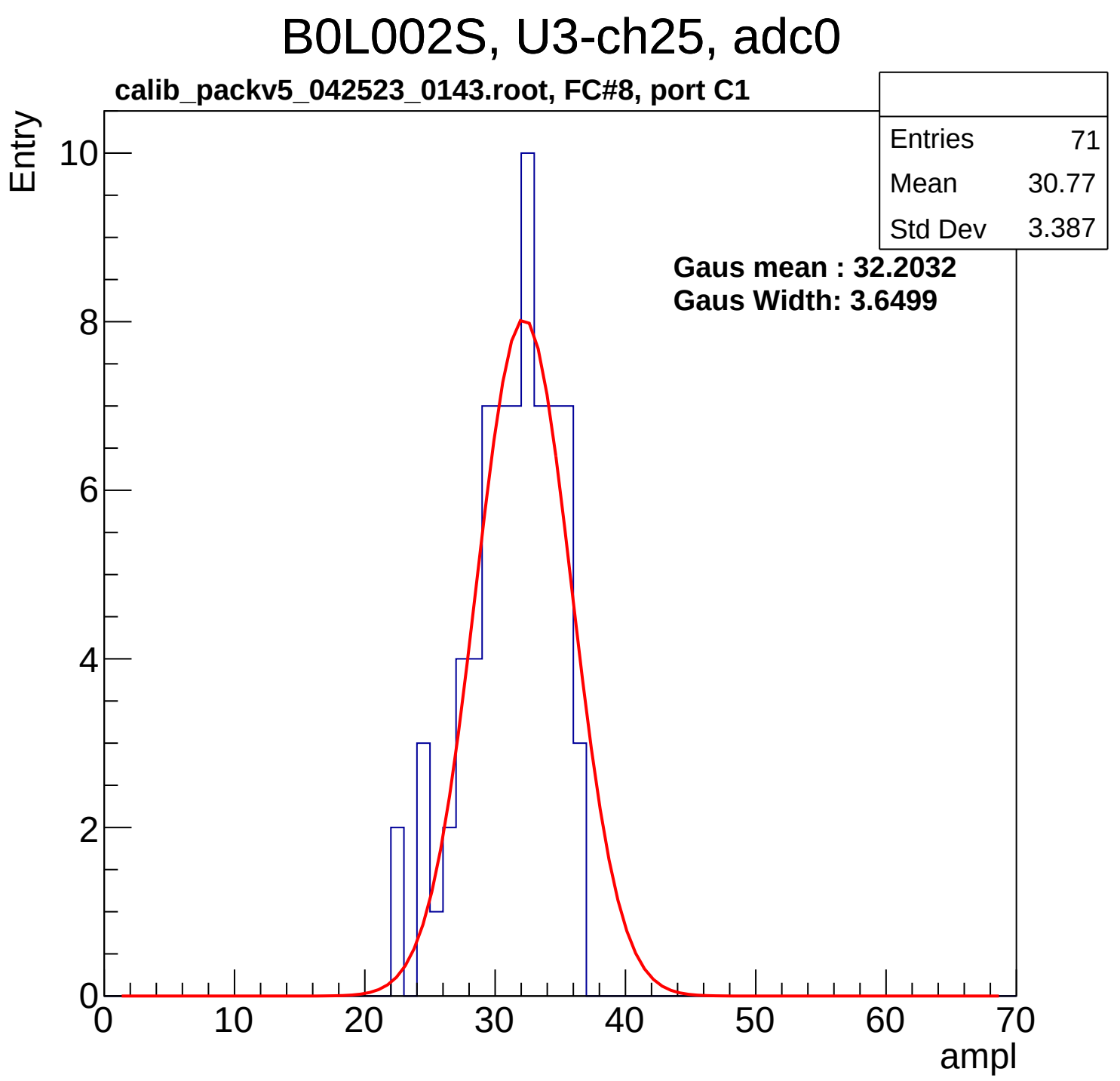

B0L002S, U3-ch24, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 50.24
Std Dev 3.603
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

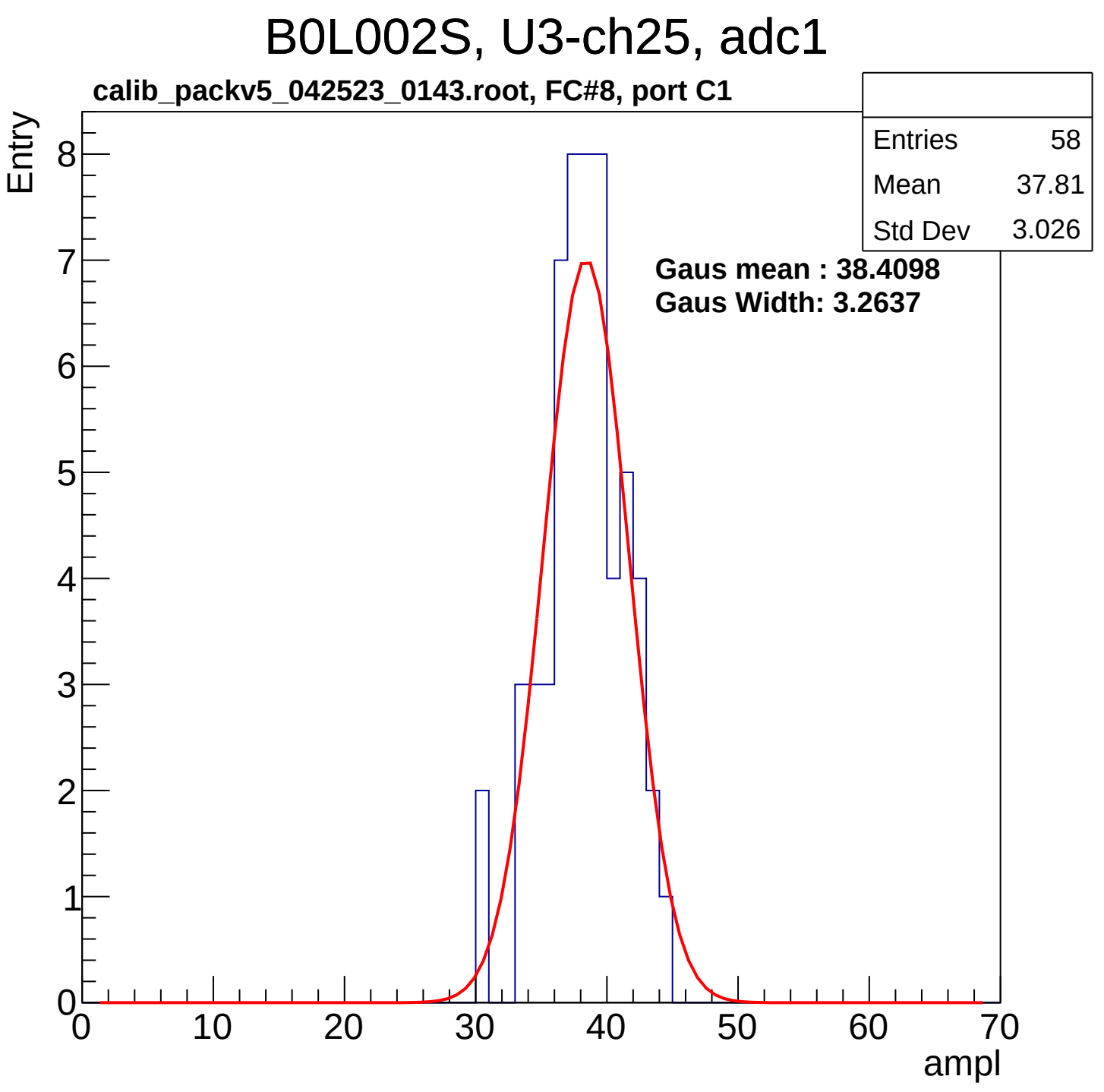

B0L002S, U3-ch24, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 56.65
Std Dev 2.574
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U3-ch24, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 58.78
Std Dev 9.128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch24, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.5
Std Dev 2.141
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U3-ch24, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch25, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 30.77
Std Dev 3.387
Gaus mean : 32.2032
Gaus Width: 3.6499
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch25, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 37.81
Std Dev 3.026
Gaus mean : 38.4098
Gaus Width: 3.2637
Entry
ampl
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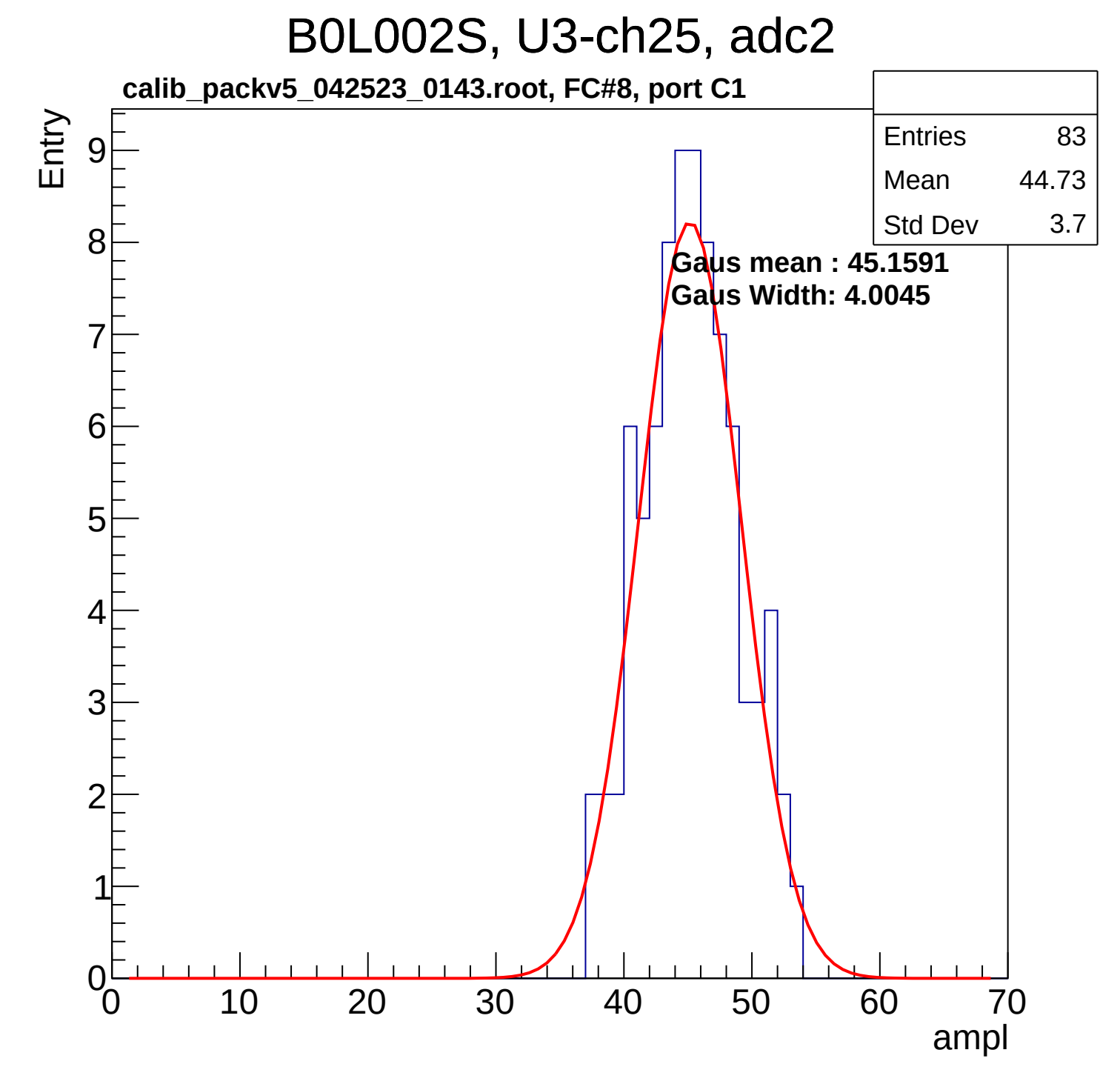
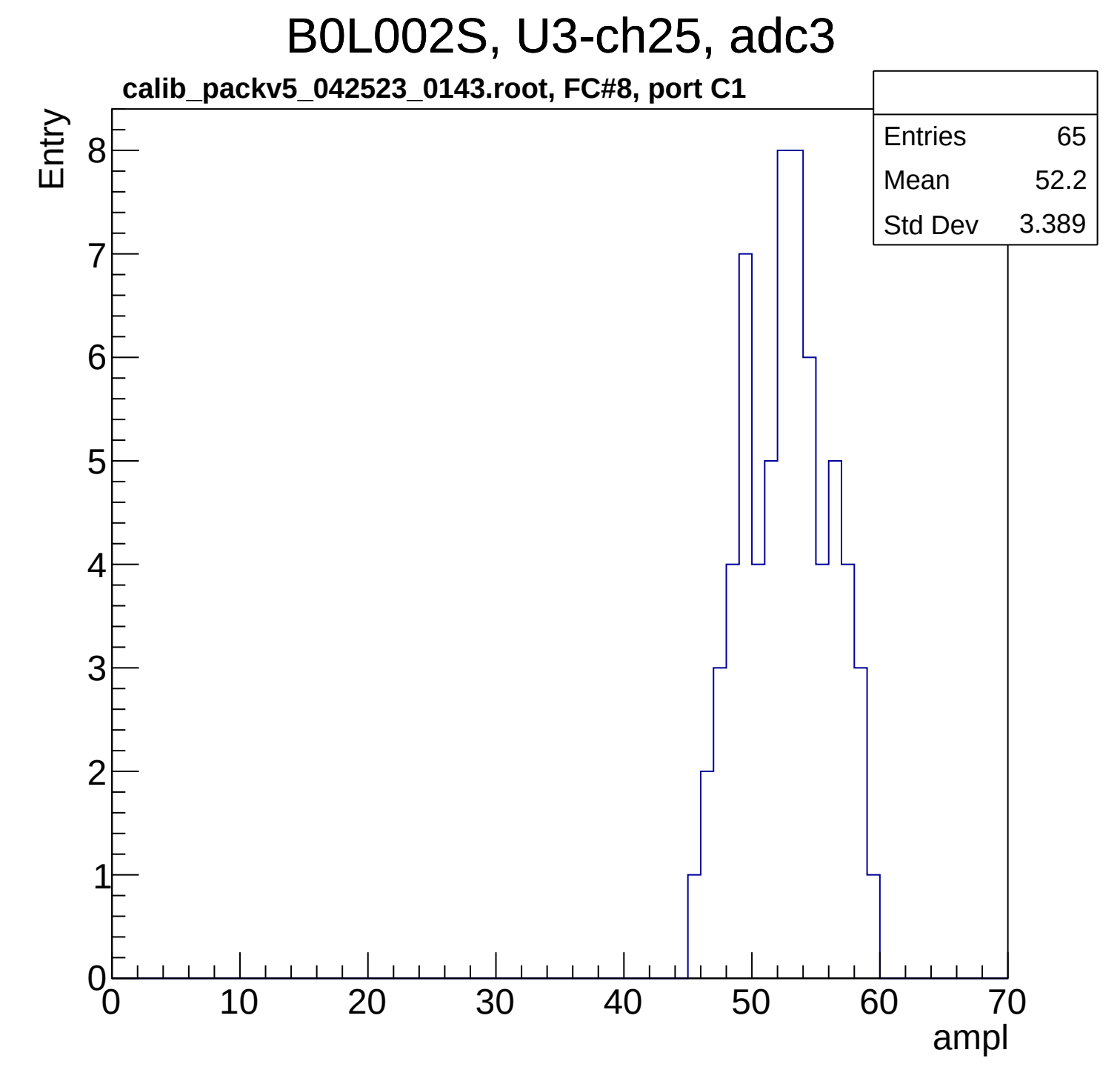
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch25, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 44.73
Std Dev 3.7
Gaus mean : 45.1591
Gaus Width: 4.0045
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch25, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 52.2
Std Dev 3.389
Entry
ampl
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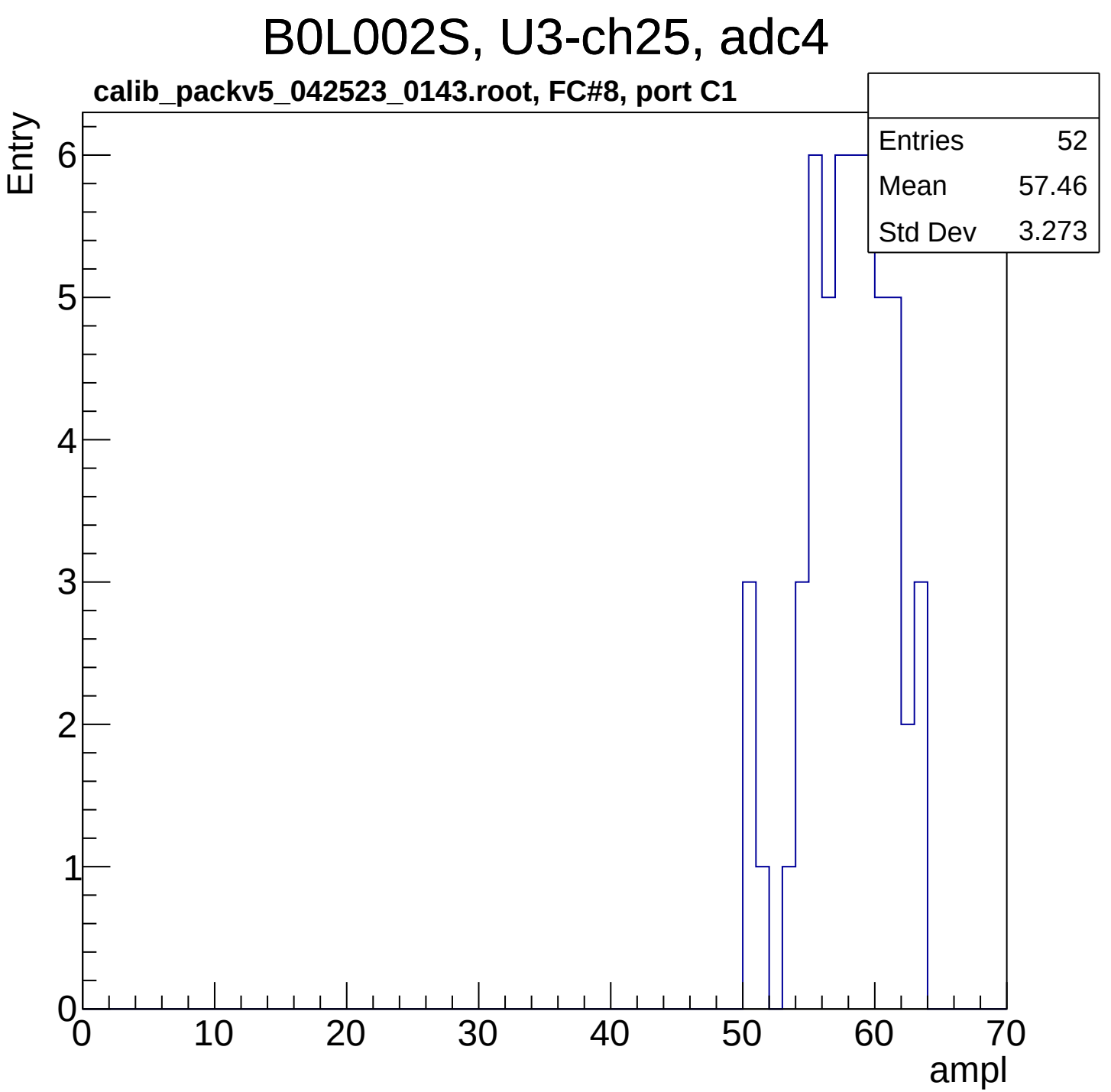
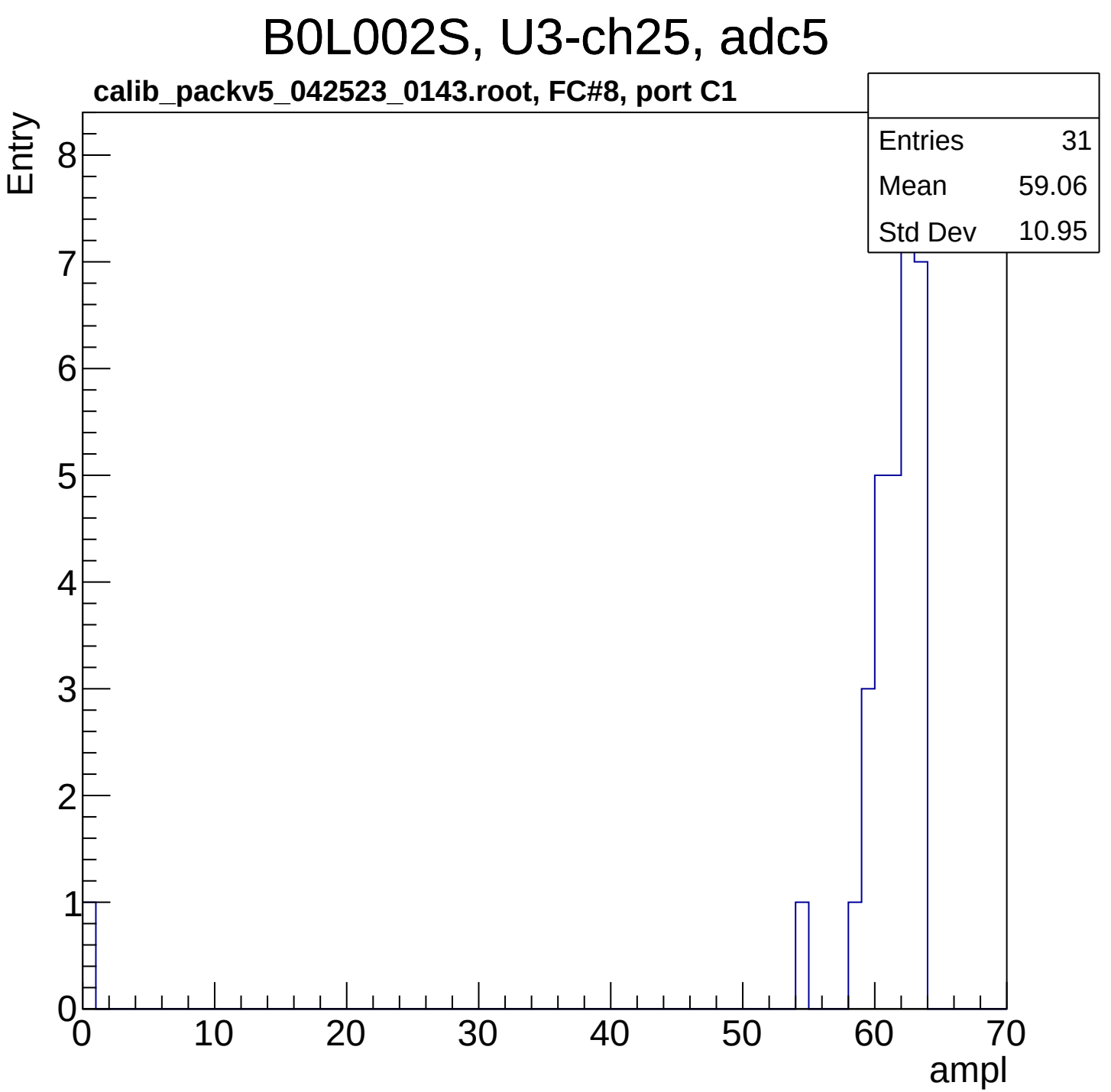
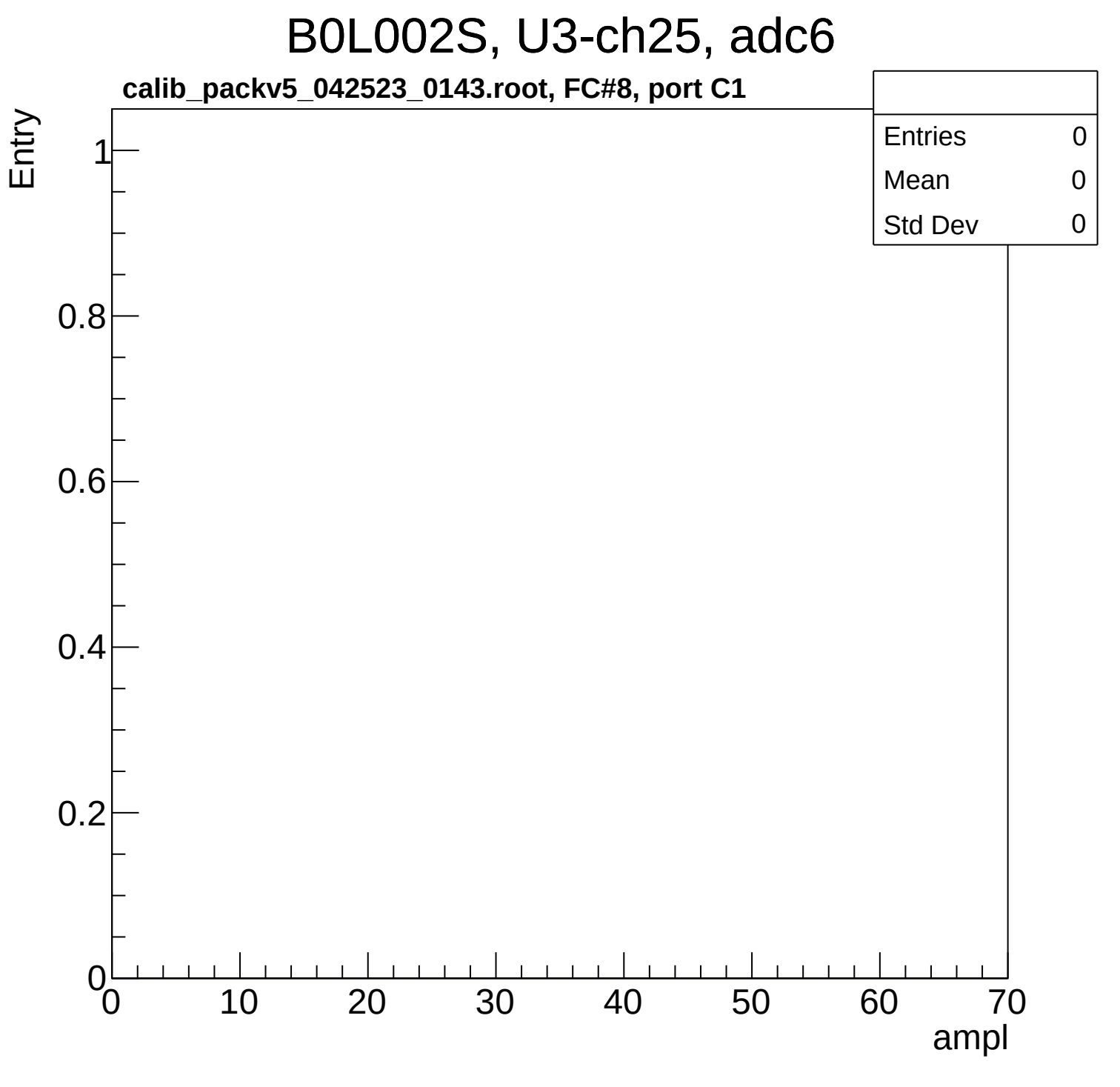
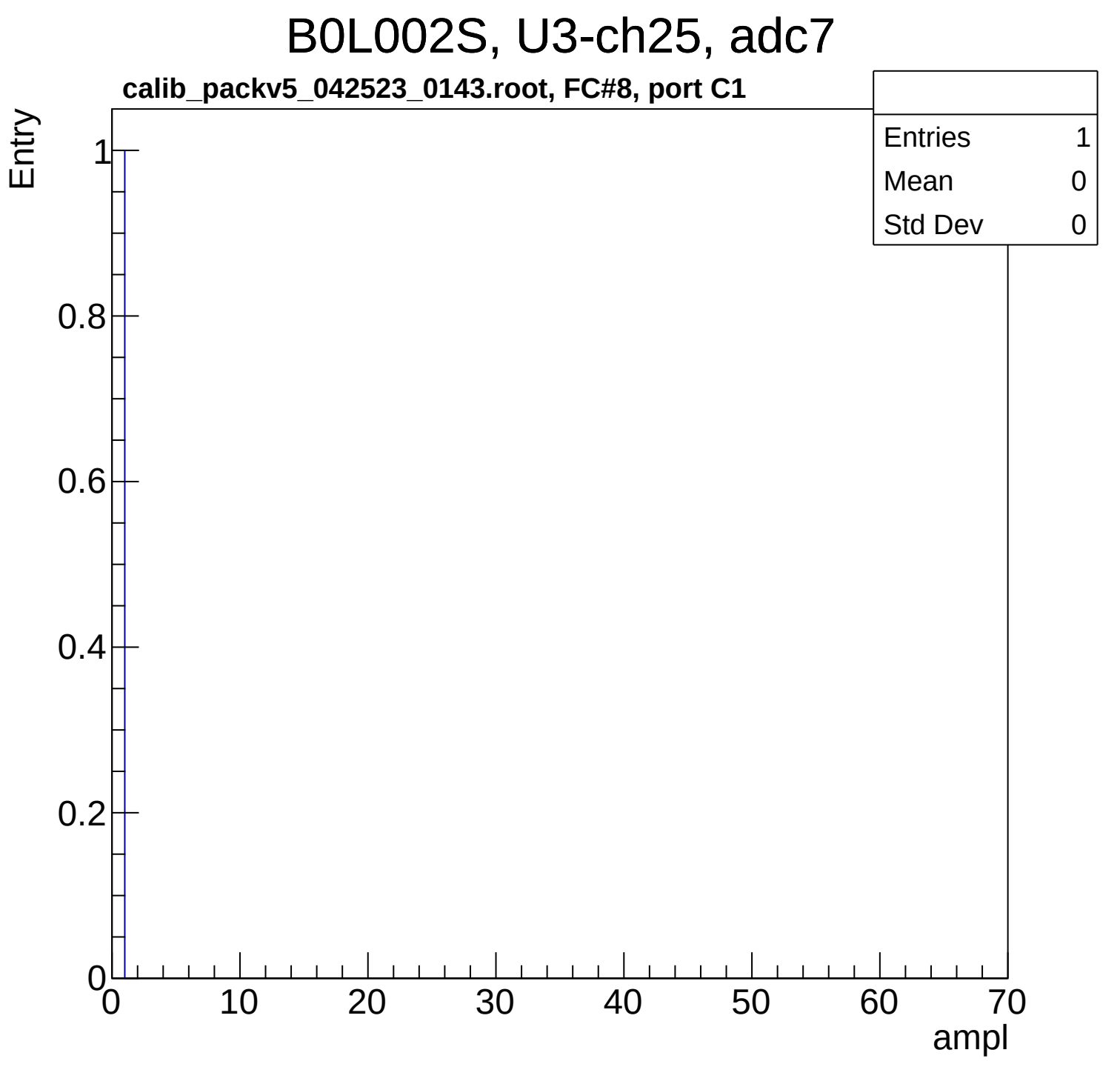
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch25, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.46
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U3-ch25, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.06
Std Dev 10.95
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch25, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch25, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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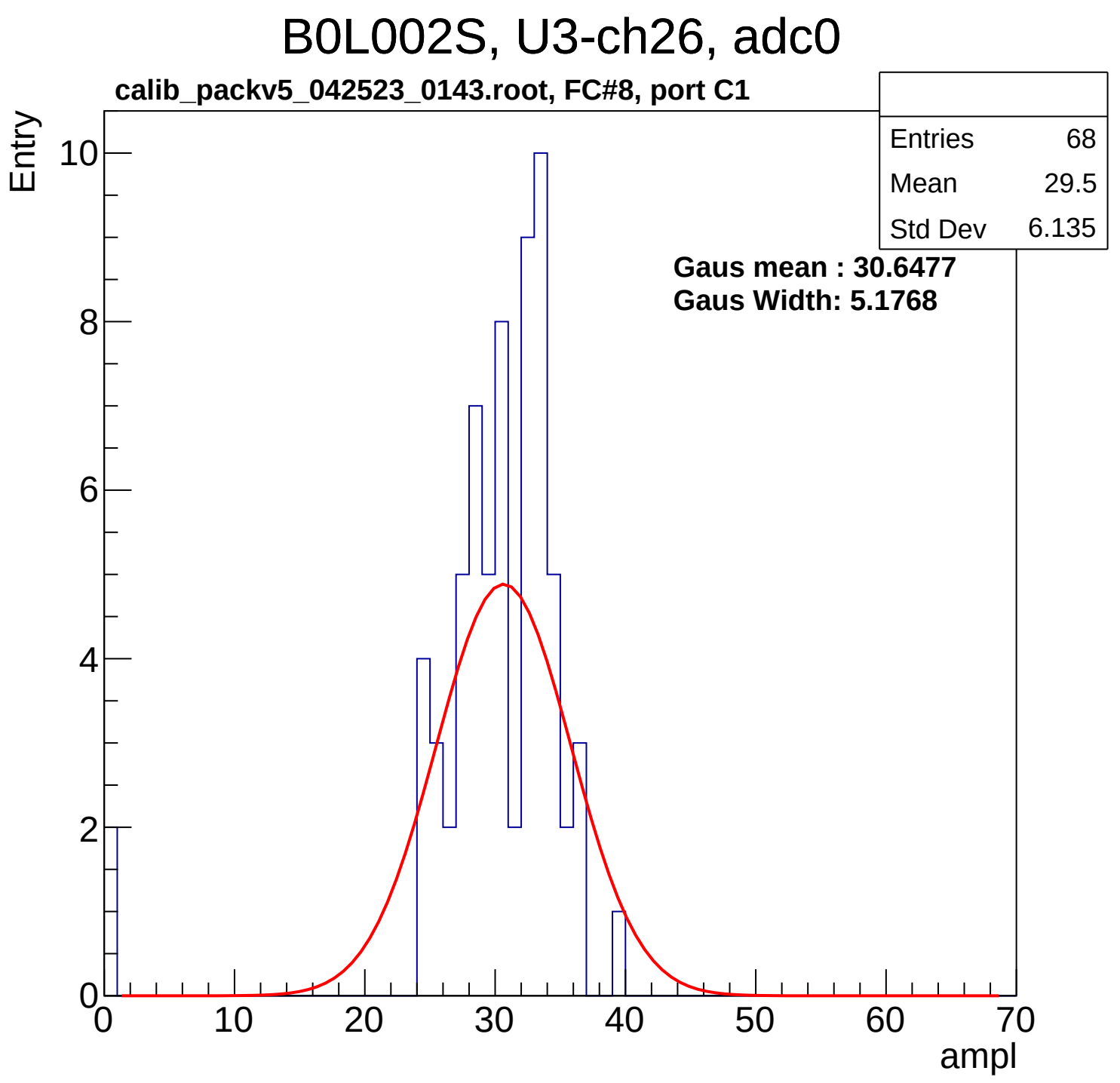
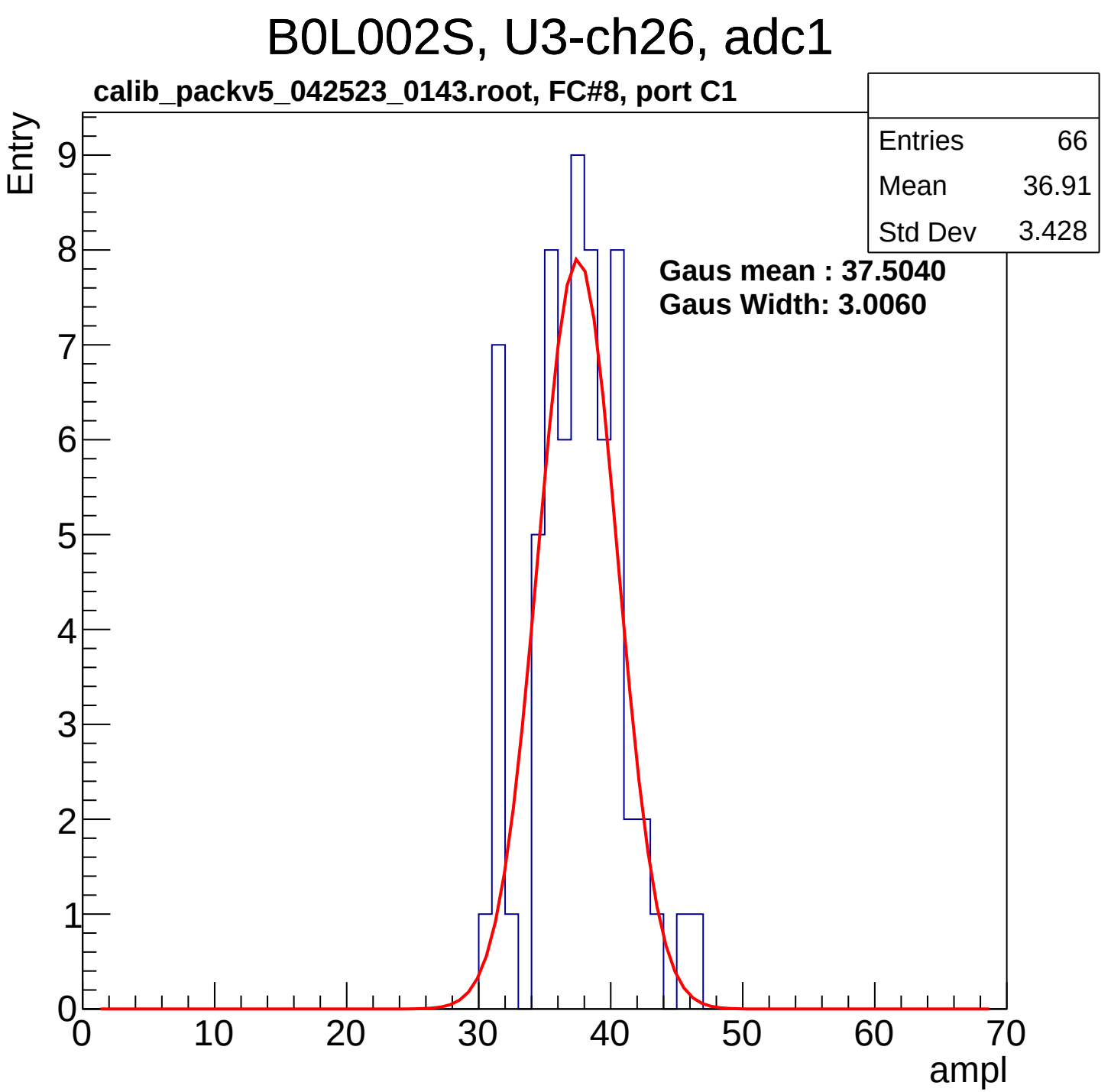
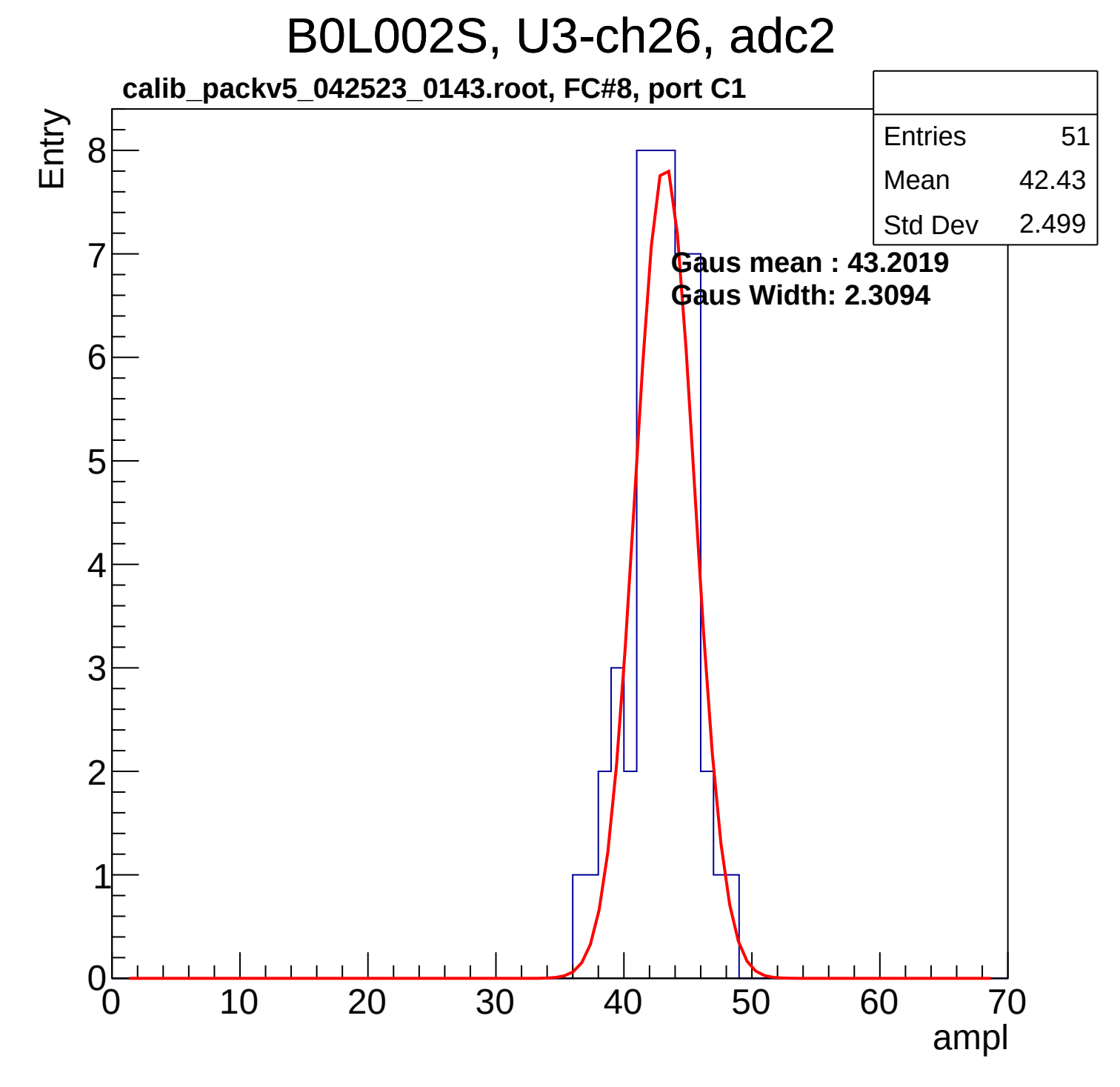
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch26, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.5
Std Dev 6.135
Gaus mean : 30.6477
Gaus Width: 5.1768
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch26, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 36.91
Std Dev 3.428
Gaus mean : 37.5040
Gaus Width: 3.0060
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch26, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 42.43
Std Dev 2.499
Gaus mean : 43.2019
Gaus Width: 2.3094
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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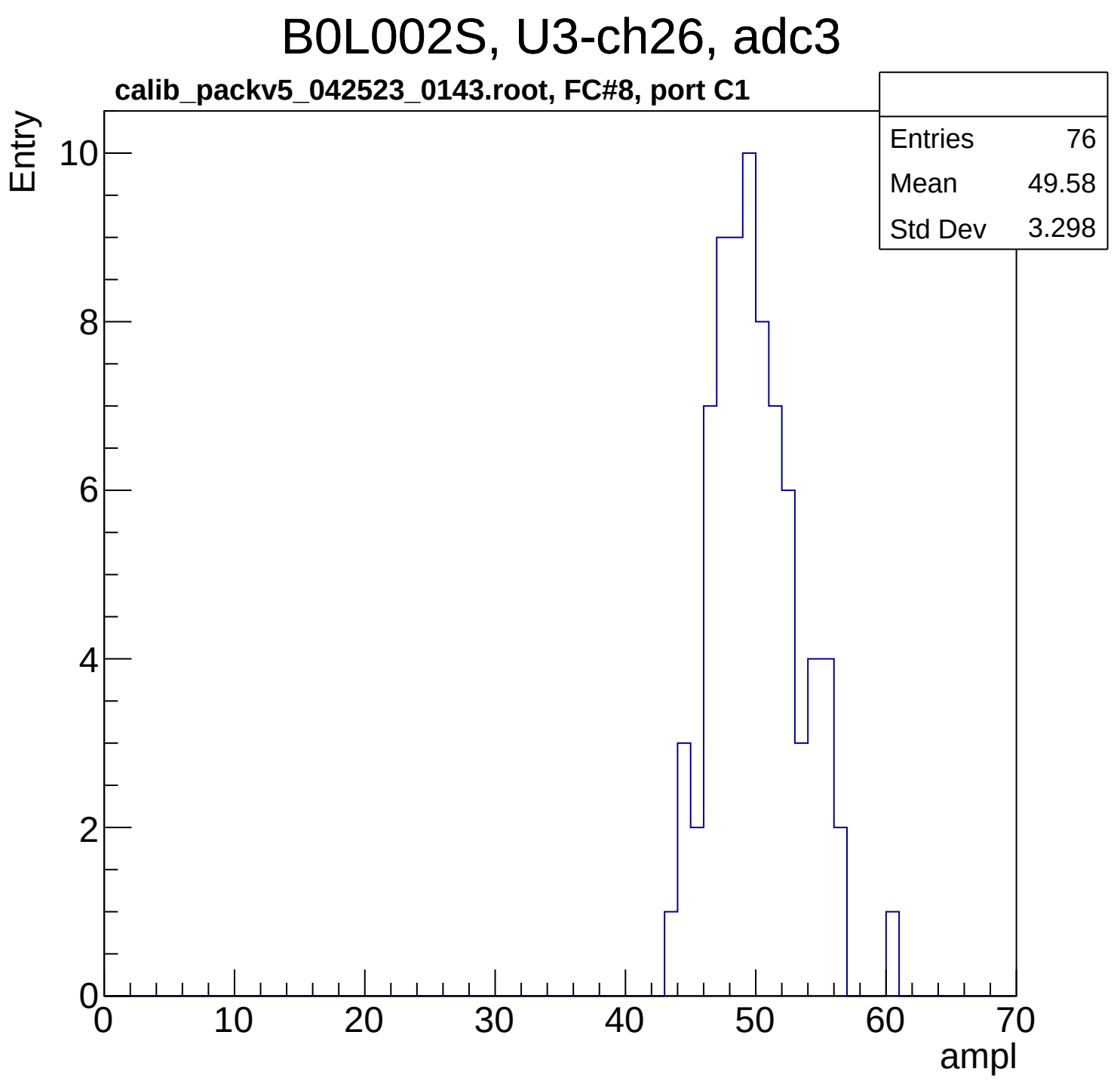
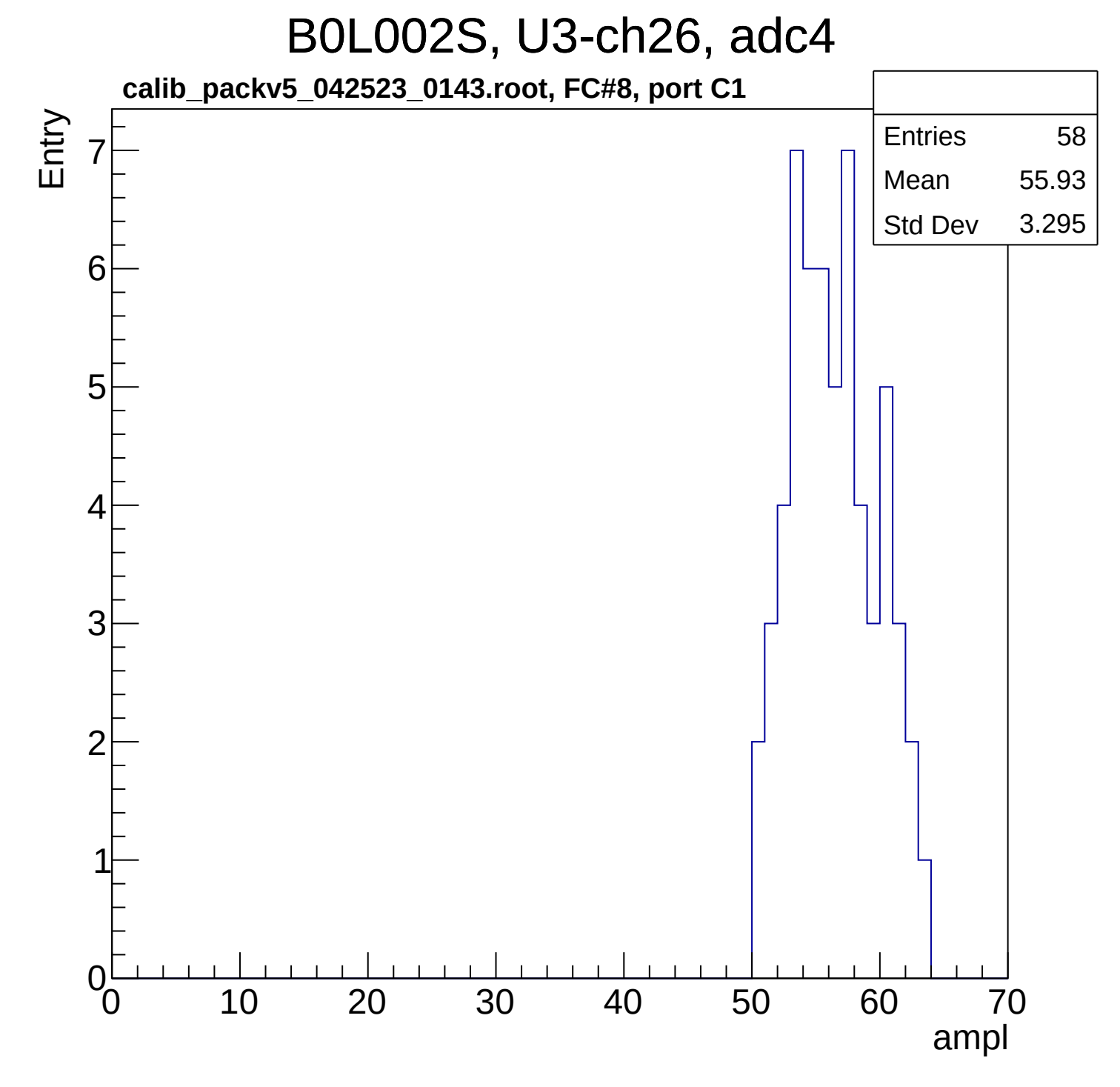
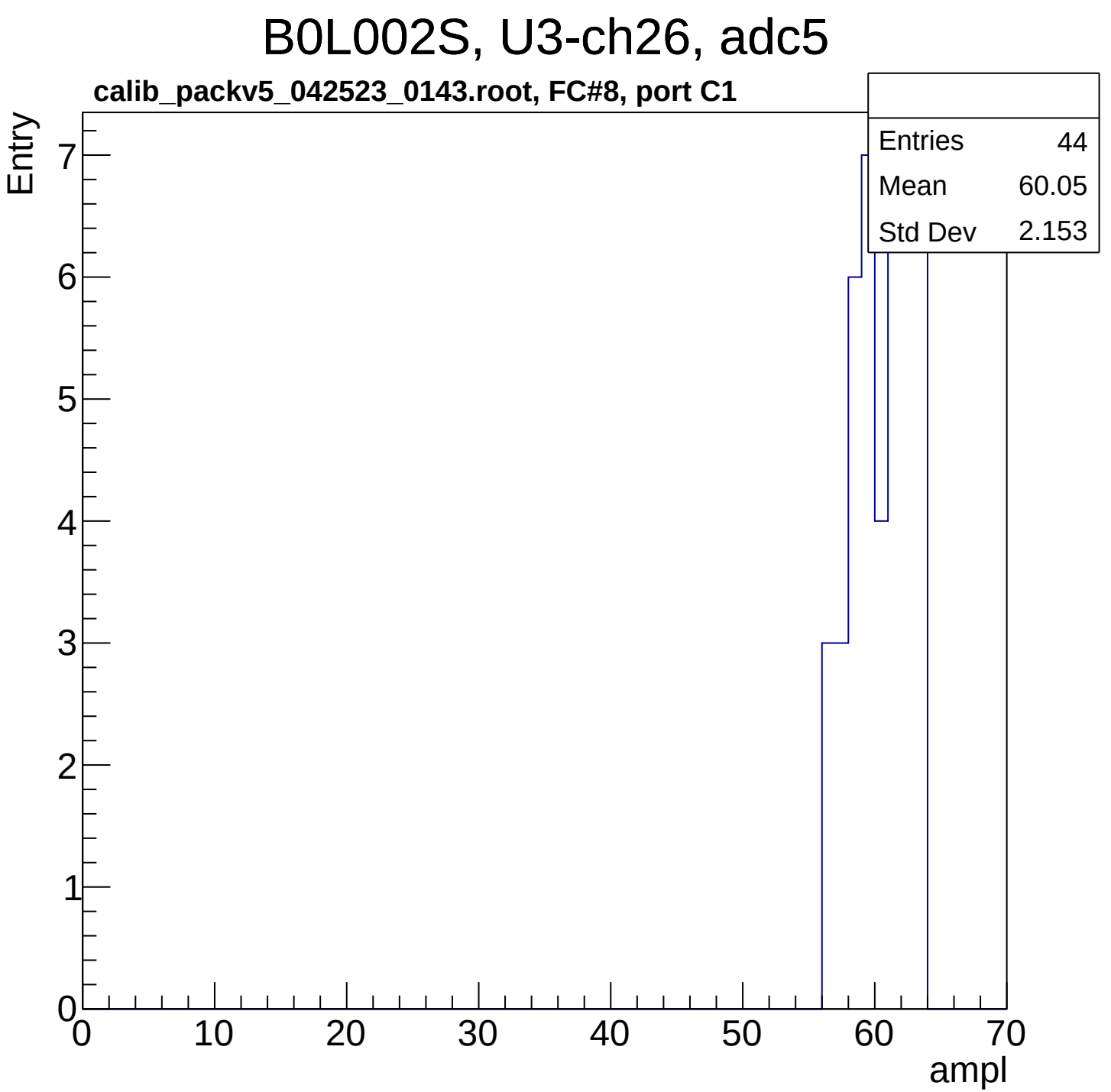
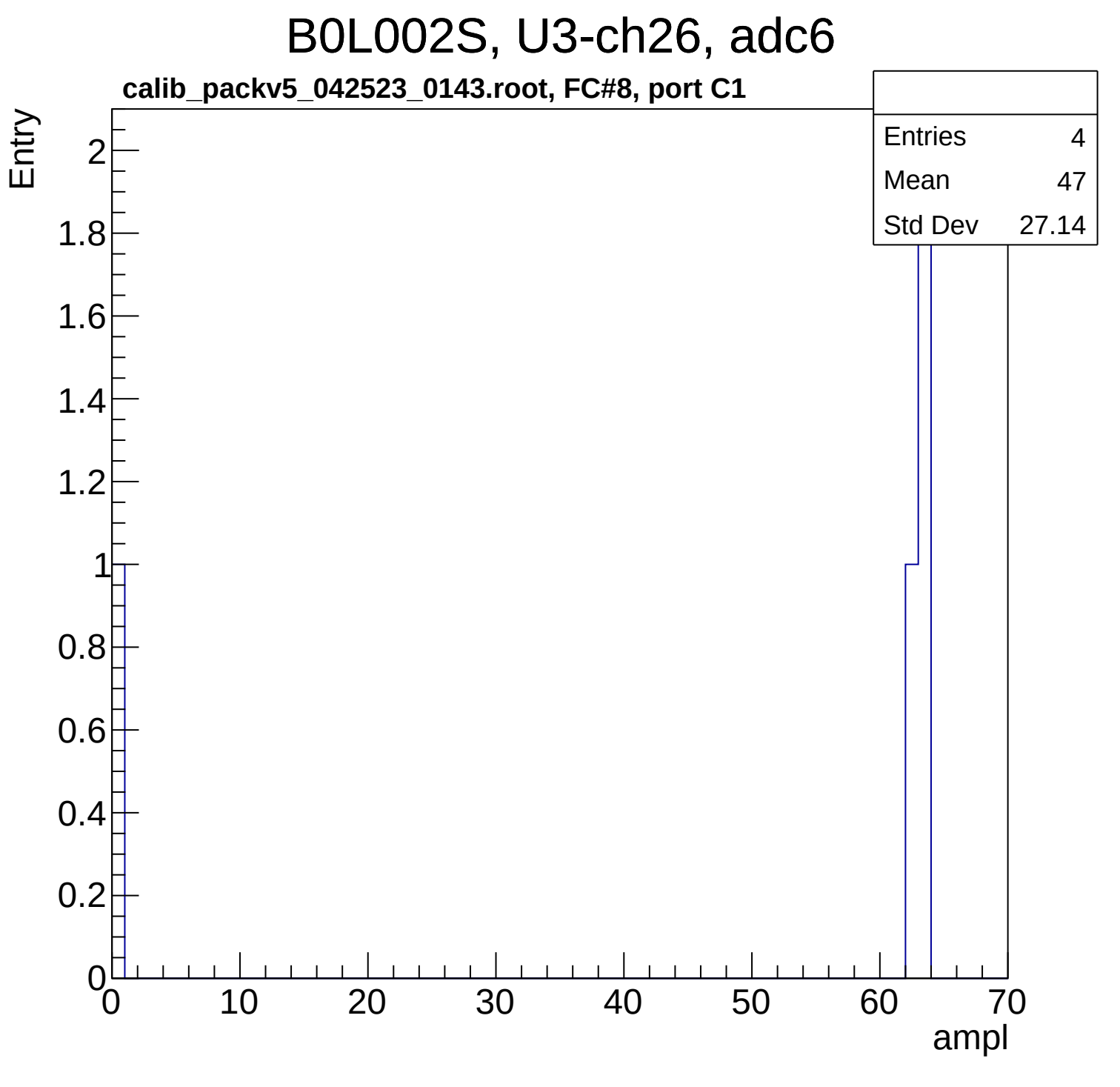
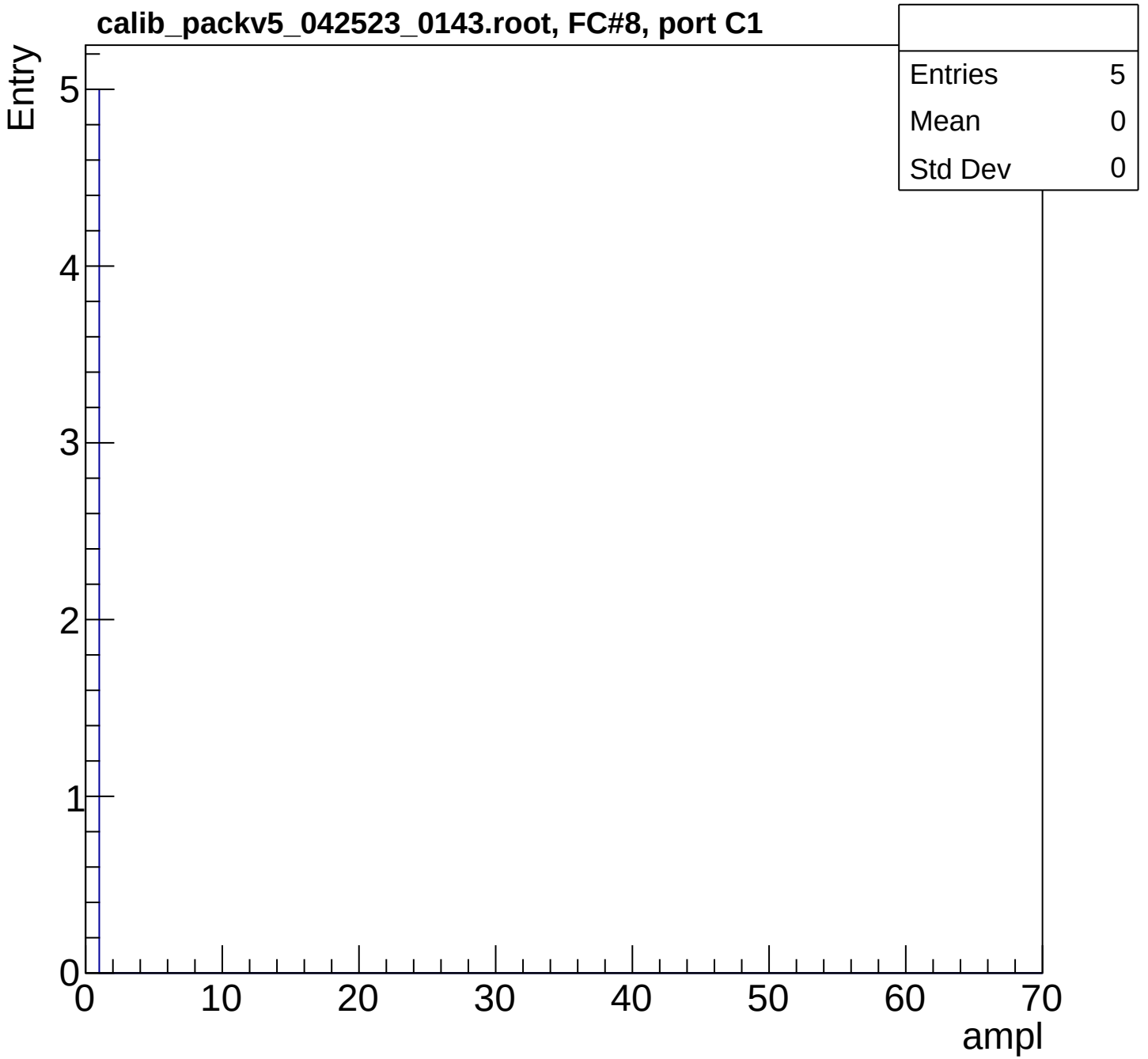
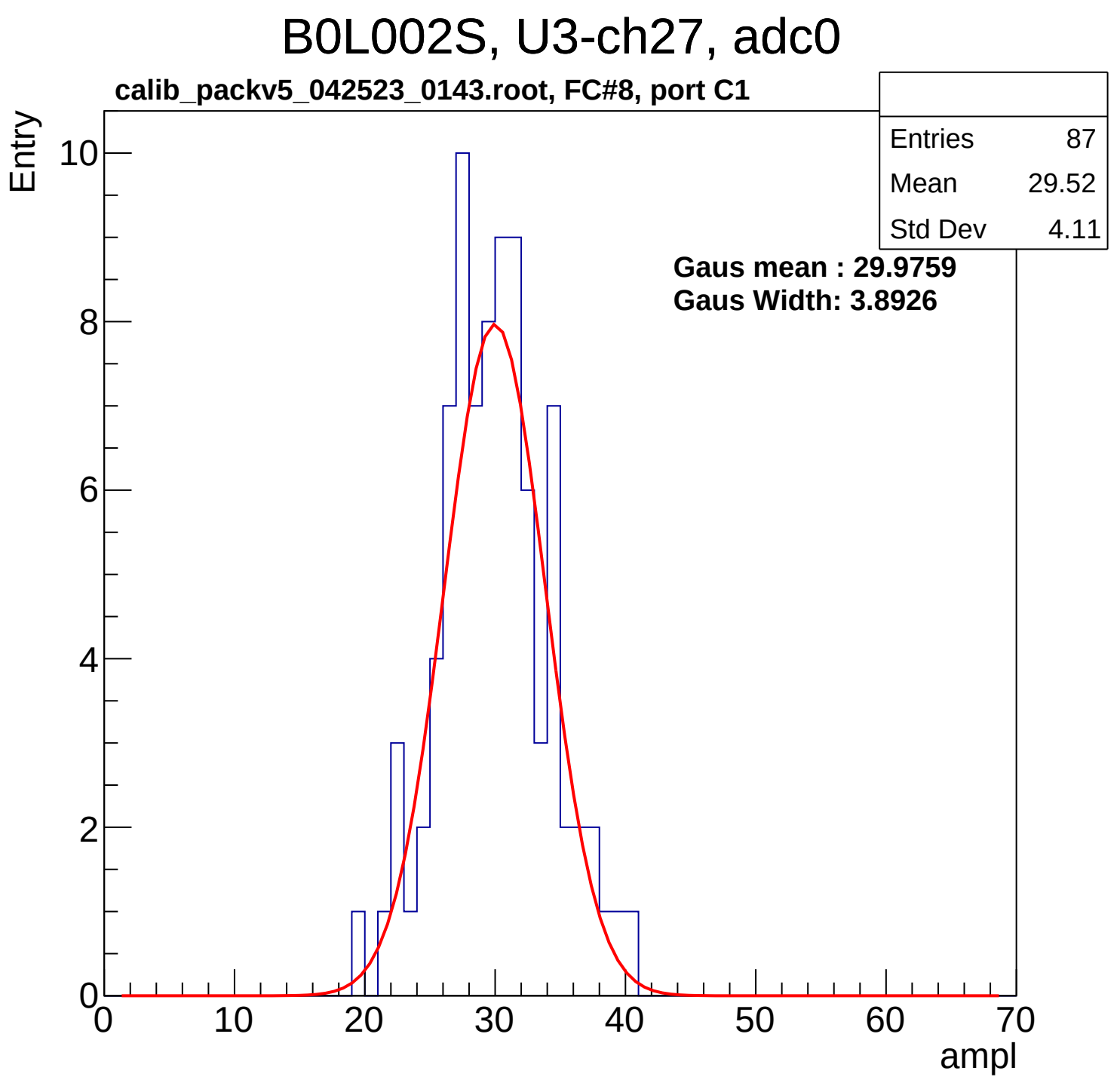

B0L002S, U3-ch26, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 49.58
Std Dev 3.298
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

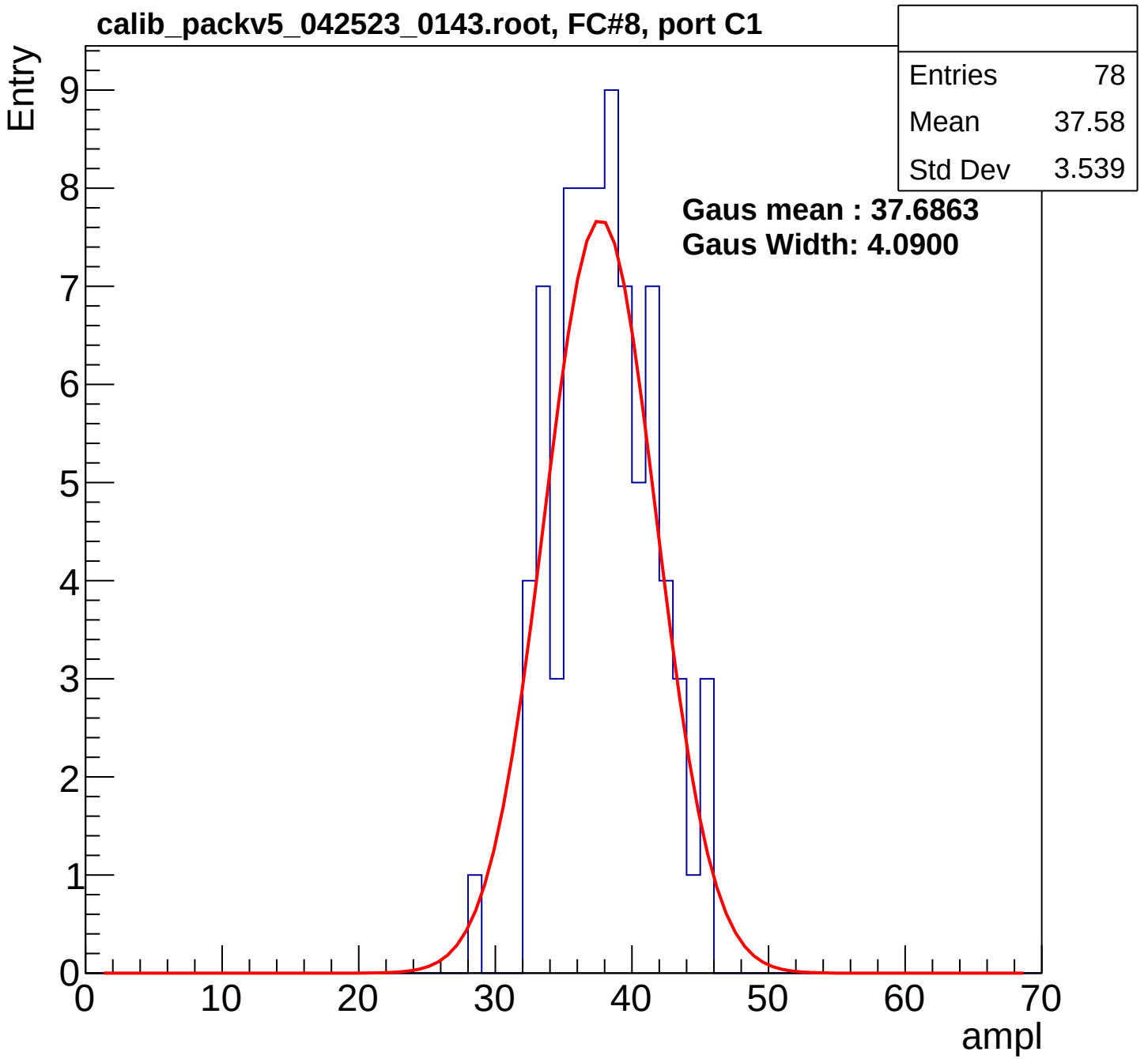

B0L002S, U3-ch26, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 55.93
Std Dev 3.295
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch26, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 60.05
Std Dev 2.153
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch26, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch26, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 0
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch27, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 29.52
Std Dev 4.11
Gaus mean : 29.9759
Gaus Width: 3.8926
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch27, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 37.58
Std Dev 3.539
Gaus mean : 37.6863
Gaus Width: 4.0900
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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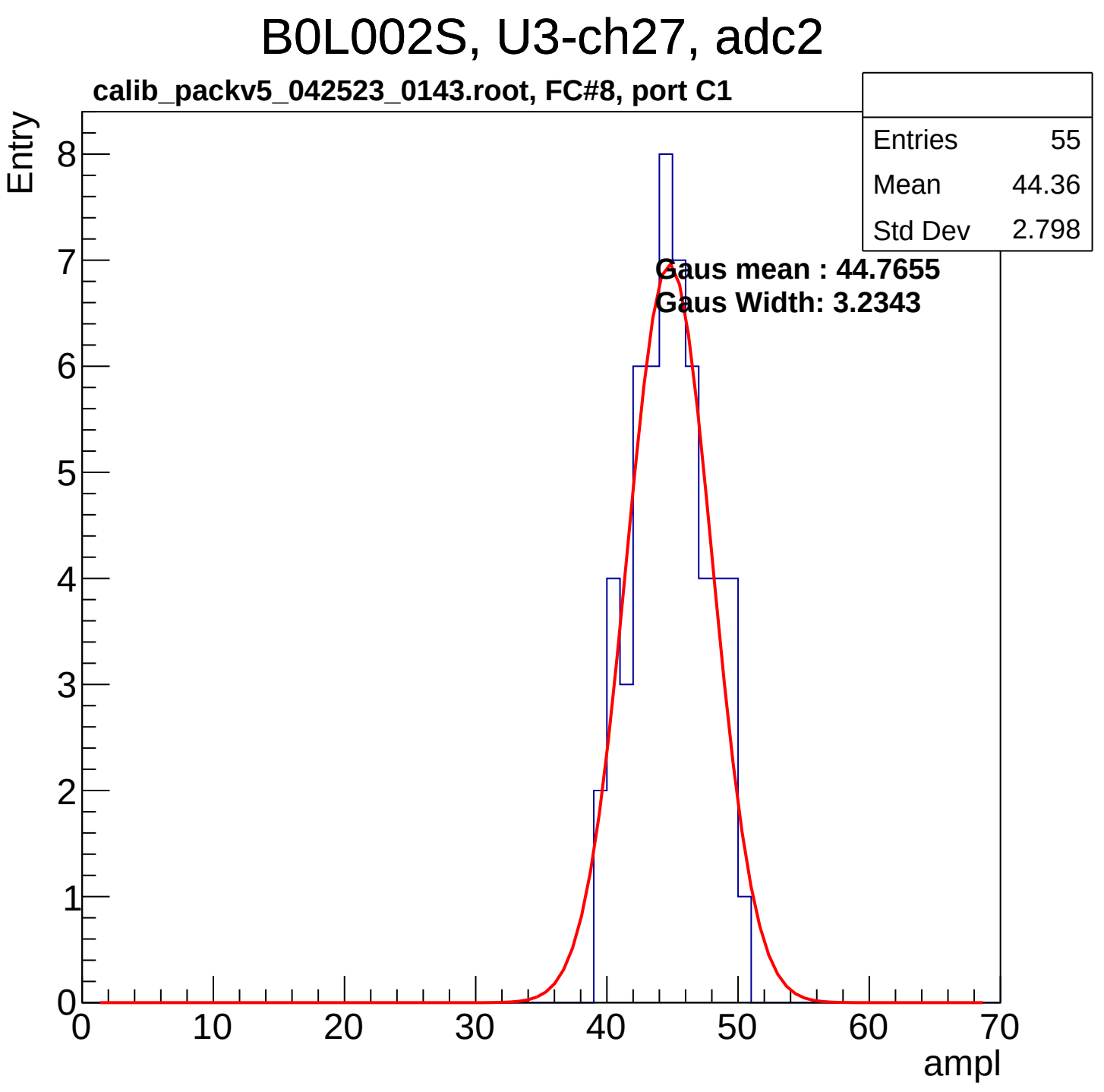
B0L002S, U3-ch27, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 44.36
Std Dev 2.798
Gaus mean : 44.7655
Gaus Width: 3.2343
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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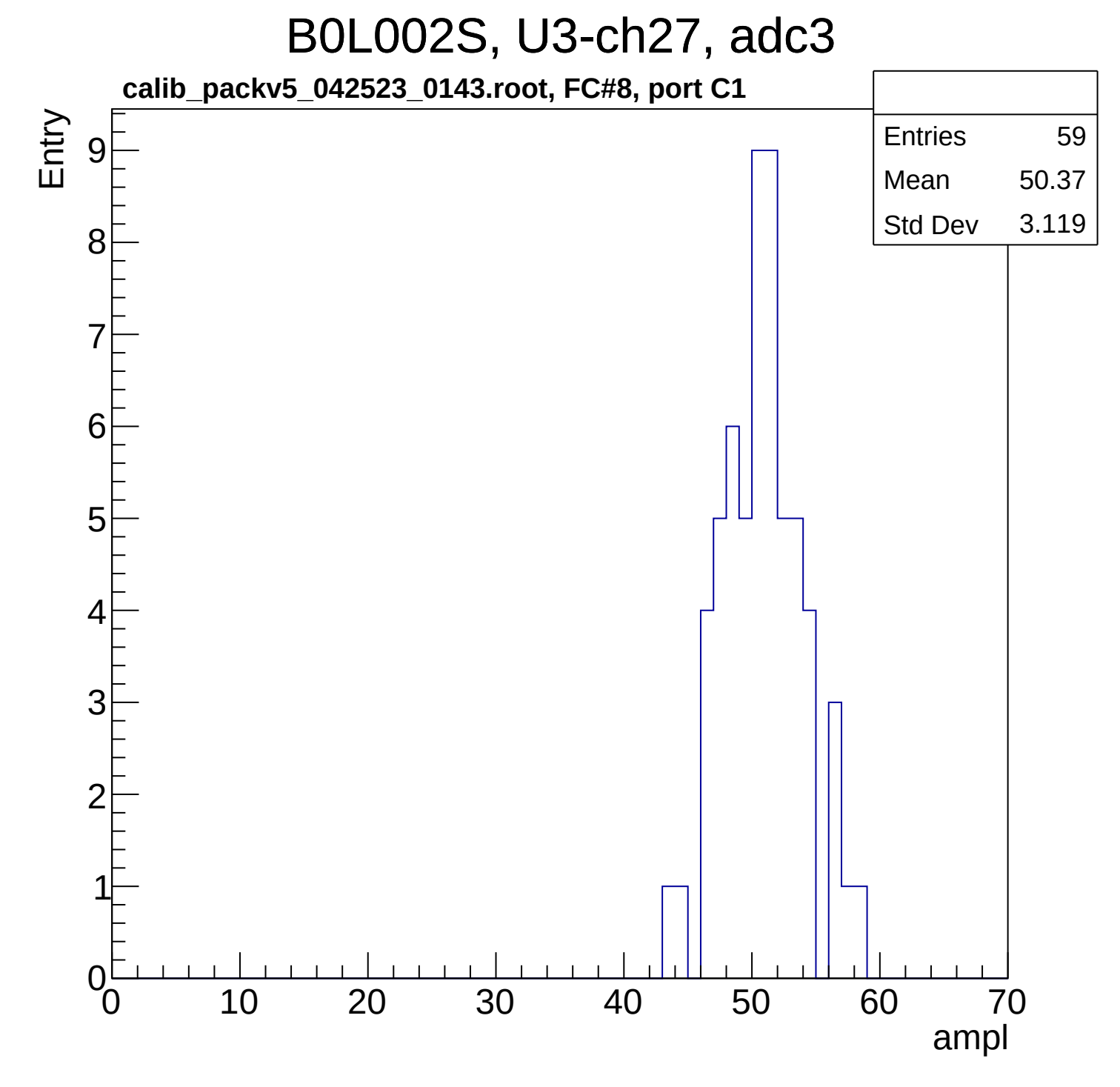
B0L002S, U3-ch27, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.37
Std Dev 3.119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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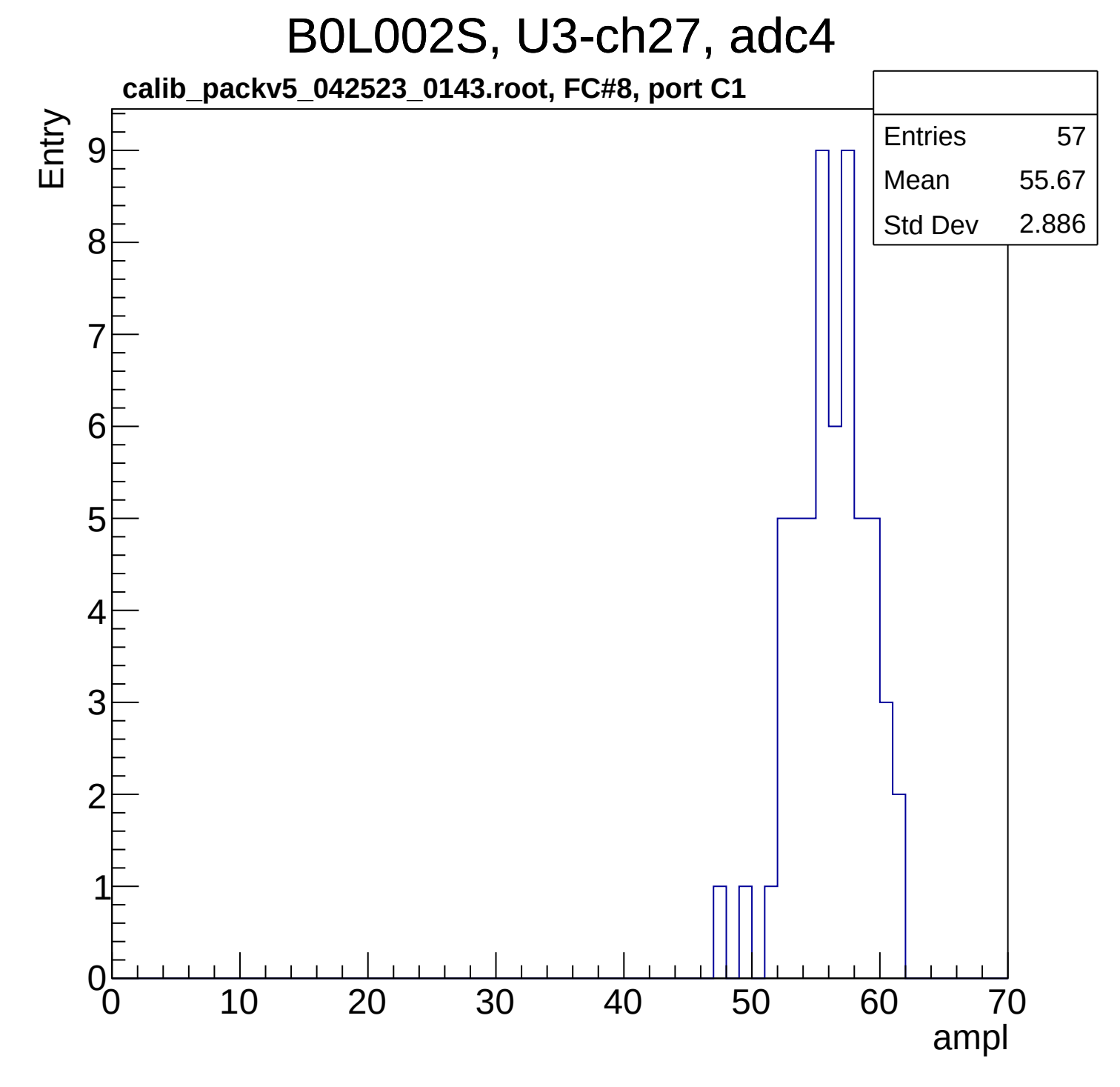
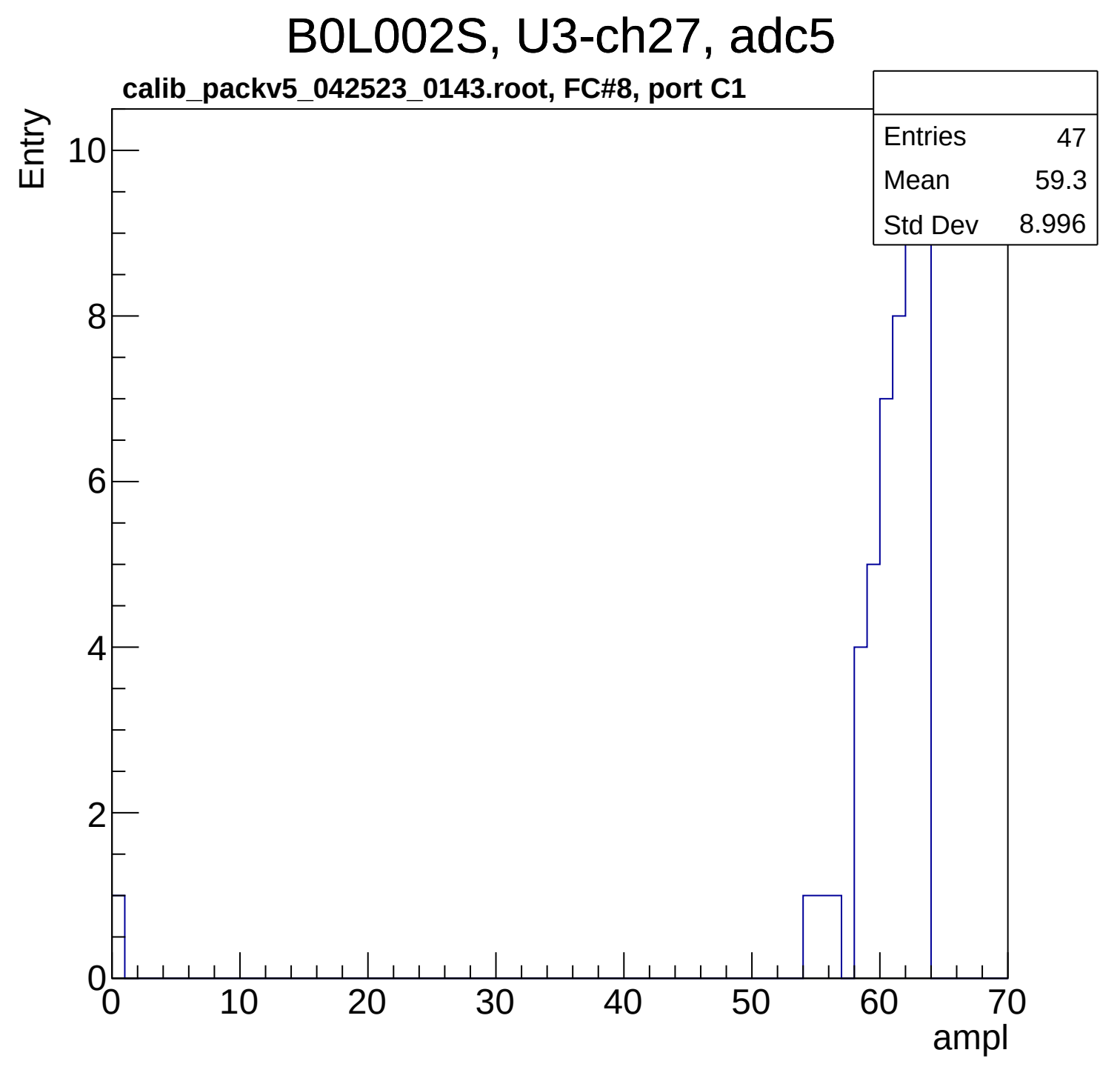
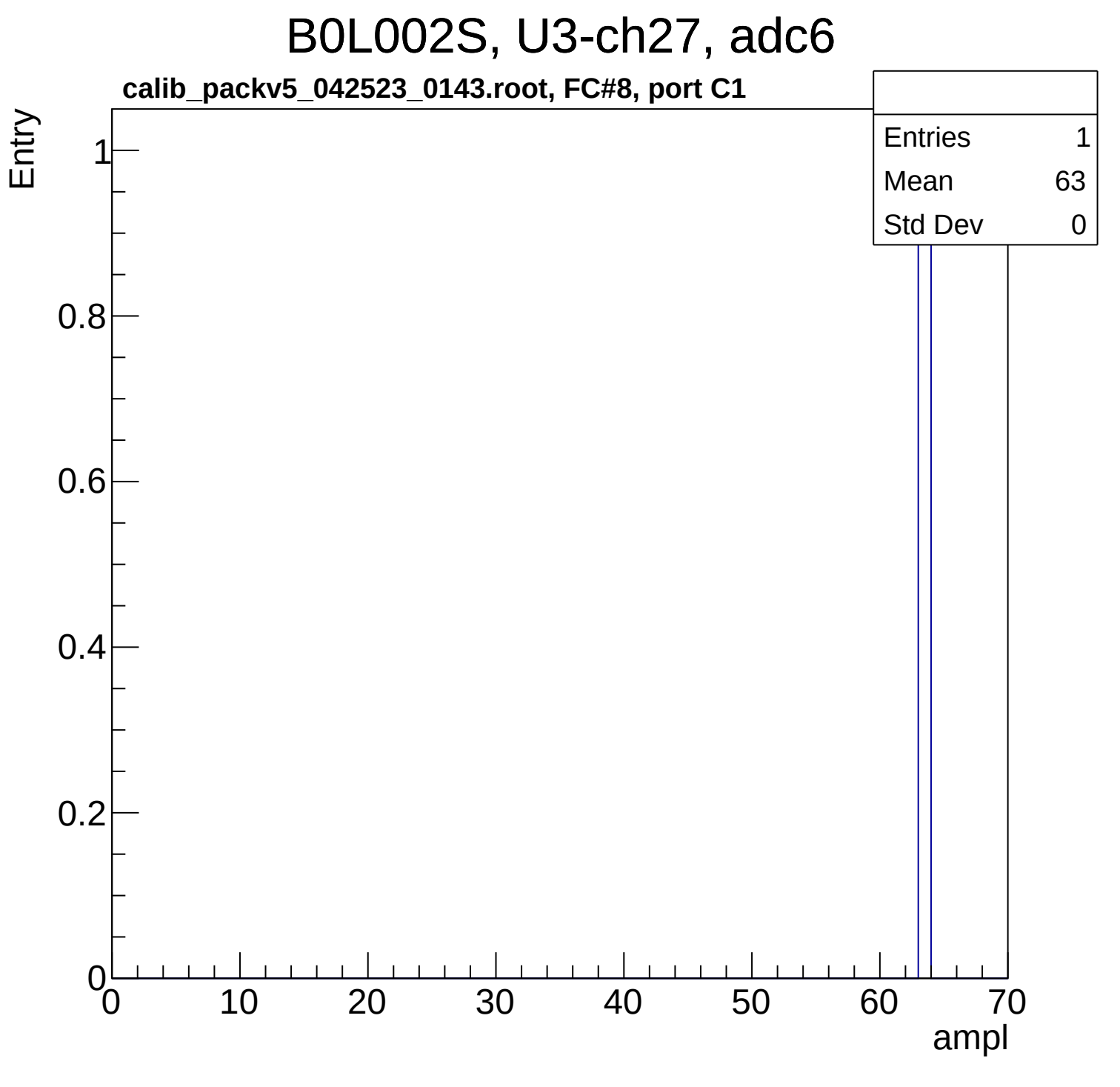
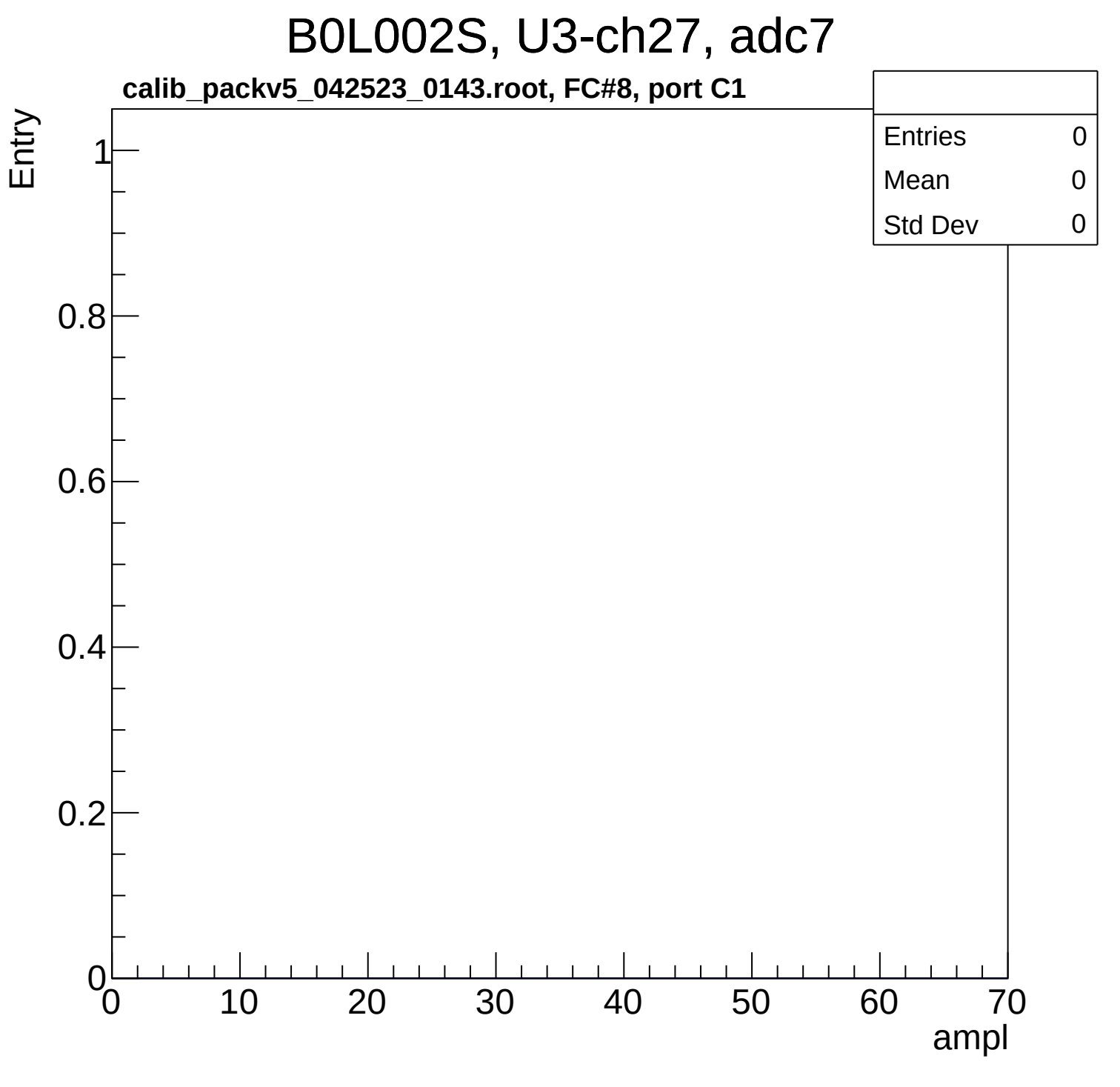
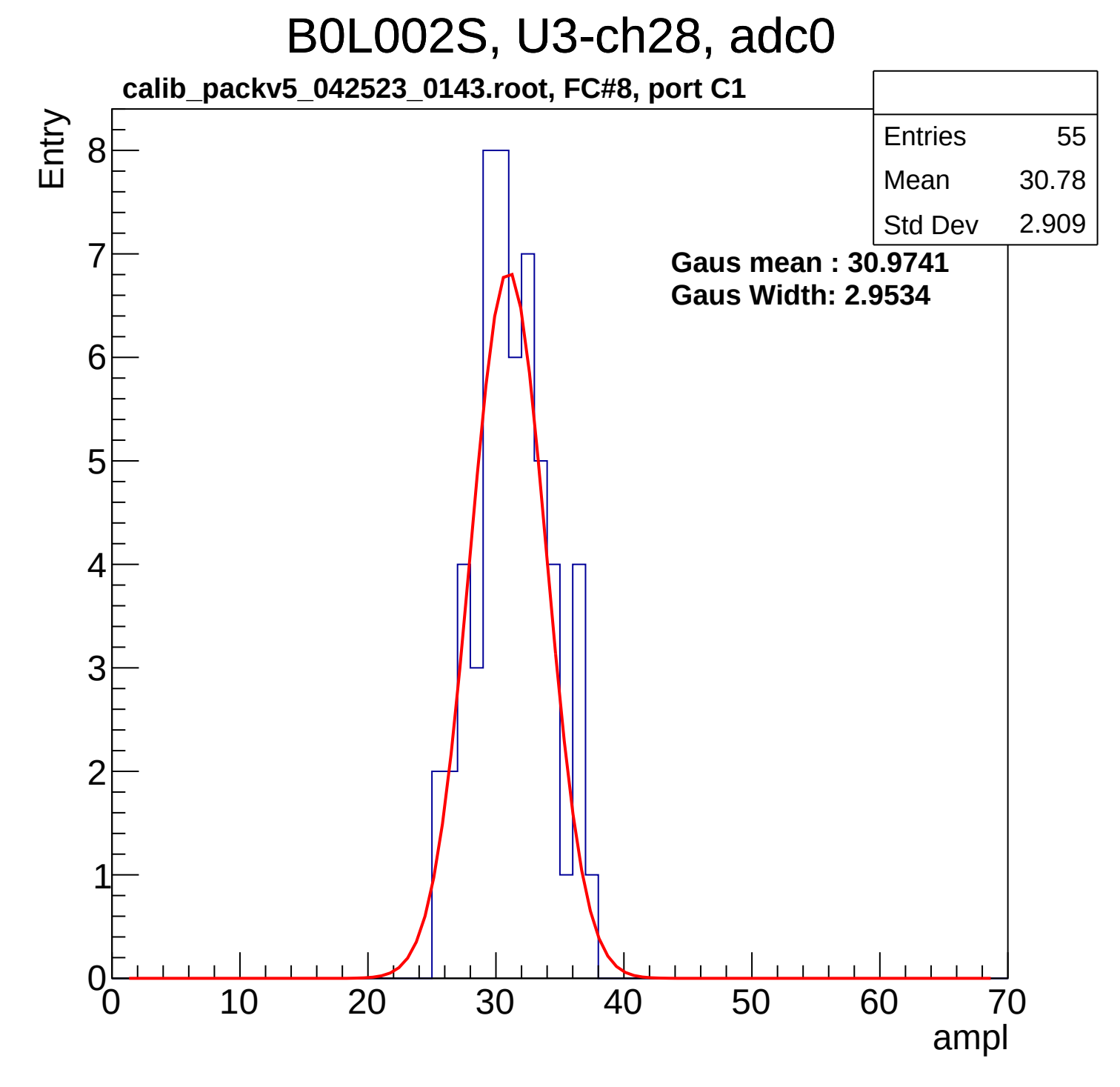
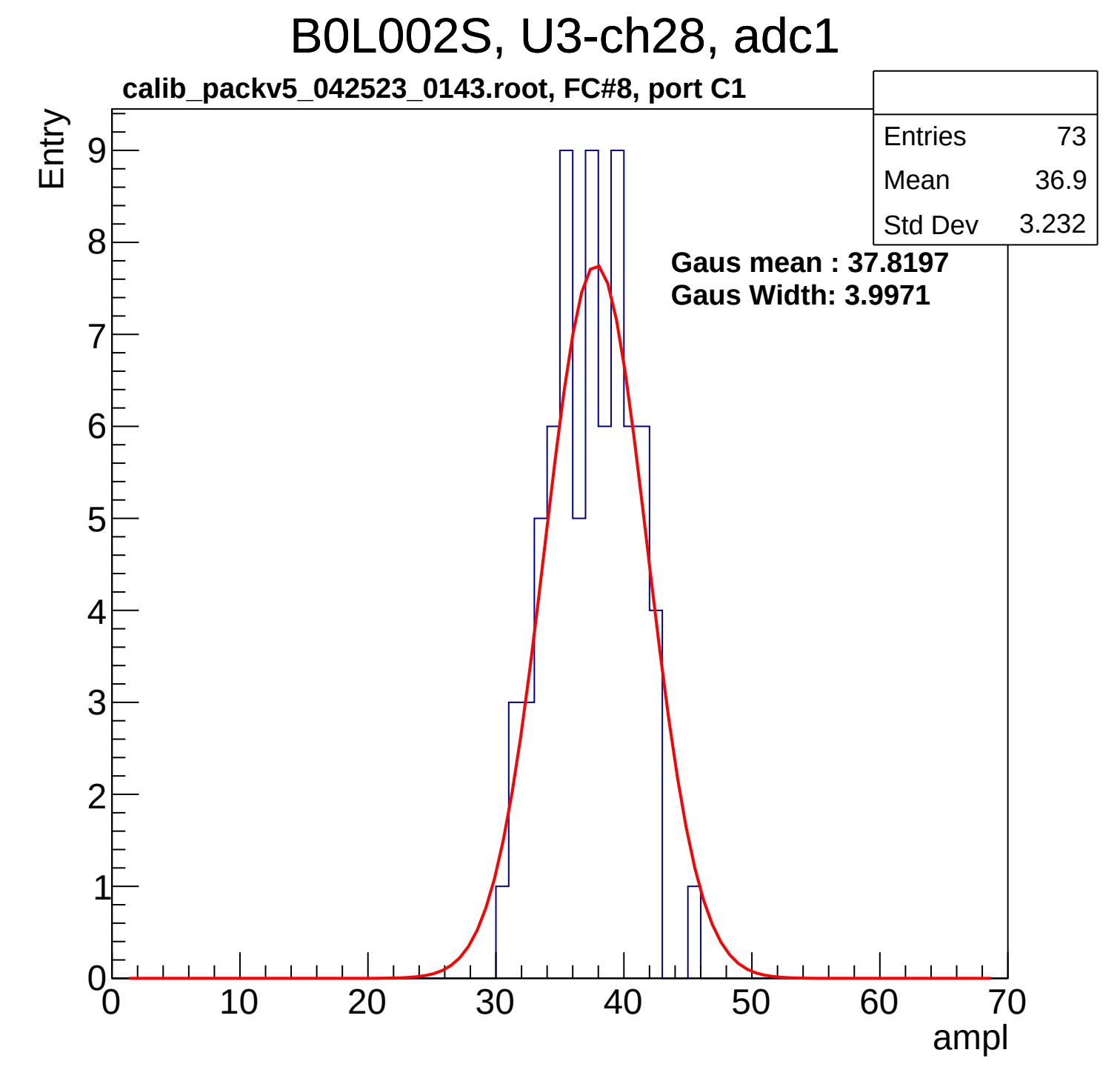

B0L002S, U3-ch27, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.67
Std Dev 2.886
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

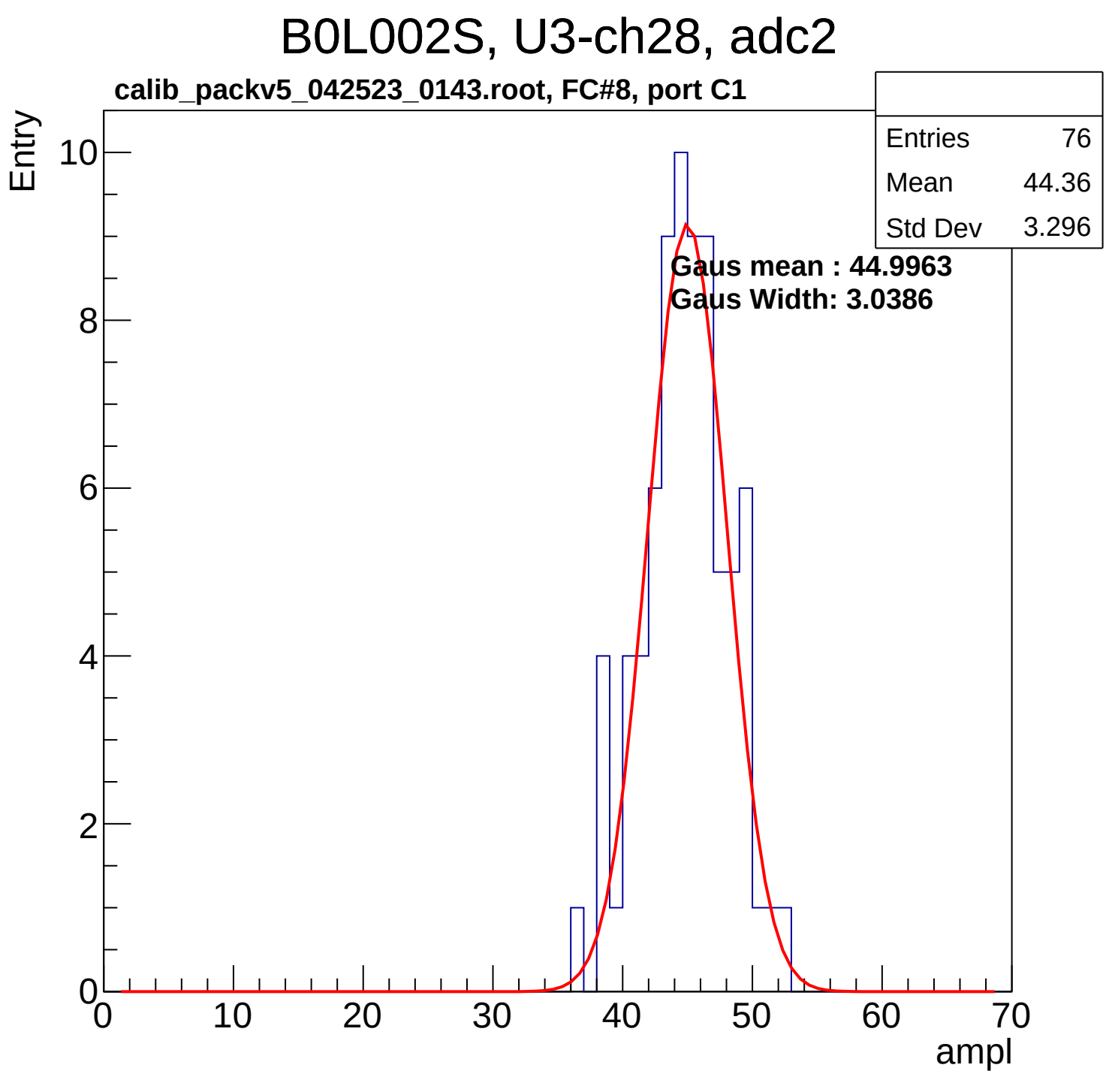

B0L002S, U3-ch27, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 59.3
Std Dev 8.996
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

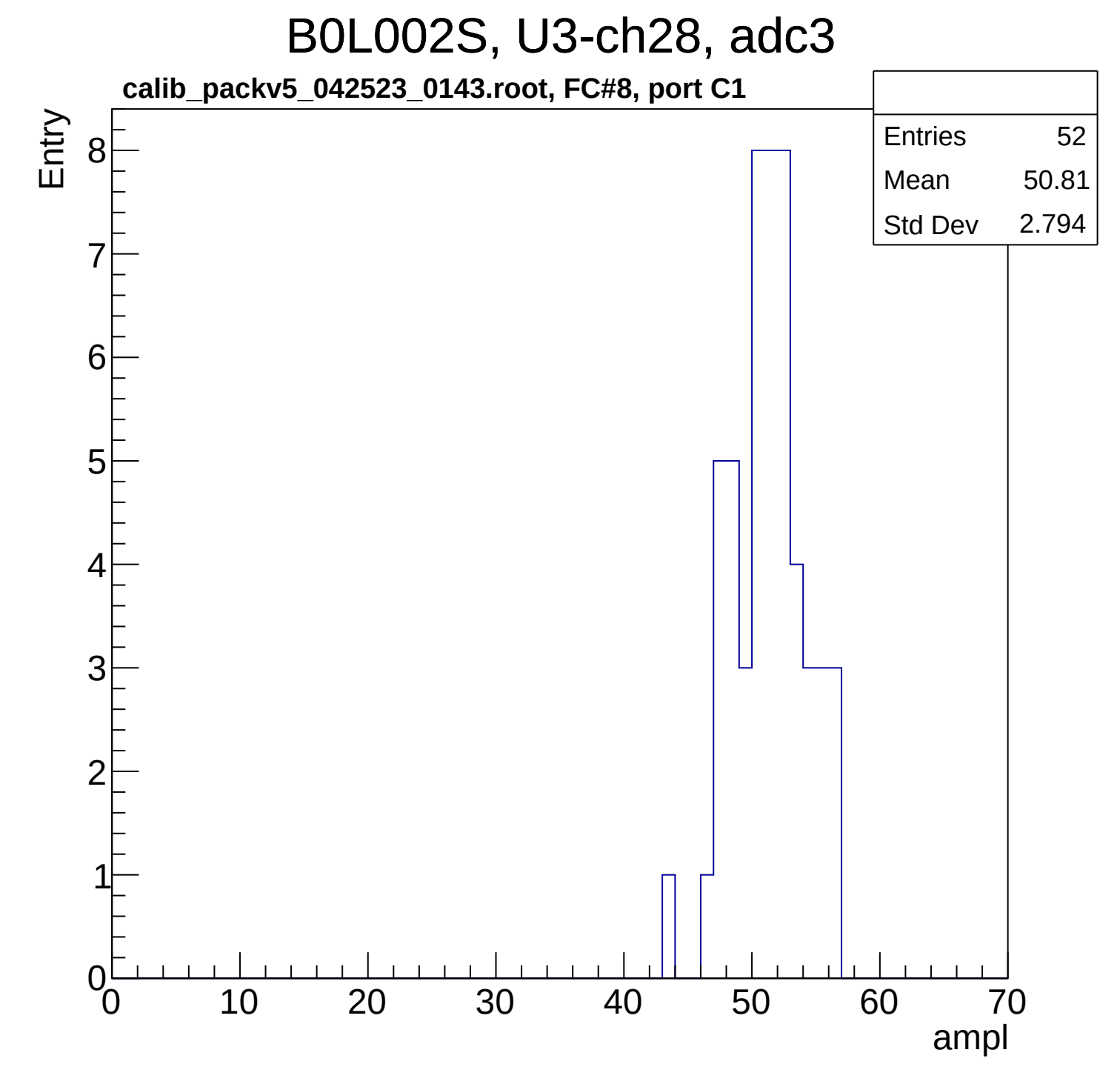

B0L002S, U3-ch27, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

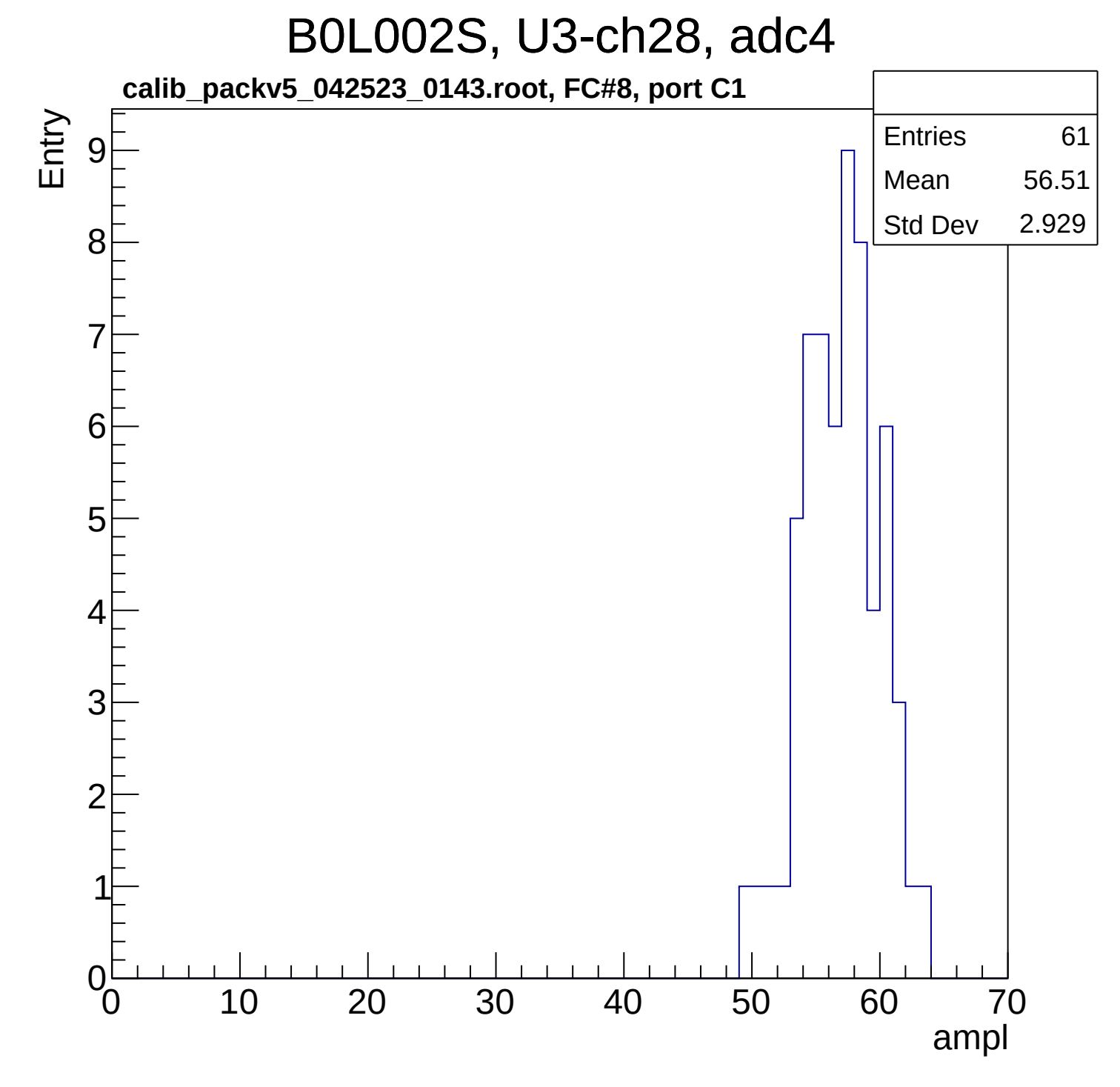

B0L002S, U3-ch27, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

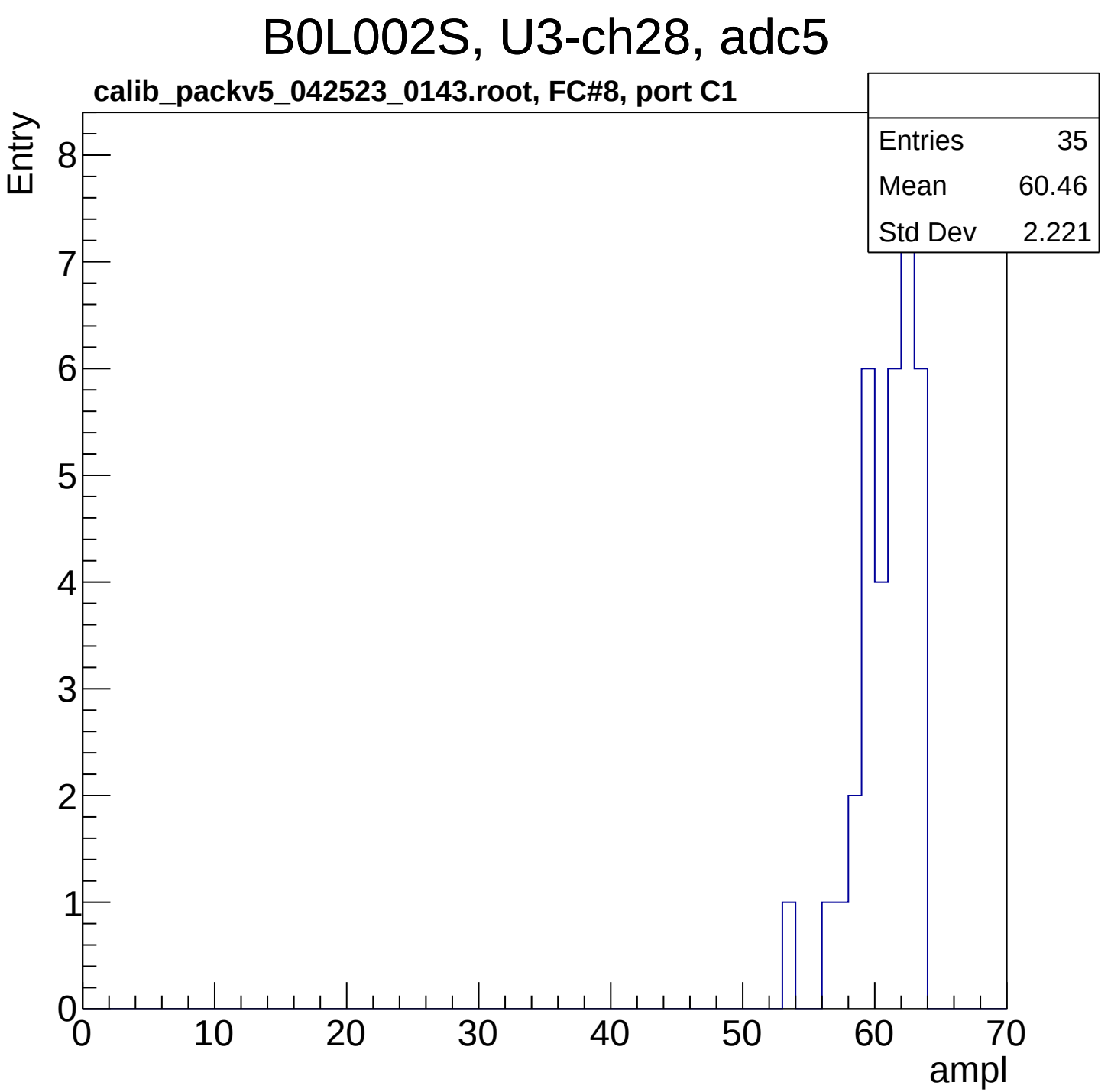

B0L002S, U3-ch28, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 30.78
Std Dev 2.909
Gaus mean : 30.9741
Gaus Width: 2.9534
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

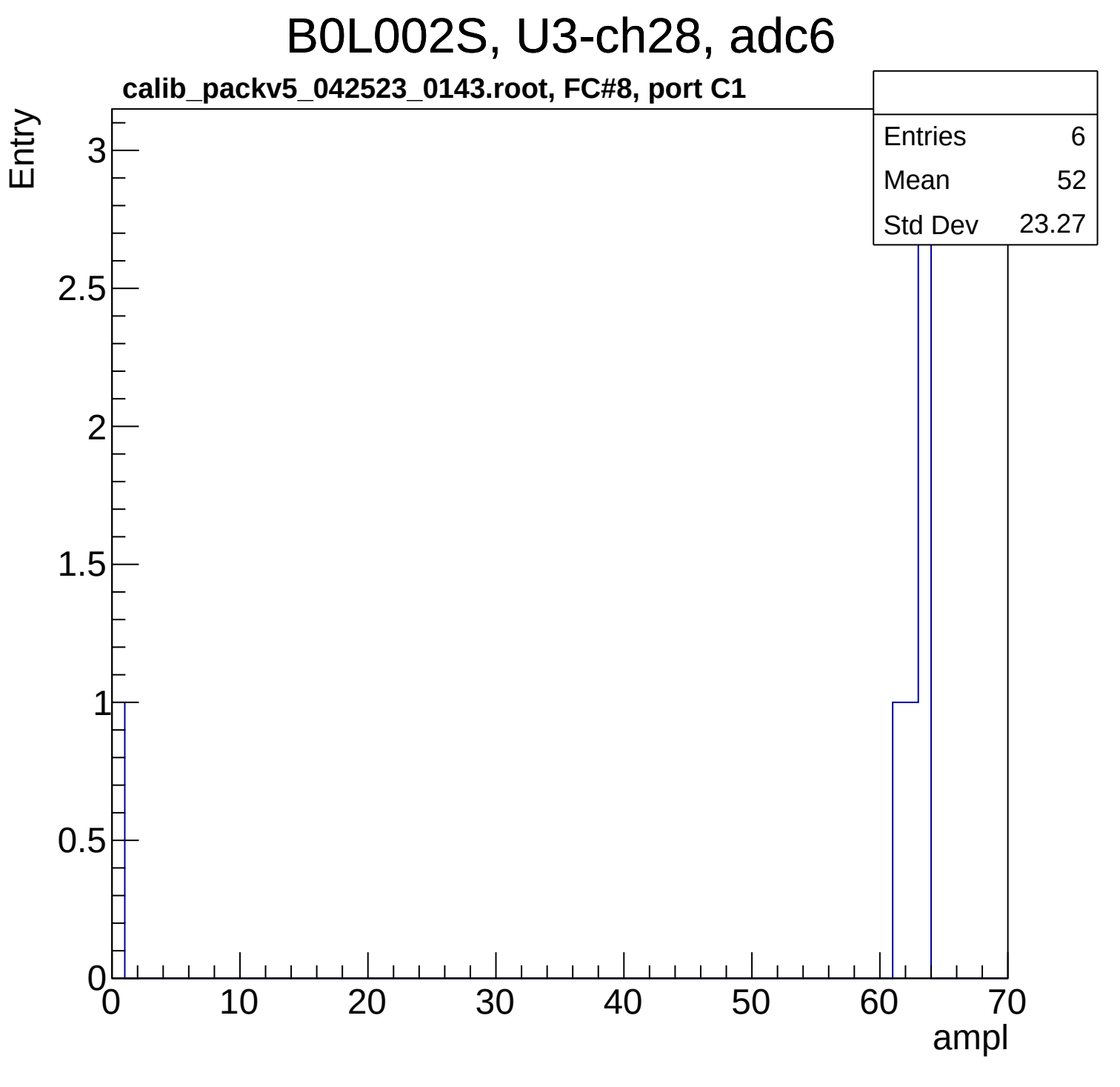

B0L002S, U3-ch28, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 36.9
Std Dev 3.232
Gaus mean : 37.8197
Gaus Width: 3.9971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

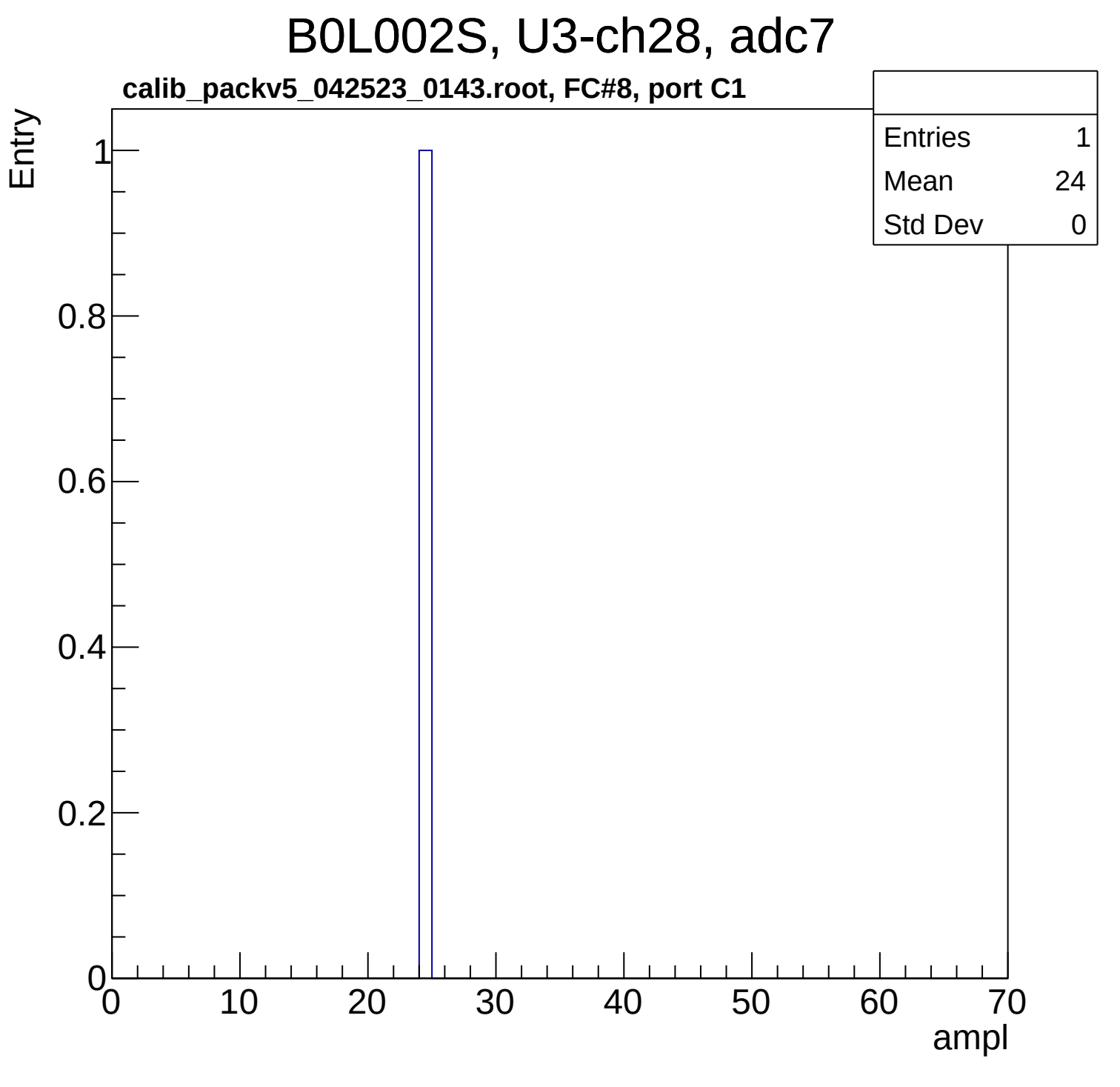

B0L002S, U3-ch28, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 44.36
Std Dev 3.296
Gaus mean : 44.9963
Gaus Width: 3.0386
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

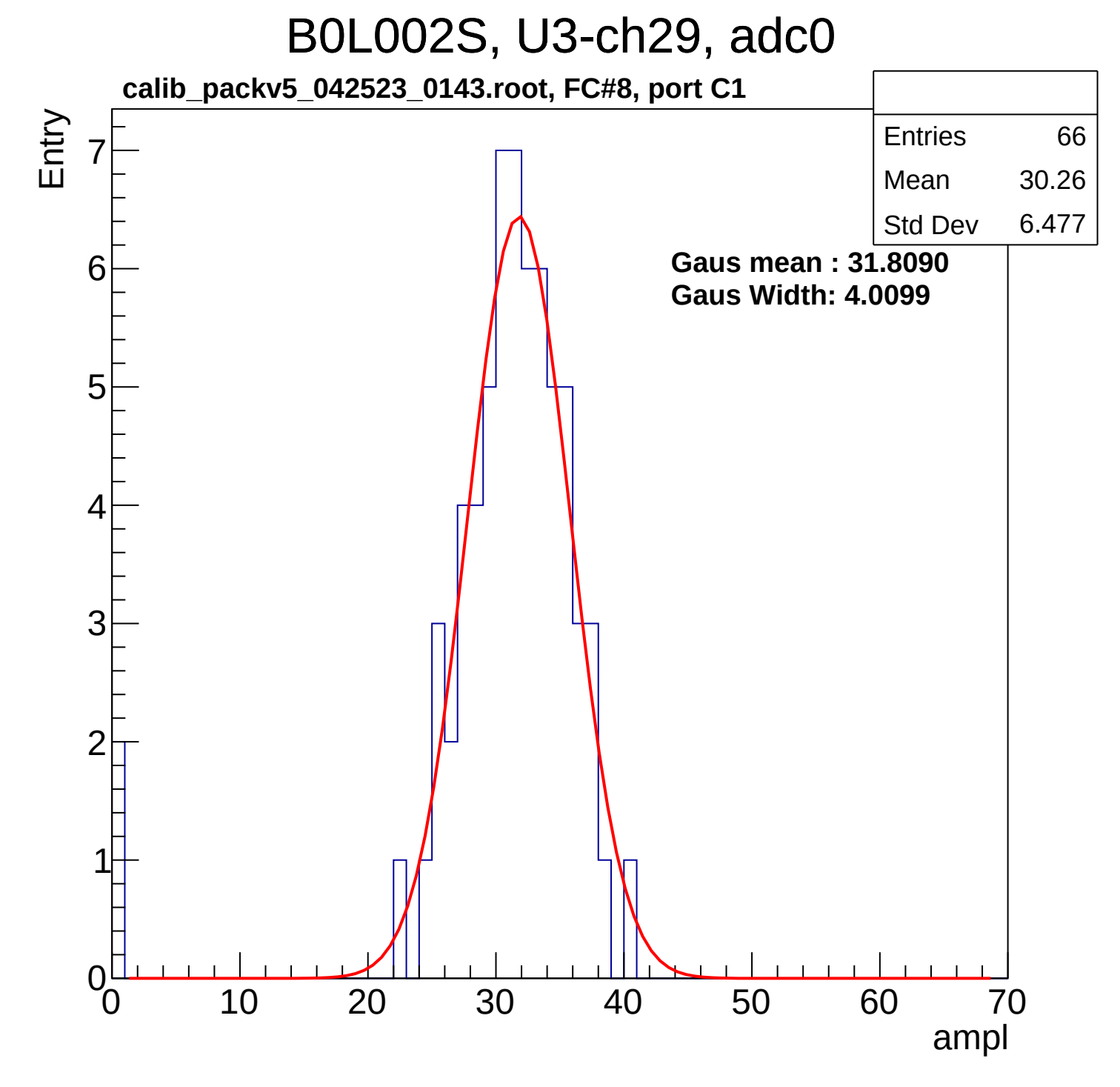

B0L002S, U3-ch28, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 50.81
Std Dev 2.794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

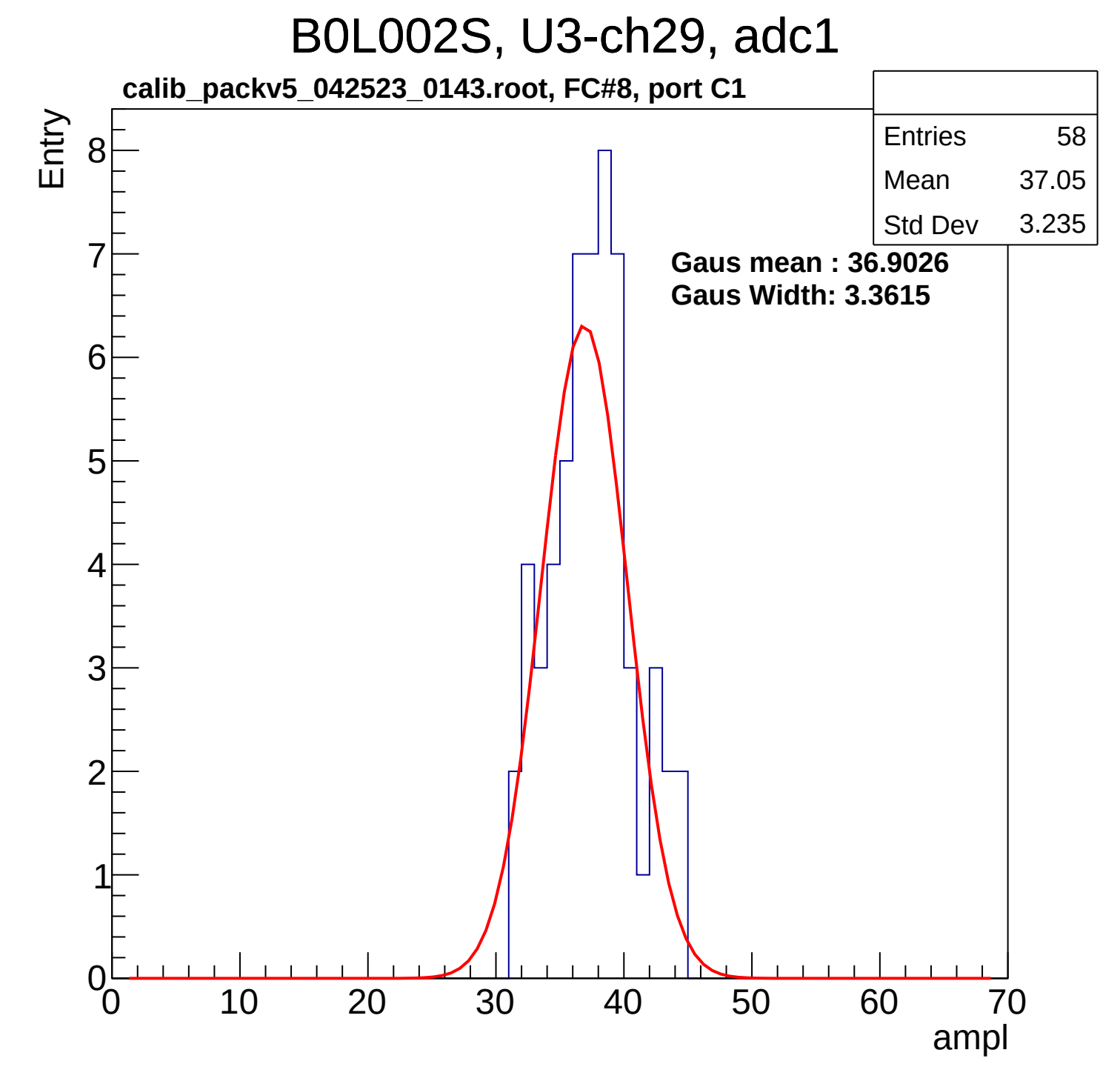

B0L002S, U3-ch28, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.51
Std Dev 2.929
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

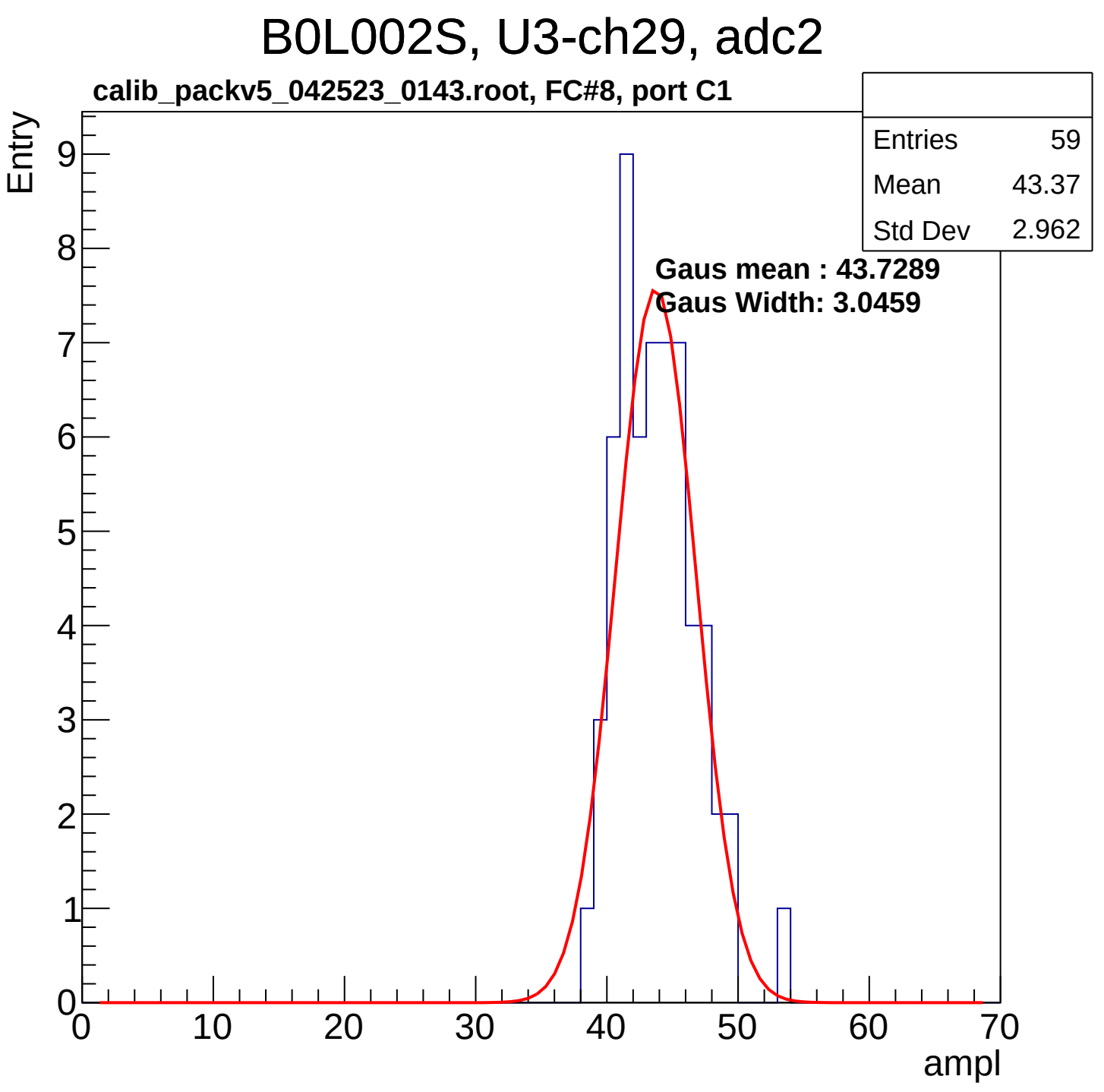

B0L002S, U3-ch28, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 60.46
Std Dev 2.221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

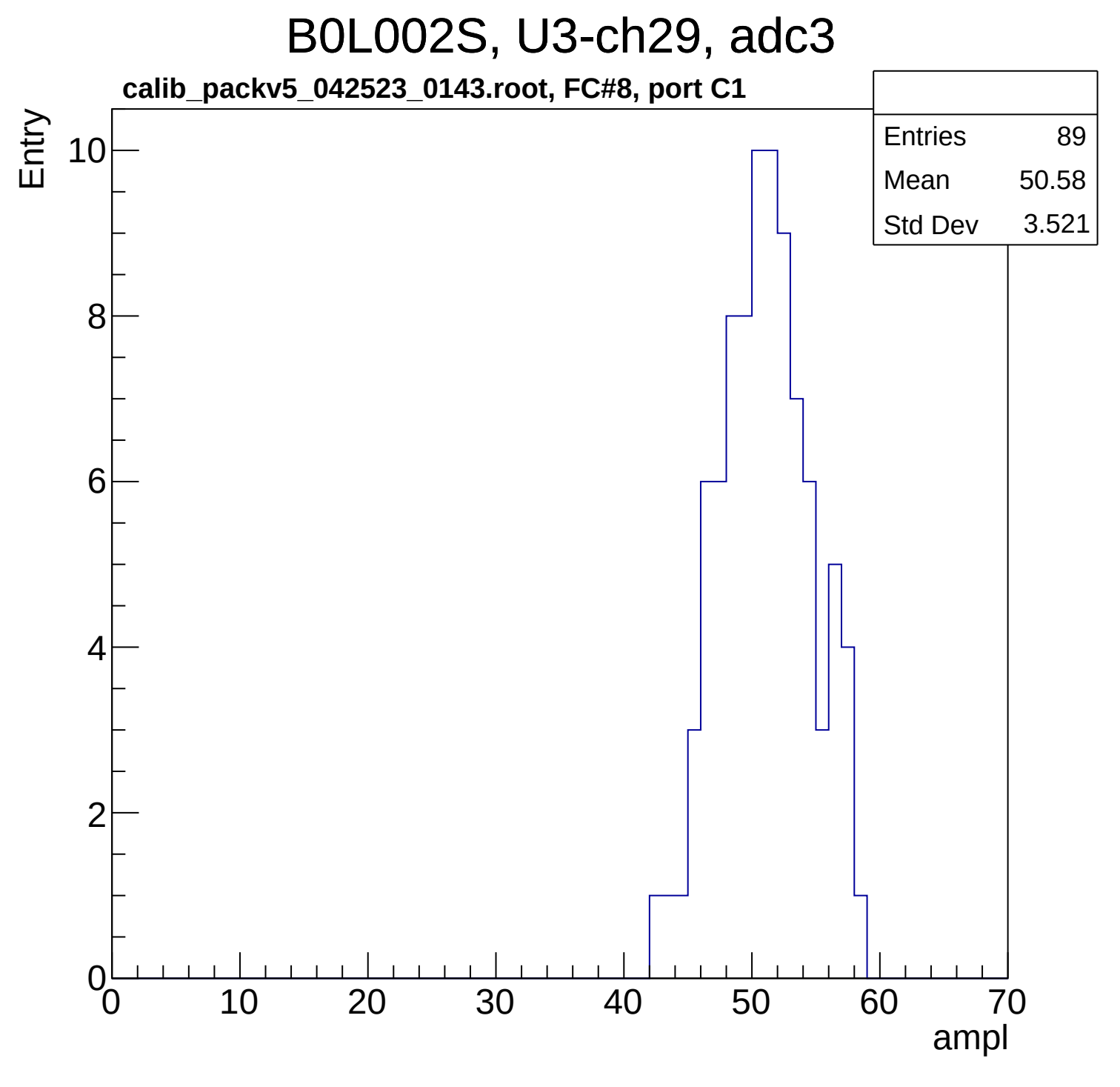

B0L002S, U3-ch28, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

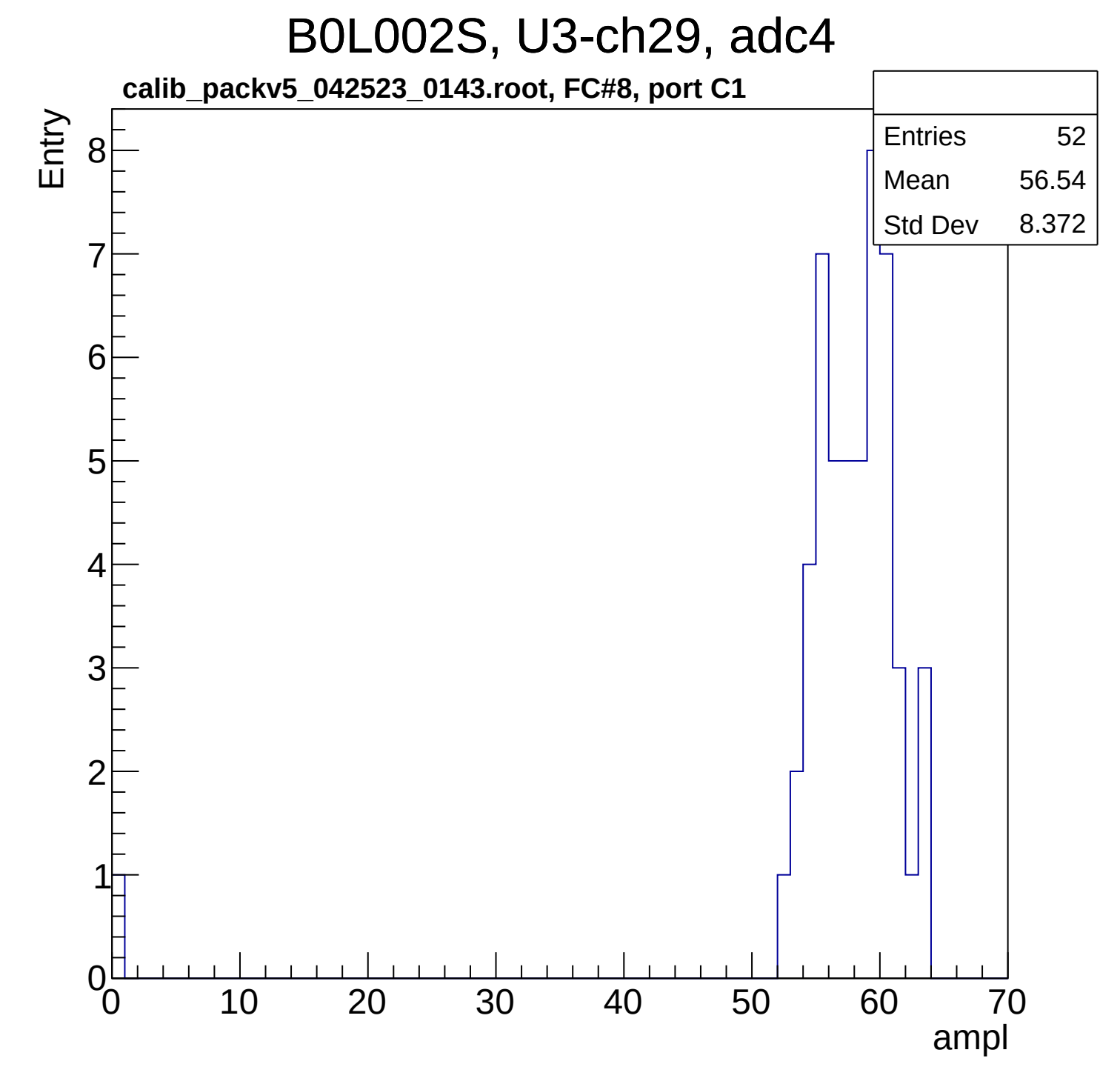

B0L002S, U3-ch28, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch29, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.26
Std Dev 6.477
Gaus mean : 31.8090
Gaus Width: 4.0099
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch29, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 37.05
Std Dev 3.235
Gaus mean : 36.9026
Gaus Width: 3.3615
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch29, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.37
Std Dev 2.962
Gaus mean : 43.7289
Gaus Width: 3.0459
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch29, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 50.58
Std Dev 3.521
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch29, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 56.54
Std Dev 8.372
Entry
ampl
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1
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0
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60
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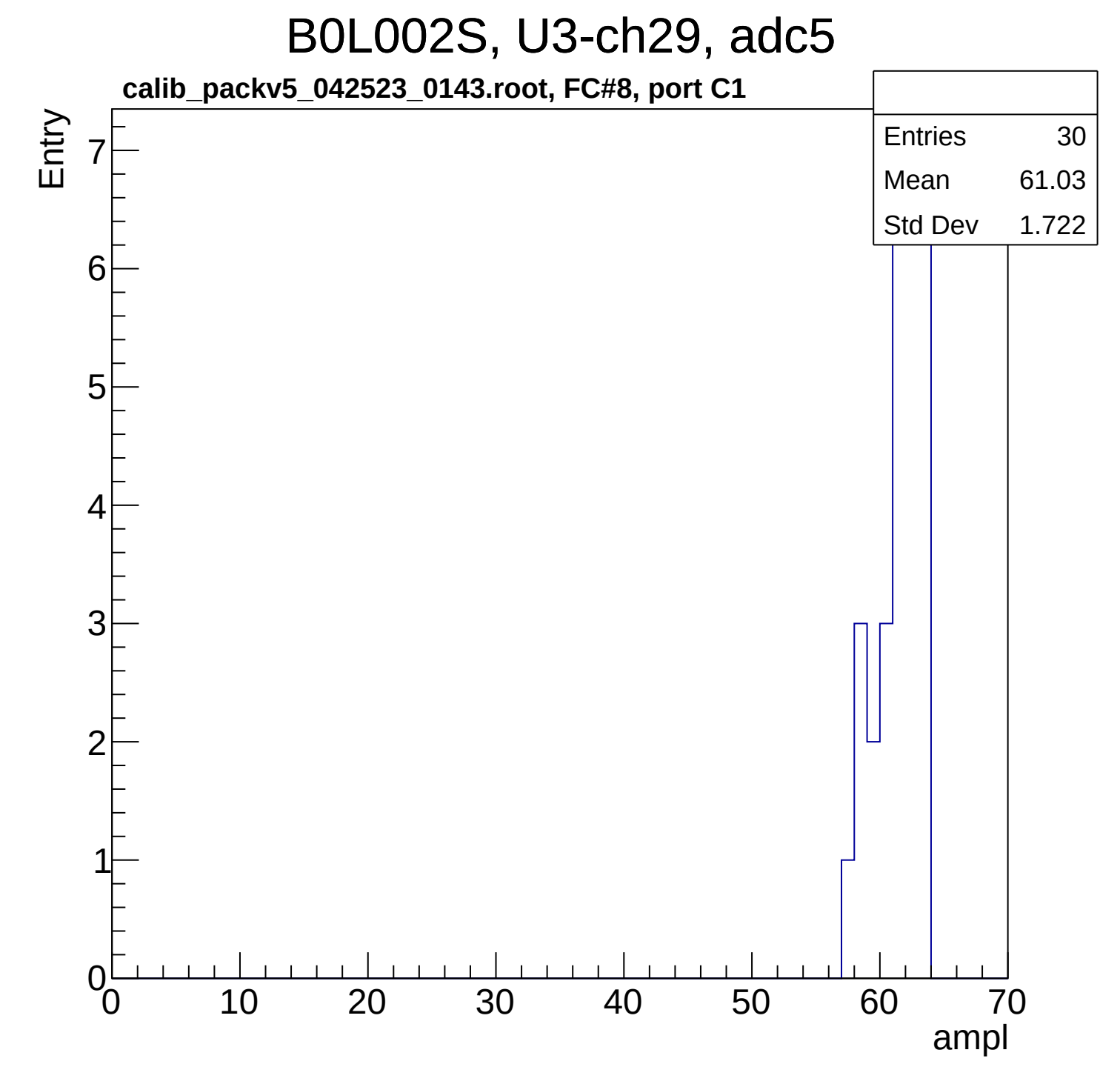
B0L002S, U3-ch29, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 61.03
Std Dev 1.722
Entry
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1
2
3
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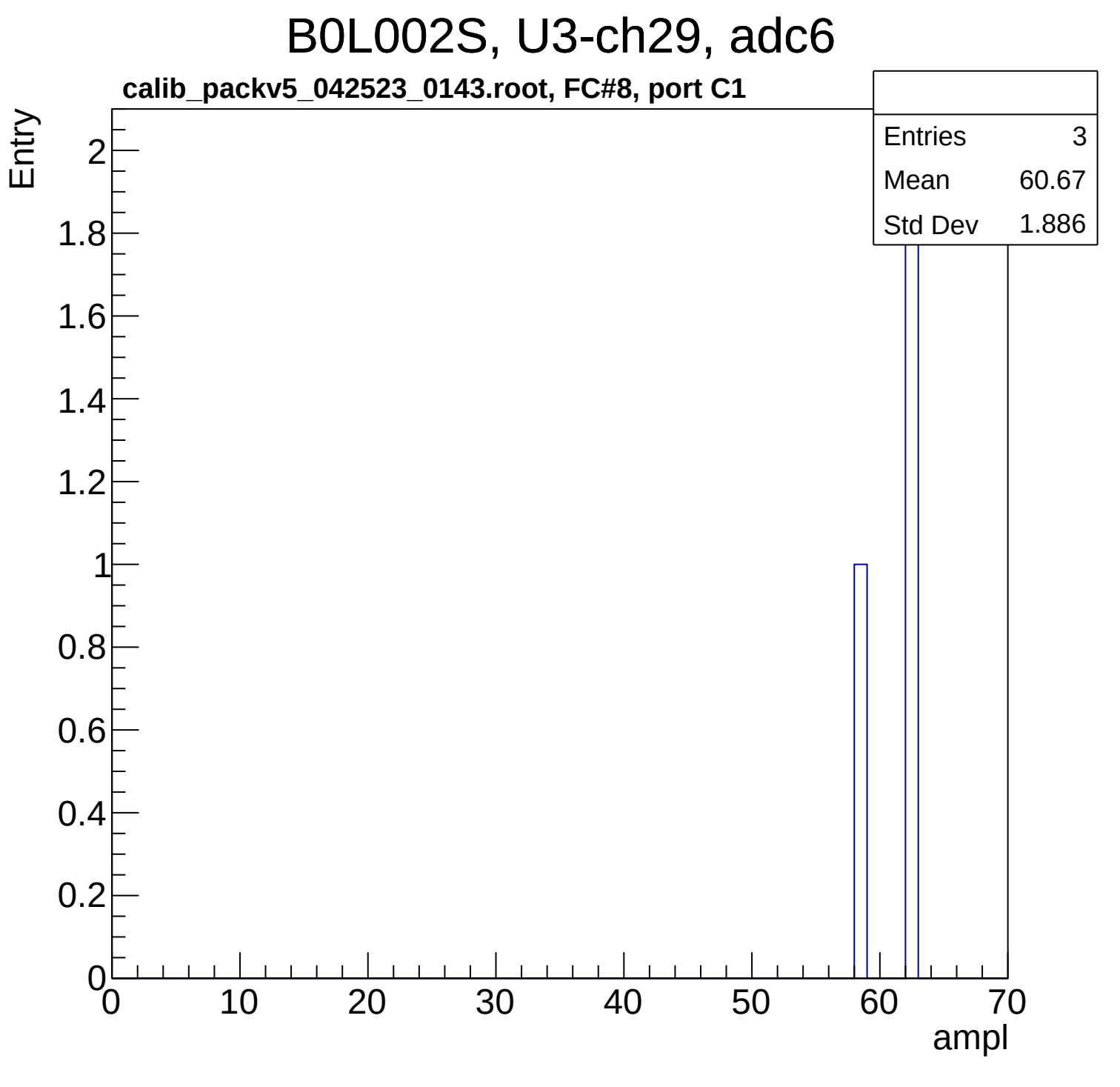
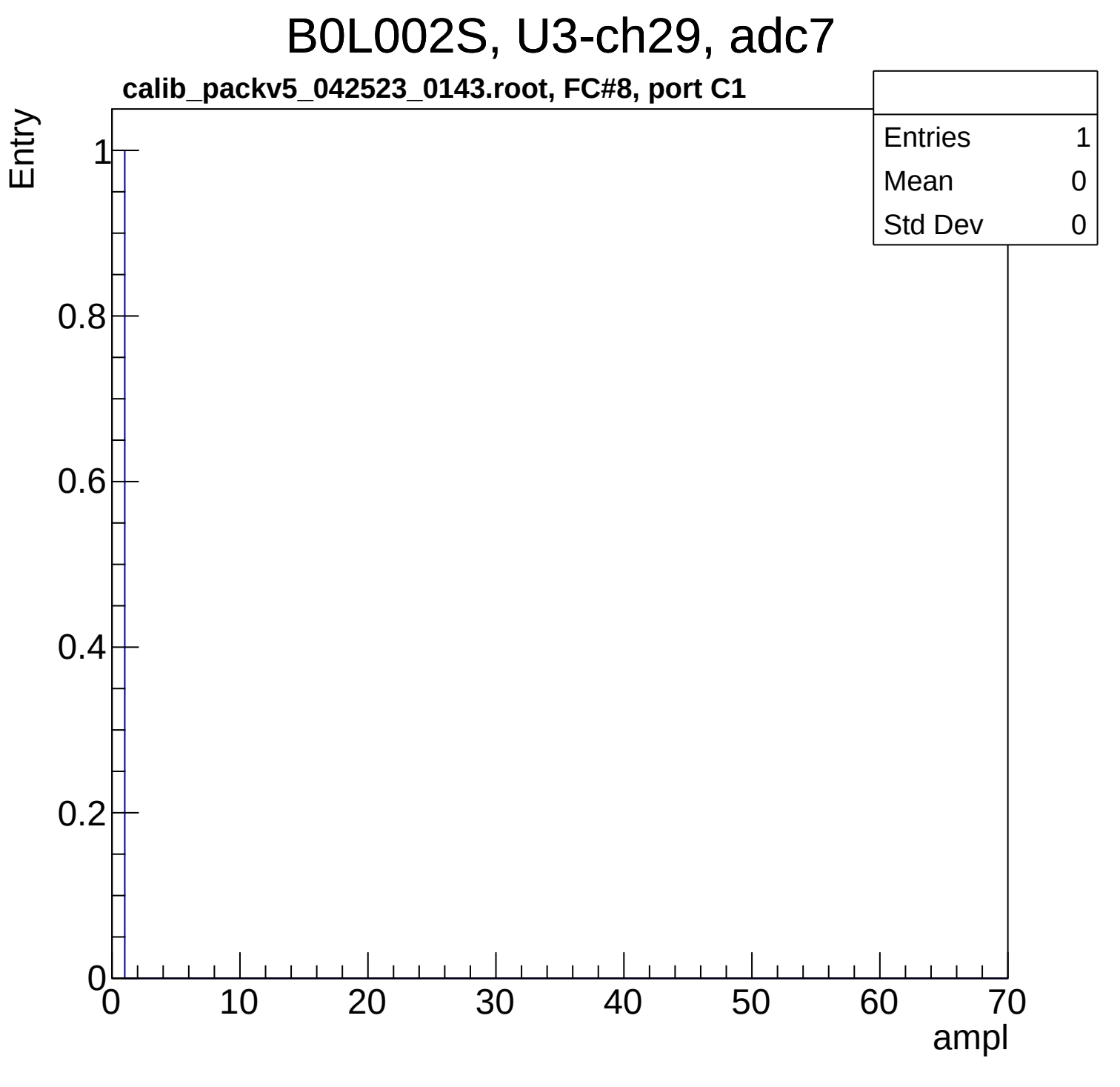
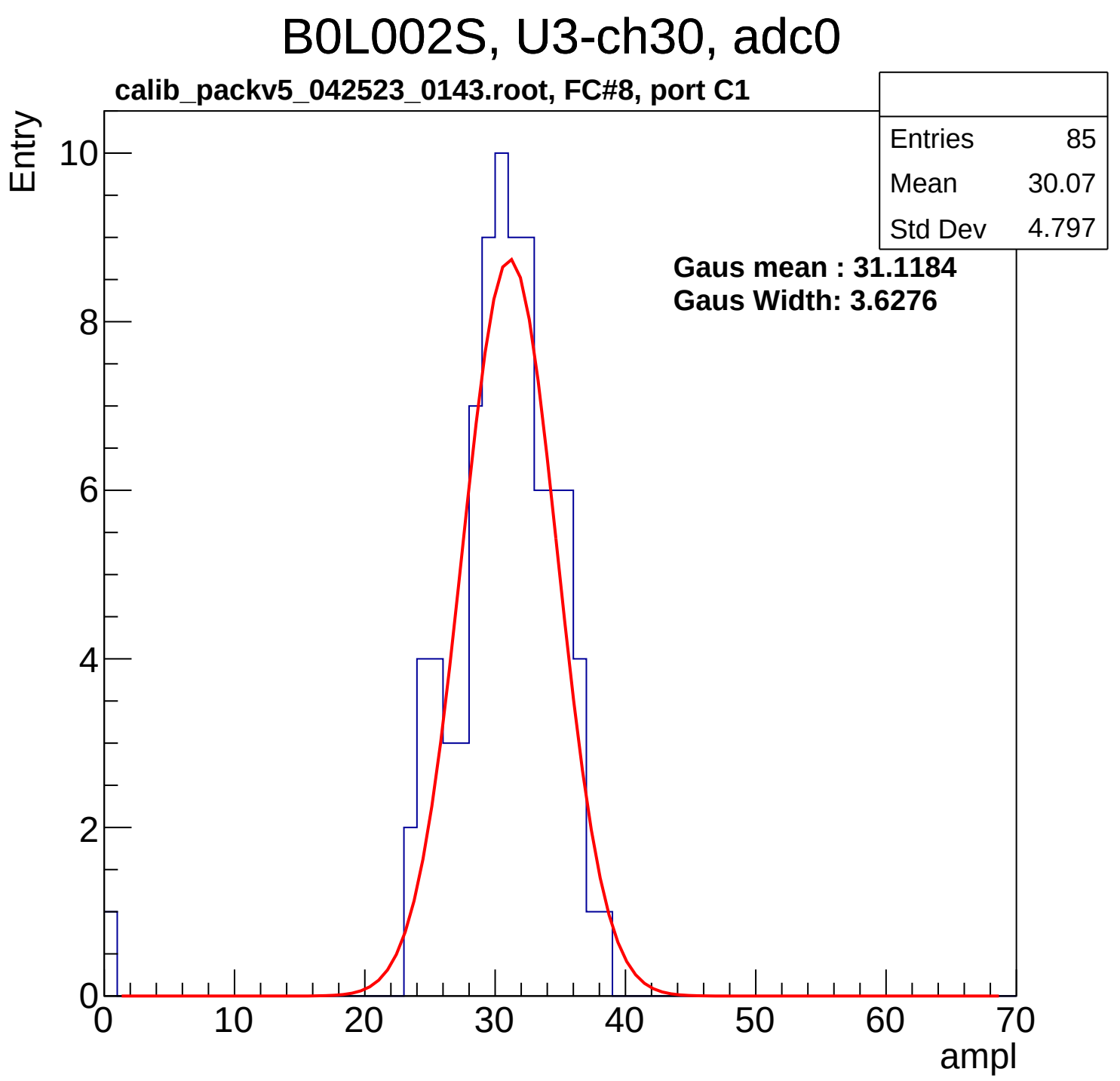
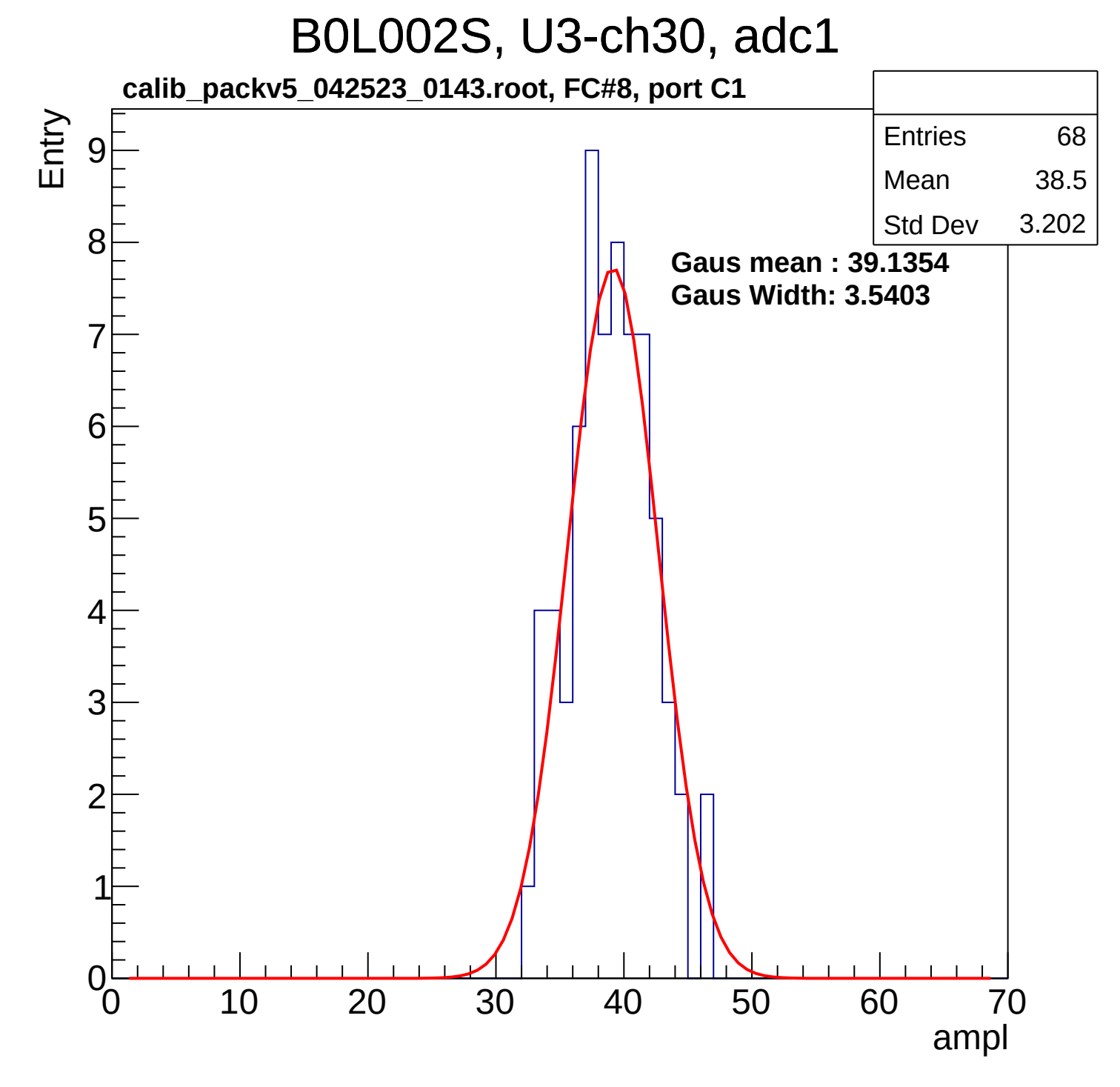
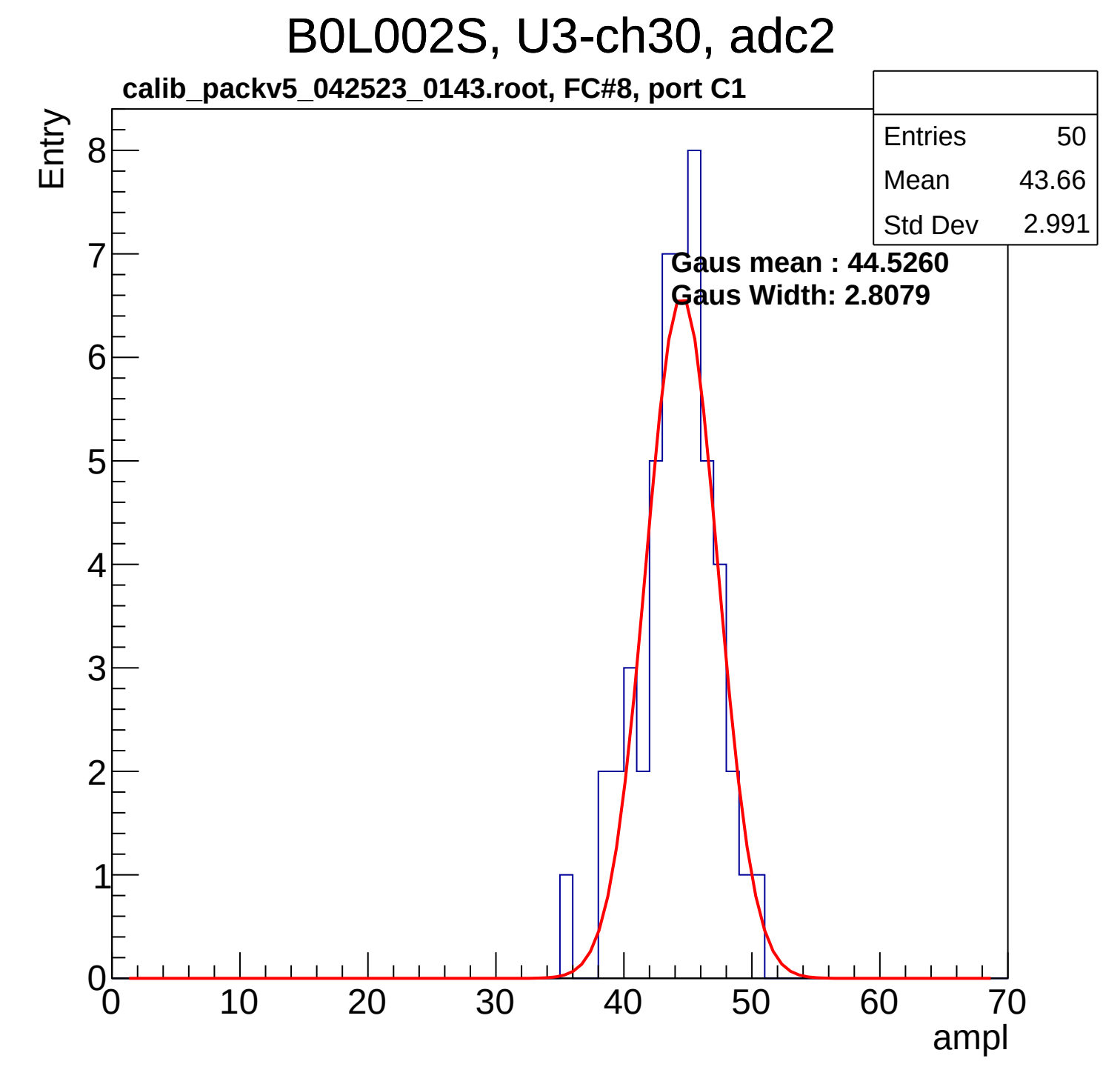
5
6
7
0
10
20
30
40
50
60
70
ampl

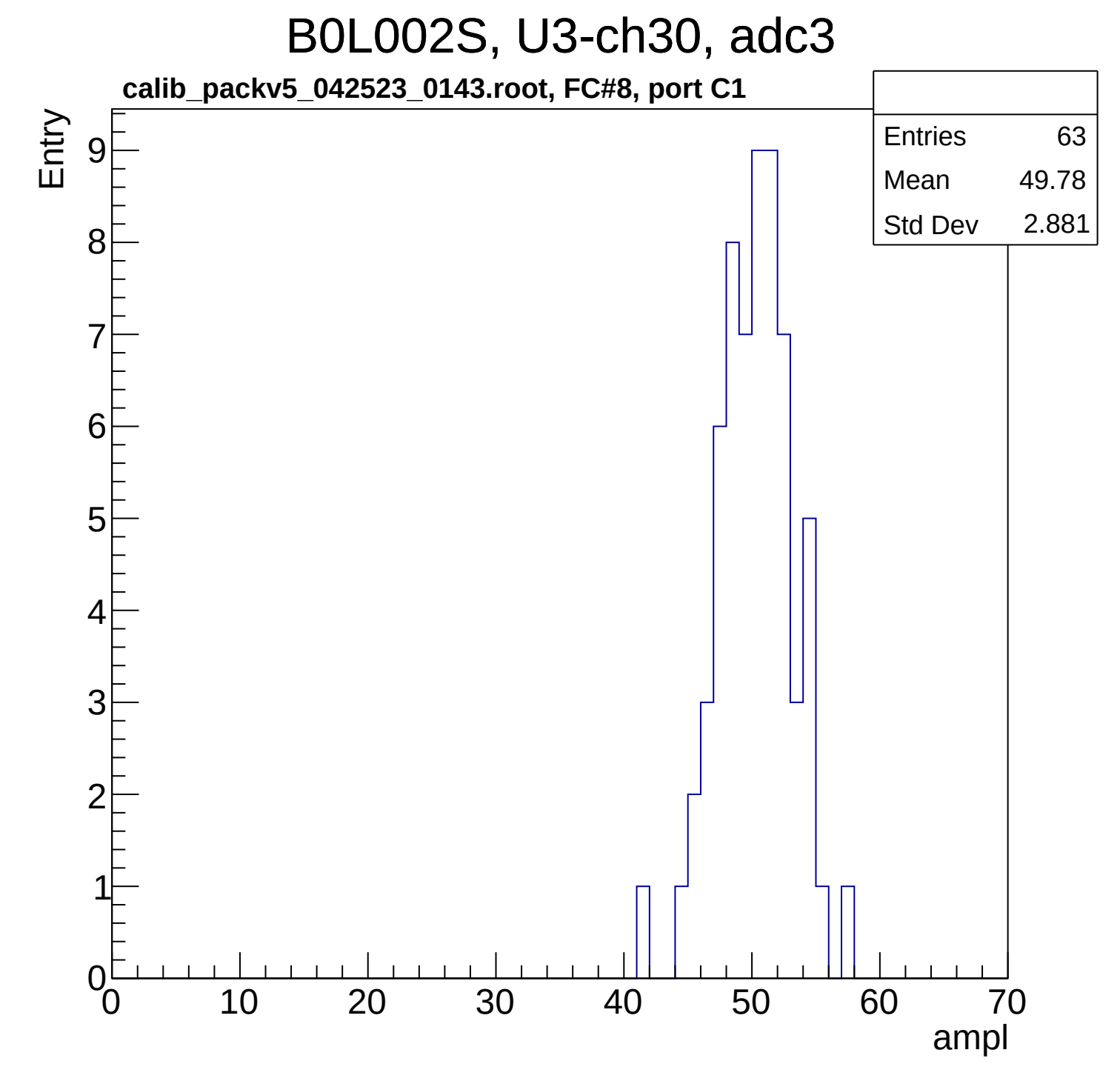

B0L002S, U3-ch29, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 60.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch29, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch30, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 30.07
Std Dev 4.797
Gaus mean : 31.1184
Gaus Width: 3.6276
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch30, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 38.5
Std Dev 3.202
Gaus mean : 39.1354
Gaus Width: 3.5403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch30, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 43.66
Std Dev 2.991
Gaus mean : 44.5260
Gaus Width: 2.8079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch30, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 49.78
Std Dev 2.881
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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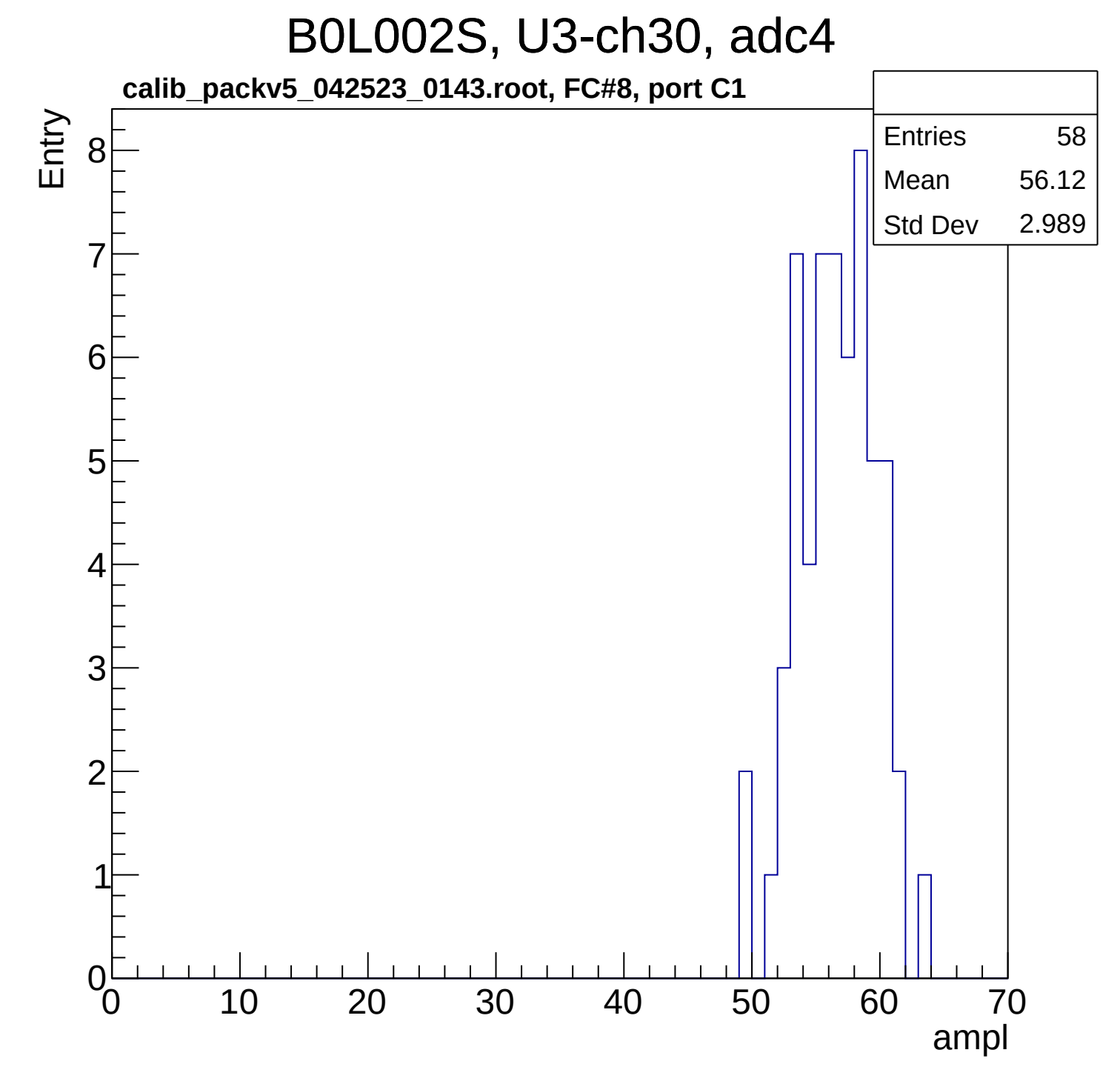
B0L002S, U3-ch30, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 56.12
Std Dev 2.989
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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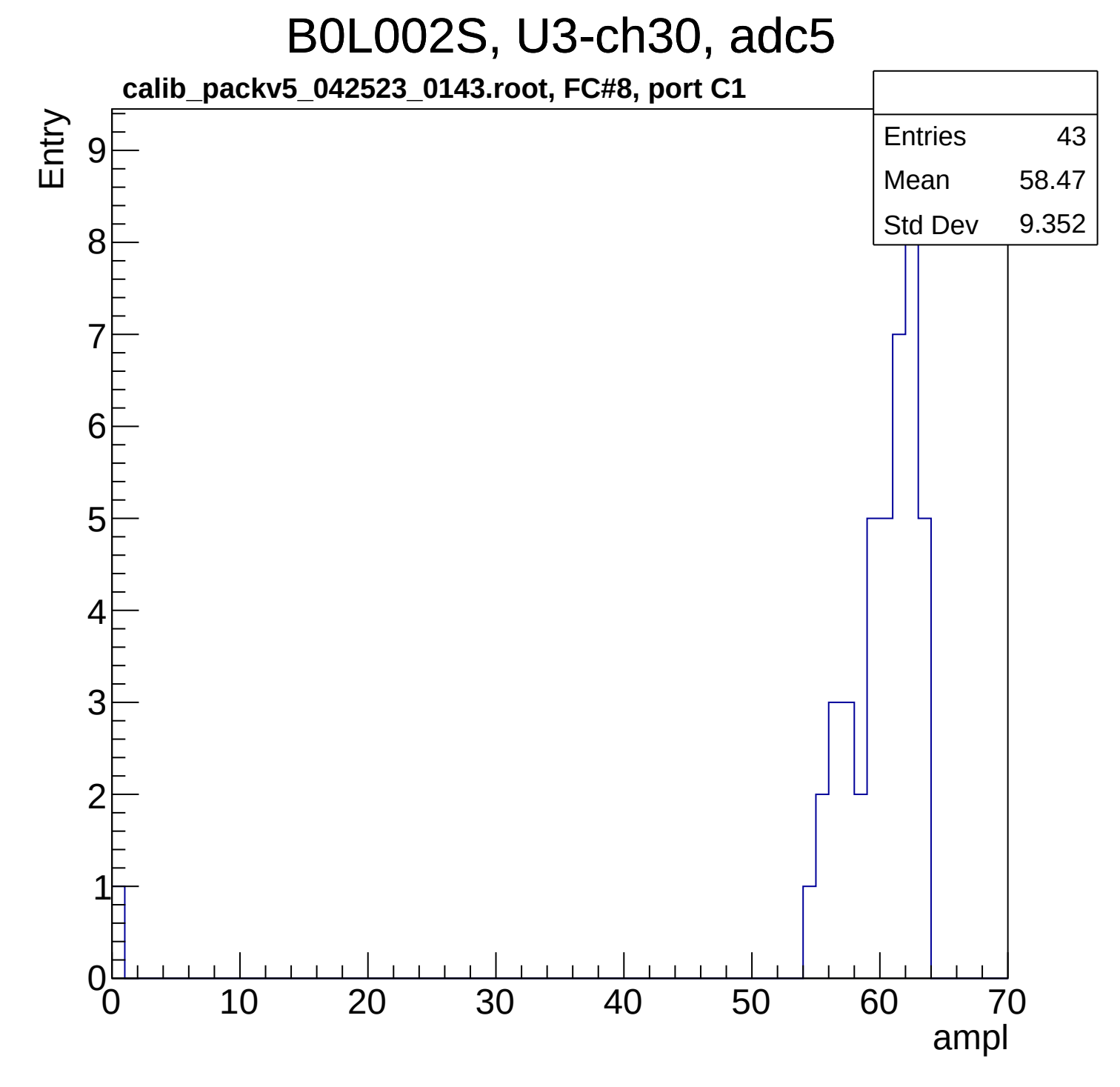
B0L002S, U3-ch30, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 58.47
Std Dev 9.352
Entry
ampl
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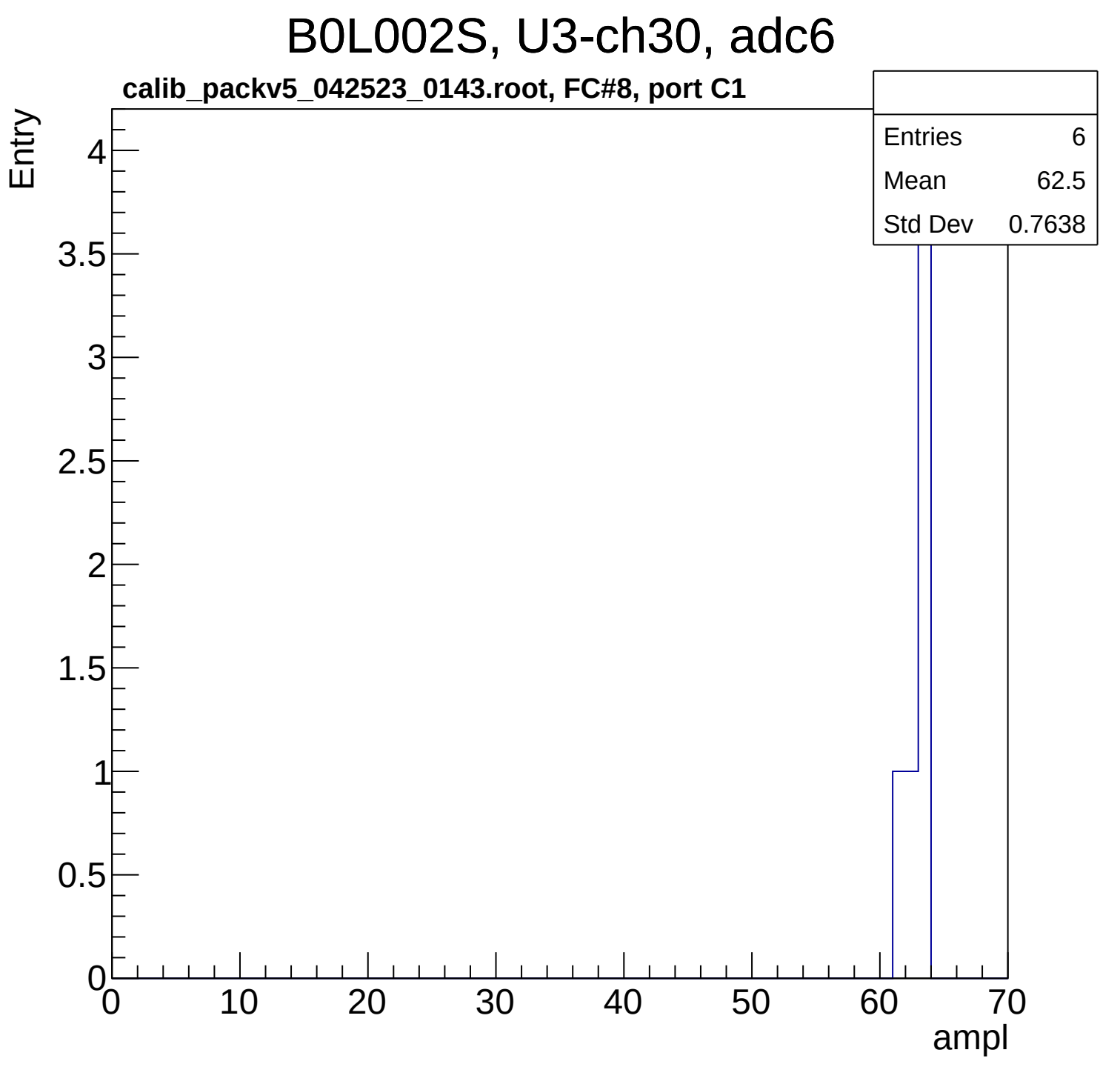
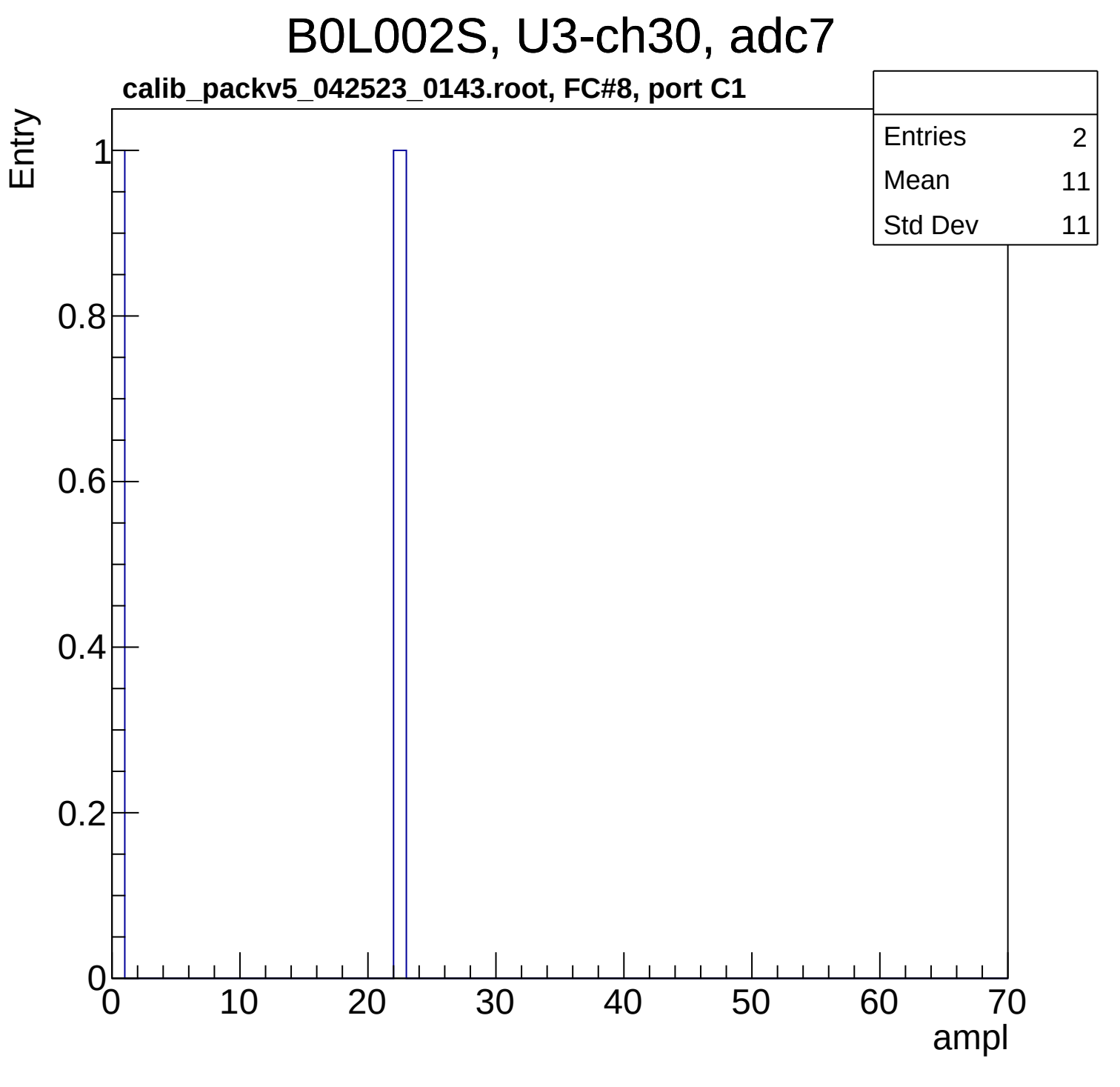
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch30, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U3-ch30, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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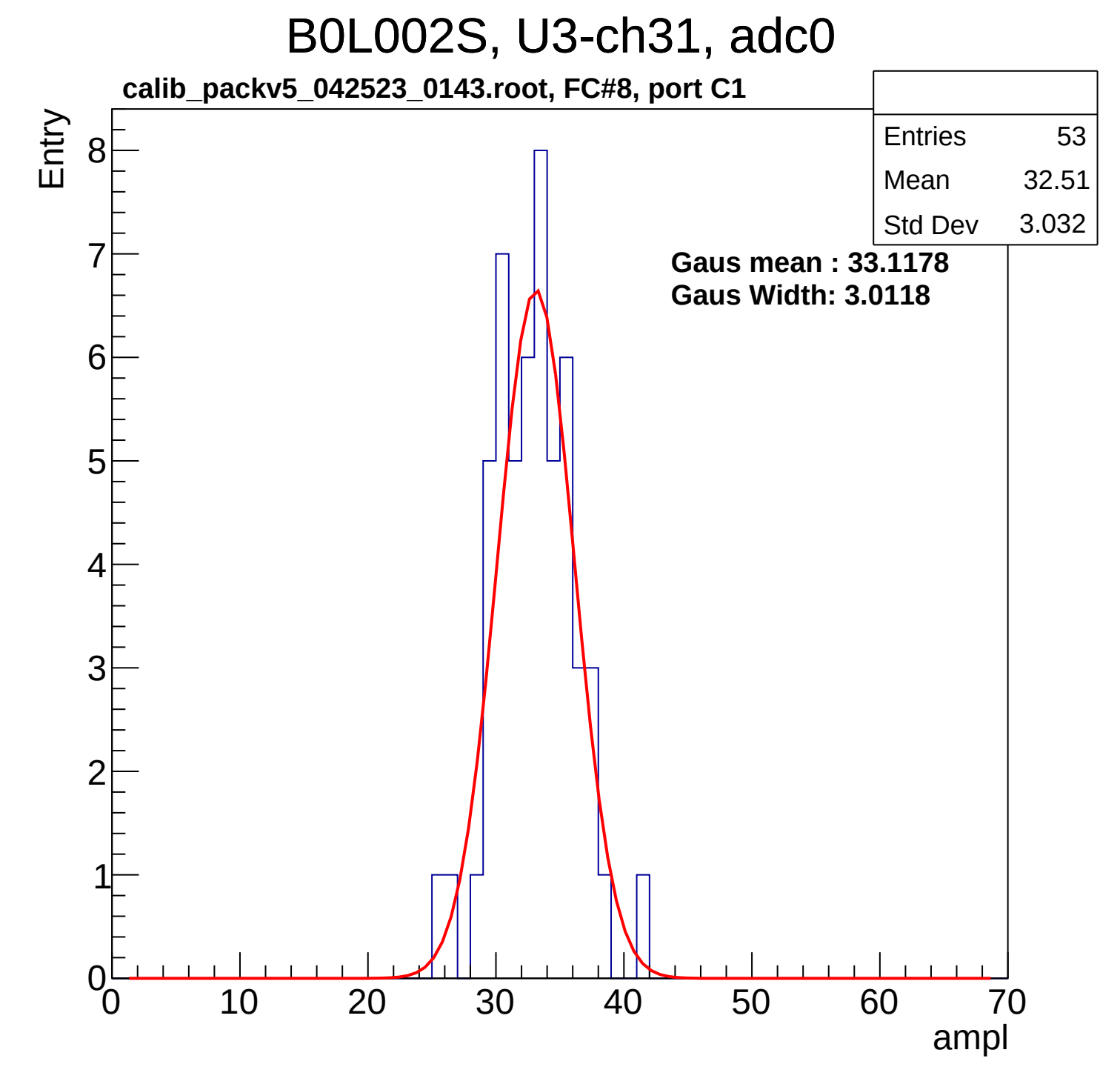
50
60
70
ampl


B0L002S, U3-ch31, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 32.51
Std Dev 3.032
Gaus mean : 33.1178
Gaus Width: 3.0118
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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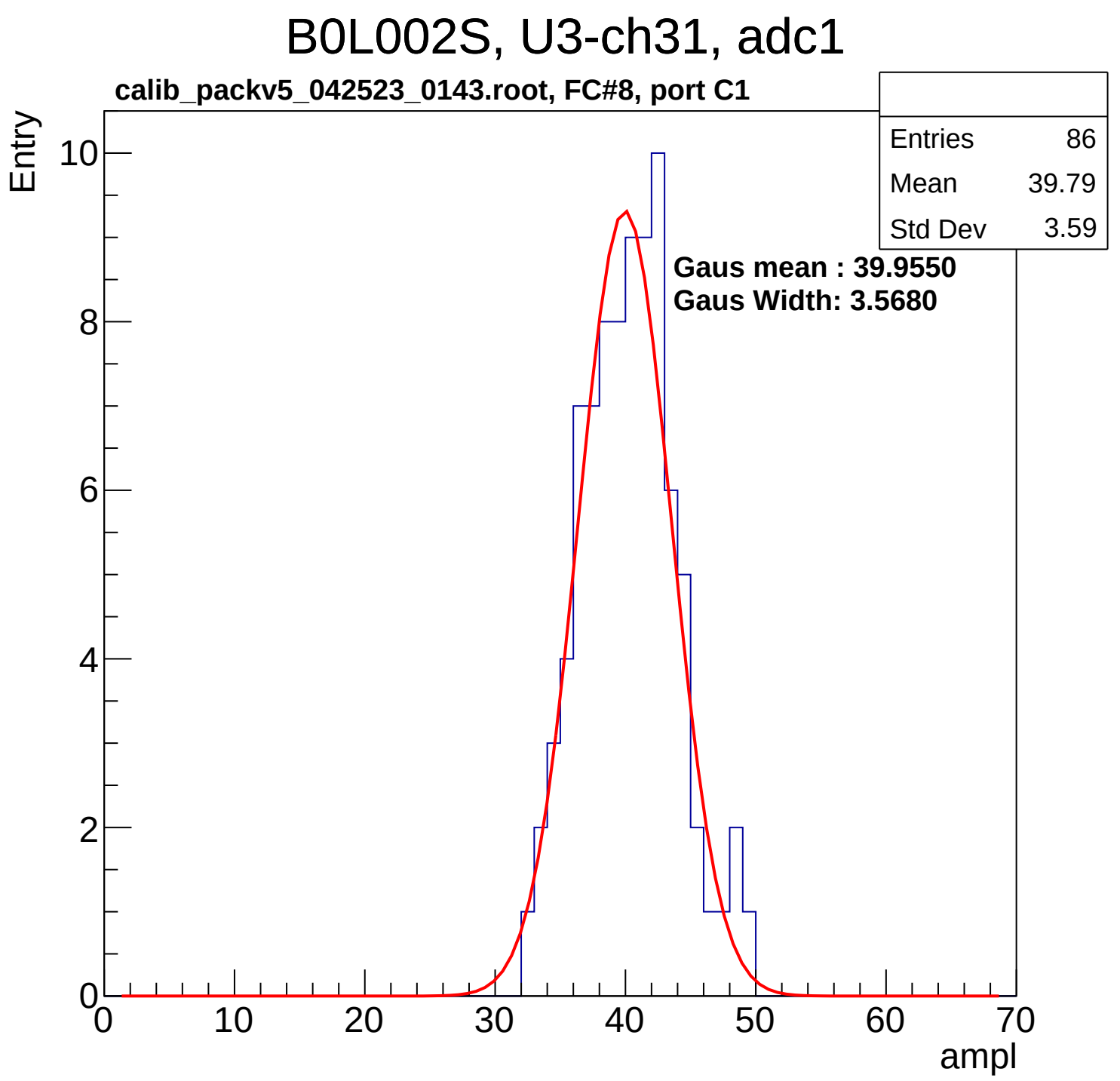
B0L002S, U3-ch31, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 39.79
Std Dev 3.59
Gaus mean : 39.9550
Gaus Width: 3.5680
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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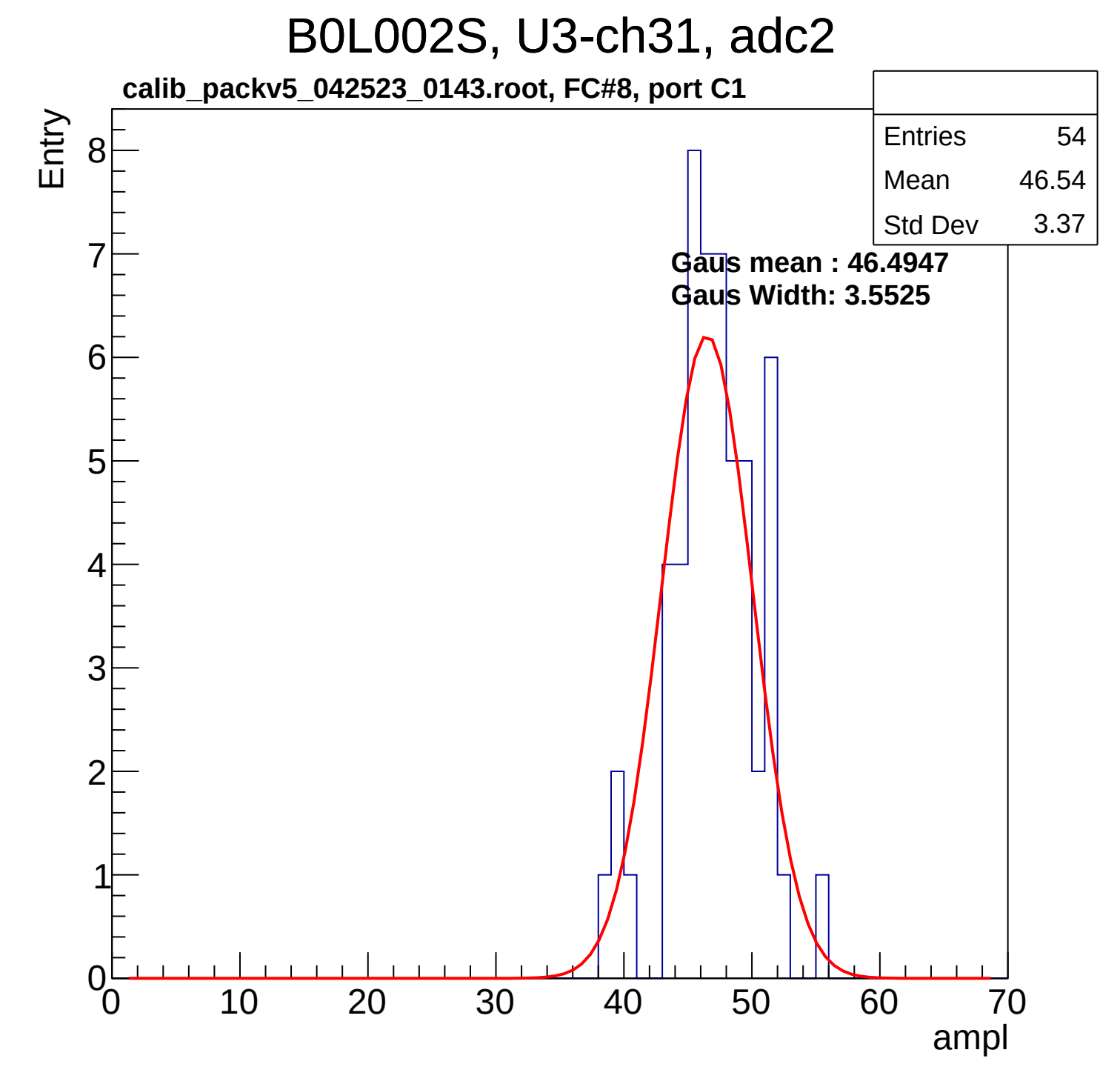
B0L002S, U3-ch31, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 46.54
Std Dev 3.37
Gaus mean : 46.4947
Gaus Width: 3.5525
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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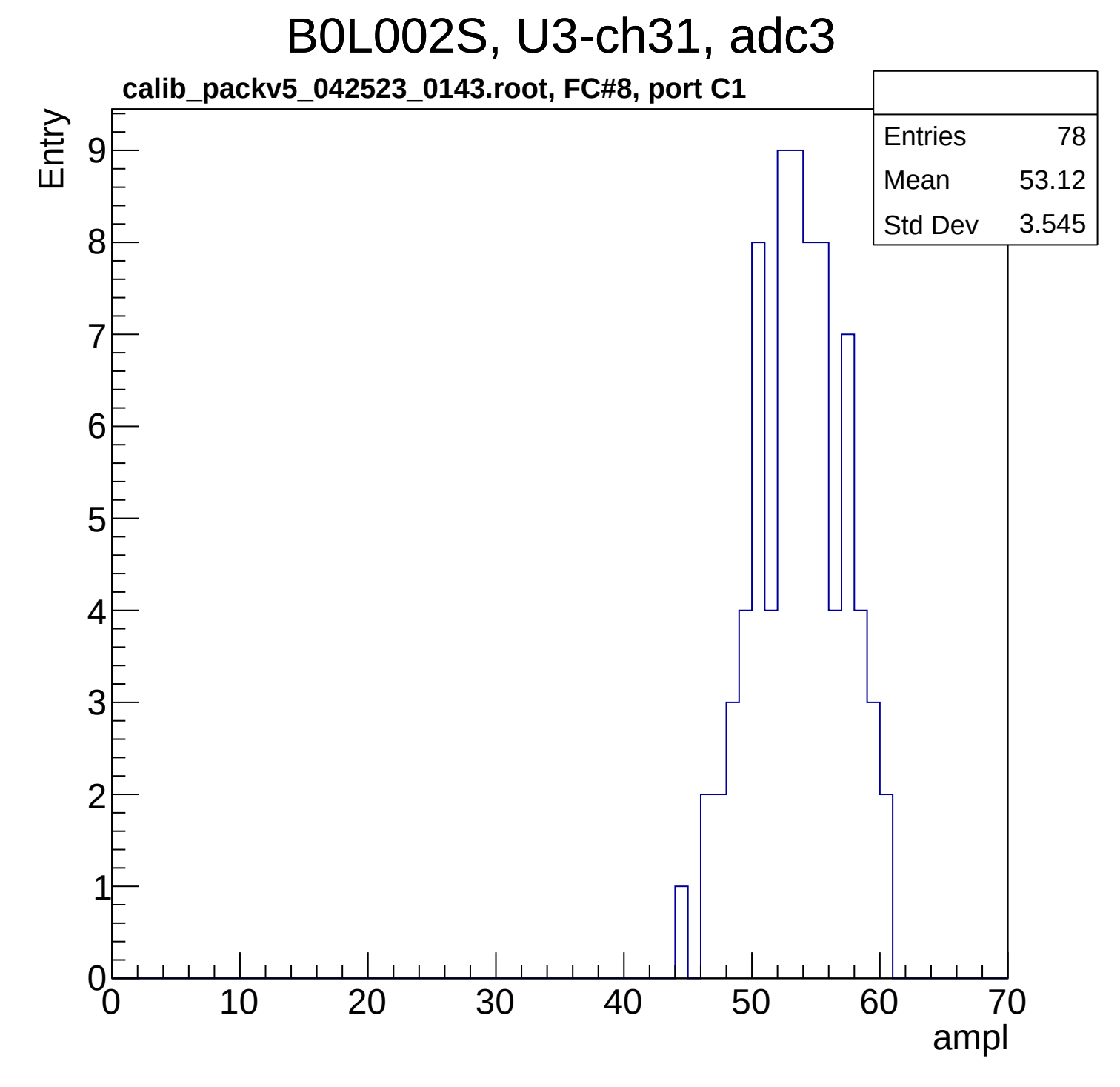
B0L002S, U3-ch31, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 53.12
Std Dev 3.545
Entry
ampl
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2
3
4
5
6
7
8
9
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60
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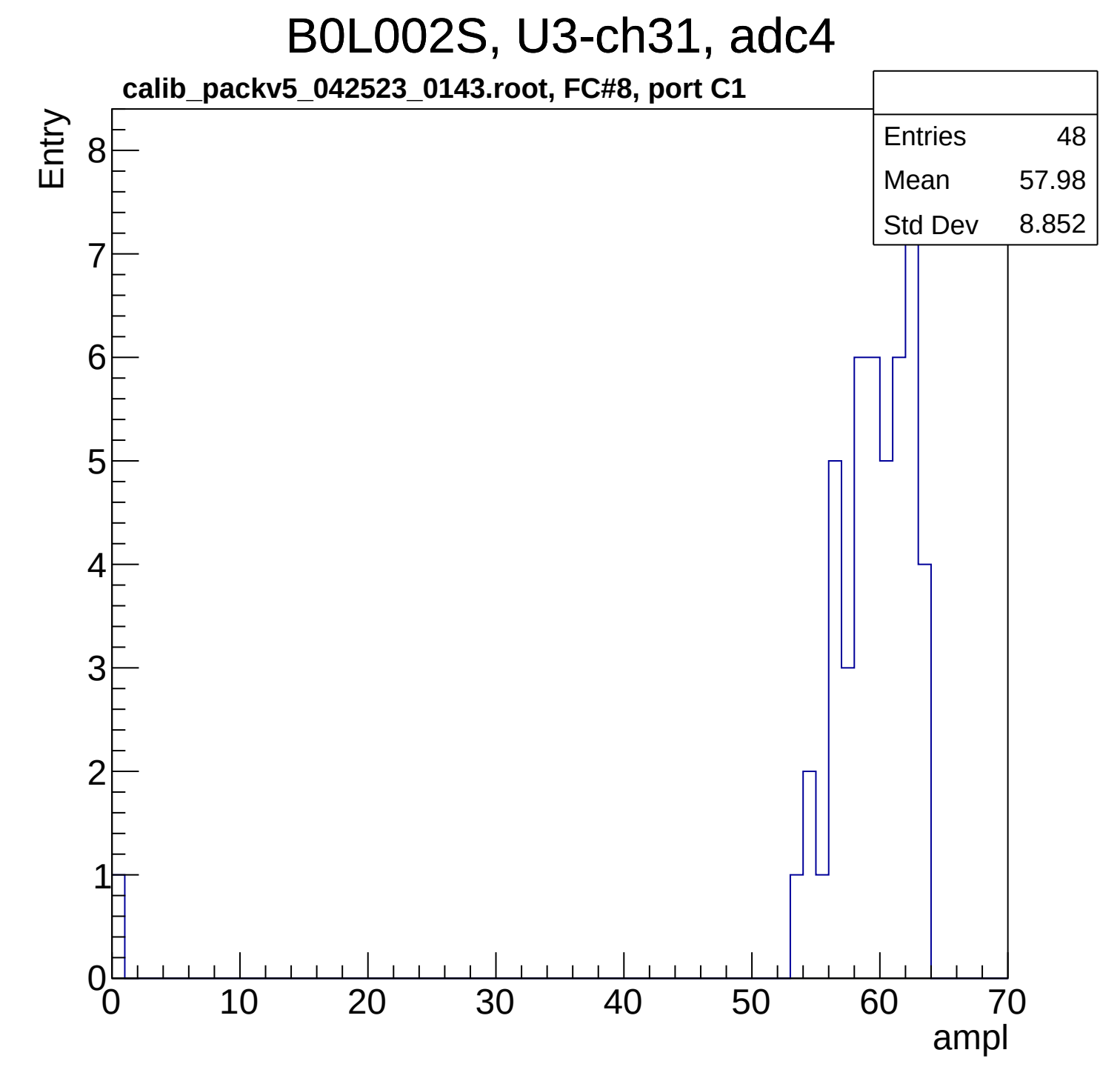
B0L002S, U3-ch31, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.98
Std Dev 8.852
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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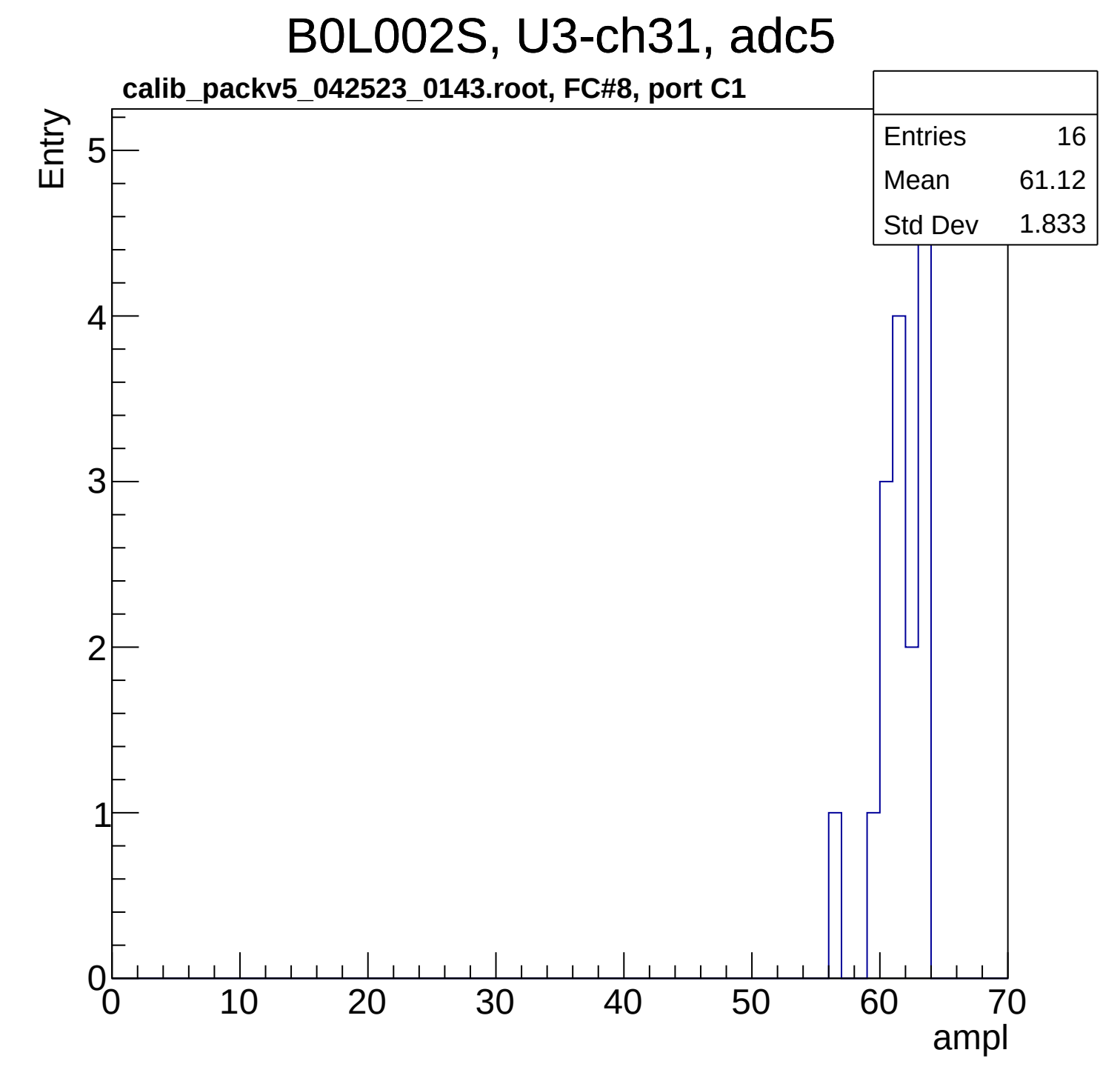
B0L002S, U3-ch31, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 61.12
Std Dev 1.833
Entry
0
1
2
3
4
5
0
10
20
30
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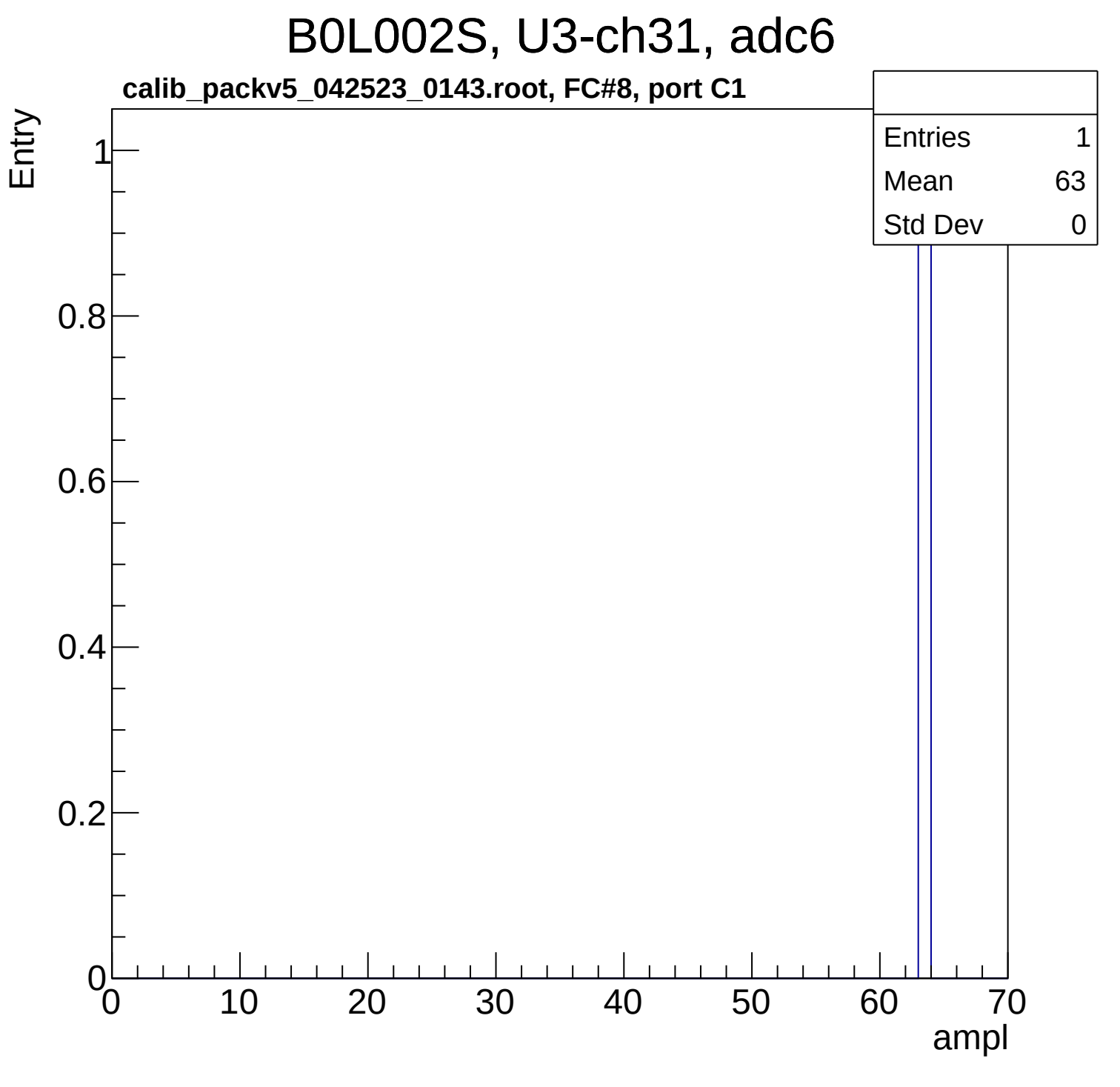
50
60
70
ampl


B0L002S, U3-ch31, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
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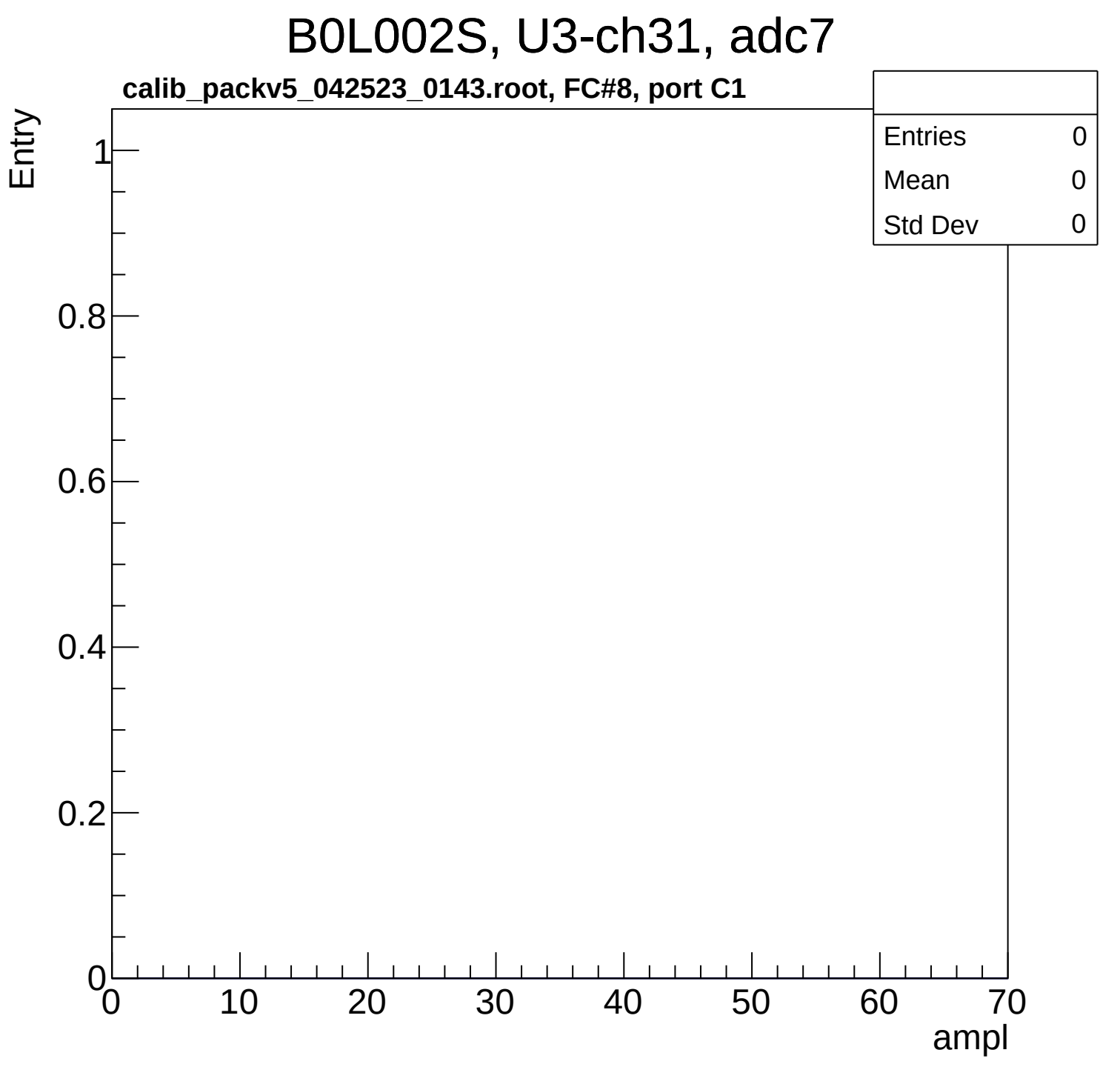
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch31, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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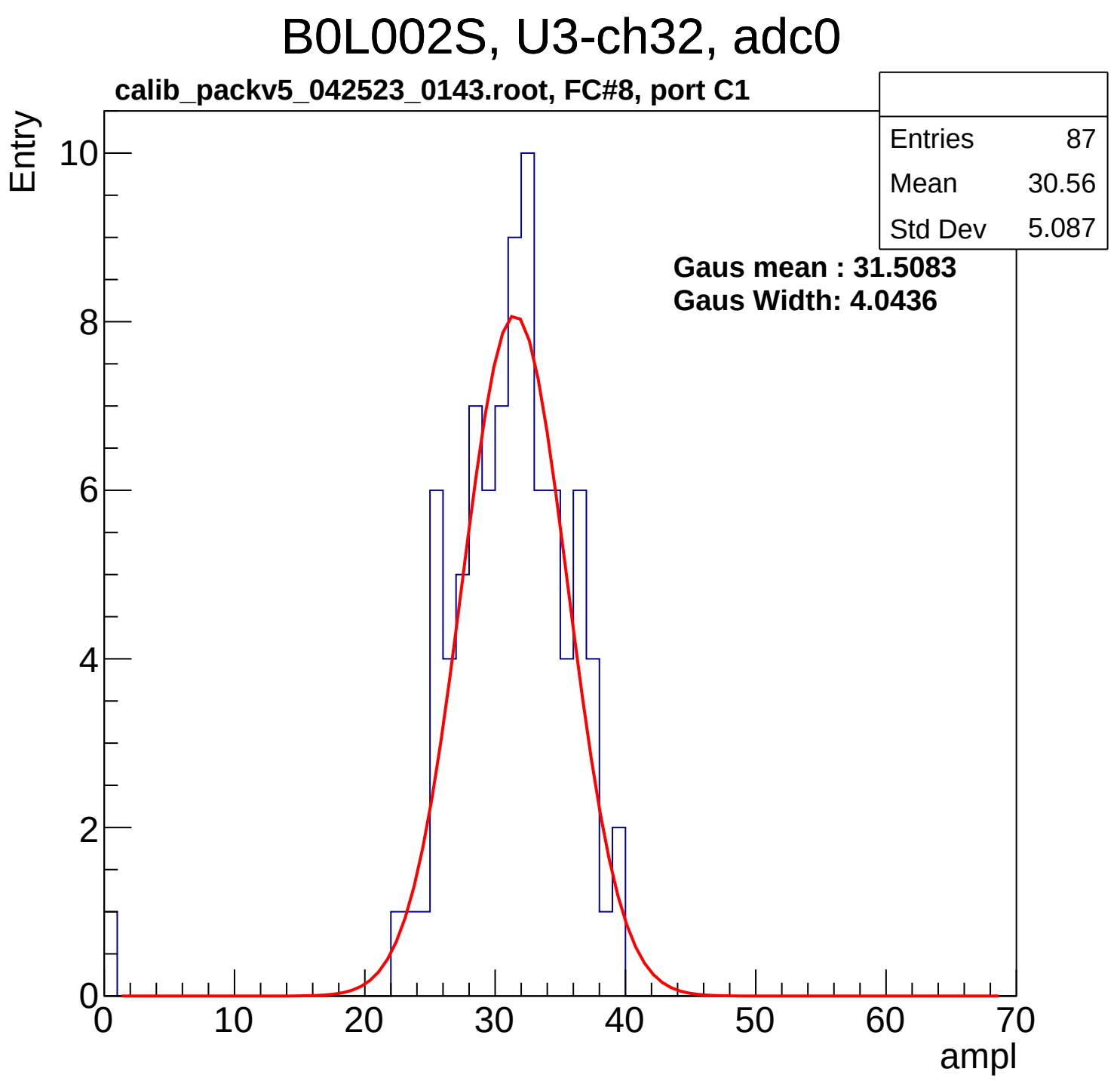
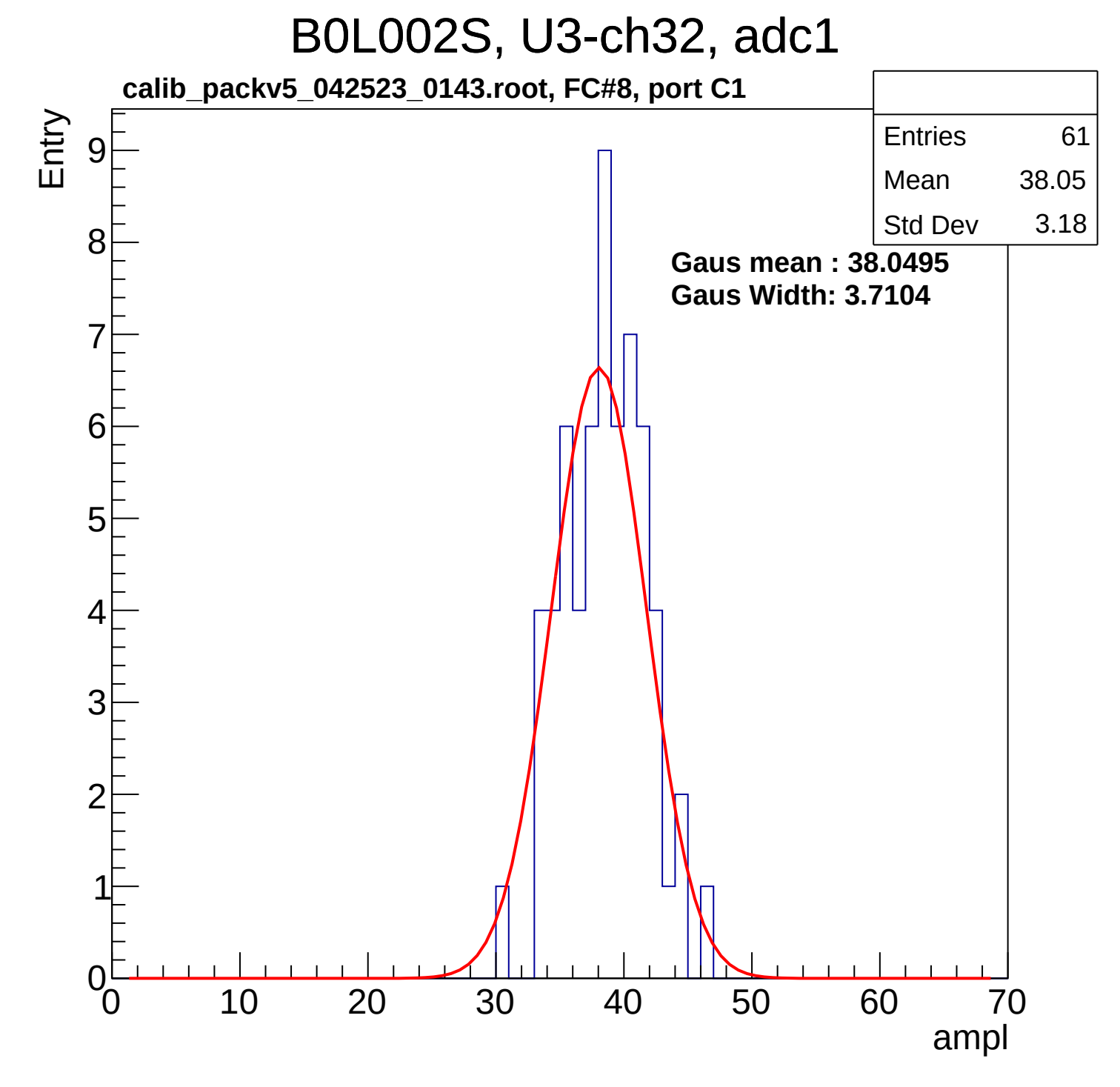
50
60
70
ampl


B0L002S, U3-ch32, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 30.56
Std Dev 5.087
Gaus mean : 31.5083
Gaus Width: 4.0436
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch32, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 38.05
Std Dev 3.18
Gaus mean : 38.0495
Gaus Width: 3.7104
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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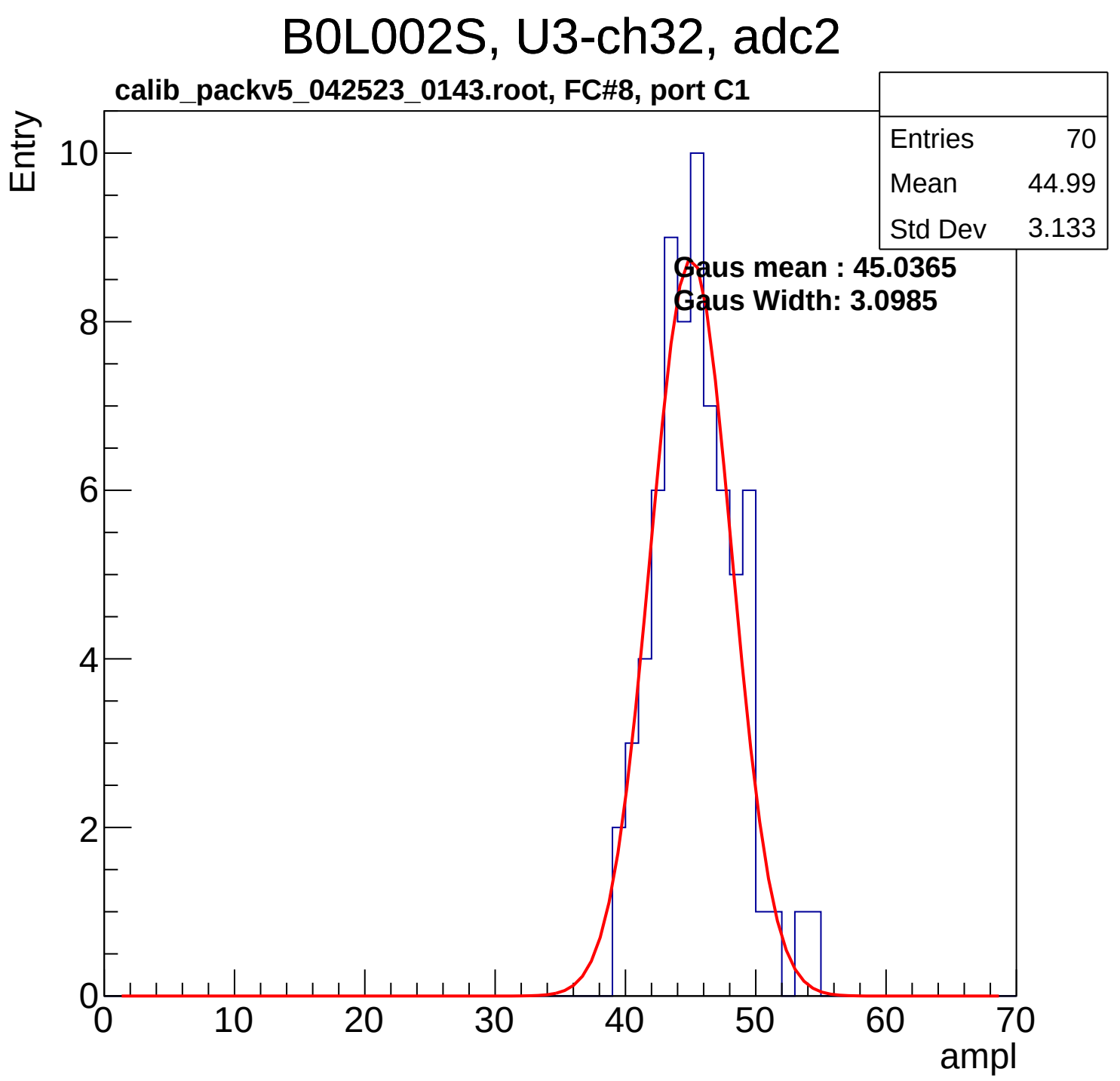
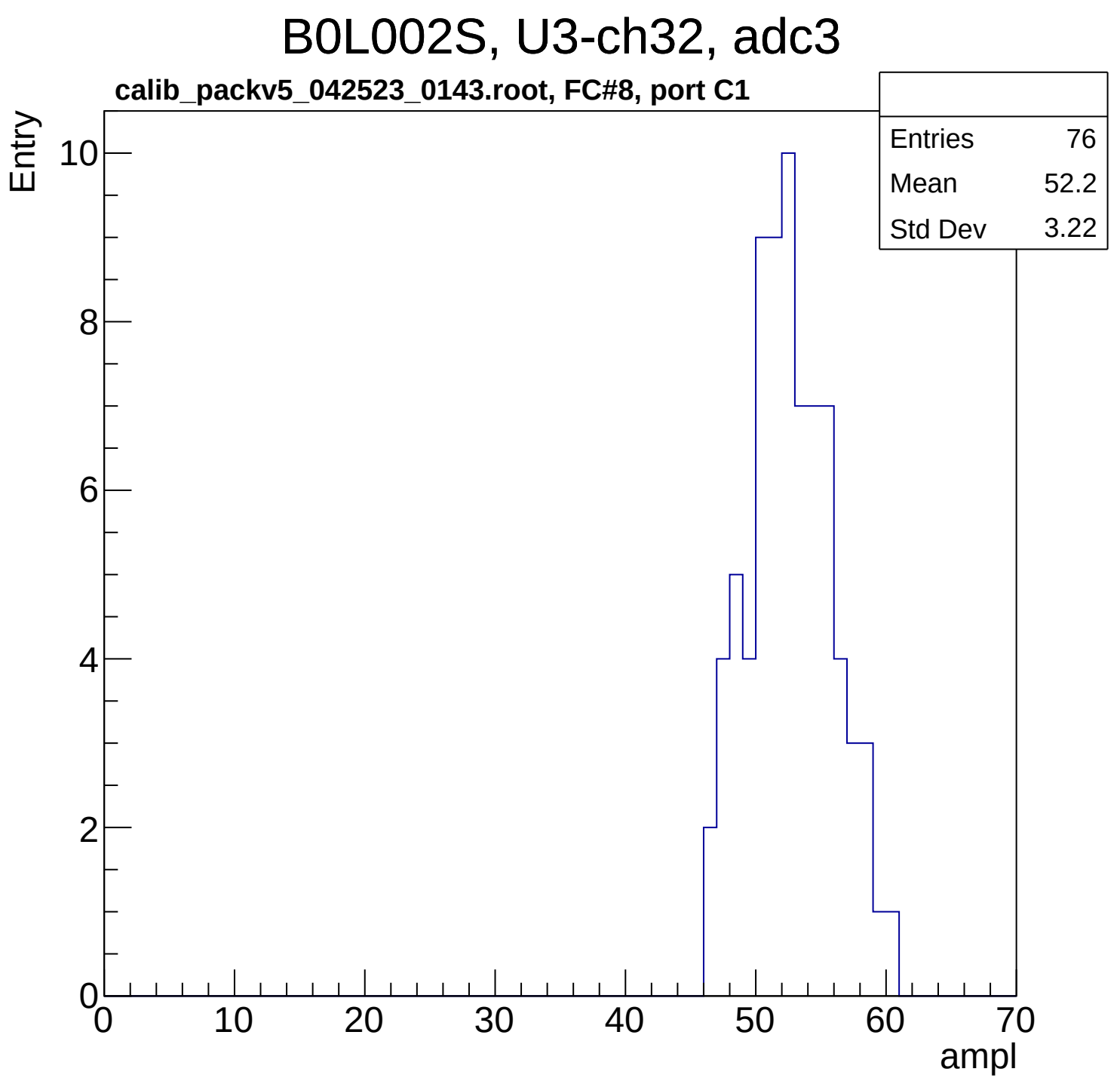
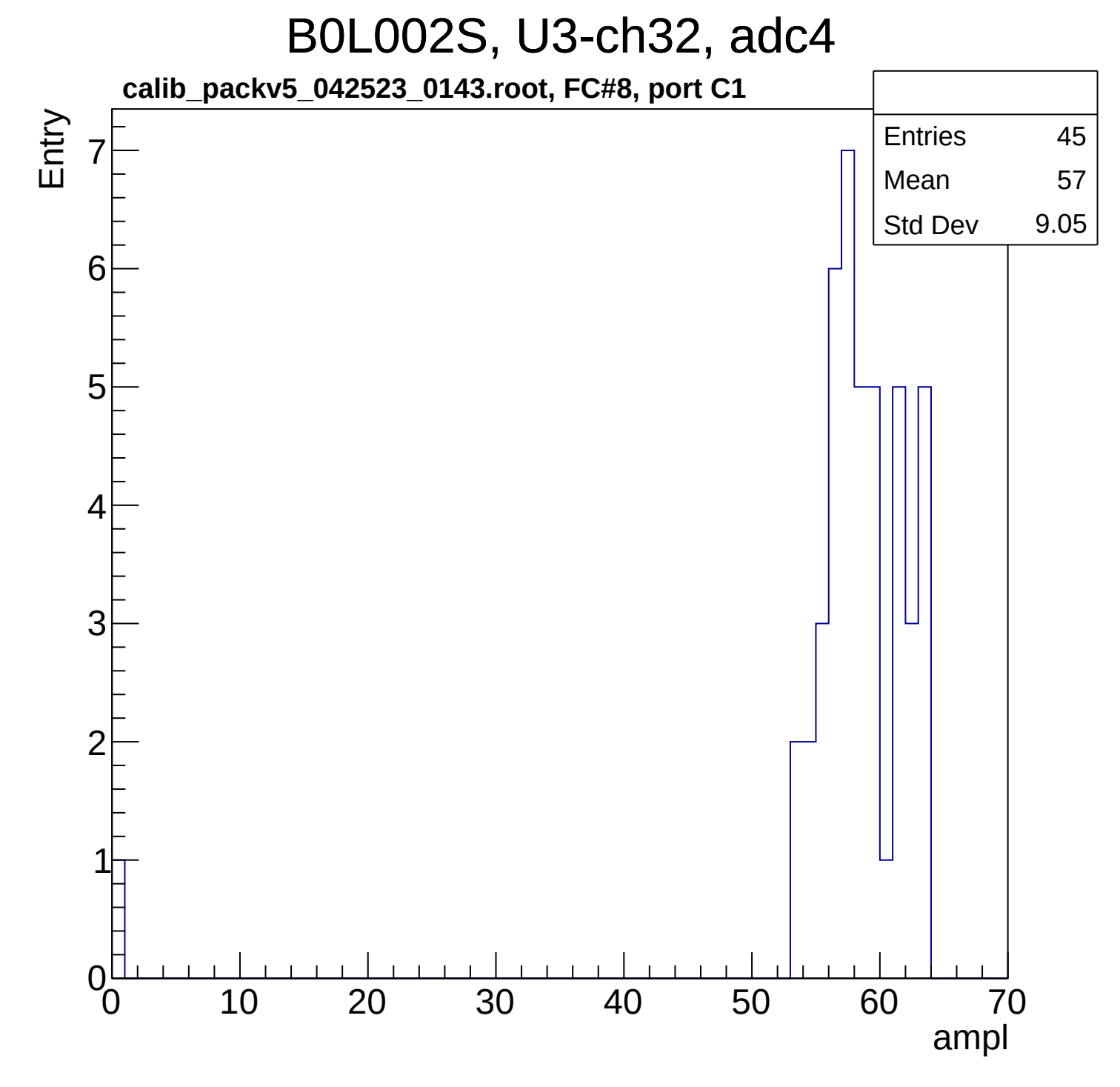
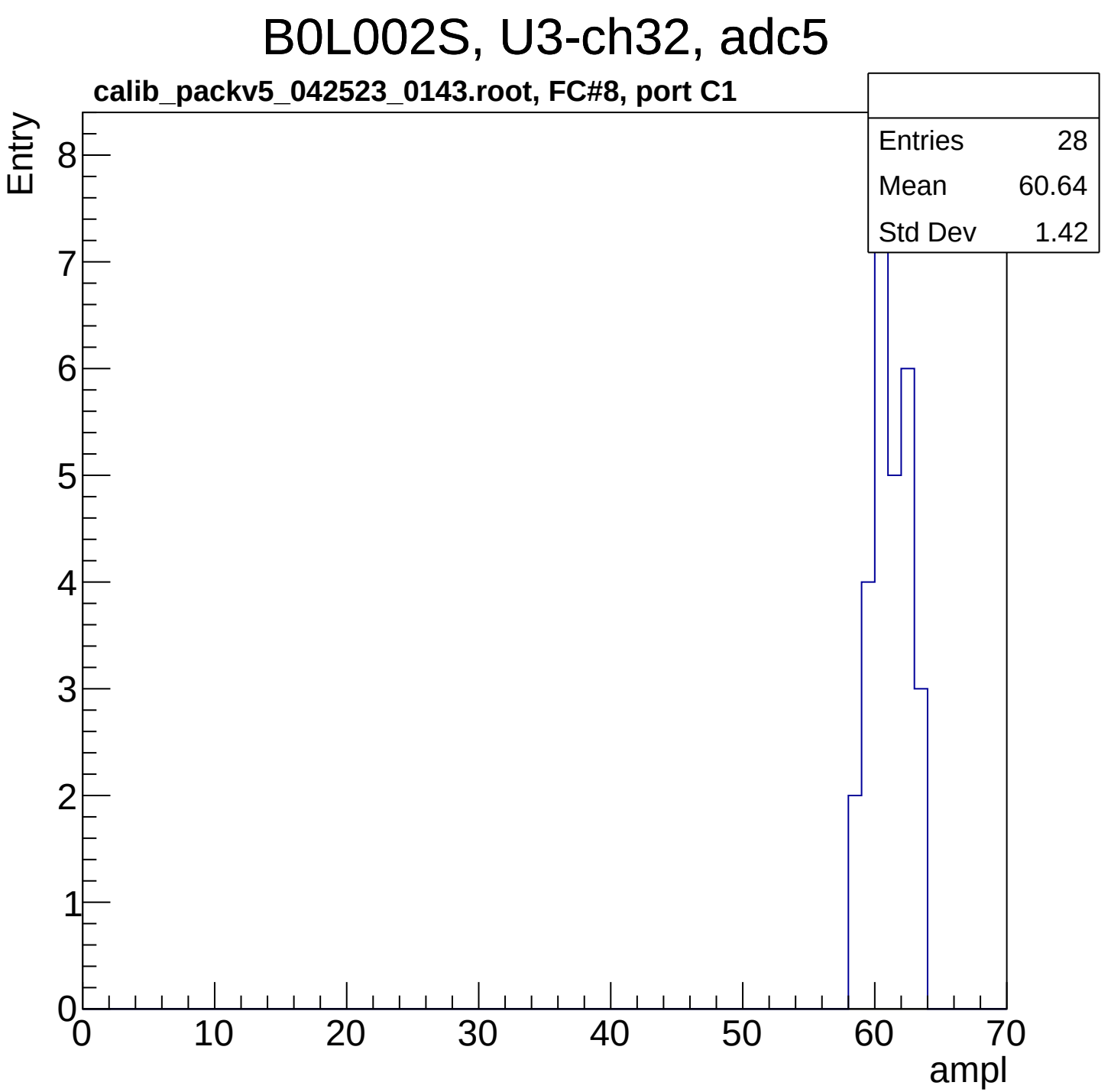

B0L002S, U3-ch32, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 44.99
Std Dev 3.133
Gaus mean : 45.0365
Gaus Width: 3.0985
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch32, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 52.2
Std Dev 3.22
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch32, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 57
Std Dev 9.05
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch32, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 60.64
Std Dev 1.42
Entry
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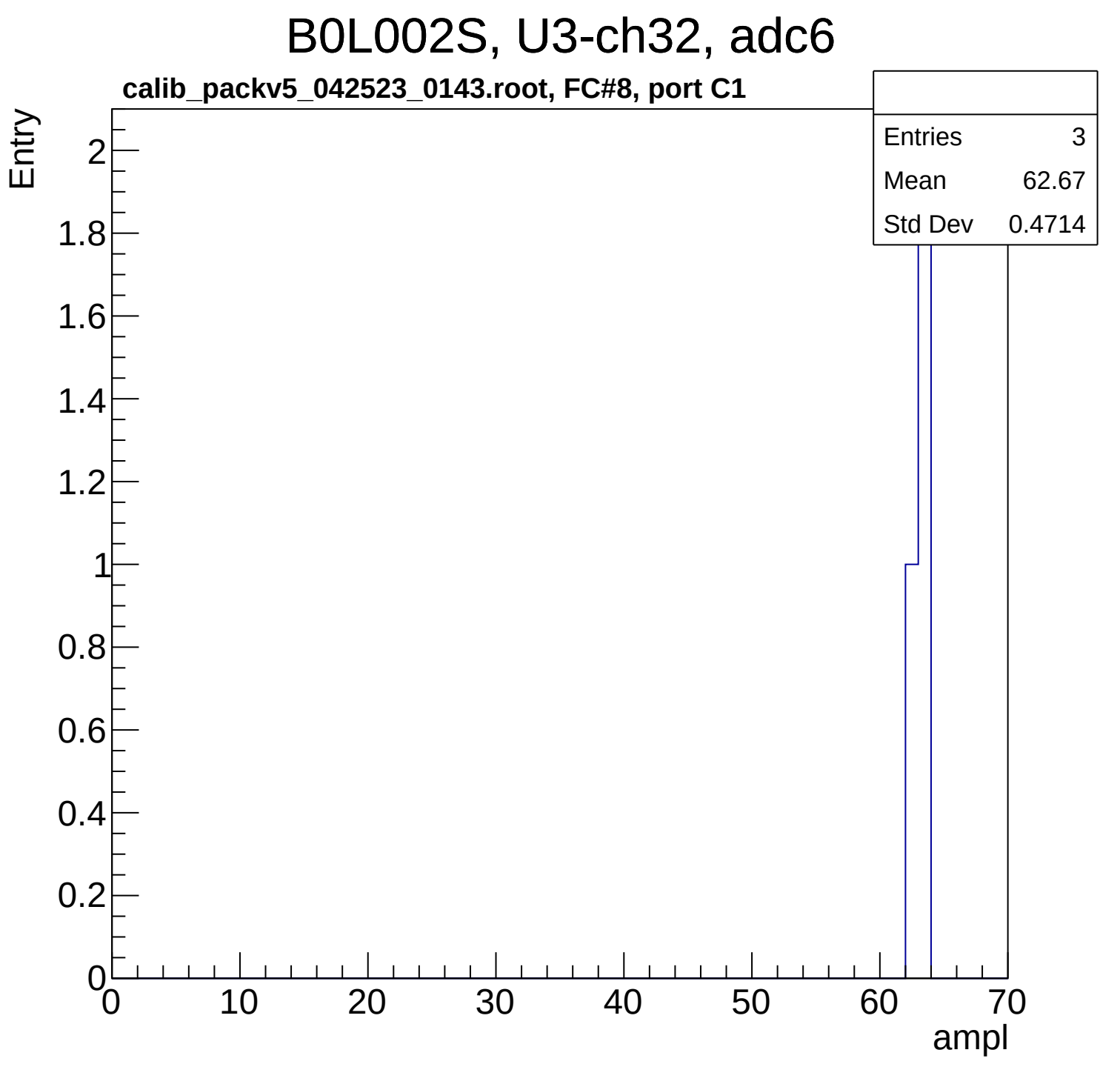
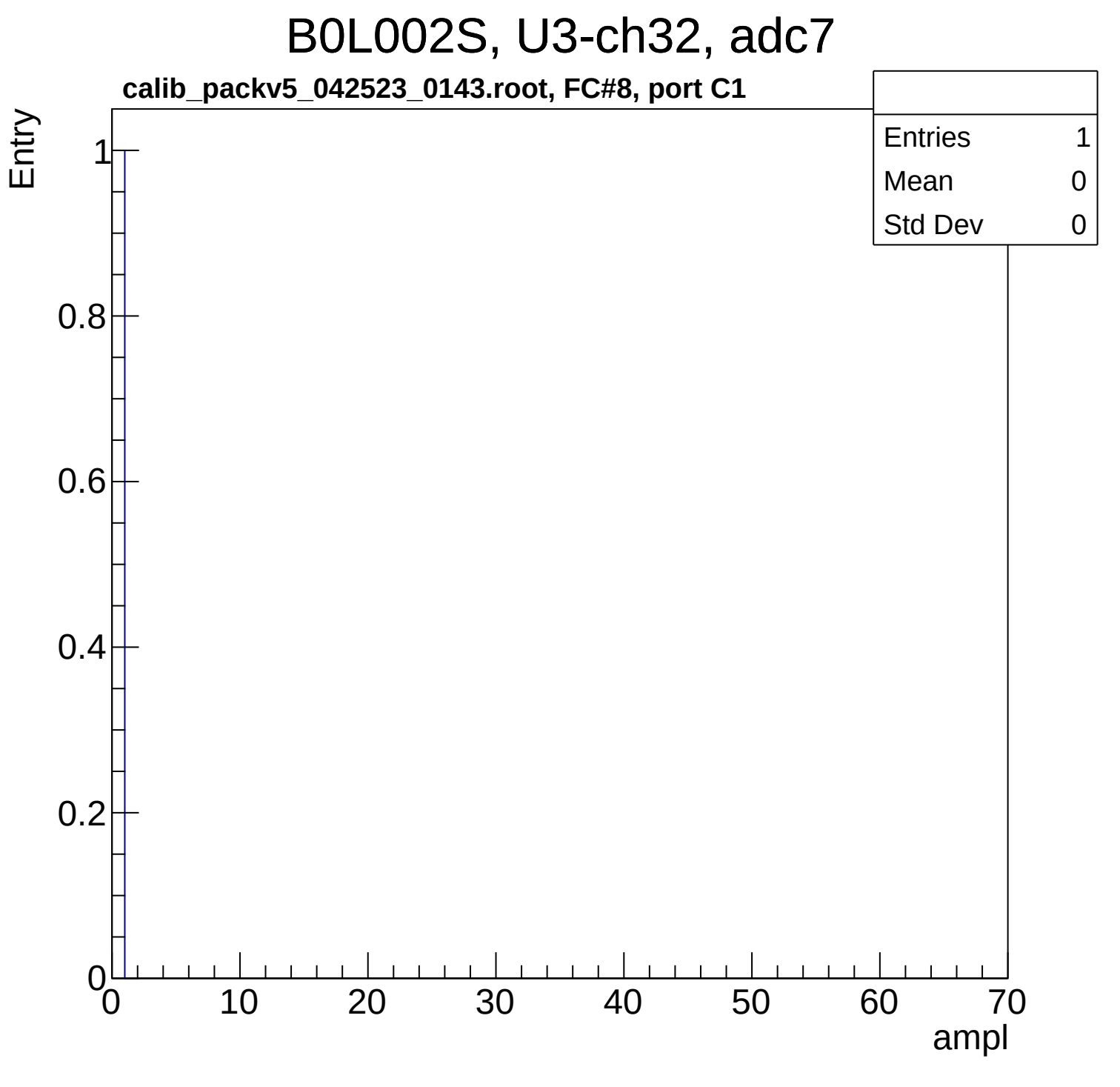
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch32, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch32, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
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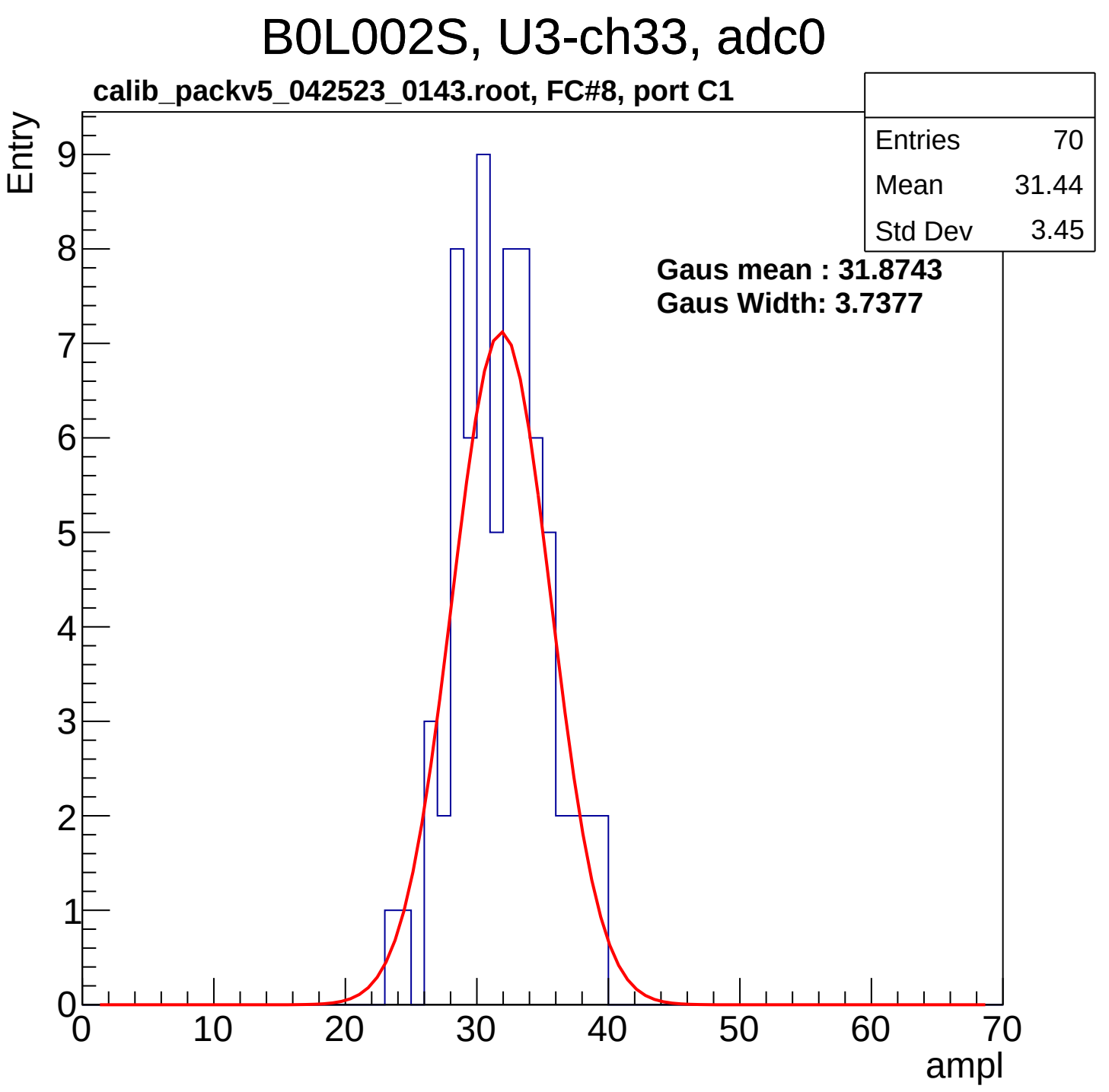
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch33, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 31.44
Std Dev 3.45
Gaus mean : 31.8743
Gaus Width: 3.7377
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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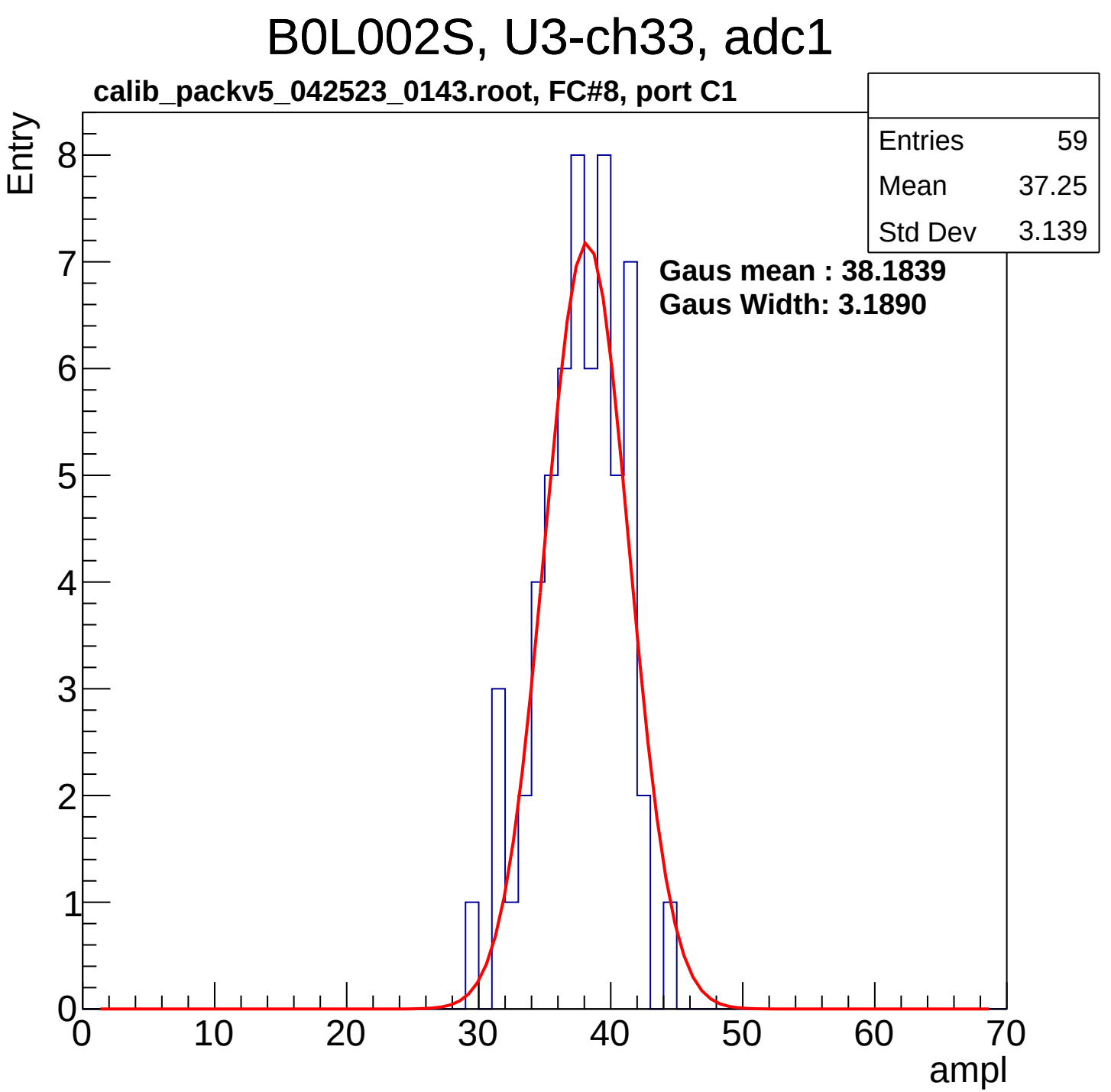
40
50
60
70


B0L002S, U3-ch33, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 37.25
Std Dev 3.139
Gaus mean : 38.1839
Gaus Width: 3.1890
Entry
ampl
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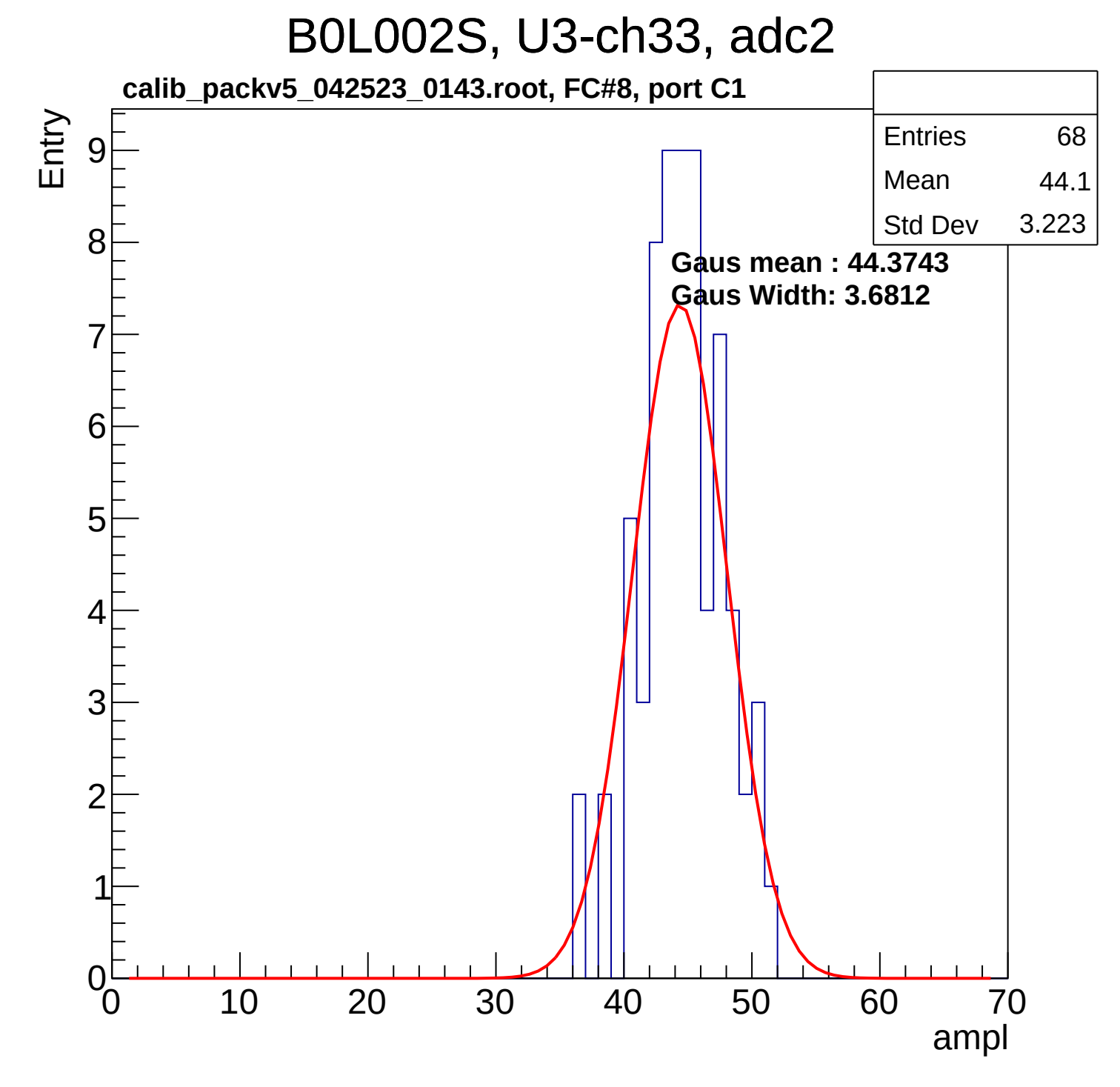
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch33, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.1
Std Dev 3.223
Gaus mean : 44.3743
Gaus Width: 3.6812
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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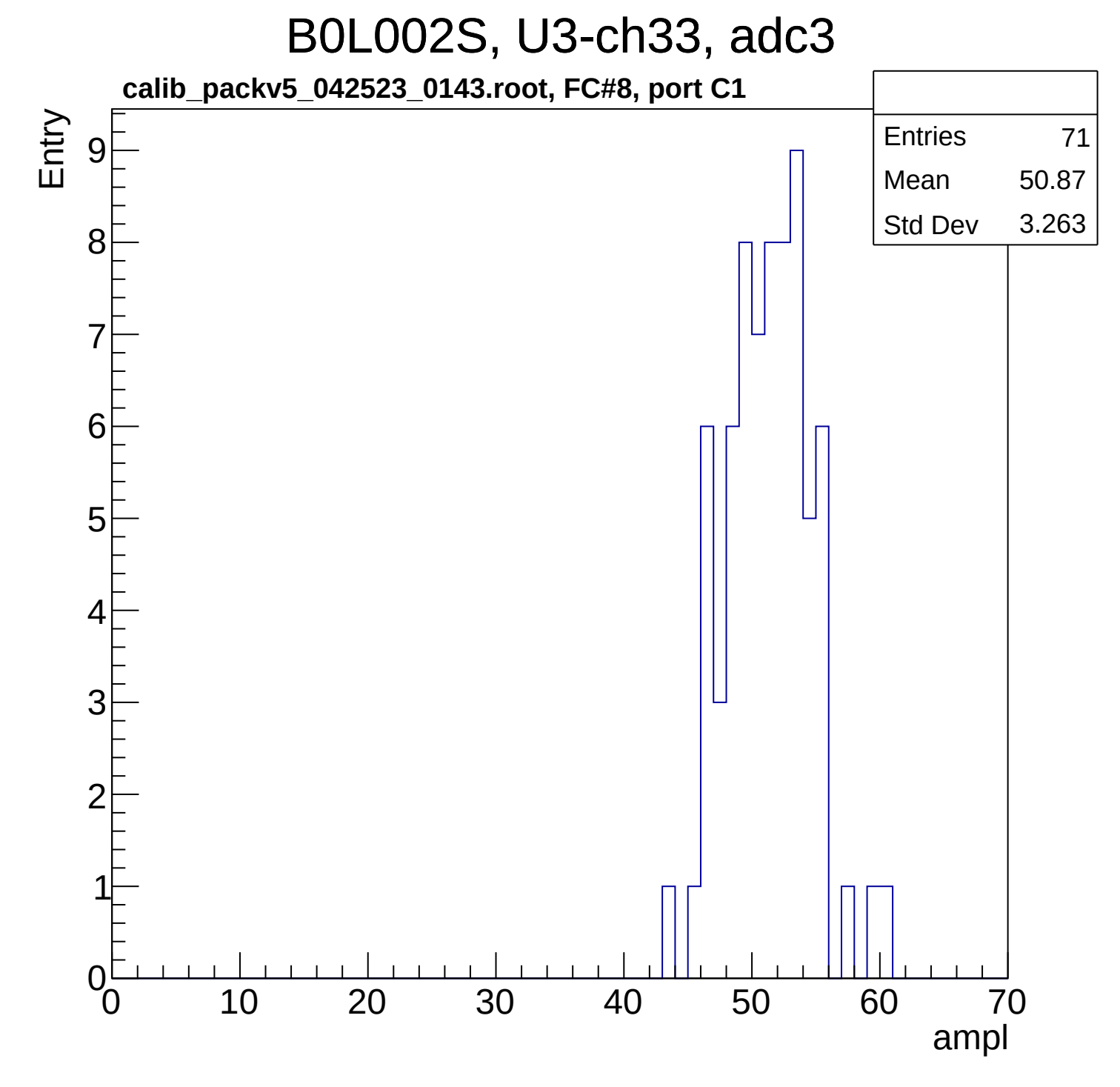
B0L002S, U3-ch33, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 50.87
Std Dev 3.263
Entry
ampl
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3
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6
7
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0
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60
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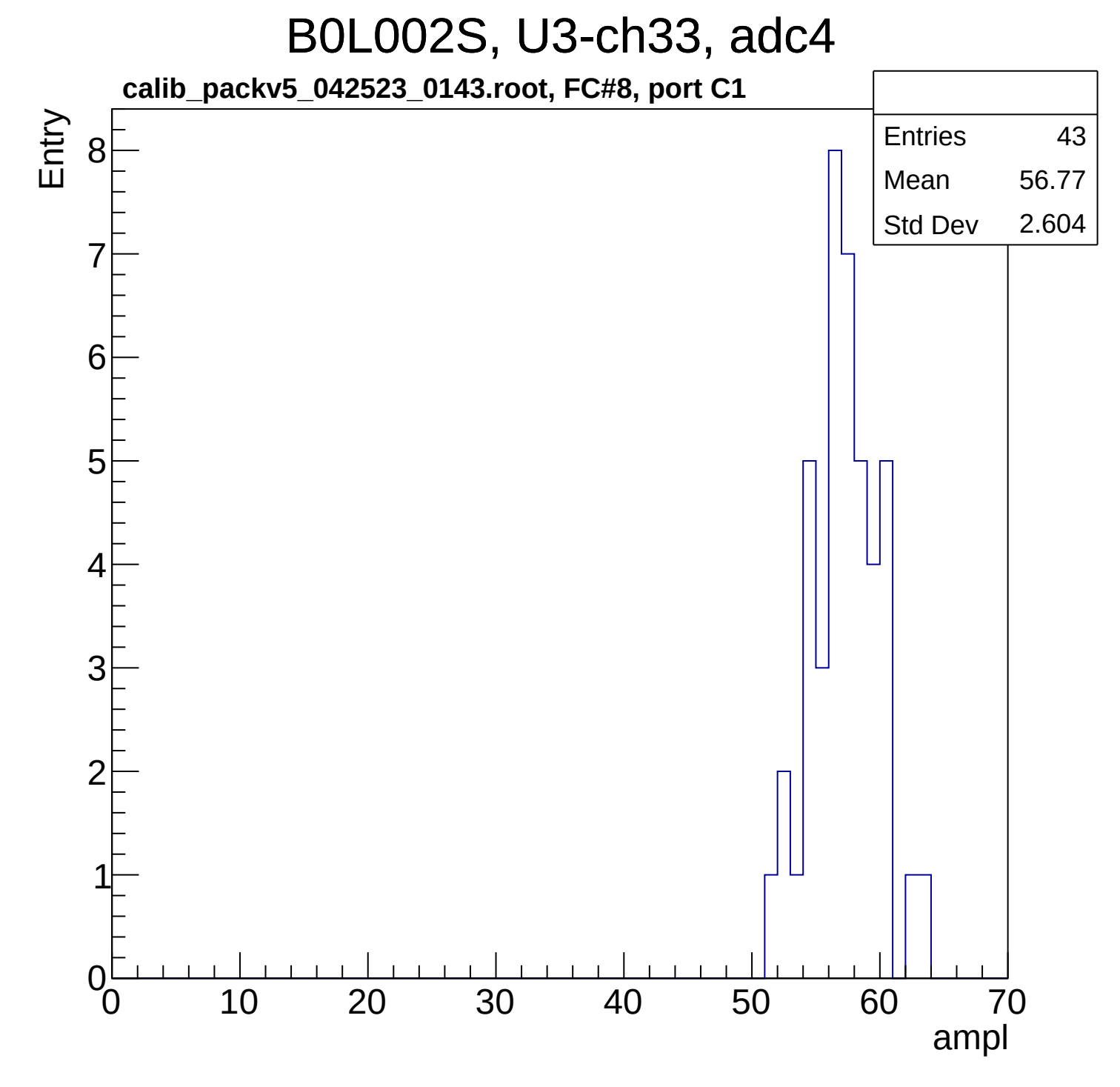
B0L002S, U3-ch33, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 56.77
Std Dev 2.604
Entry
ampl
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2
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4
5
6
7
8
0
10
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40
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60
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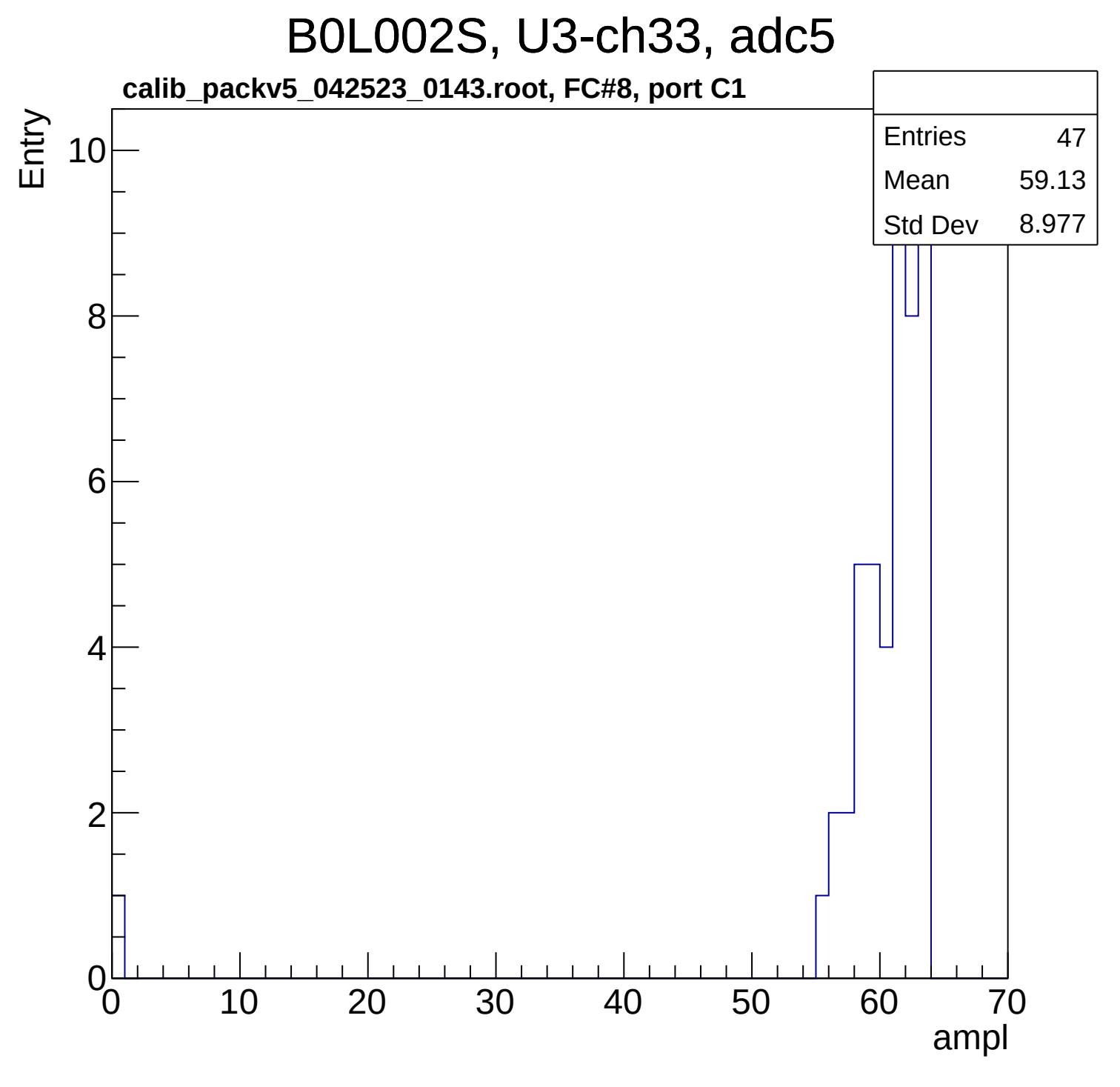
B0L002S, U3-ch33, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 59.13
Std Dev 8.977
Entry
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4
6
8
10
0
10
20
30
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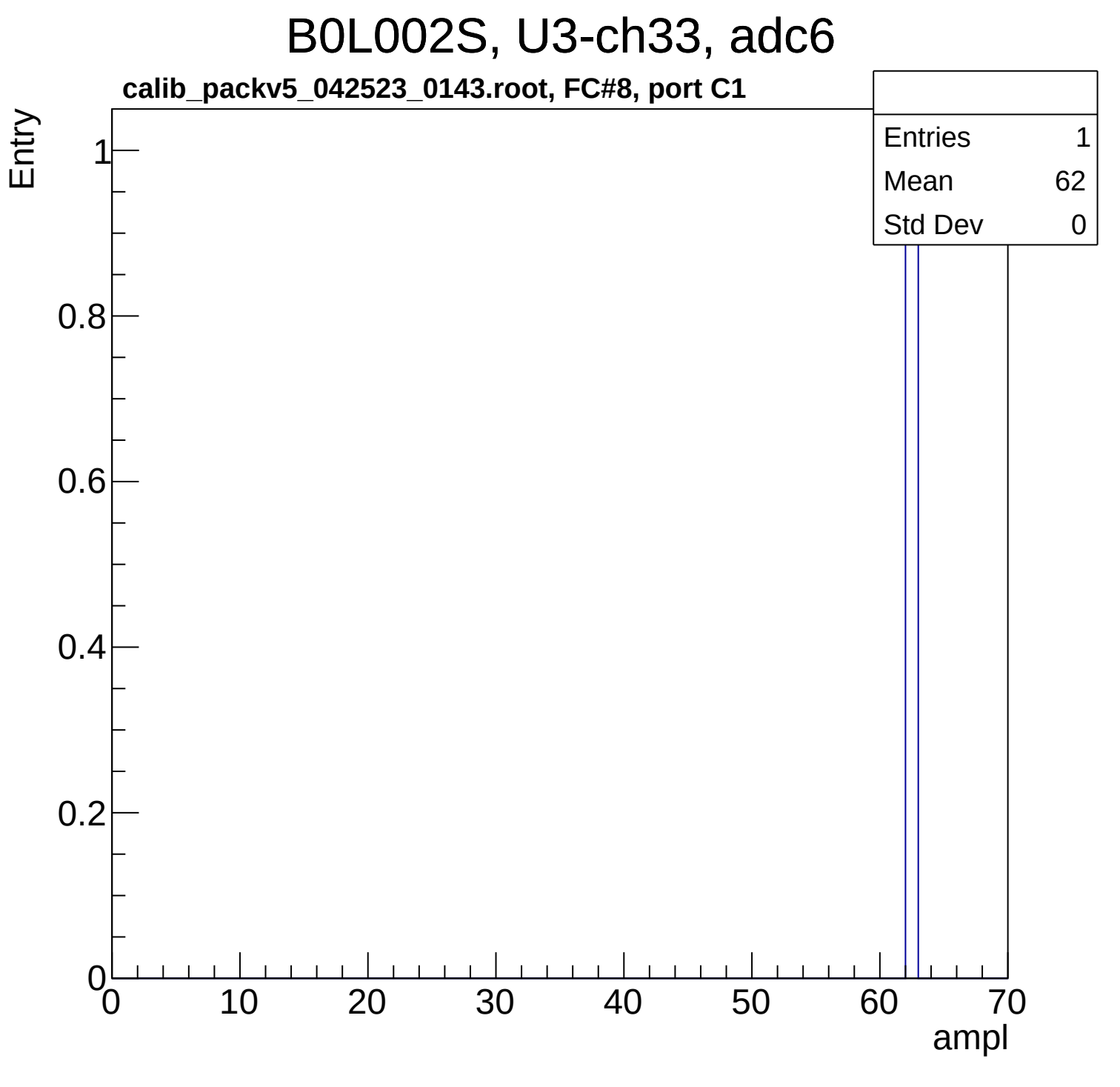
50
60
70
ampl


B0L002S, U3-ch33, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
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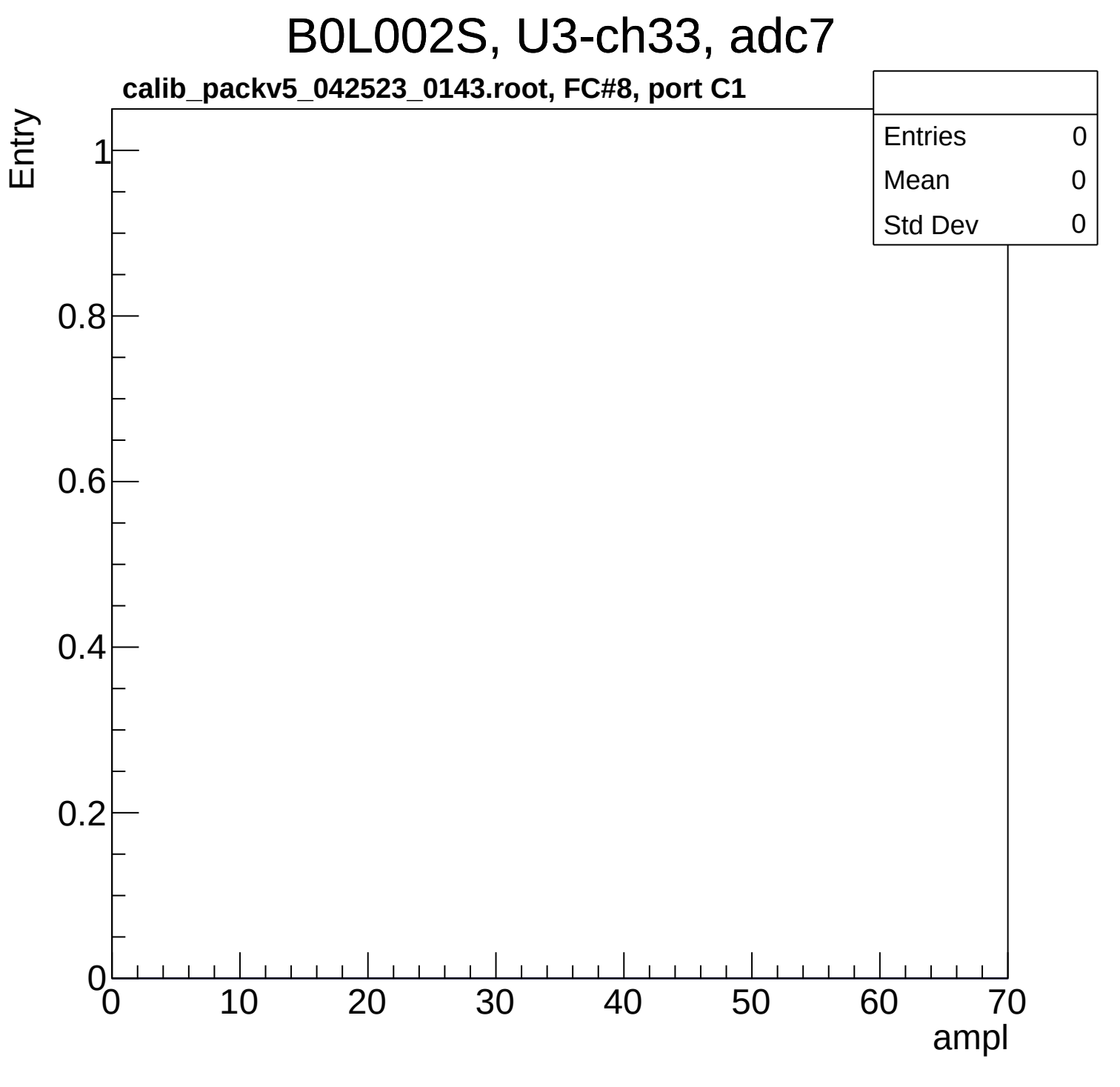
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch33, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
20
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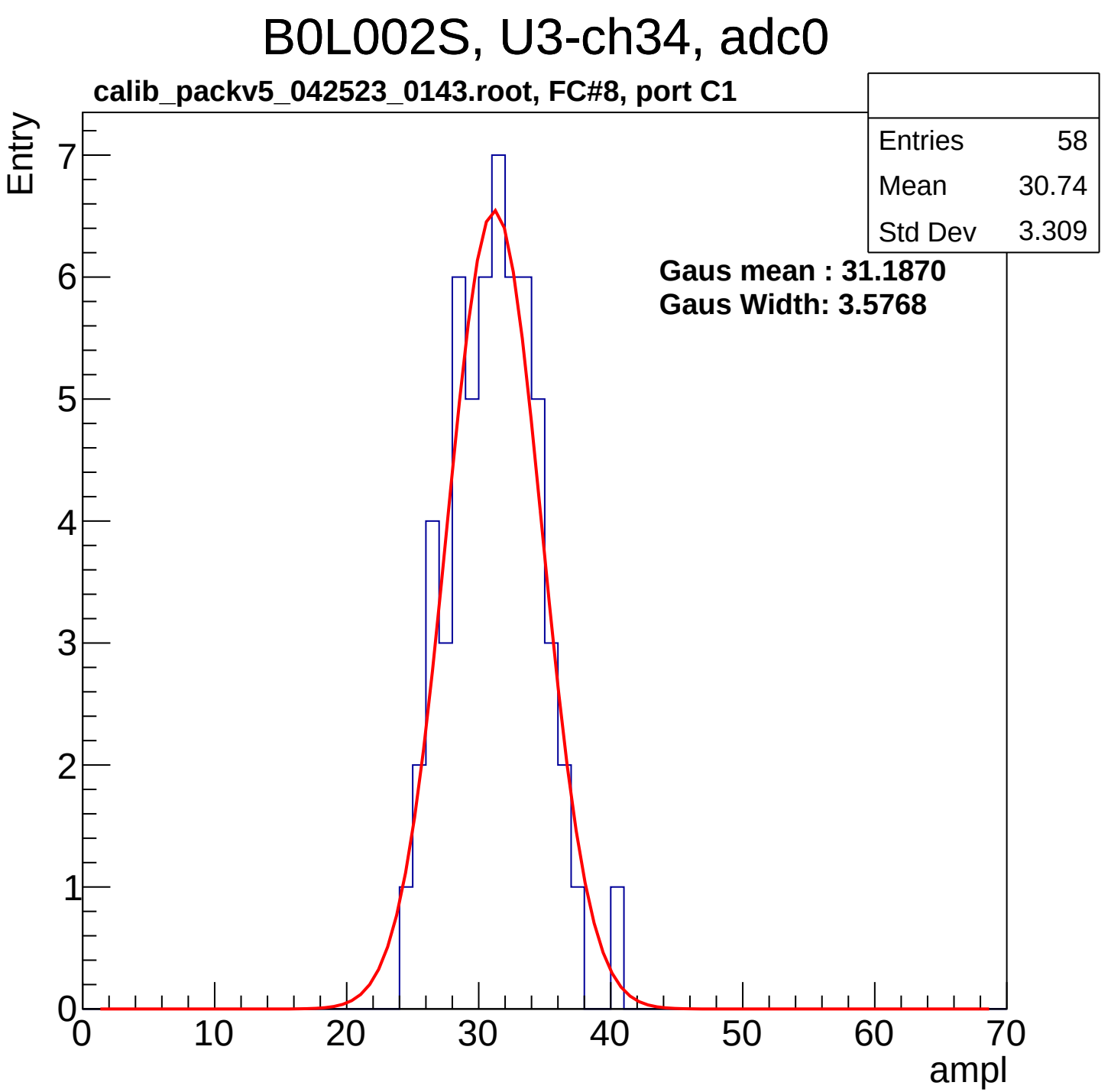
50
60
70
ampl


B0L002S, U3-ch34, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 30.74
Std Dev 3.309
Gaus mean : 31.1870
Gaus Width: 3.5768
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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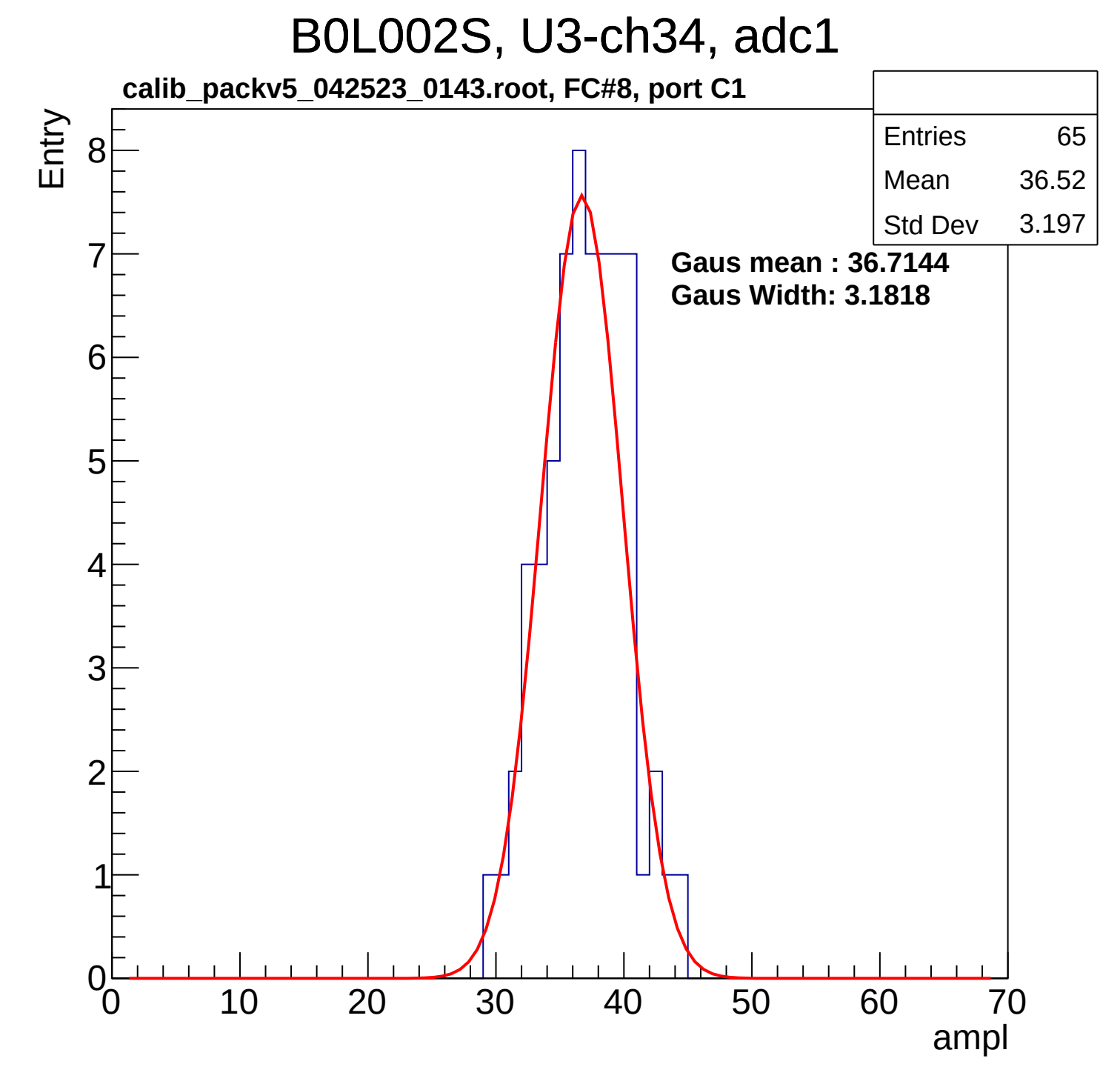
B0L002S, U3-ch34, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.52
Std Dev 3.197
Gaus mean : 36.7144
Gaus Width: 3.1818
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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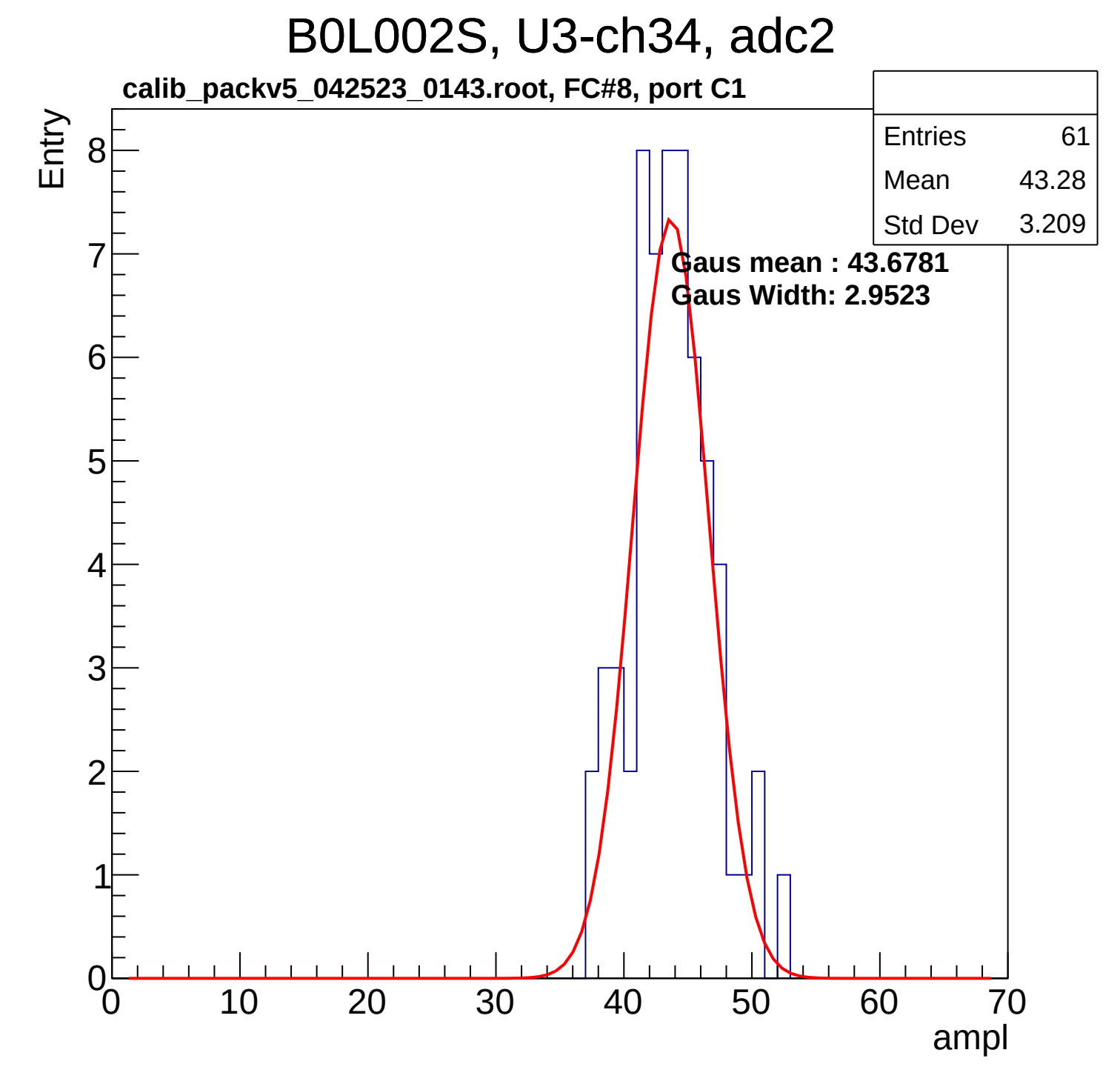
60
70


B0L002S, U3-ch34, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.28
Std Dev 3.209
Gaus mean : 43.6781
Gaus Width: 2.9523
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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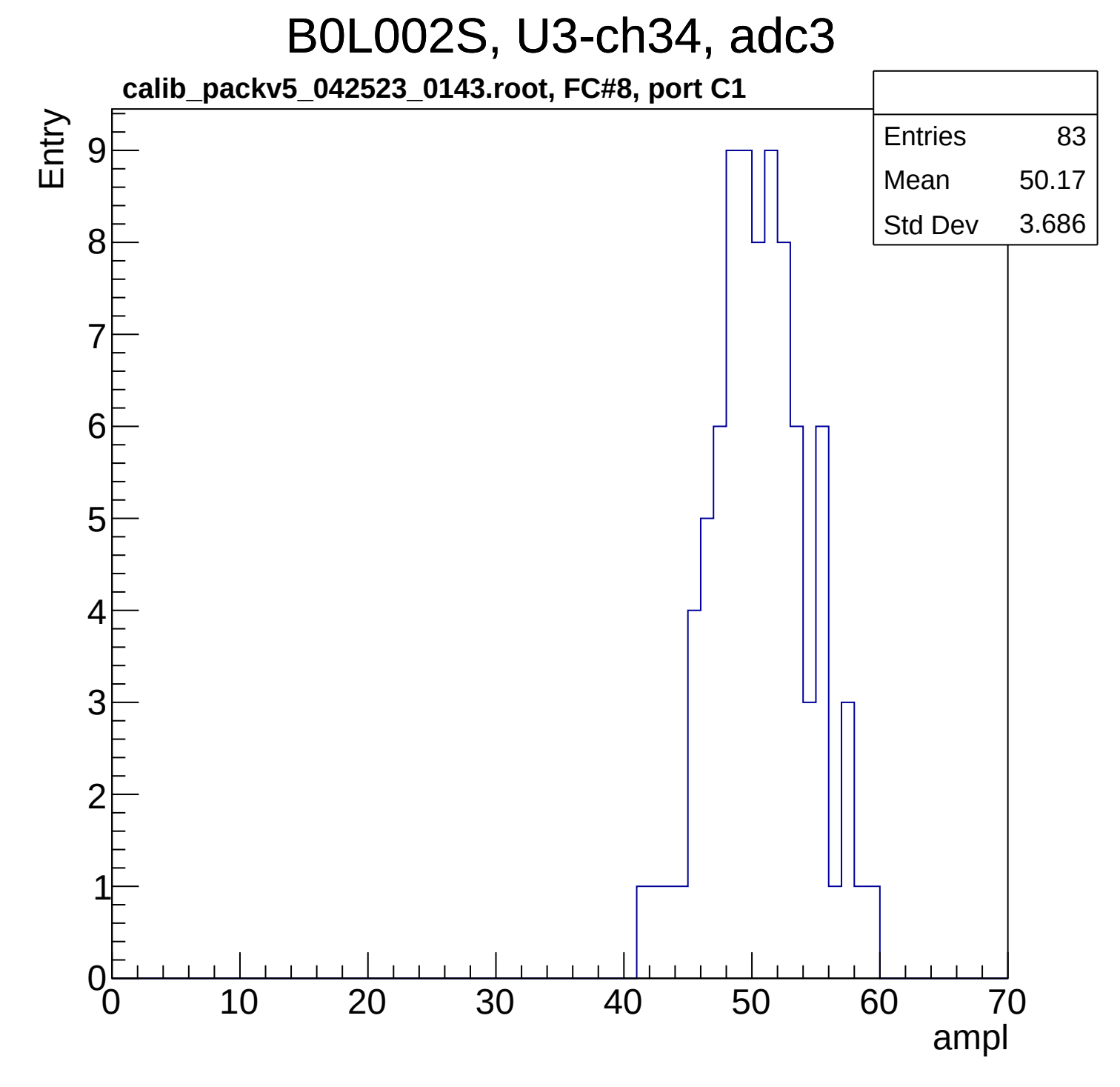
B0L002S, U3-ch34, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 50.17
Std Dev 3.686
Entry
ampl
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3
4
5
6
7
8
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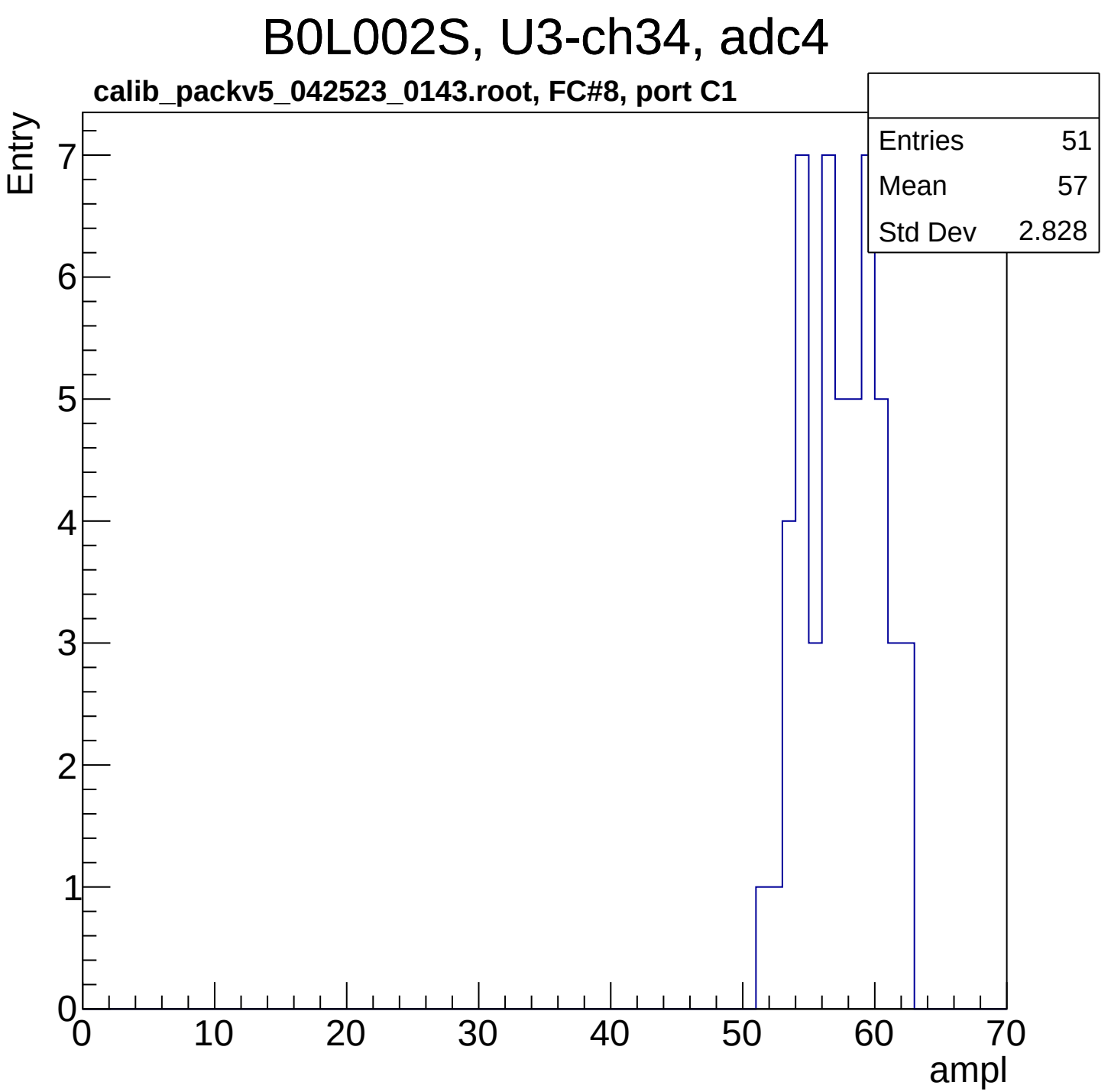
B0L002S, U3-ch34, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57
Std Dev 2.828
Entry
ampl
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2
3
4
5
6
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0
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20
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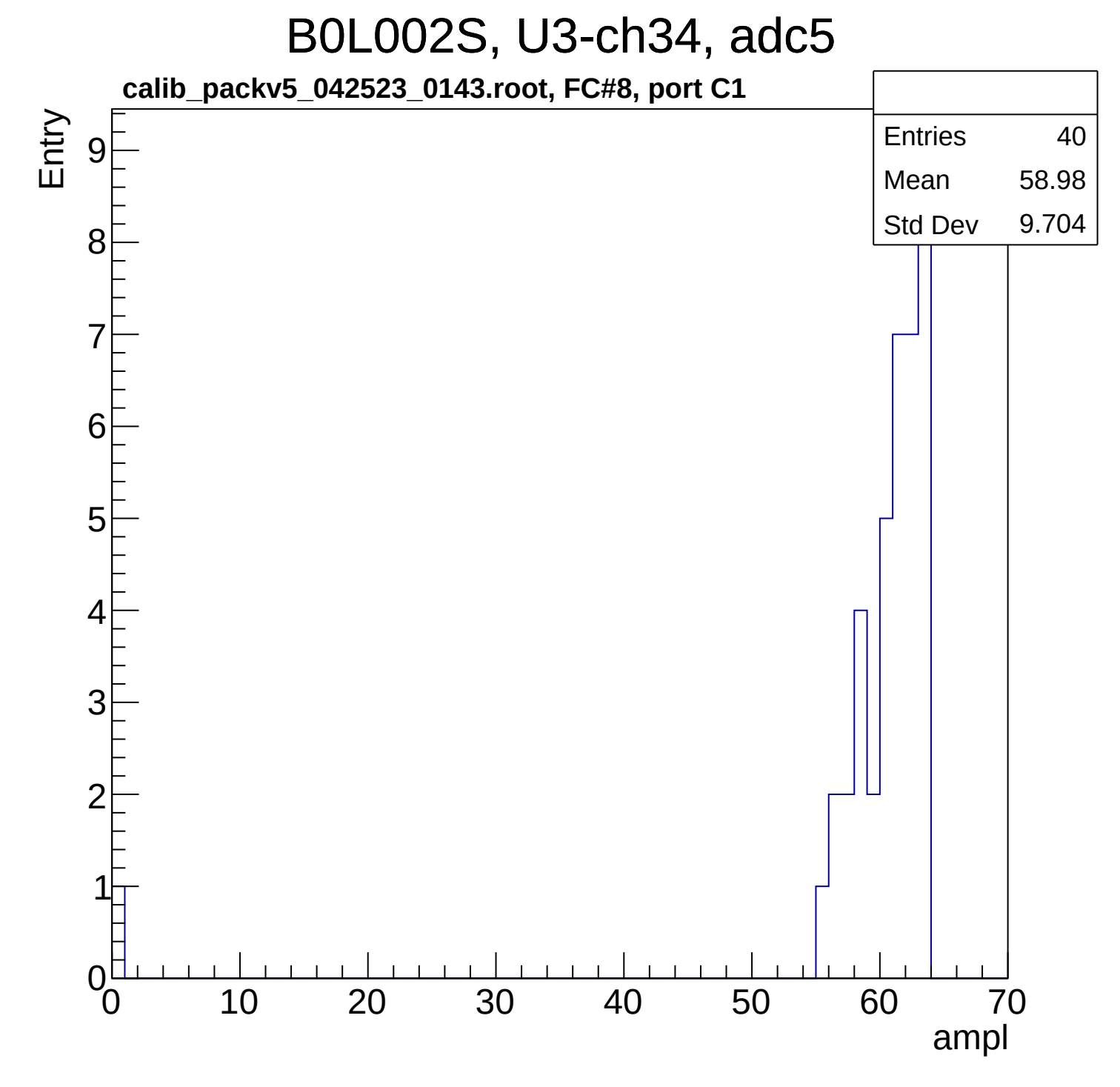
B0L002S, U3-ch34, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.98
Std Dev 9.704
Entry
ampl
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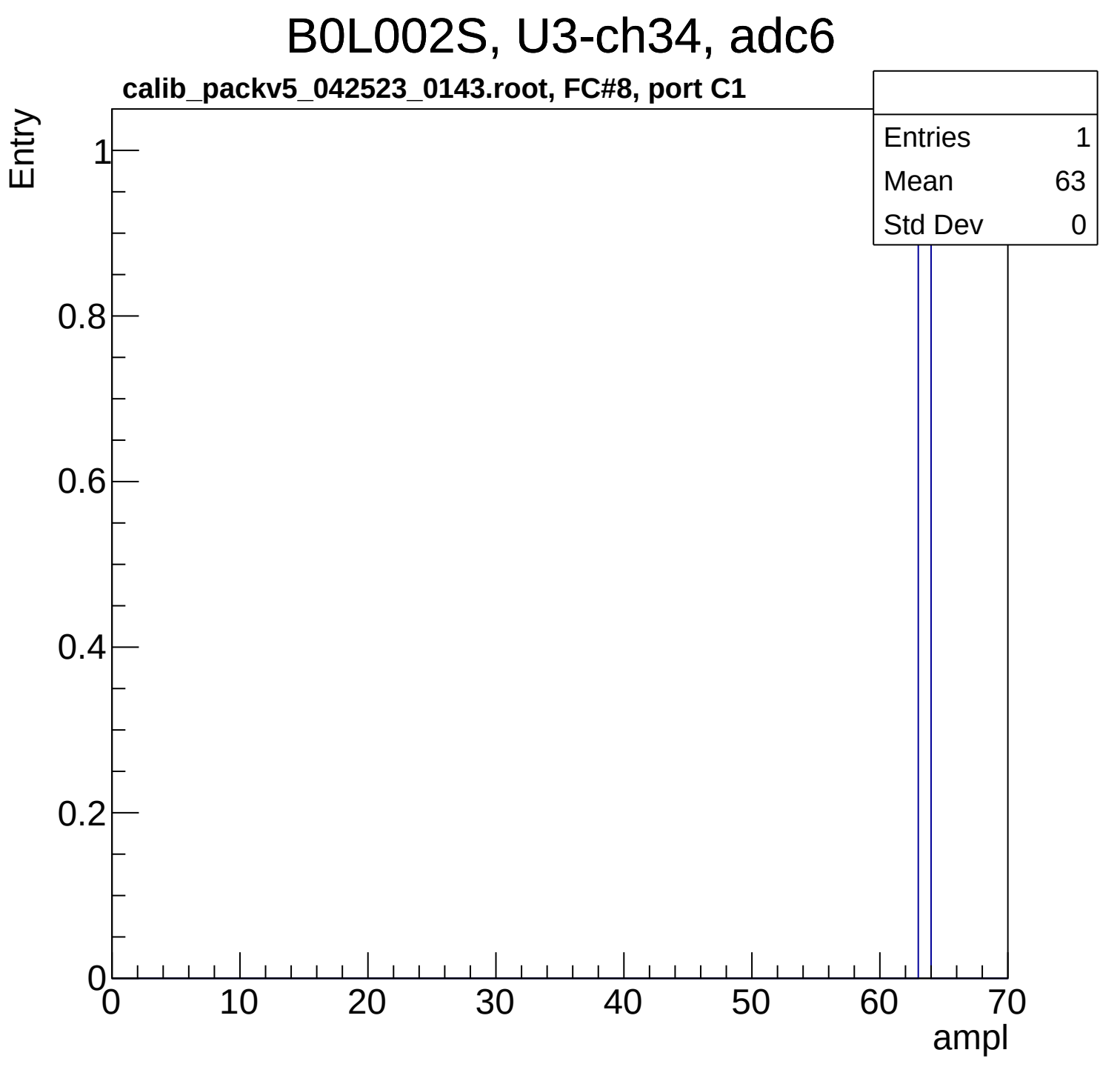
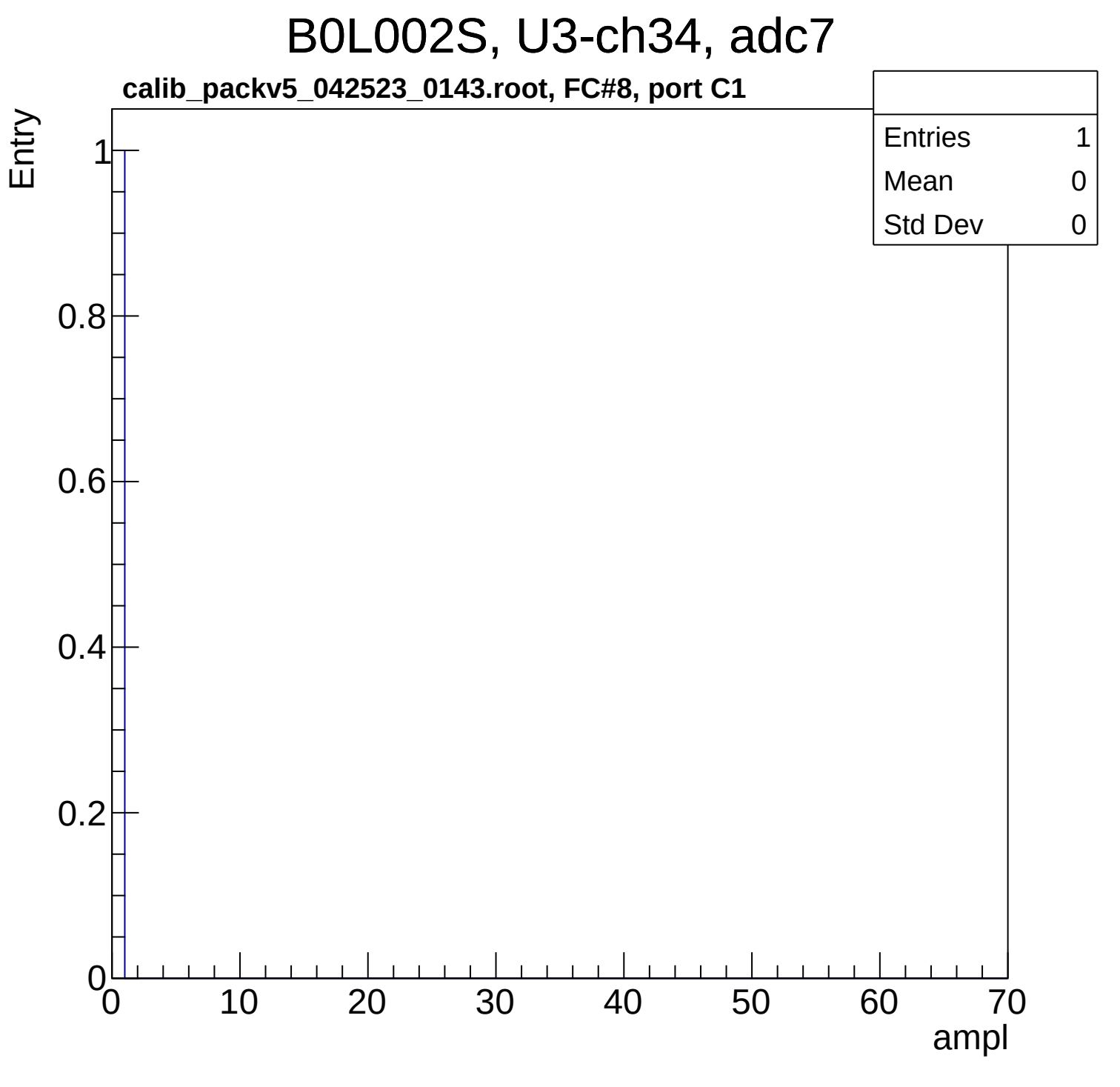

B0L002S, U3-ch34, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch34, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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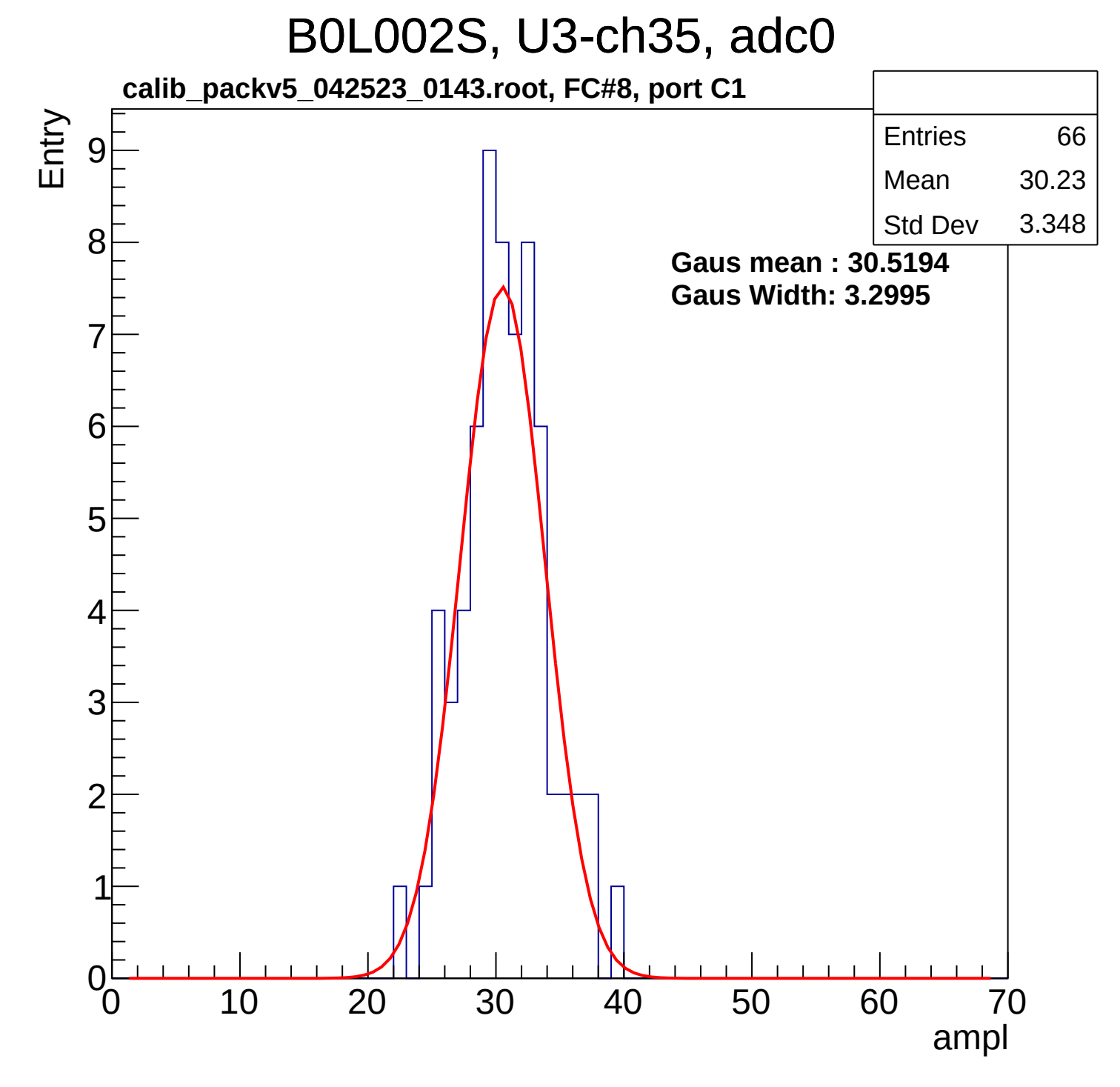
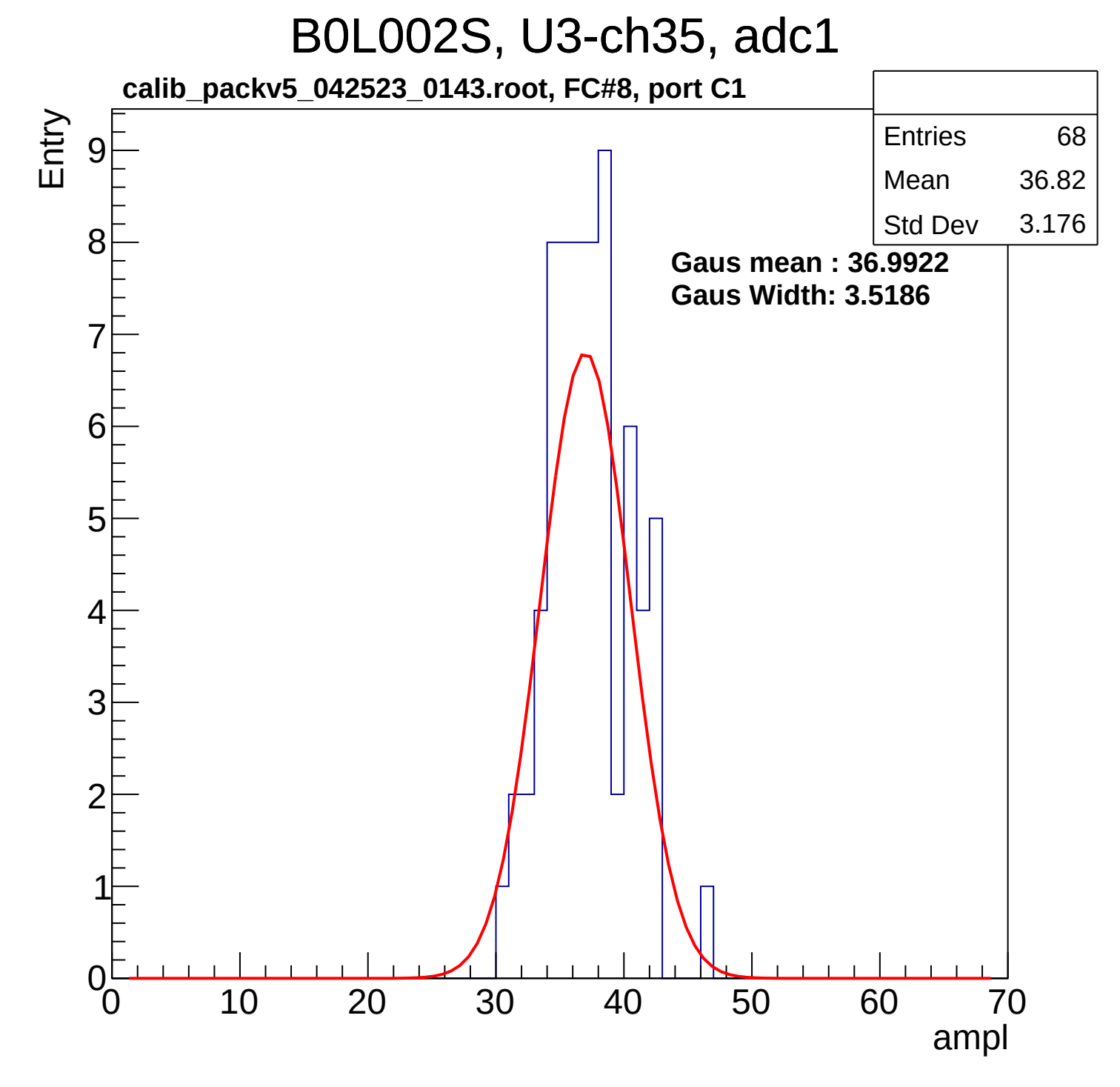
50
60
70
ampl


B0L002S, U3-ch35, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.23
Std Dev 3.348
Gaus mean : 30.5194
Gaus Width: 3.2995
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch35, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 36.82
Std Dev 3.176
Gaus mean : 36.9922
Gaus Width: 3.5186
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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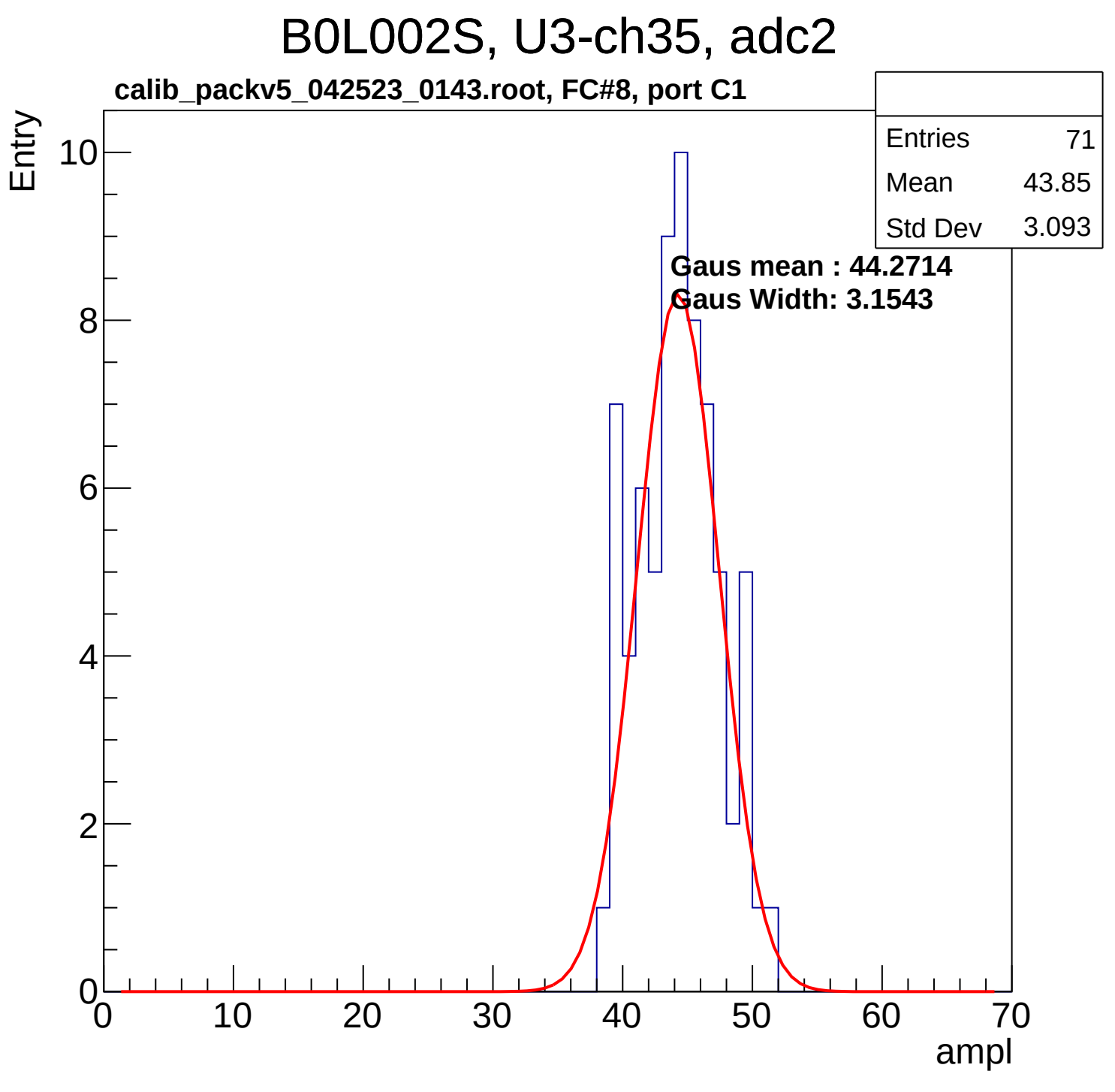
B0L002S, U3-ch35, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 43.85
Std Dev 3.093
Gaus mean : 44.2714
Gaus Width: 3.1543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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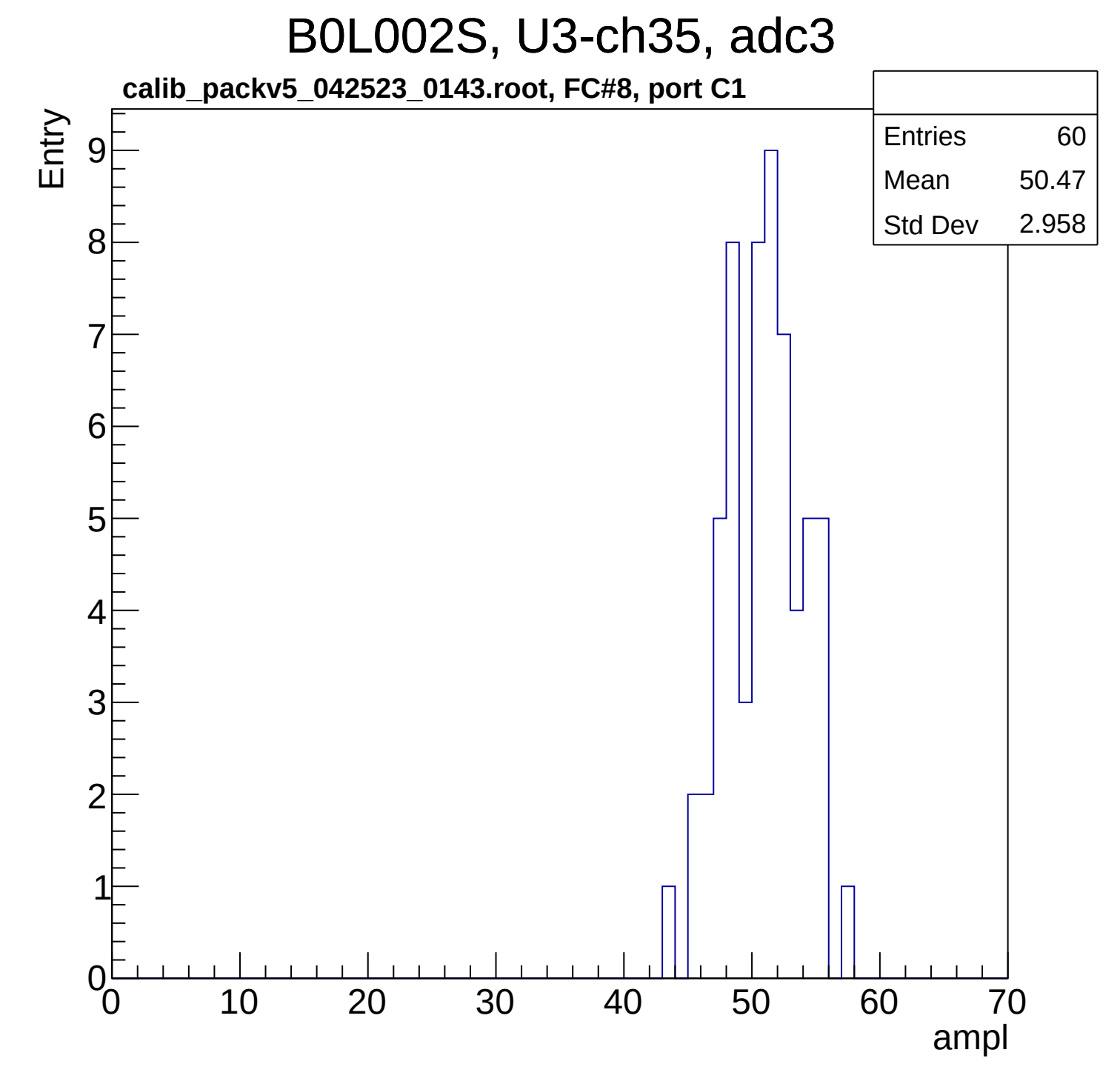
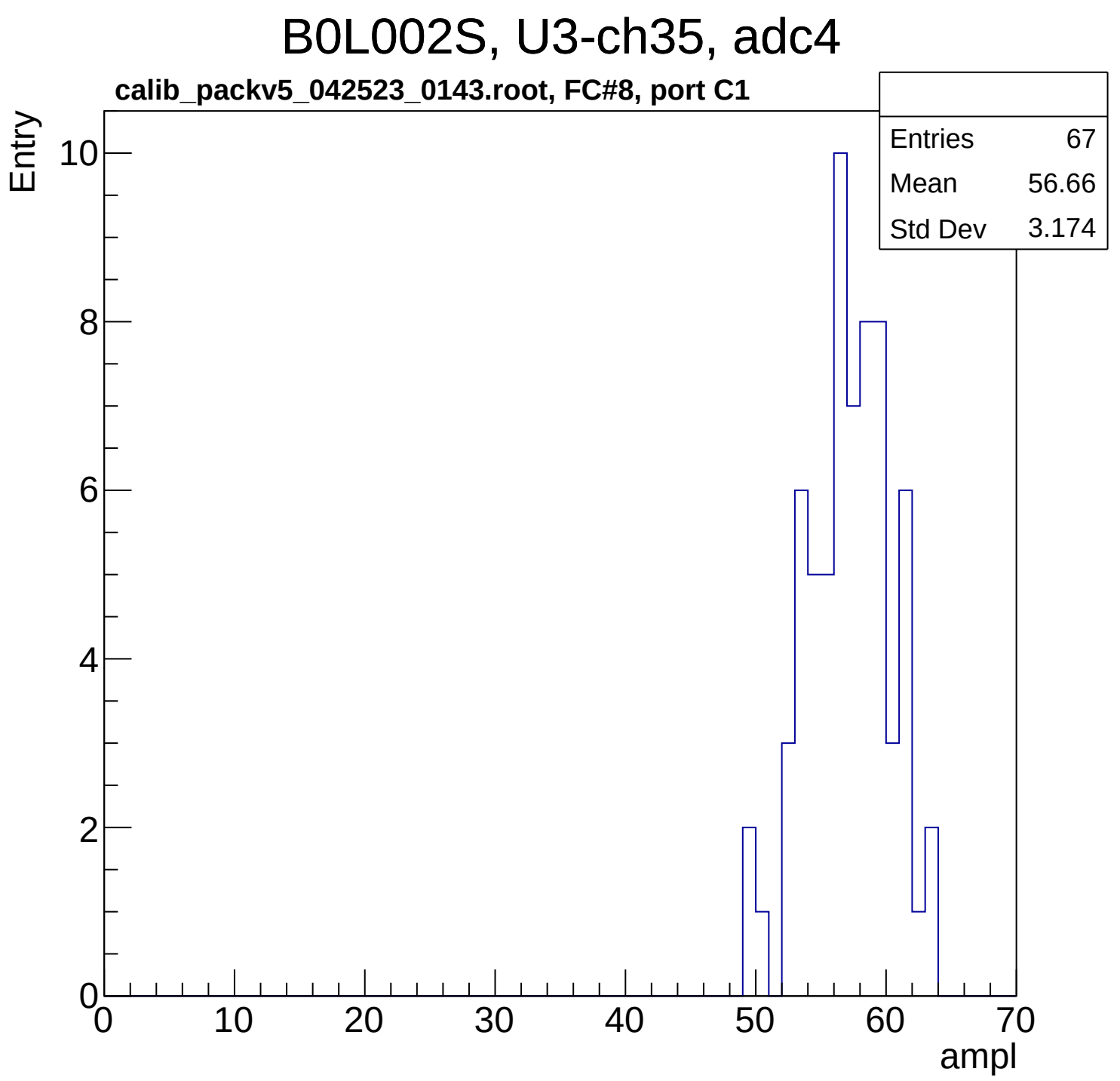
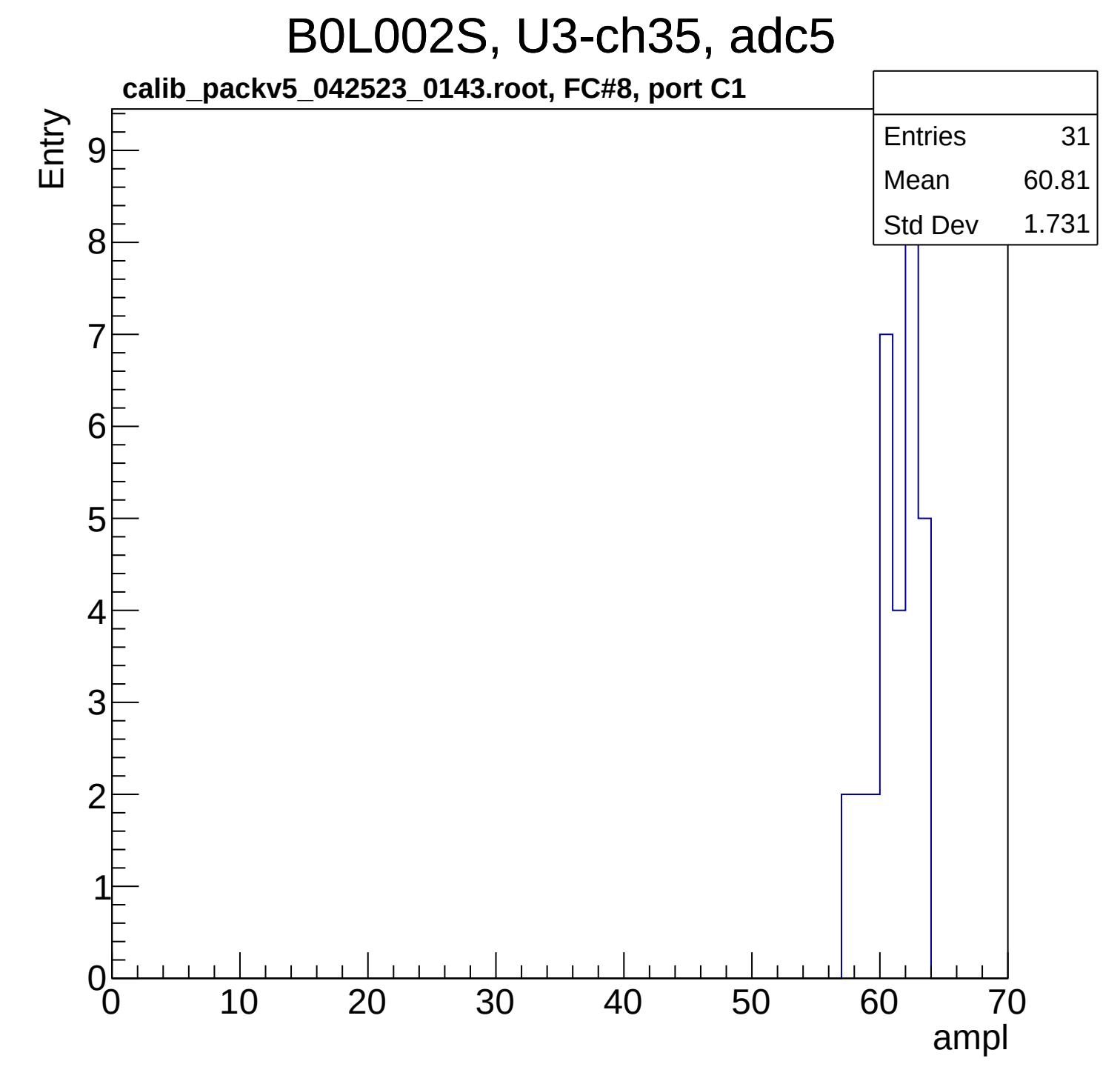

B0L002S, U3-ch35, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.47
Std Dev 2.958
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch35, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.66
Std Dev 3.174
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch35, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 60.81
Std Dev 1.731
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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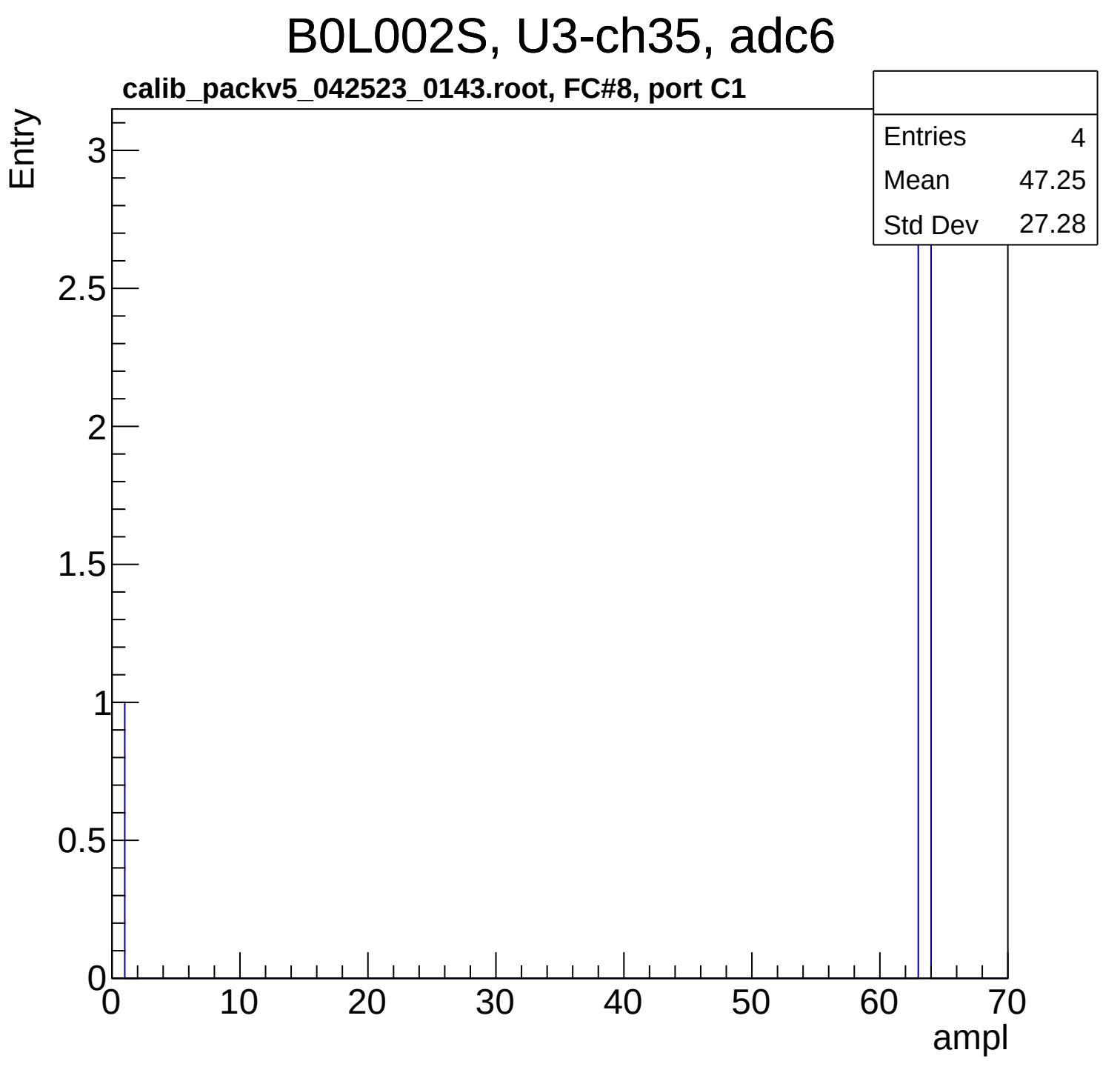
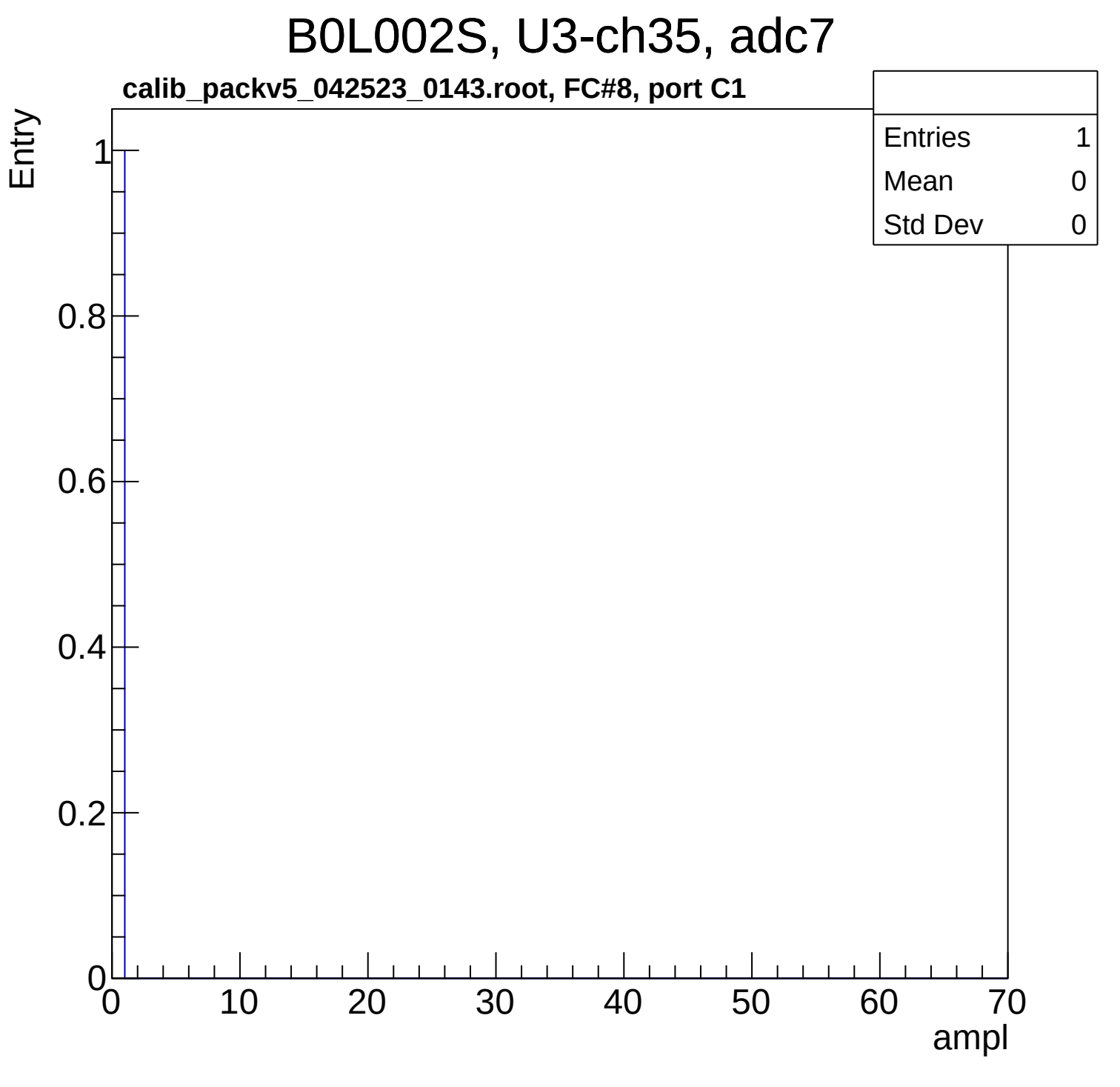
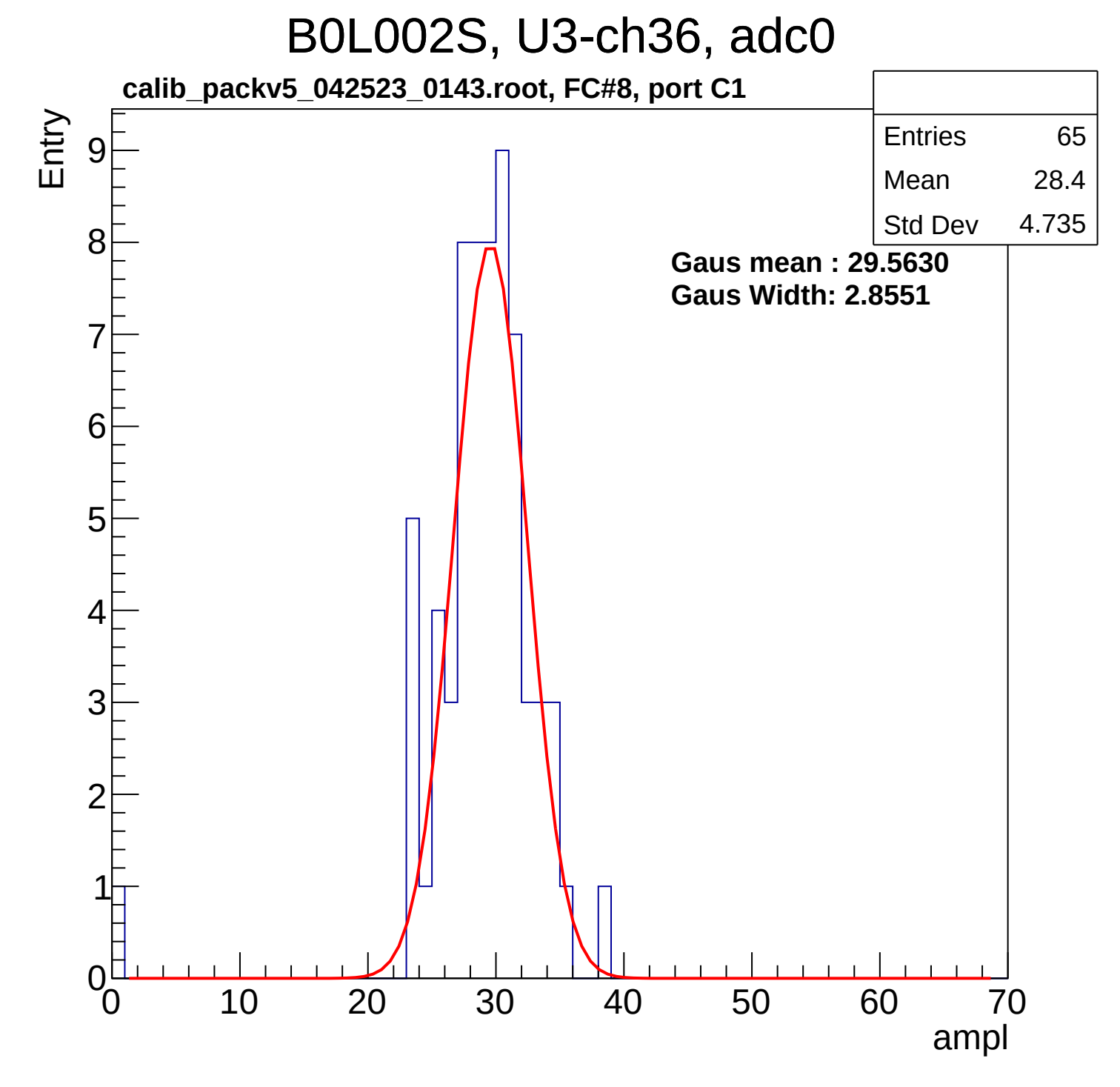
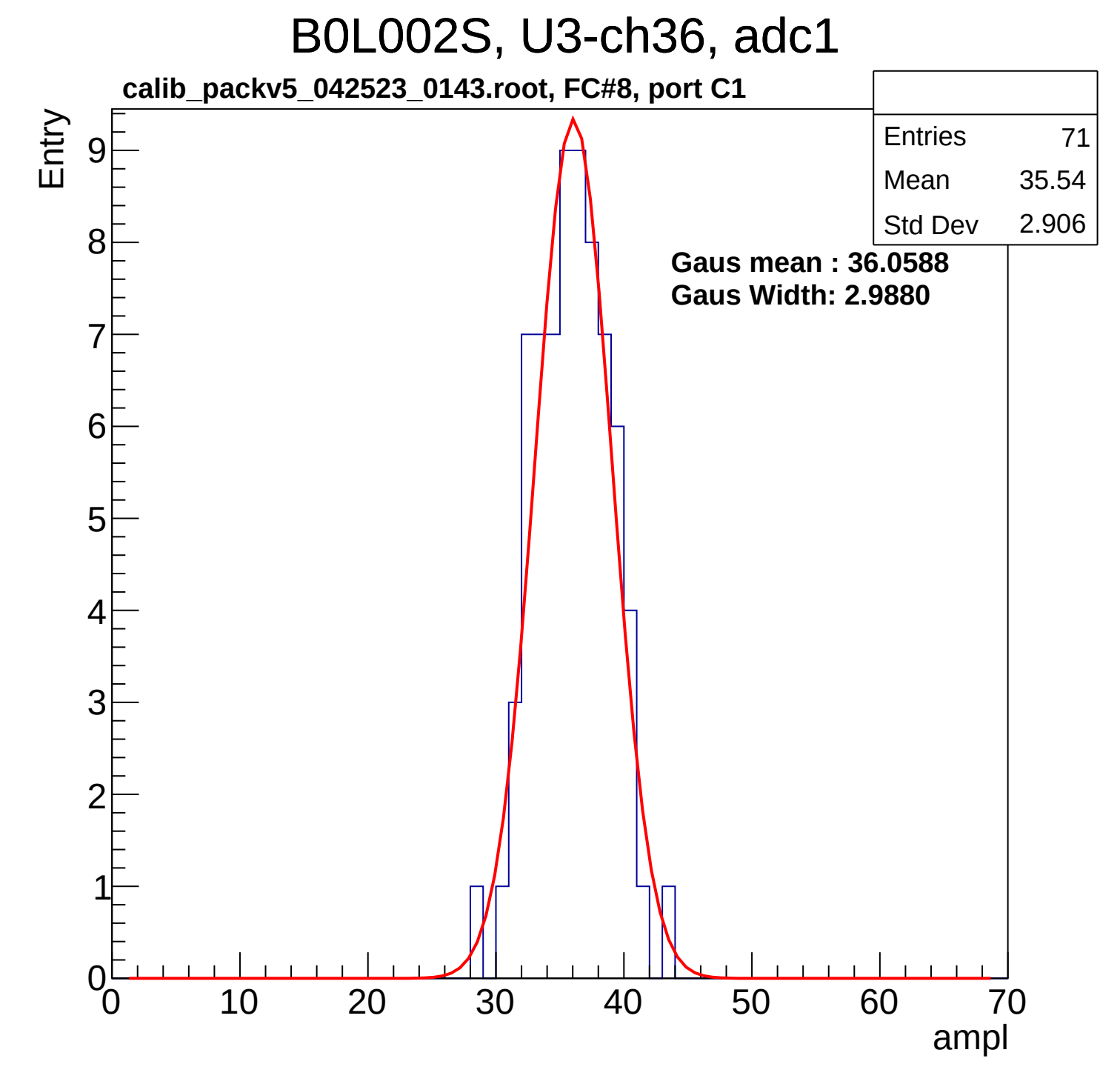
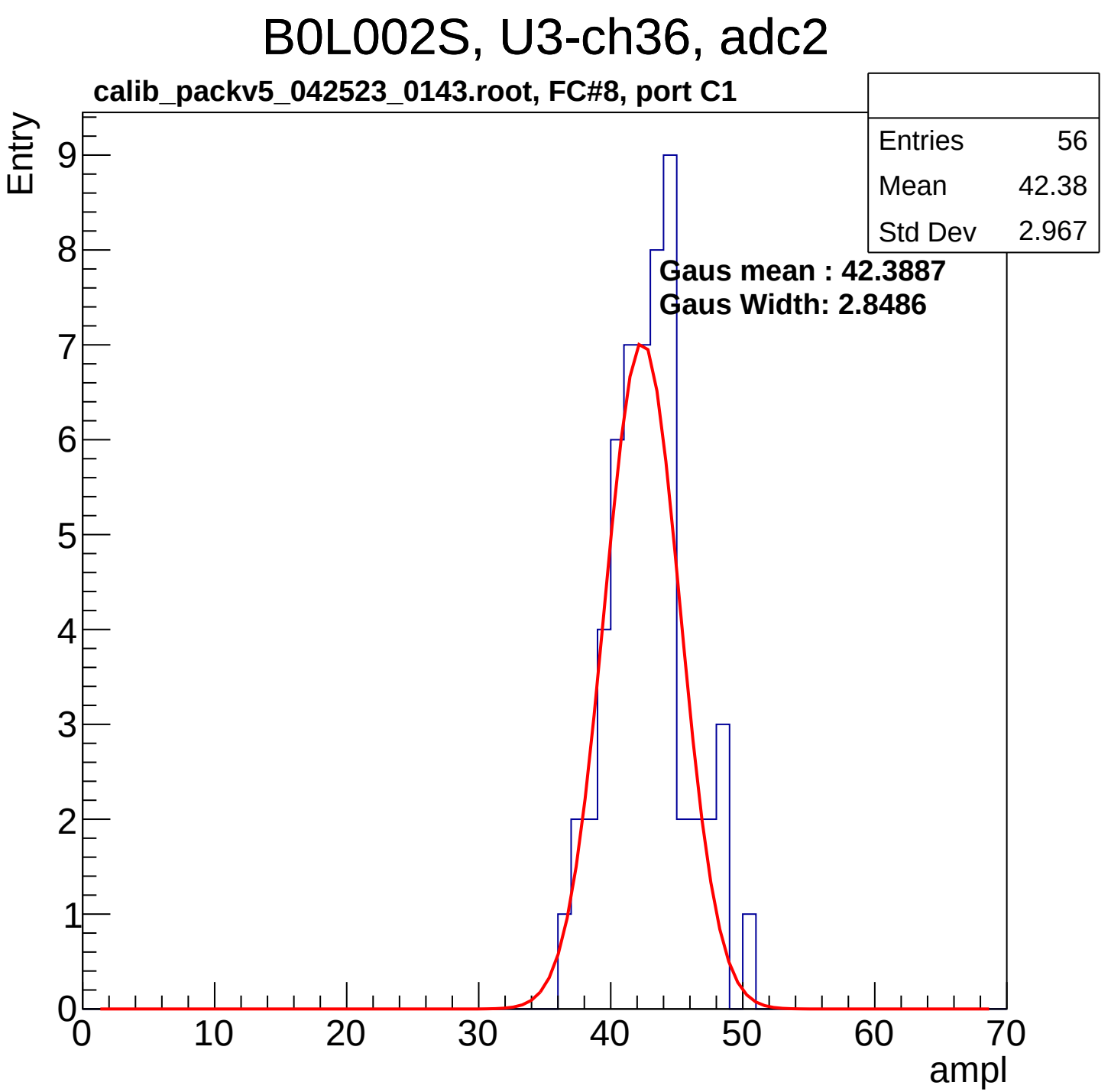
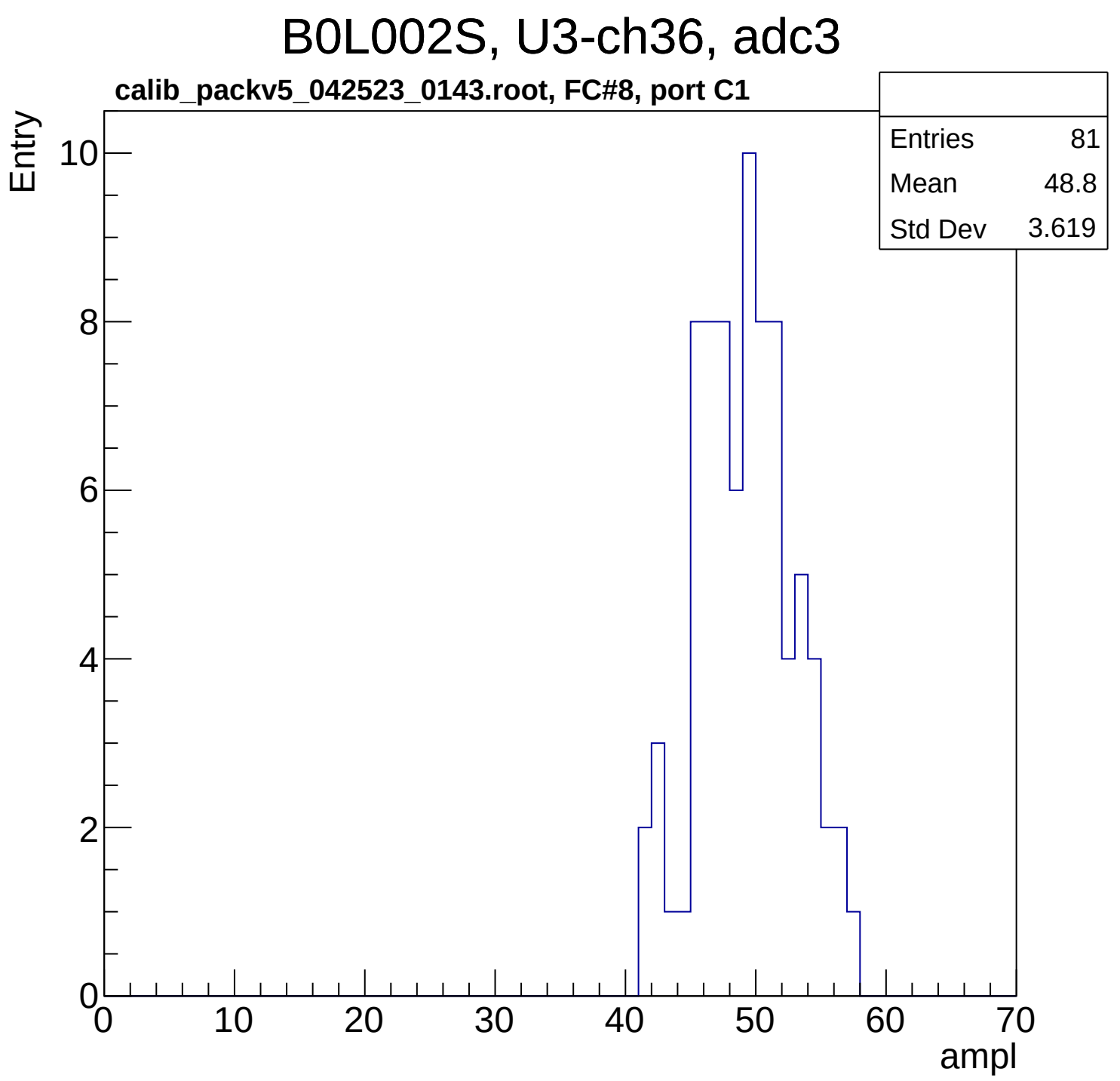

B0L002S, U3-ch35, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 47.25
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

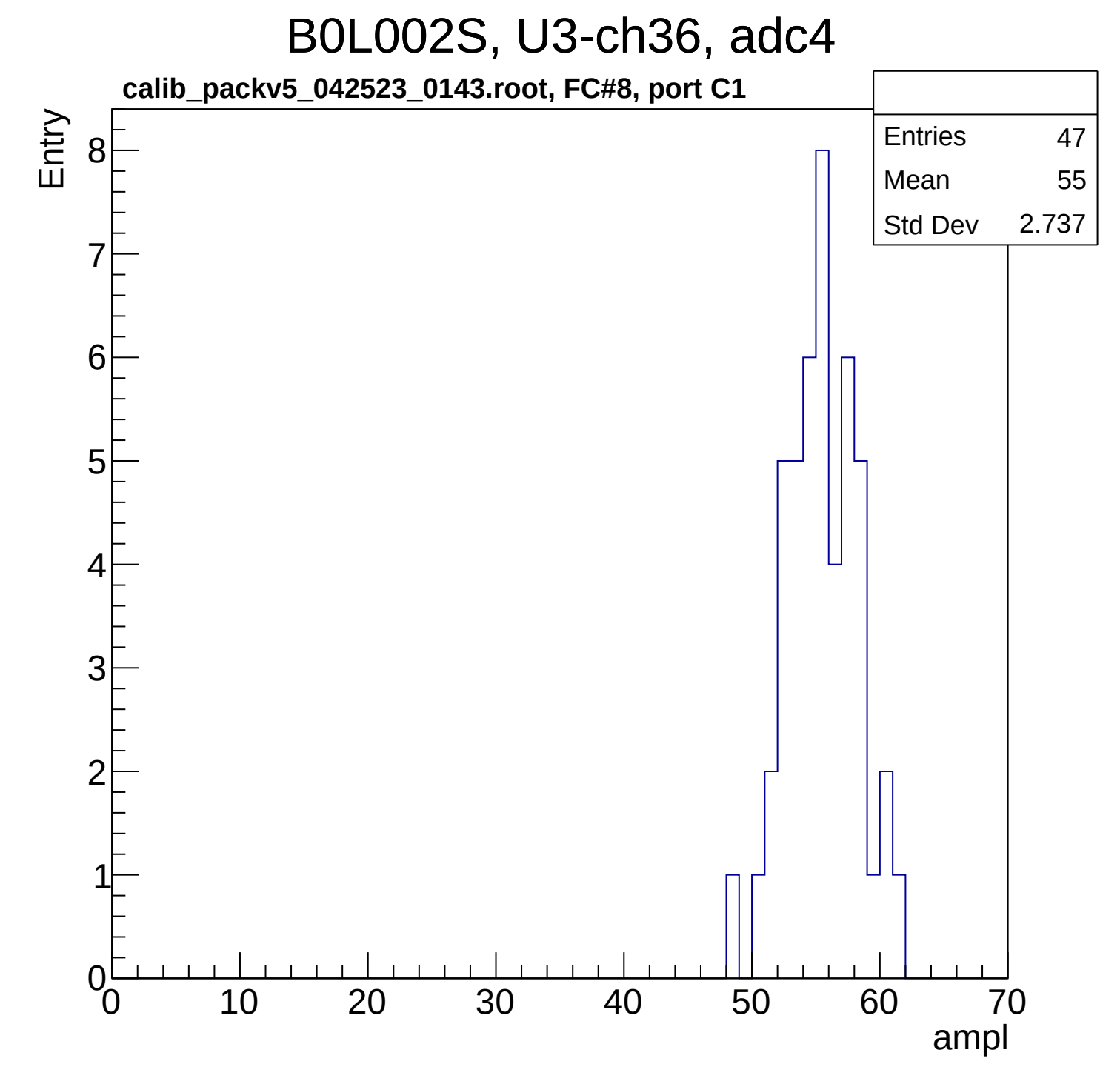

B0L002S, U3-ch35, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

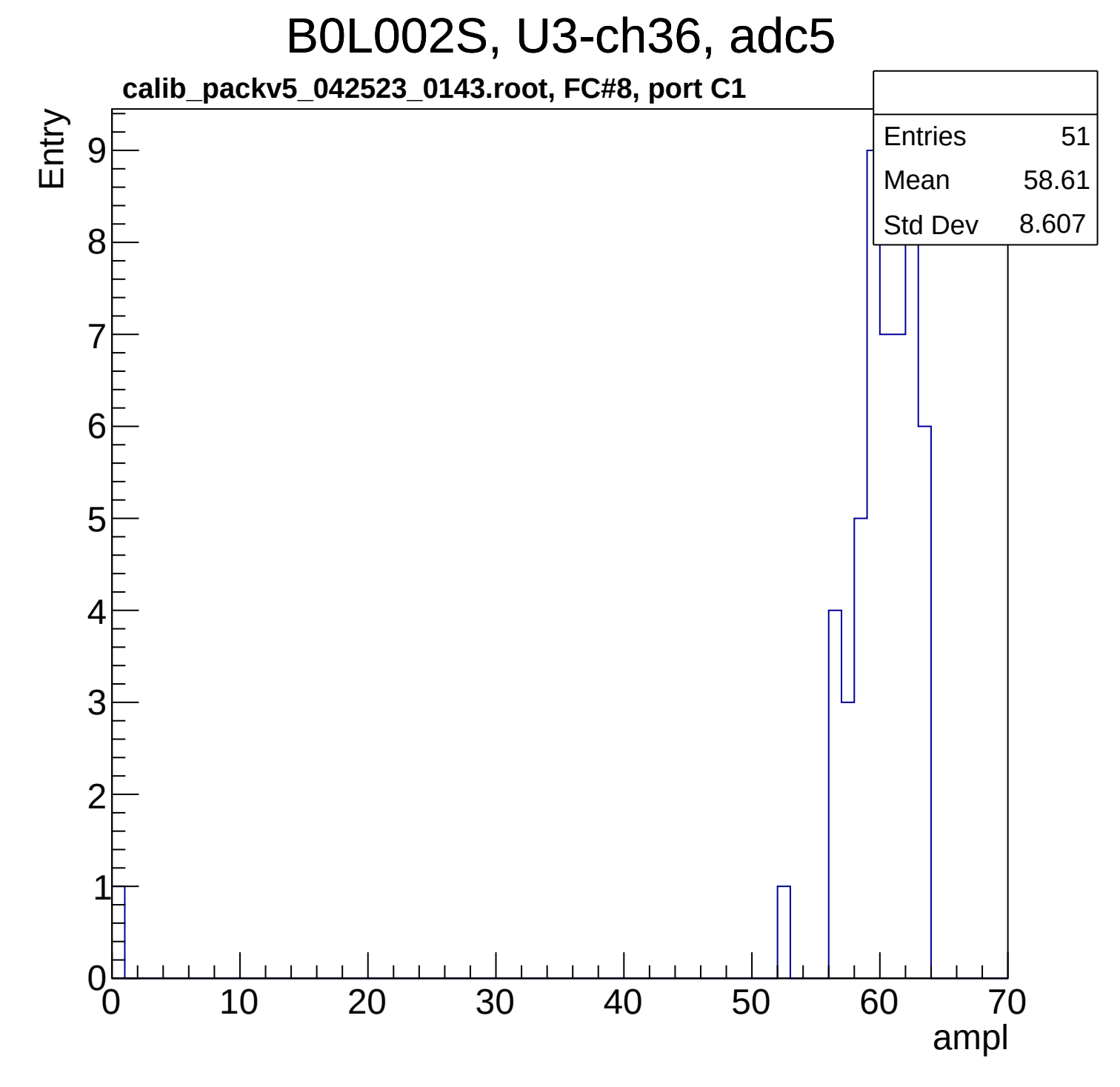

B0L002S, U3-ch36, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 28.4
Std Dev 4.735
Gaus mean : 29.5630
Gaus Width: 2.8551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

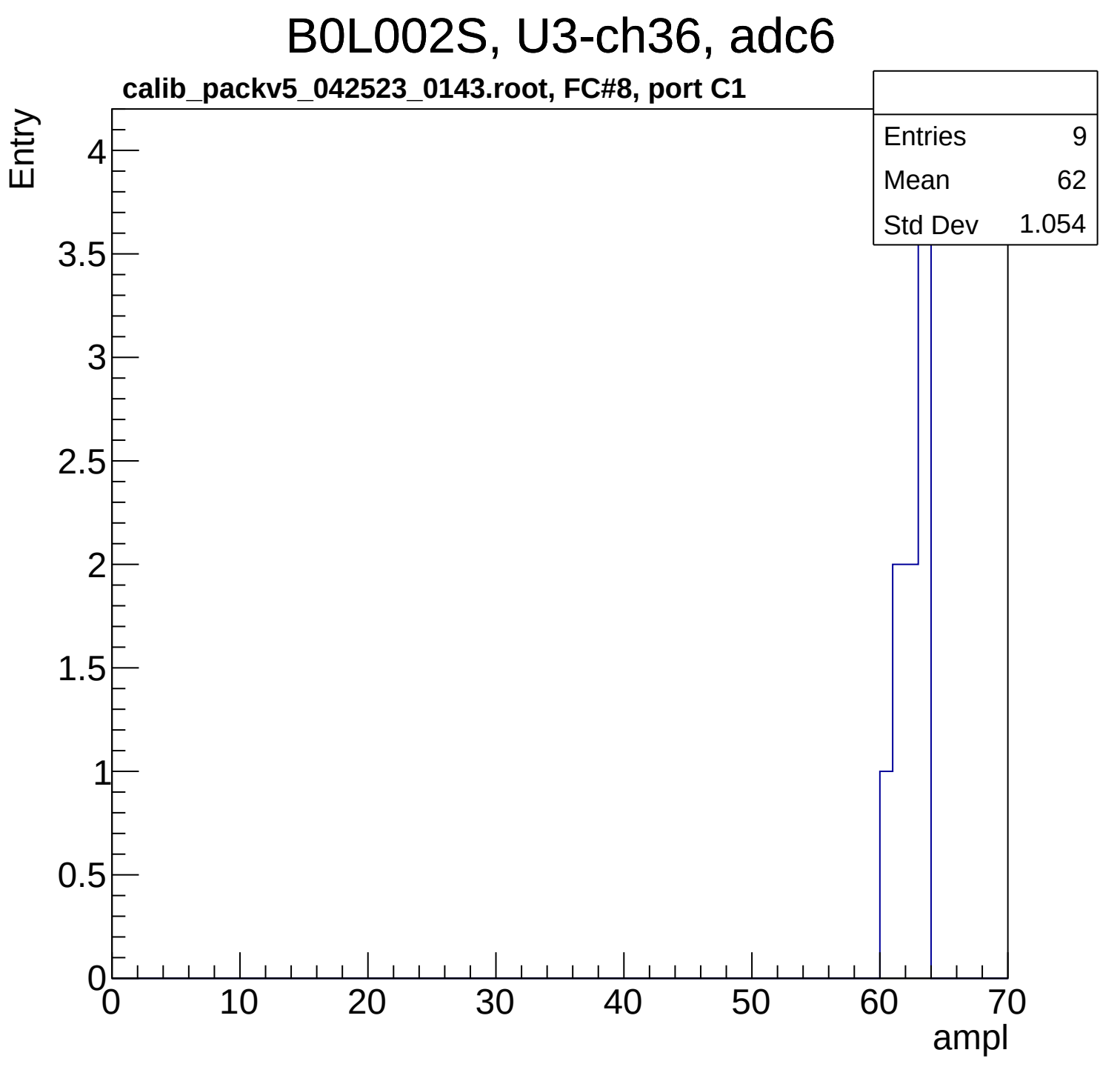

B0L002S, U3-ch36, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 35.54
Std Dev 2.906
Gaus mean : 36.0588
Gaus Width: 2.9880
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

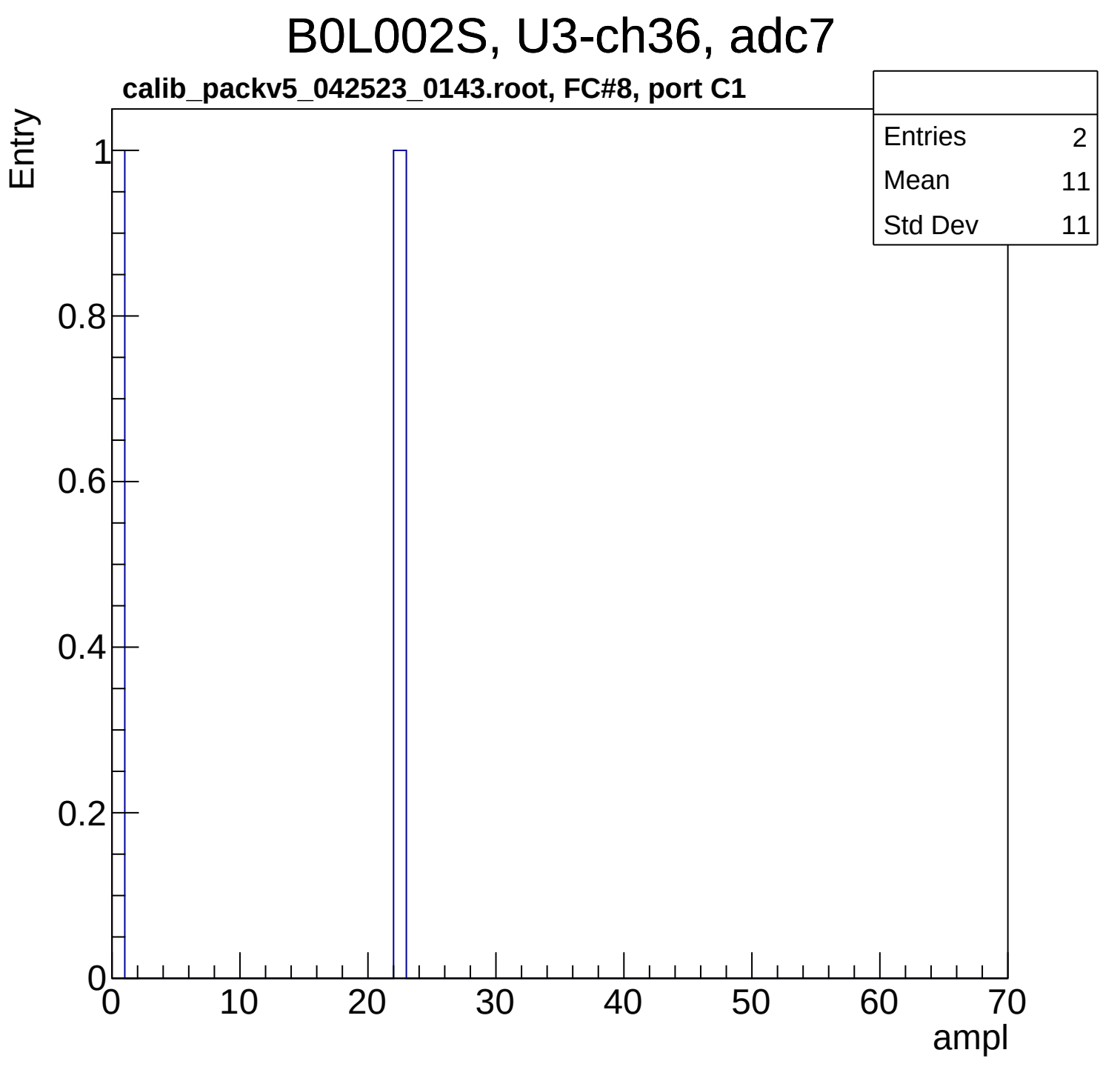

B0L002S, U3-ch36, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 42.38
Std Dev 2.967
Gaus mean : 42.3887
Gaus Width: 2.8486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch36, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 48.8
Std Dev 3.619
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch36, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 55
Std Dev 2.737
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch36, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 58.61
Std Dev 8.607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch36, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 62
Std Dev 1.054
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U3-ch36, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
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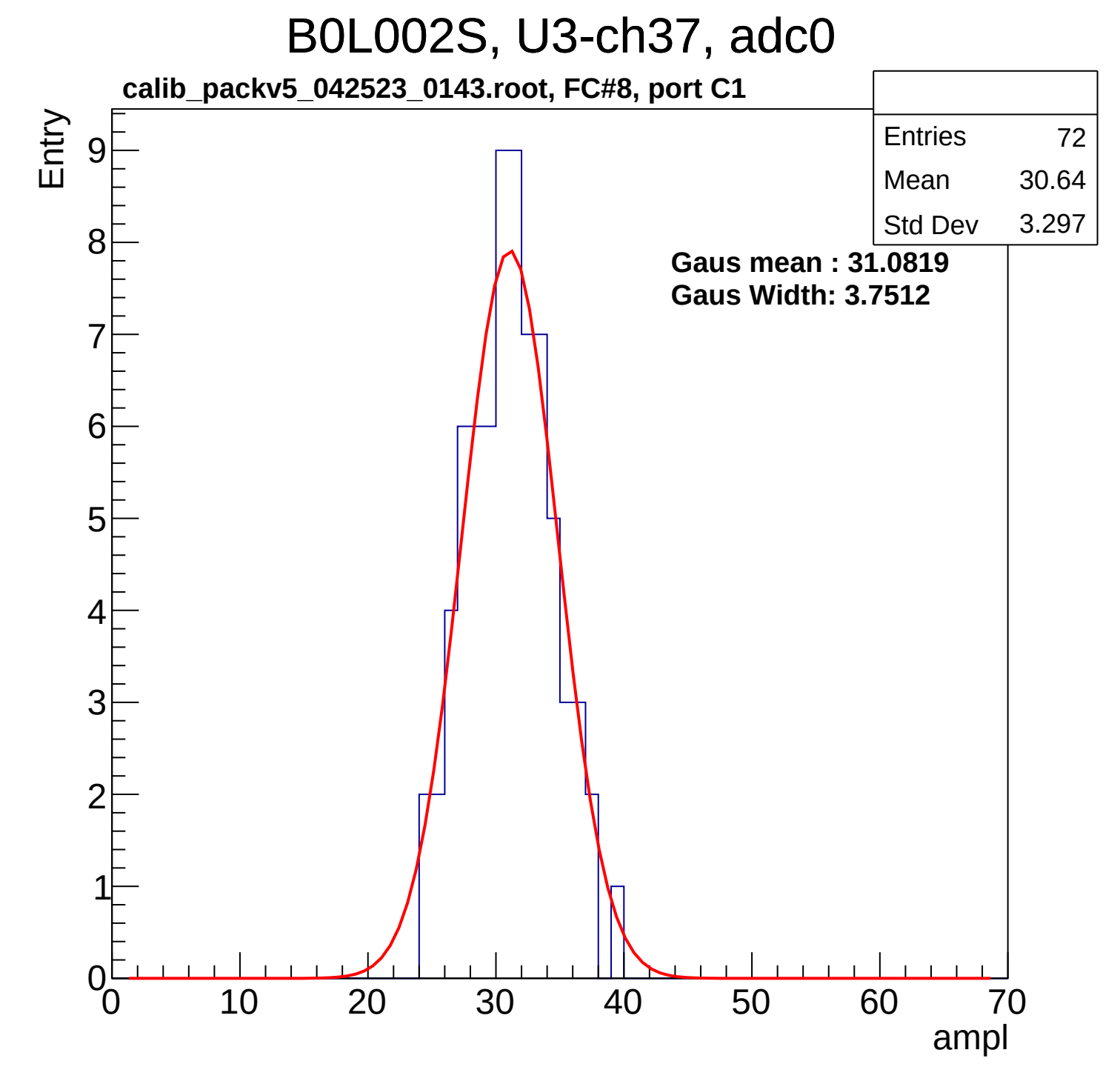
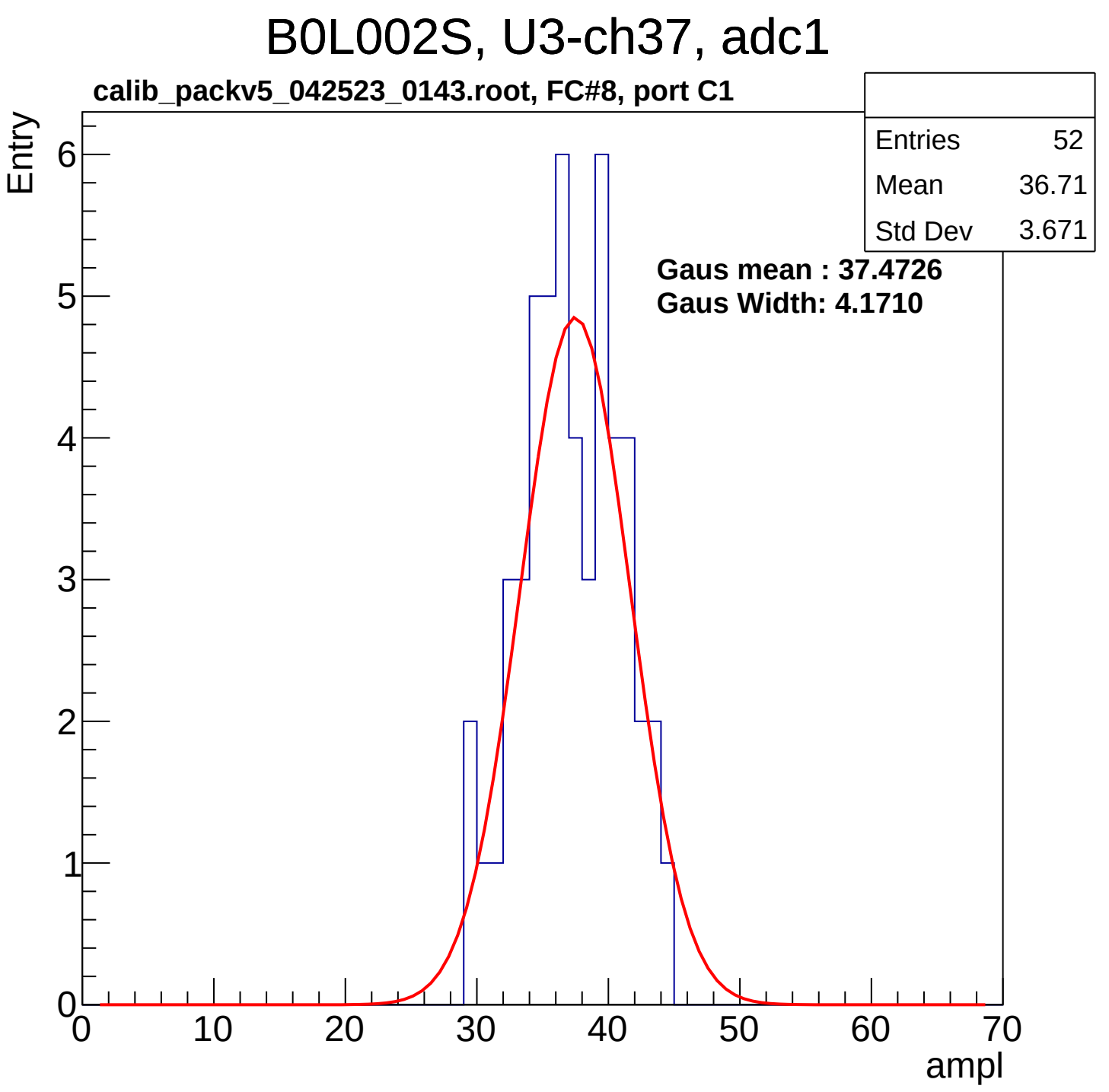
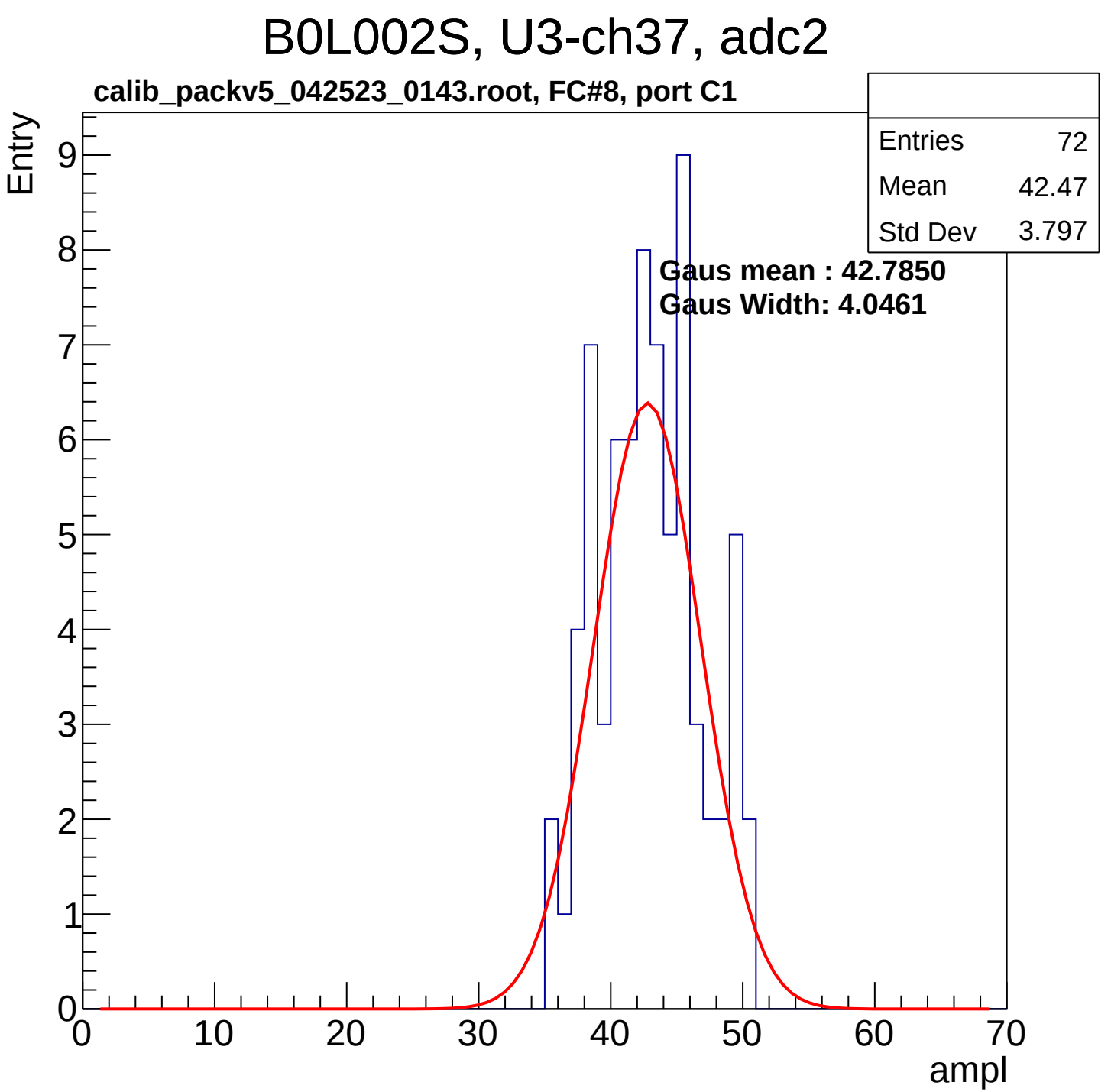
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch37, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 30.64
Std Dev 3.297
Gaus mean : 31.0819
Gaus Width: 3.7512
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U3-ch37, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 36.71
Std Dev 3.671
Gaus mean : 37.4726
Gaus Width: 4.1710
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U3-ch37, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 42.47
Std Dev 3.797
Gaus mean : 42.7850
Gaus Width: 4.0461
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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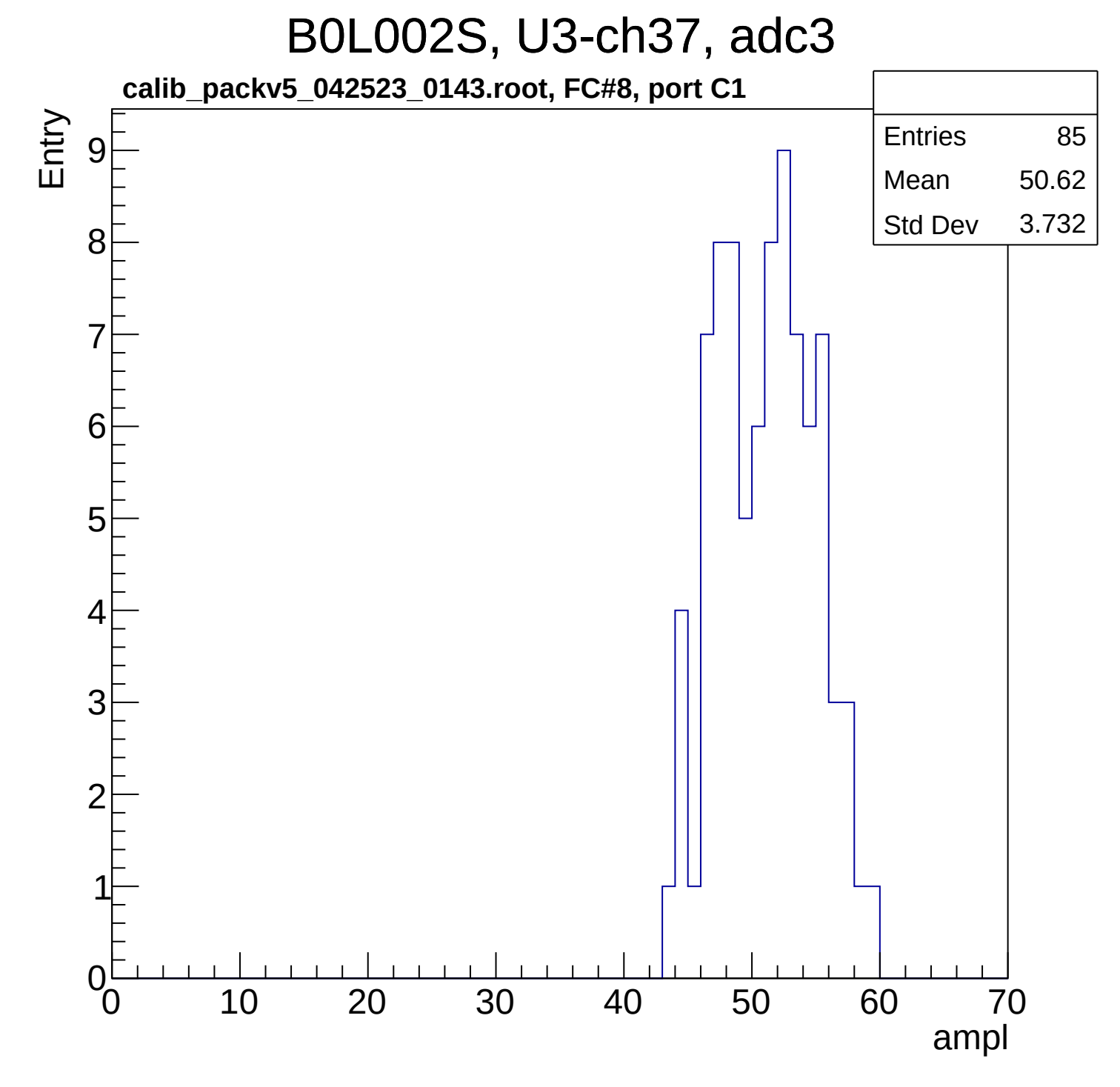
B0L002S, U3-ch37, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 50.62
Std Dev 3.732
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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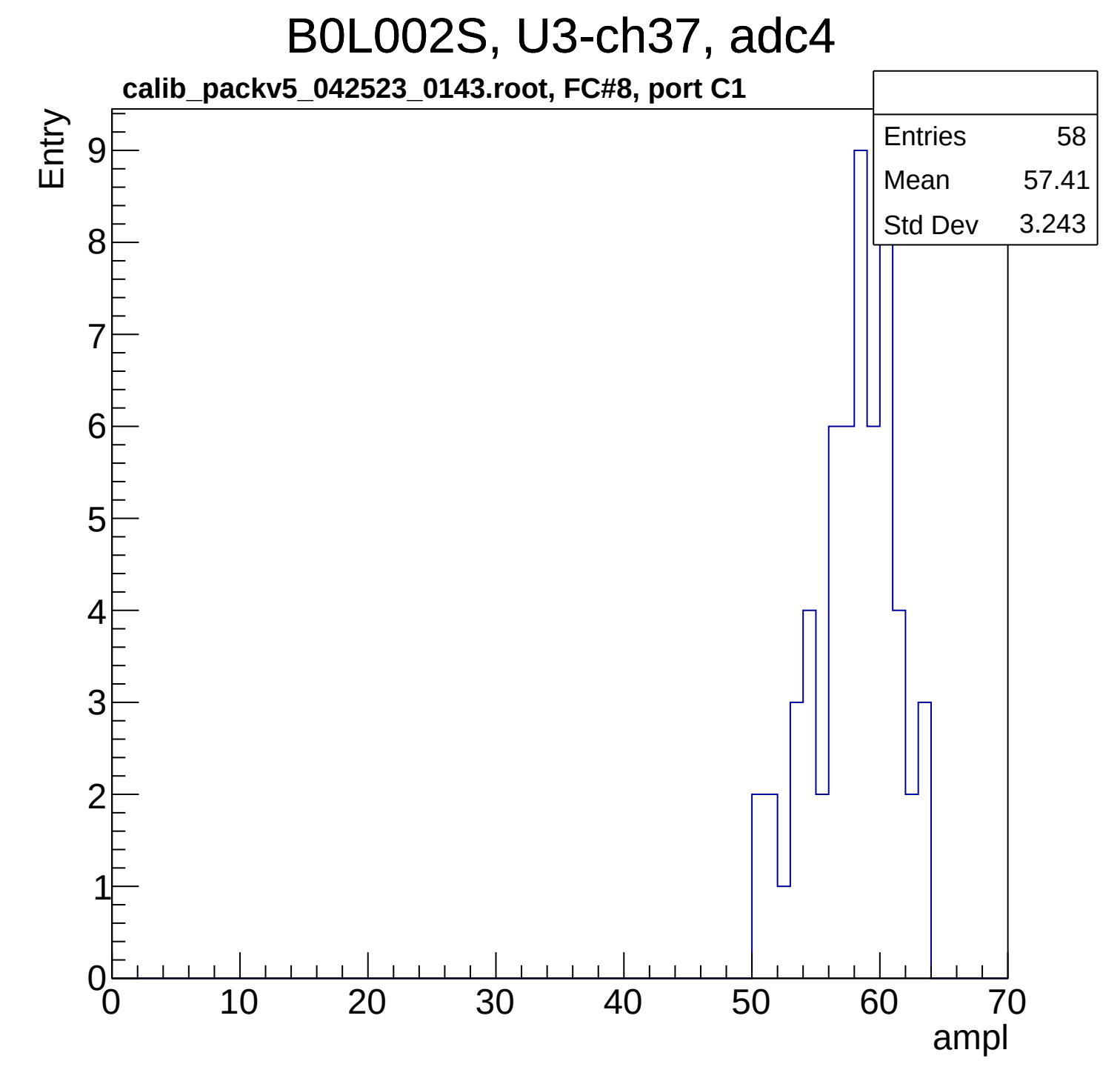
40
50
60
70


B0L002S, U3-ch37, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.41
Std Dev 3.243
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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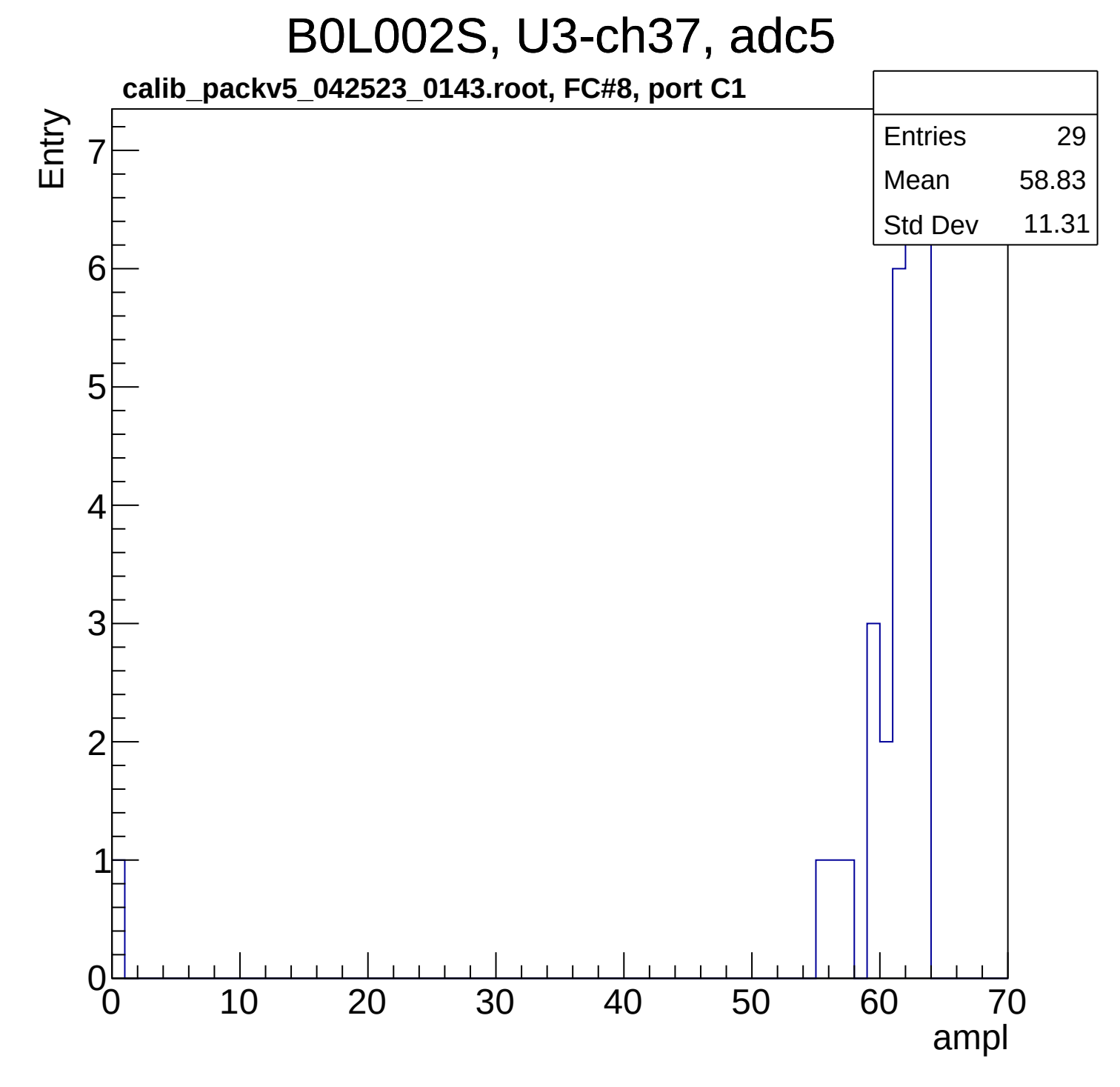
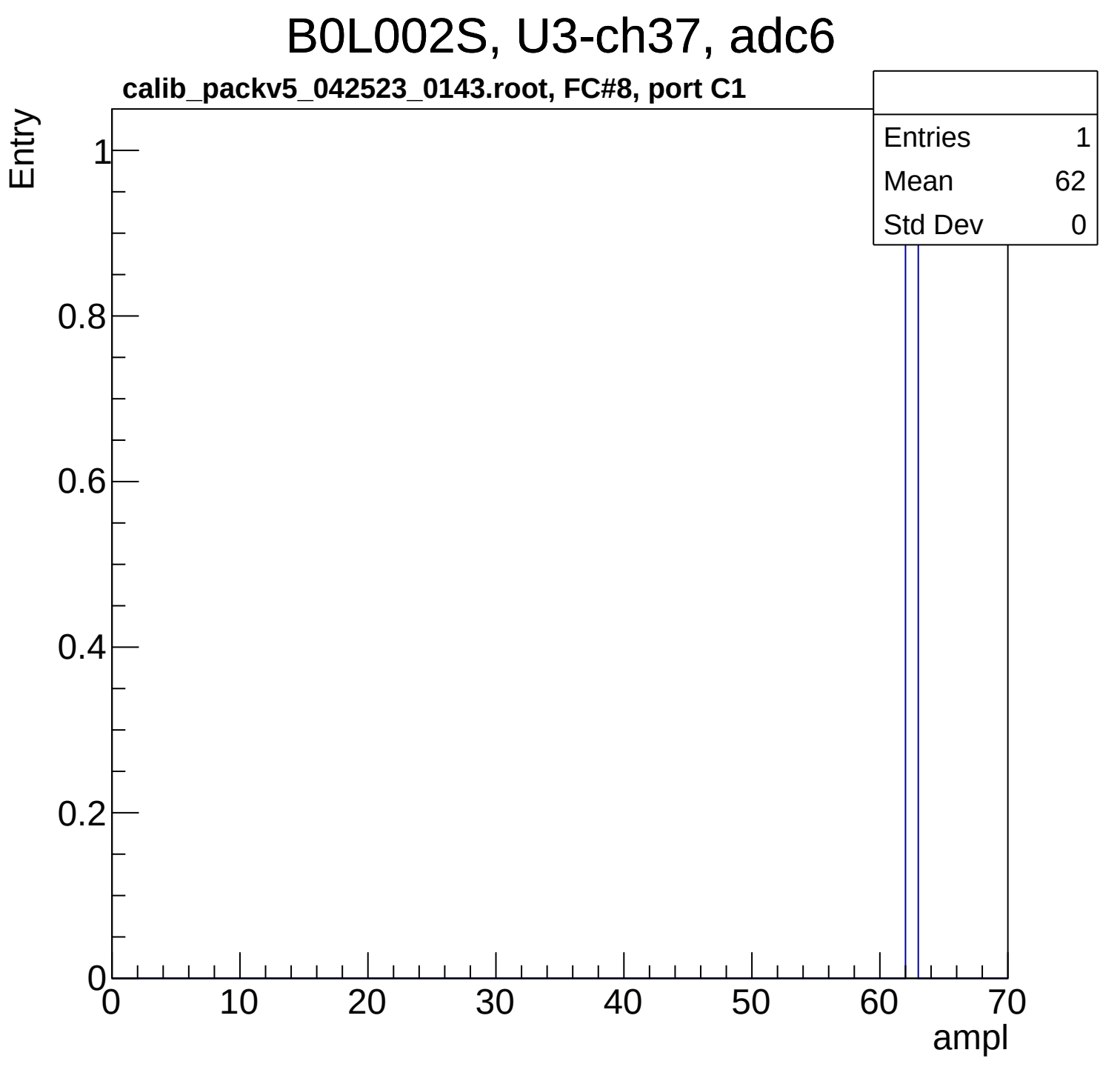
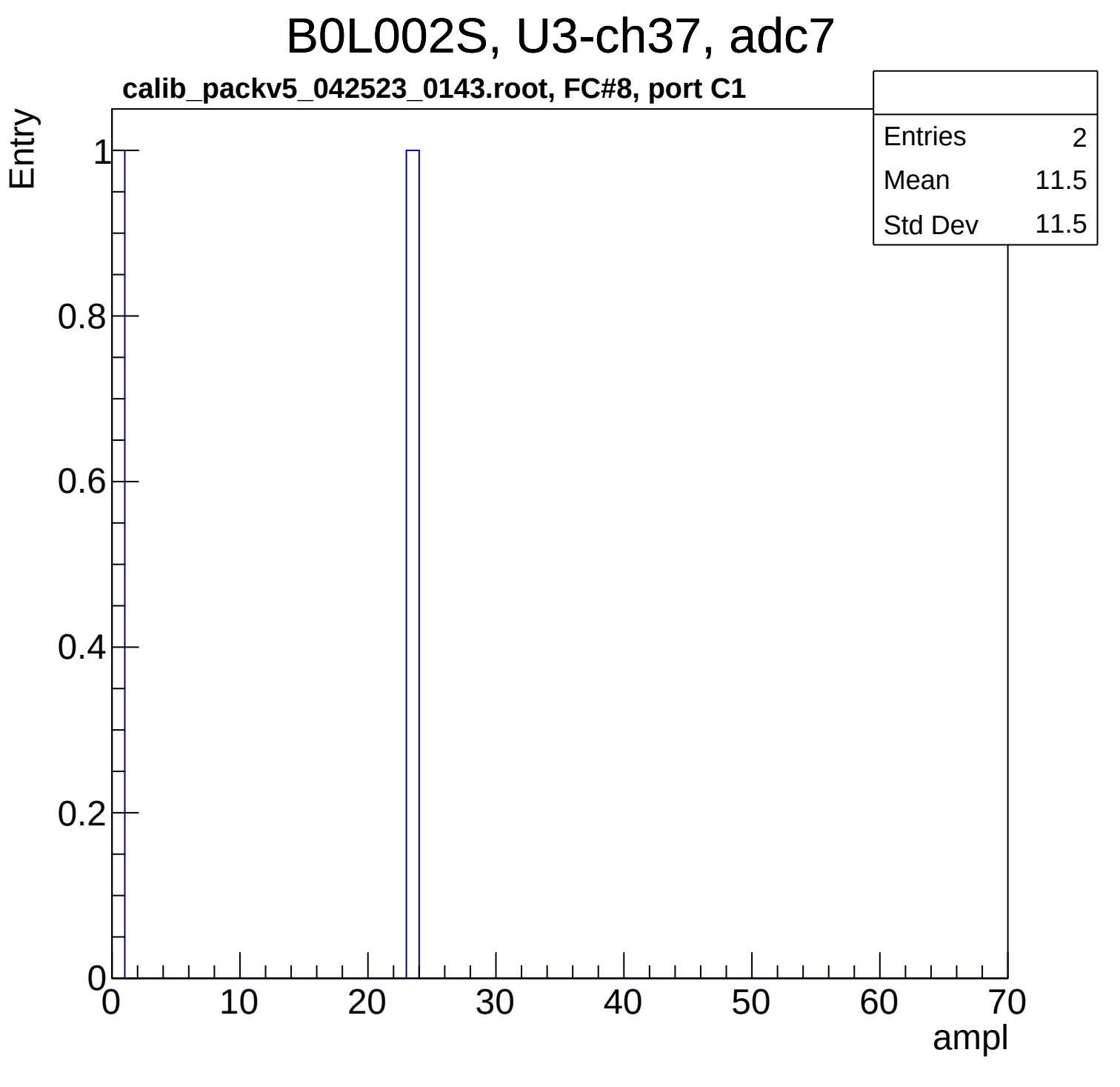
40
50
60
70


B0L002S, U3-ch37, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 58.83
Std Dev 11.31
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch37, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch37, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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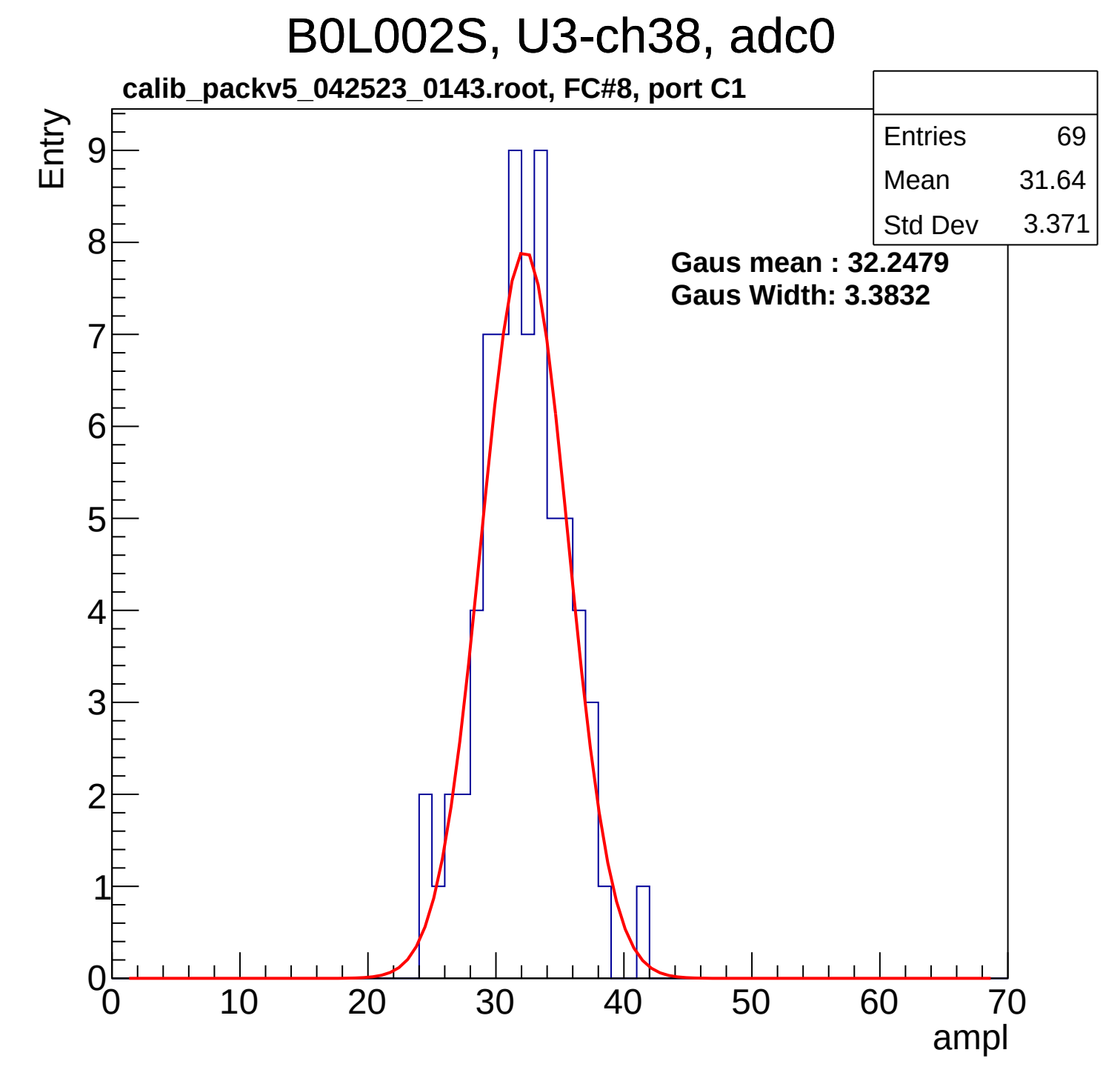
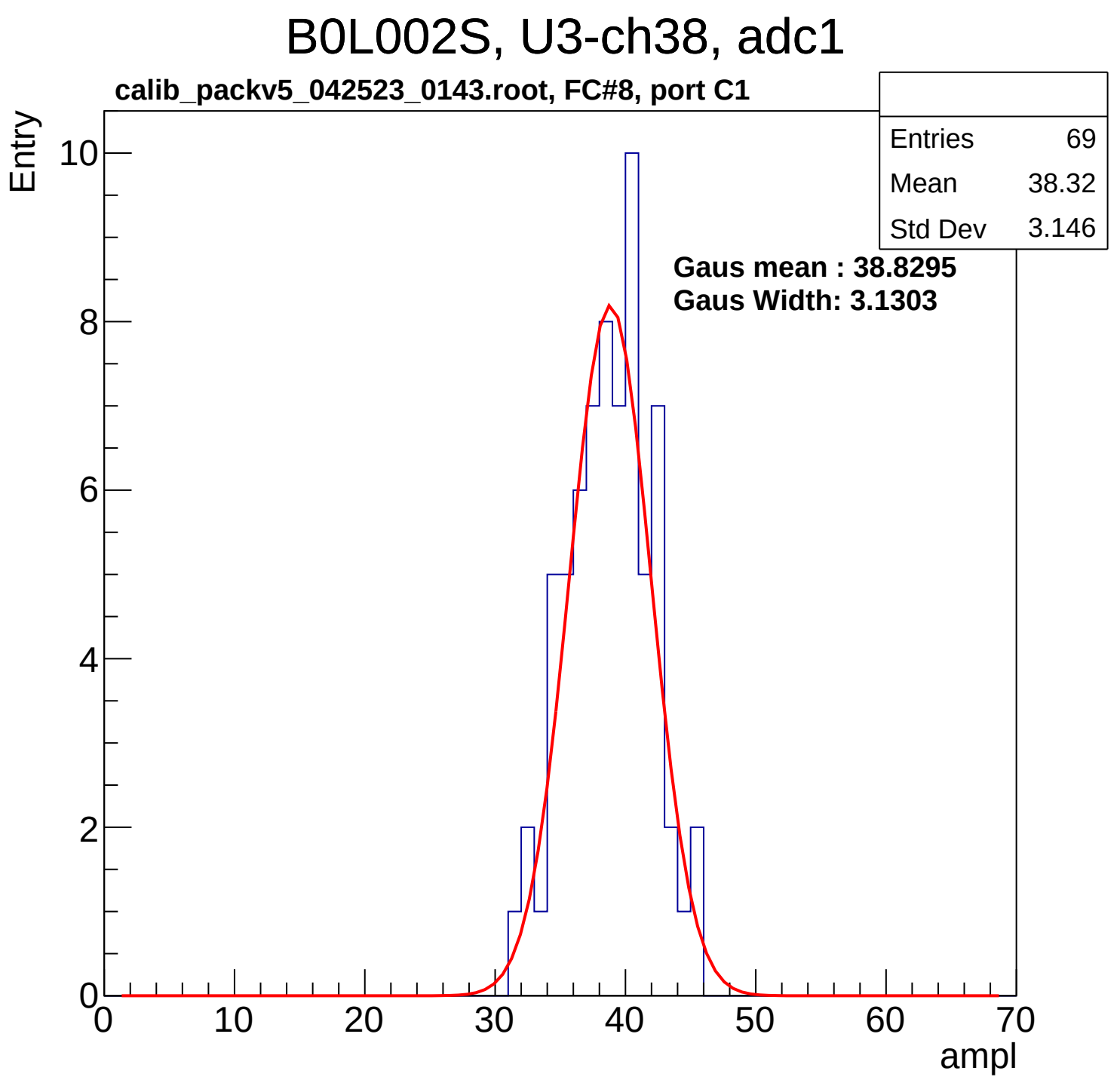
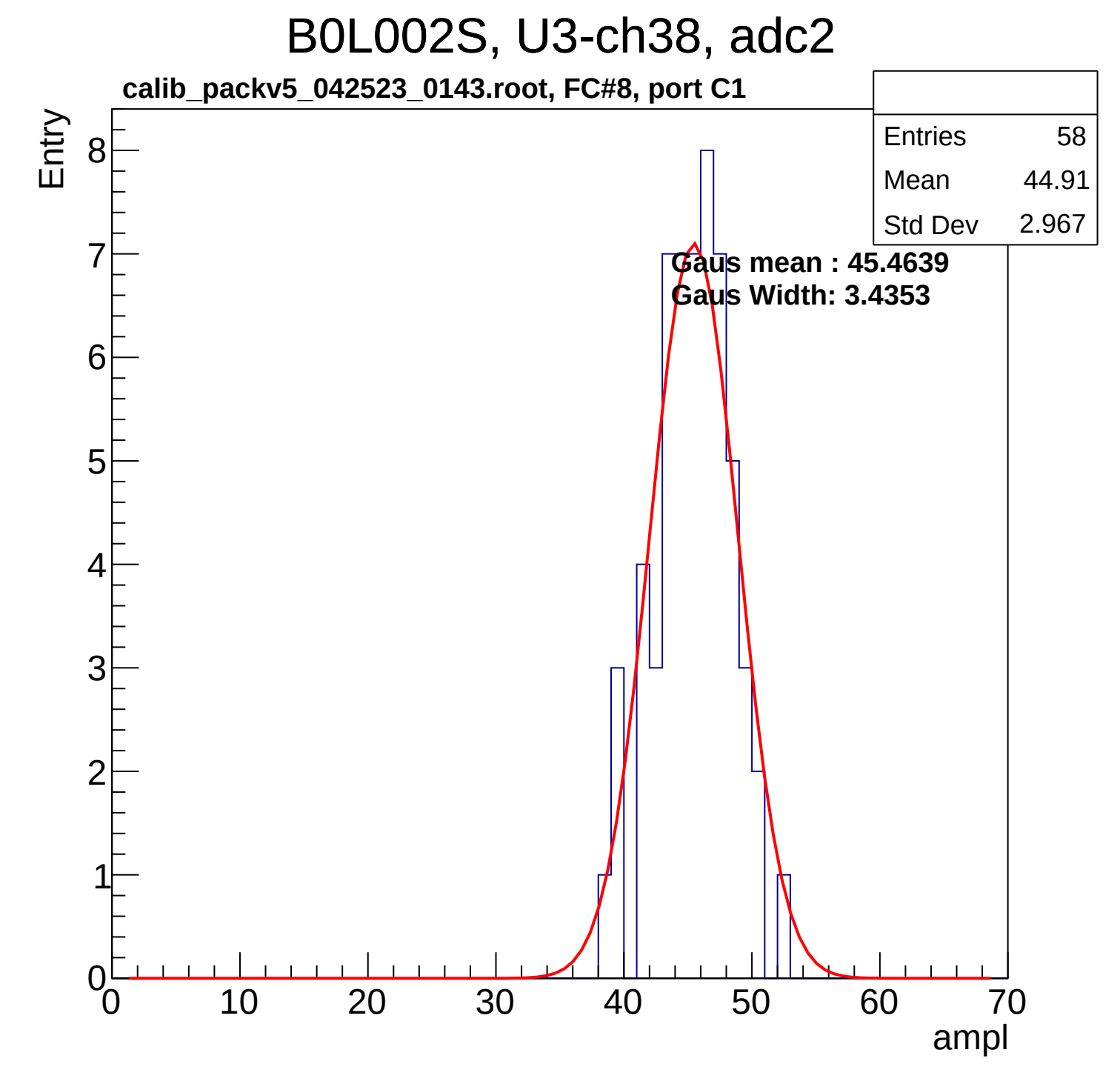
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch38, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 31.64
Std Dev 3.371
Gaus mean : 32.2479
Gaus Width: 3.3832
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch38, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 38.32
Std Dev 3.146
Gaus mean : 38.8295
Gaus Width: 3.1303
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch38, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 44.91
Std Dev 2.967
Gaus mean : 45.4639
Gaus Width: 3.4353
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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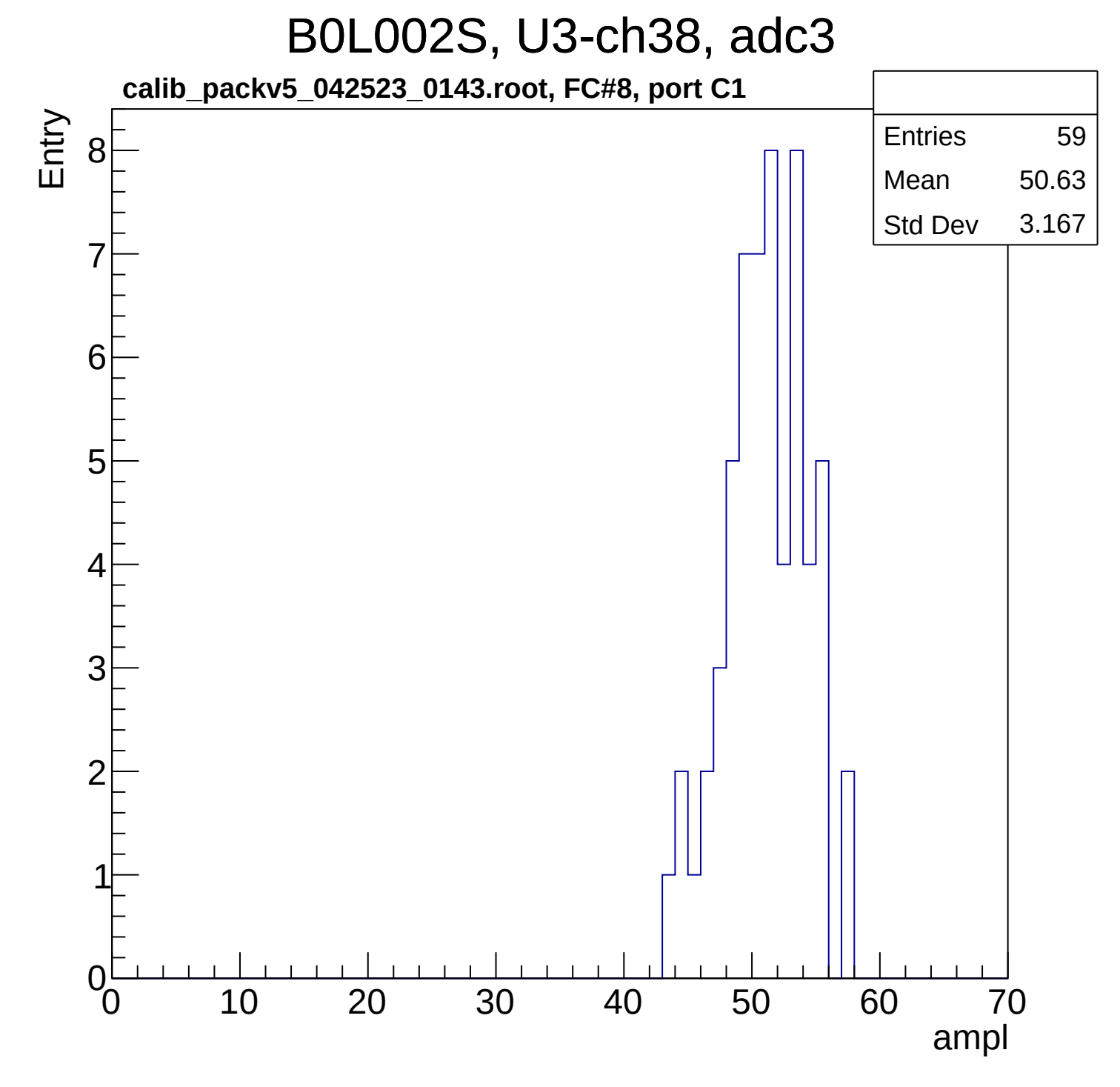
B0L002S, U3-ch38, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.63
Std Dev 3.167
Entry
ampl
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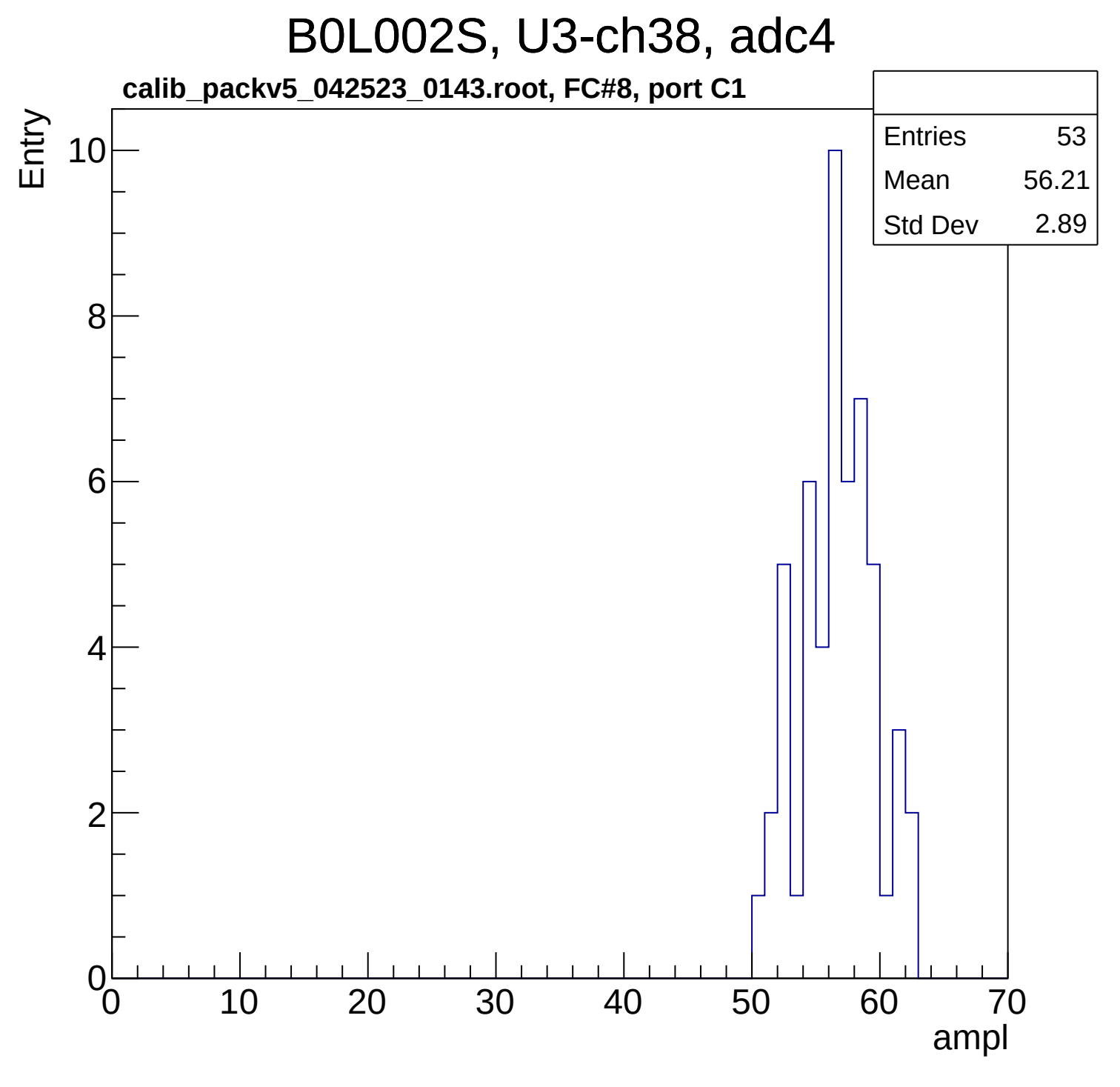
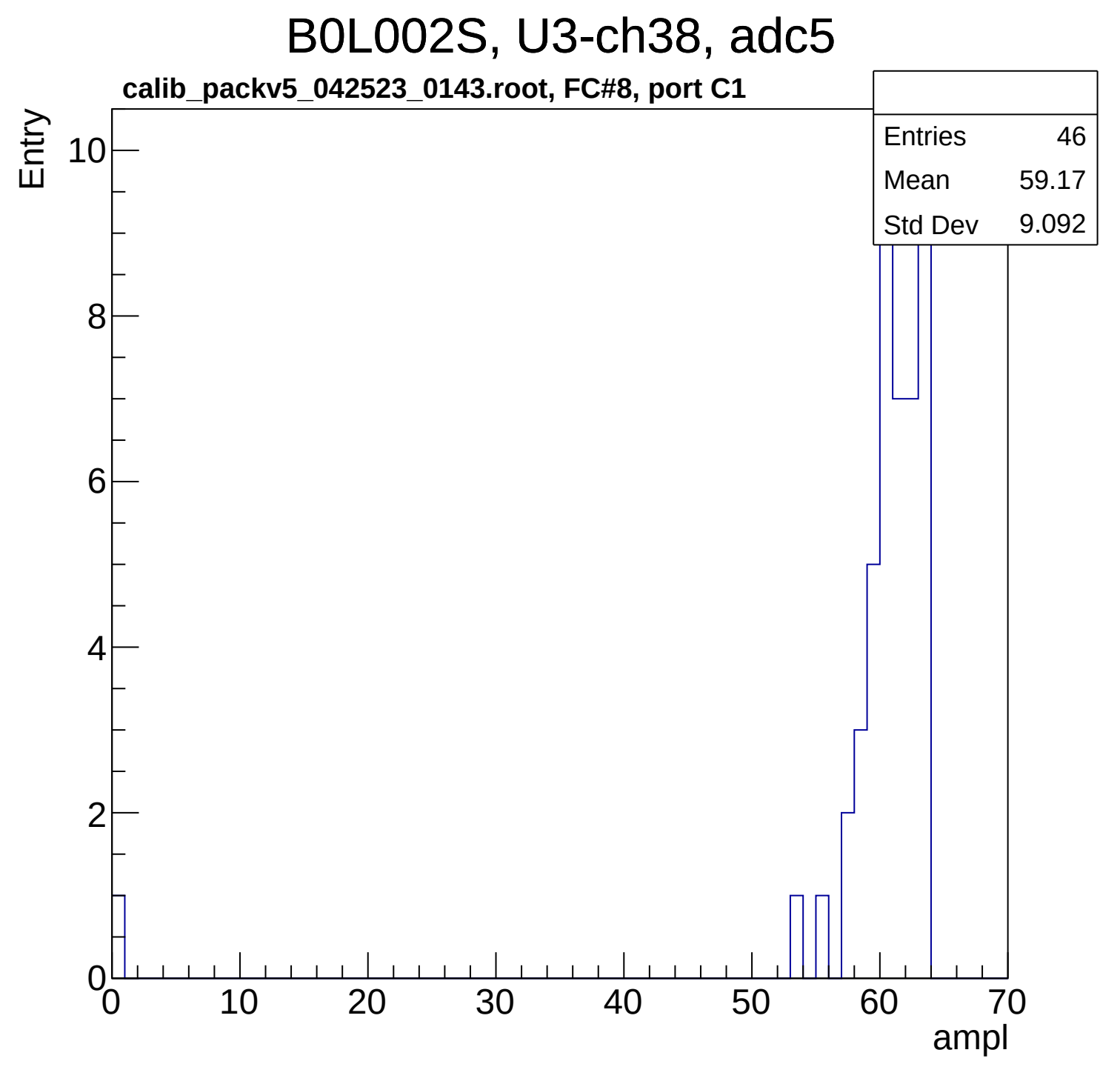
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch38, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.21
Std Dev 2.89
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch38, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.17
Std Dev 9.092
Entry
0
2
4
6
8
10
0
10
20
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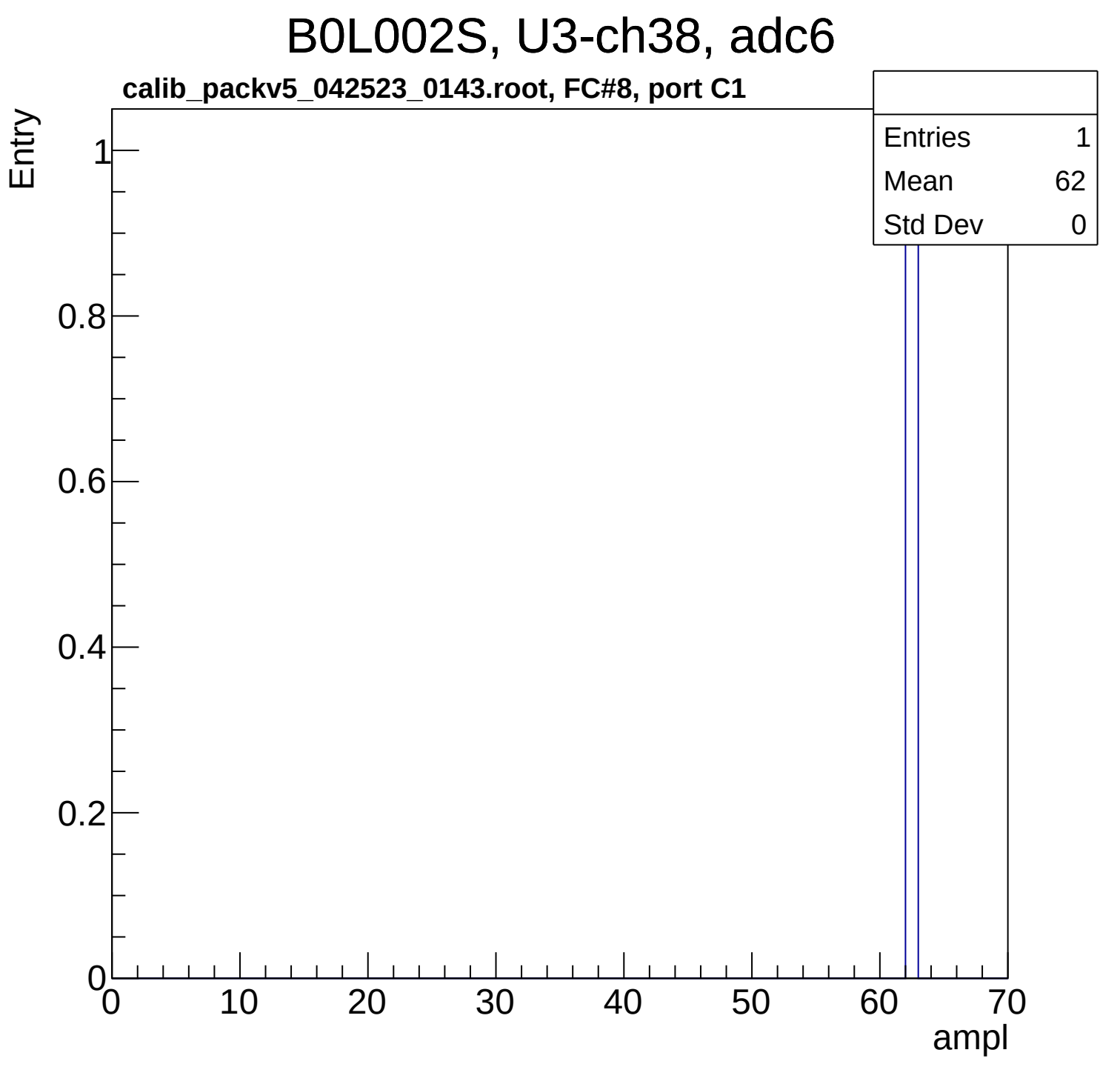
50
60
70
ampl


B0L002S, U3-ch38, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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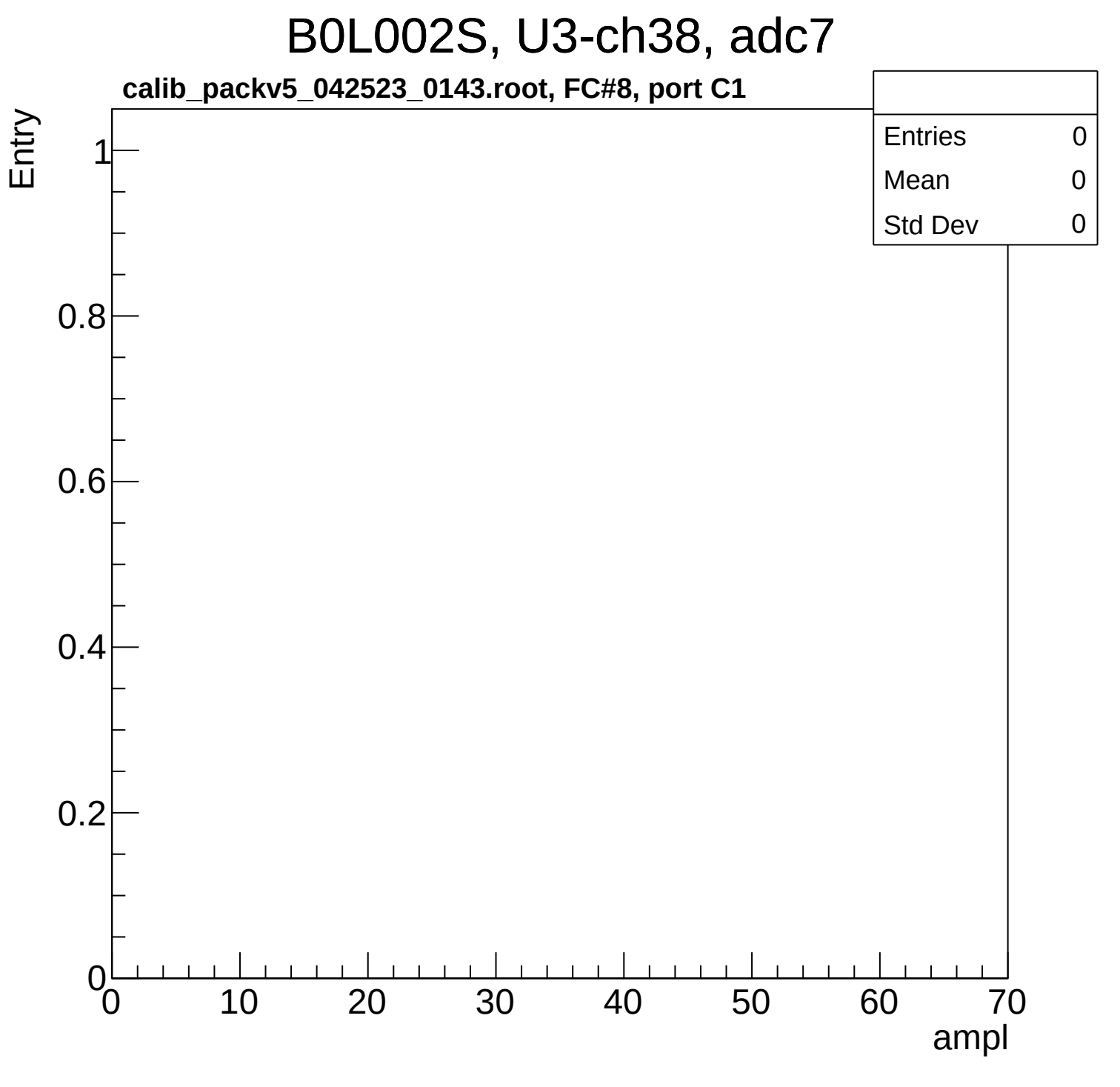
50
60
70
ampl


B0L002S, U3-ch38, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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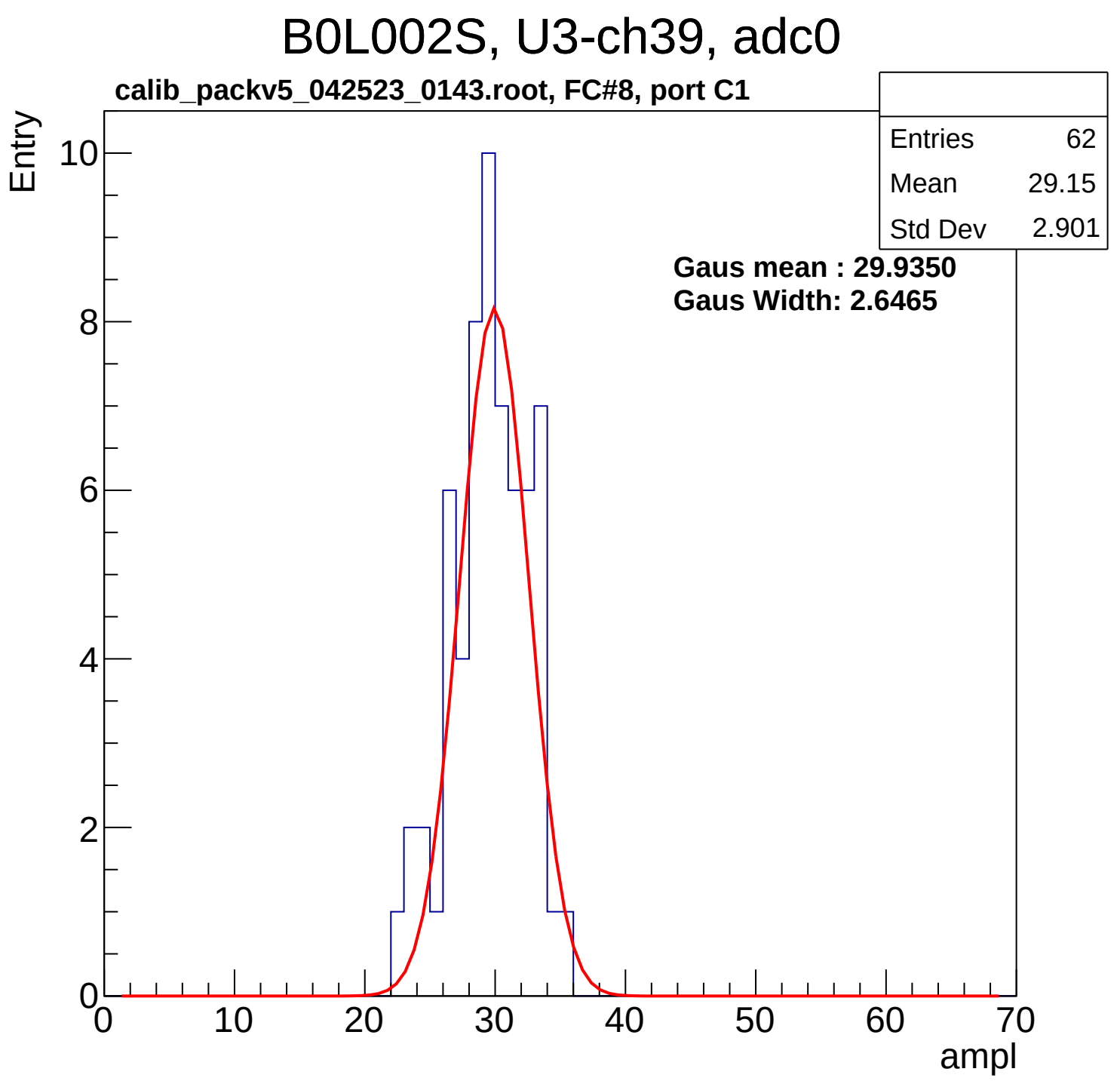
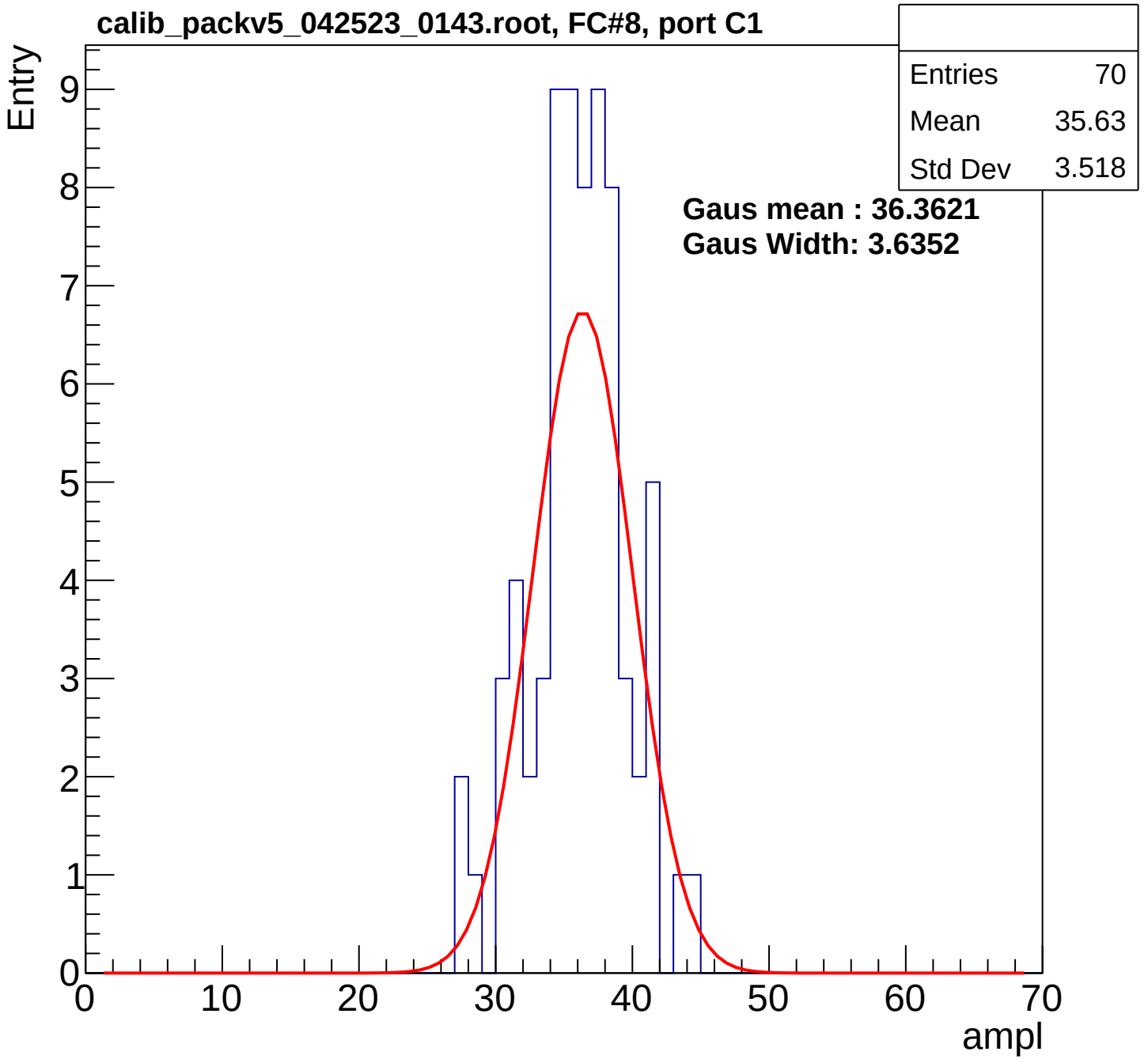
50
60
70
ampl


B0L002S, U3-ch39, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 29.15
Std Dev 2.901
Gaus mean : 29.9350
Gaus Width: 2.6465
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch39, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 35.63
Std Dev 3.518
Gaus mean : 36.3621
Gaus Width: 3.6352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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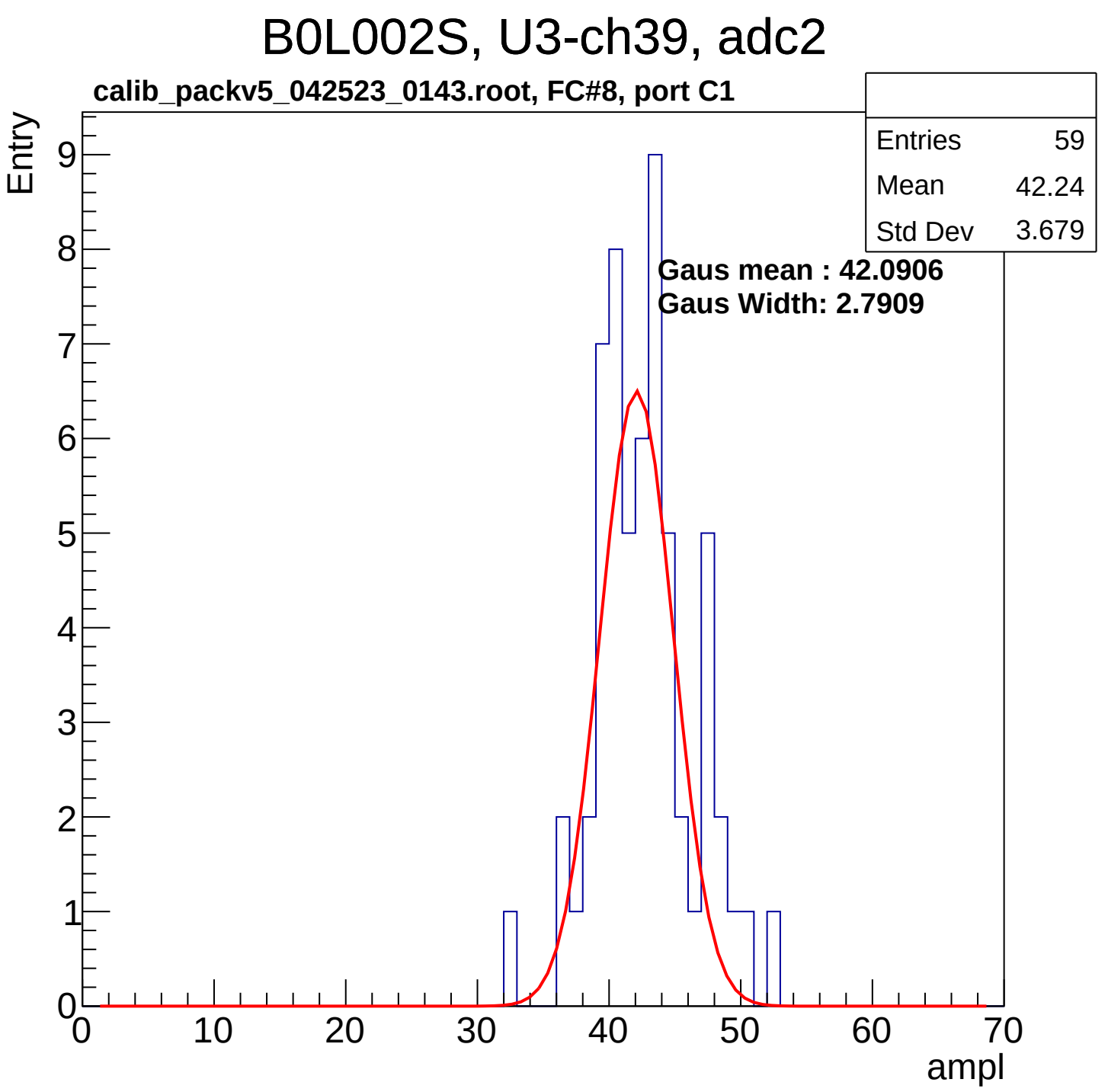
B0L002S, U3-ch39, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 42.24
Std Dev 3.679
Gaus mean : 42.0906
Gaus Width: 2.7909
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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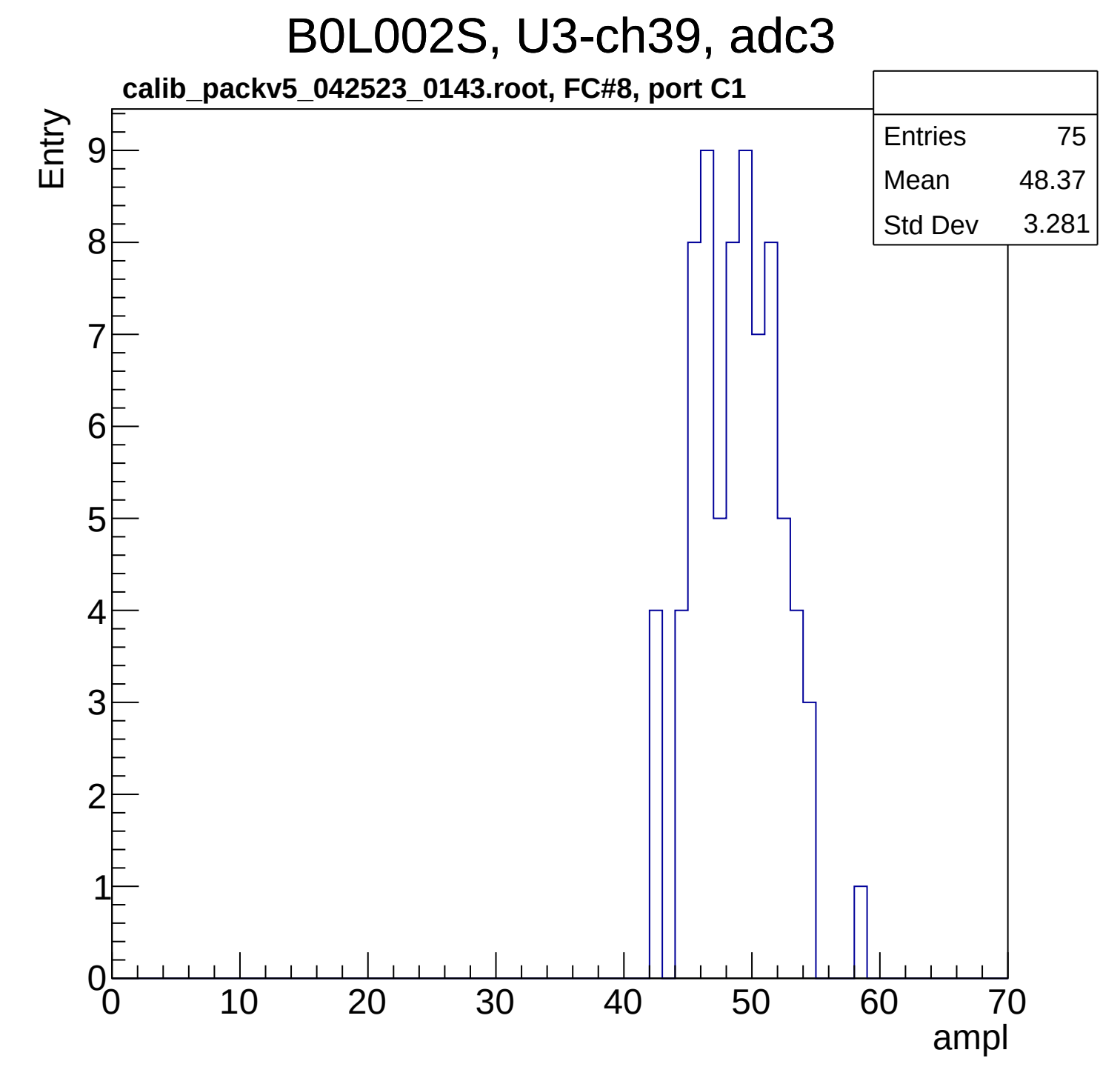
B0L002S, U3-ch39, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 48.37
Std Dev 3.281
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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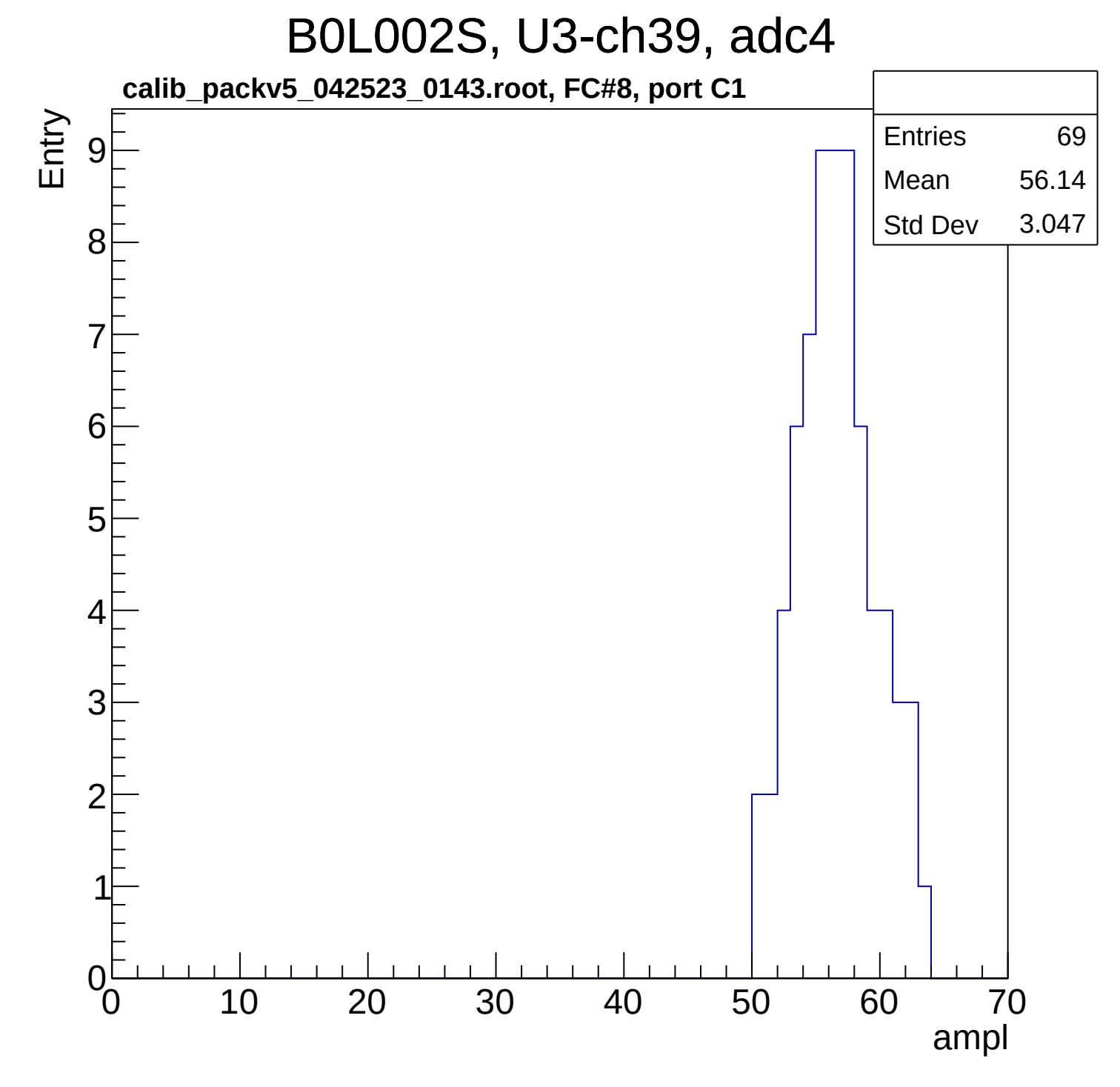
B0L002S, U3-ch39, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 56.14
Std Dev 3.047
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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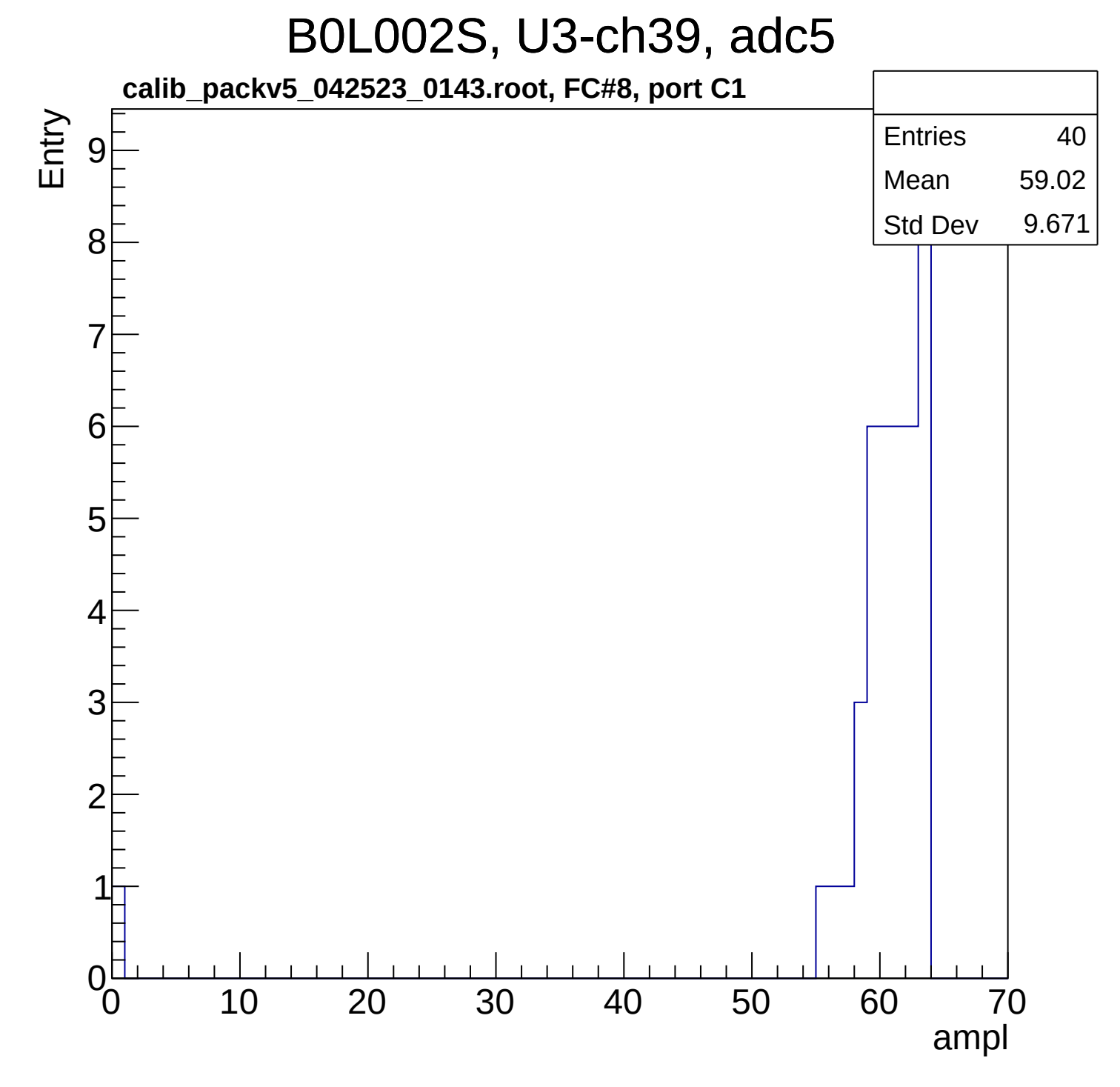
B0L002S, U3-ch39, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59.02
Std Dev 9.671
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
50
60
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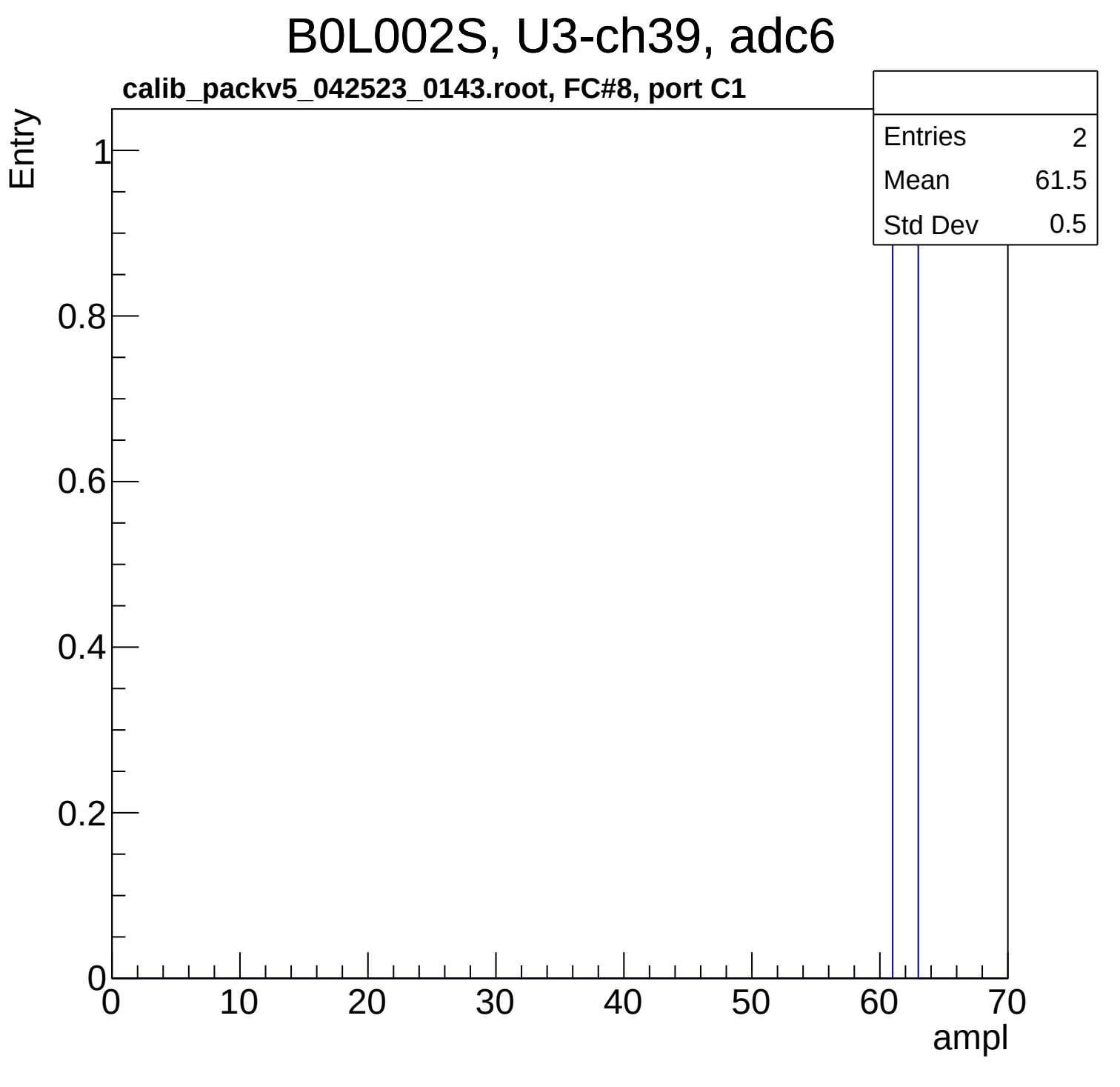
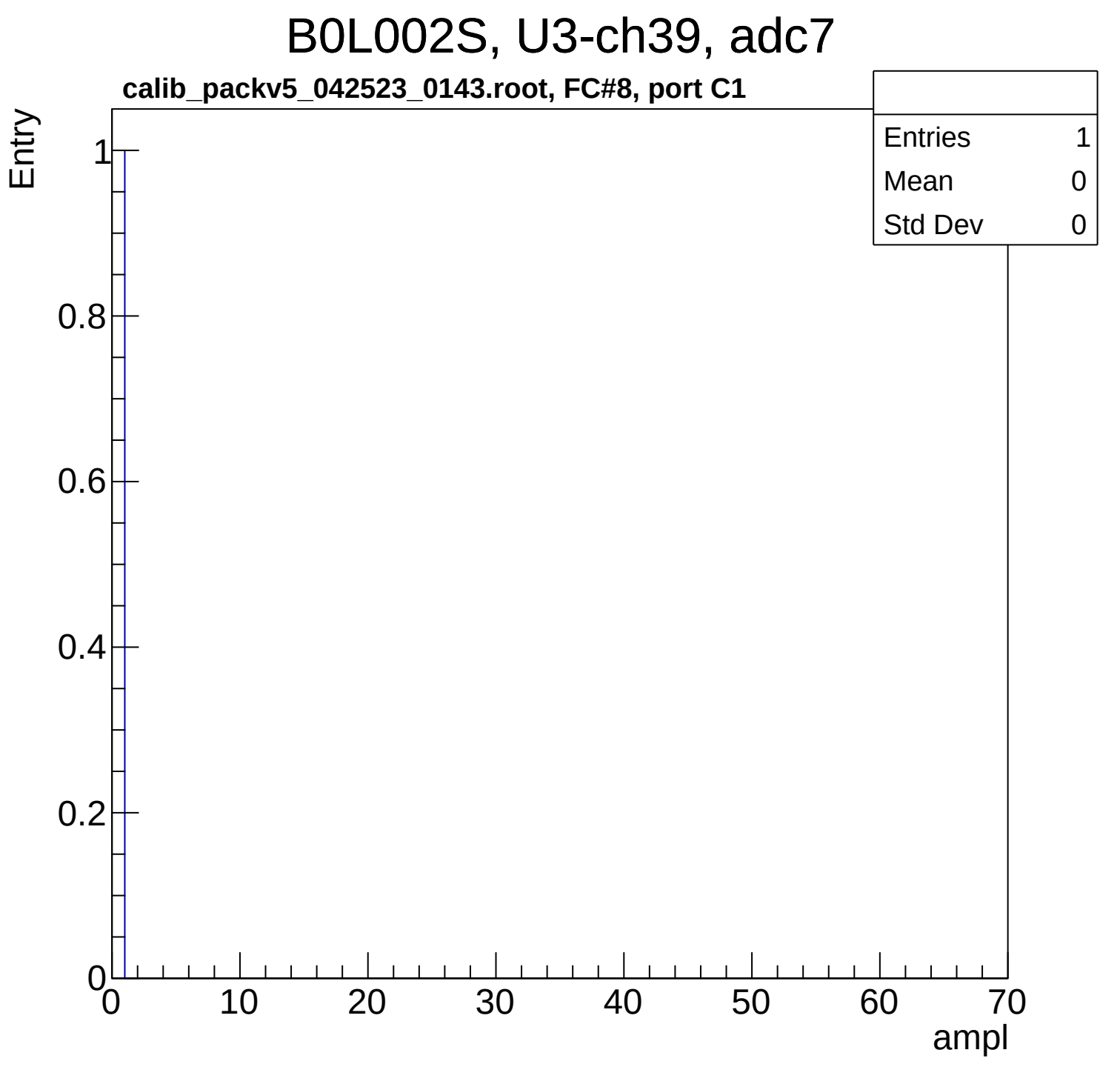
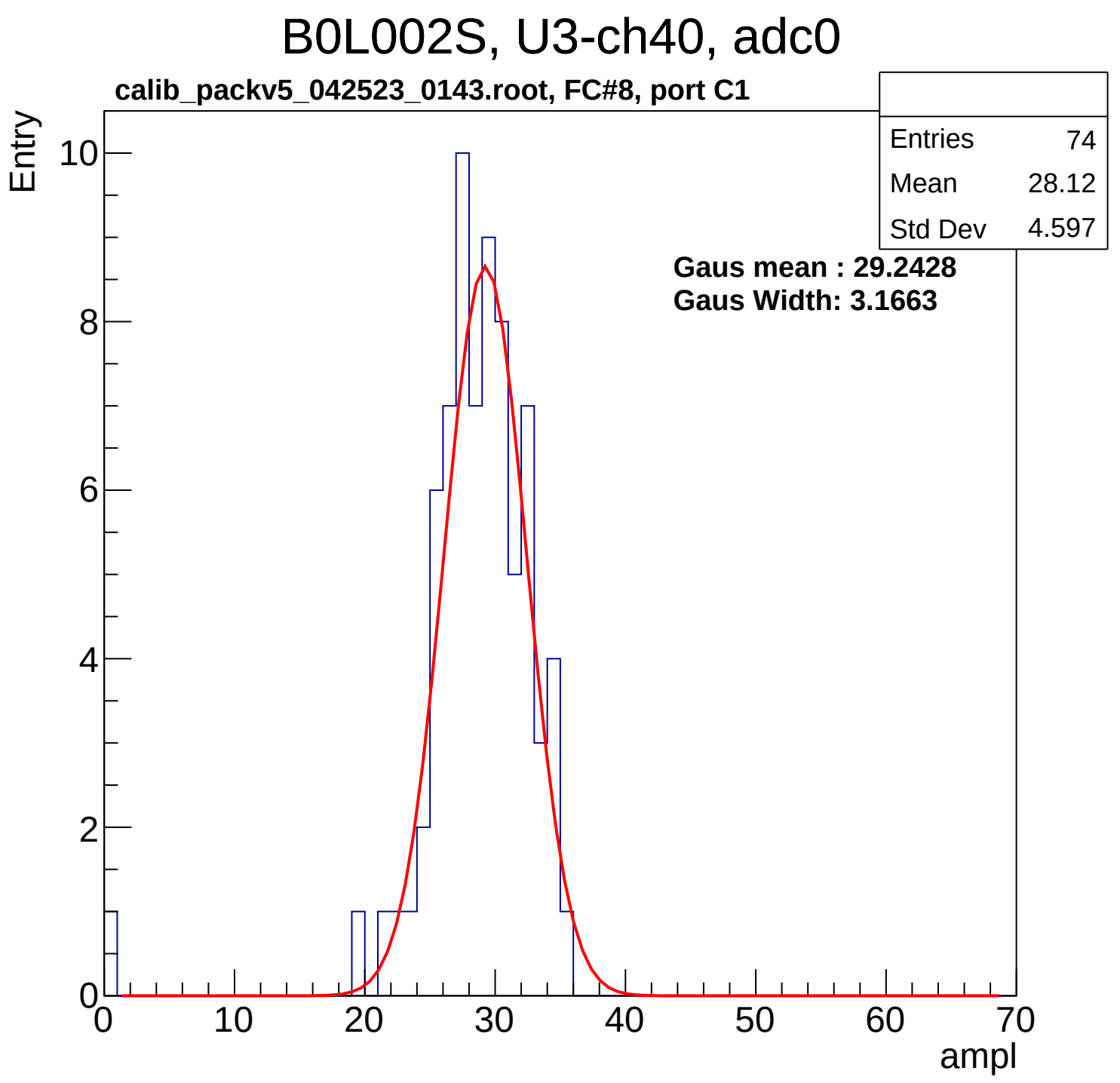
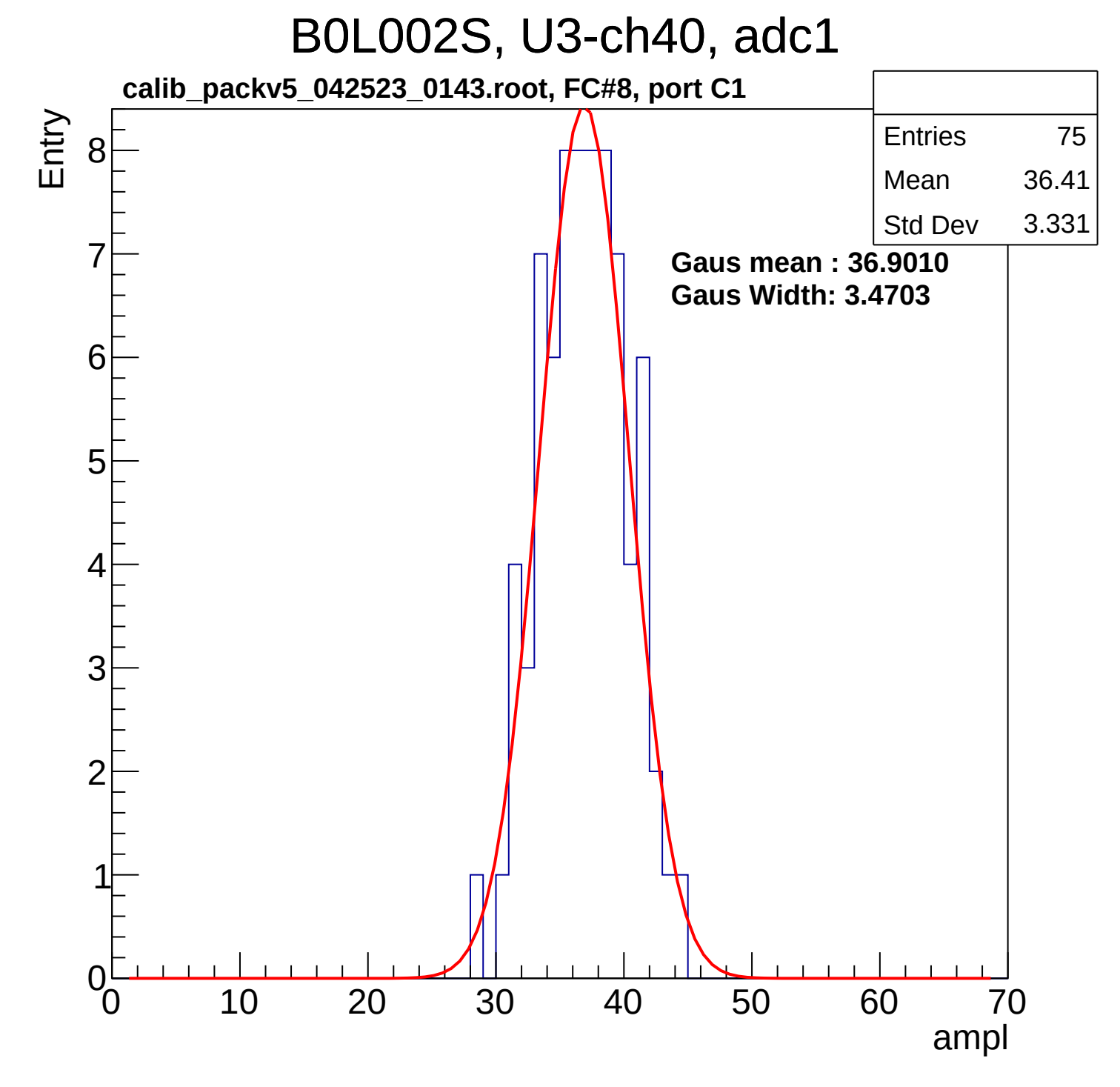
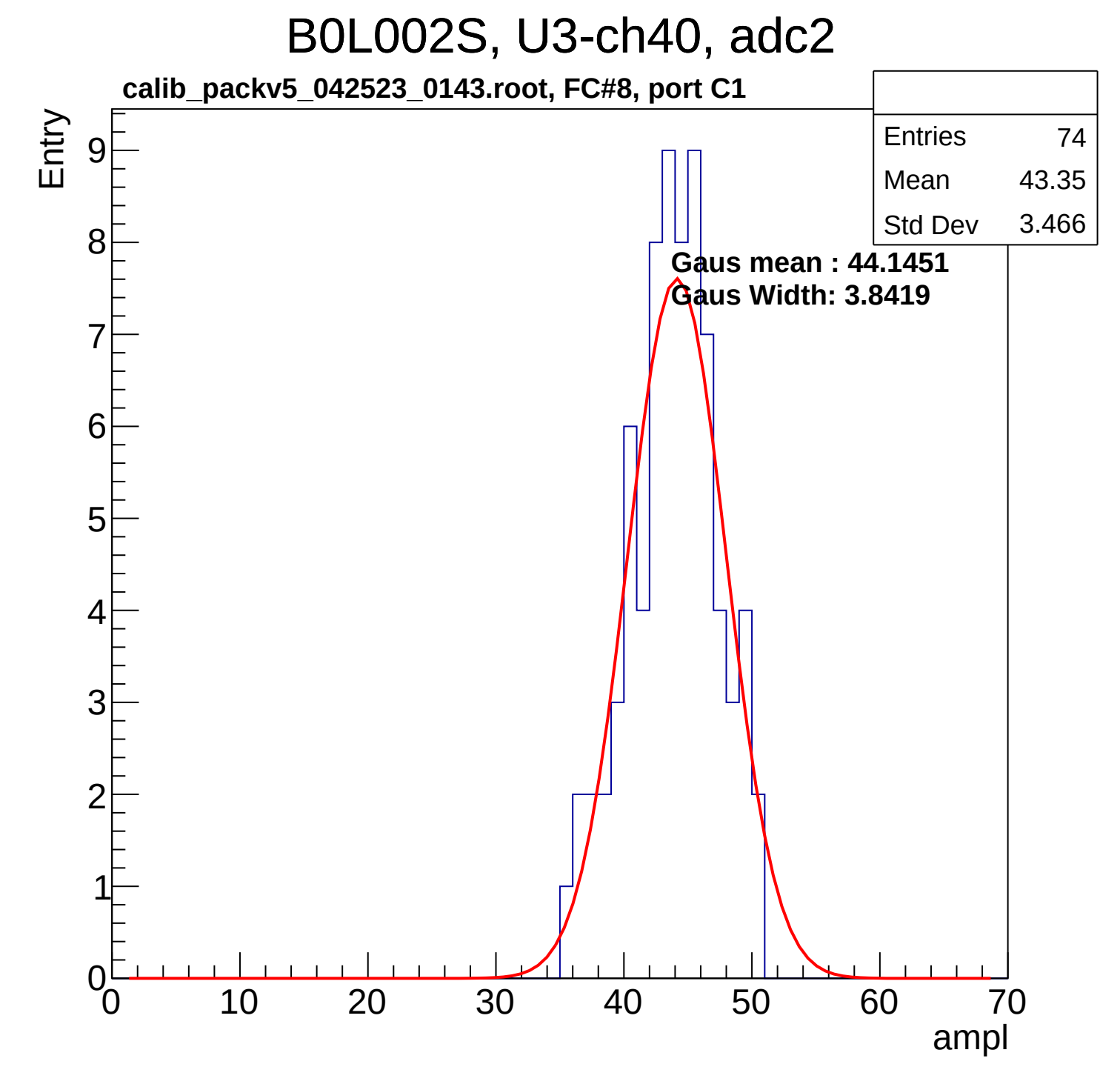

B0L002S, U3-ch39, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch39, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch40, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 28.12
Std Dev 4.597
Gaus mean : 29.2428
Gaus Width: 3.1663
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch40, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 36.41
Std Dev 3.331
Gaus mean : 36.9010
Gaus Width: 3.4703
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch40, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 43.35
Std Dev 3.466
Gaus mean : 44.1451
Gaus Width: 3.8419
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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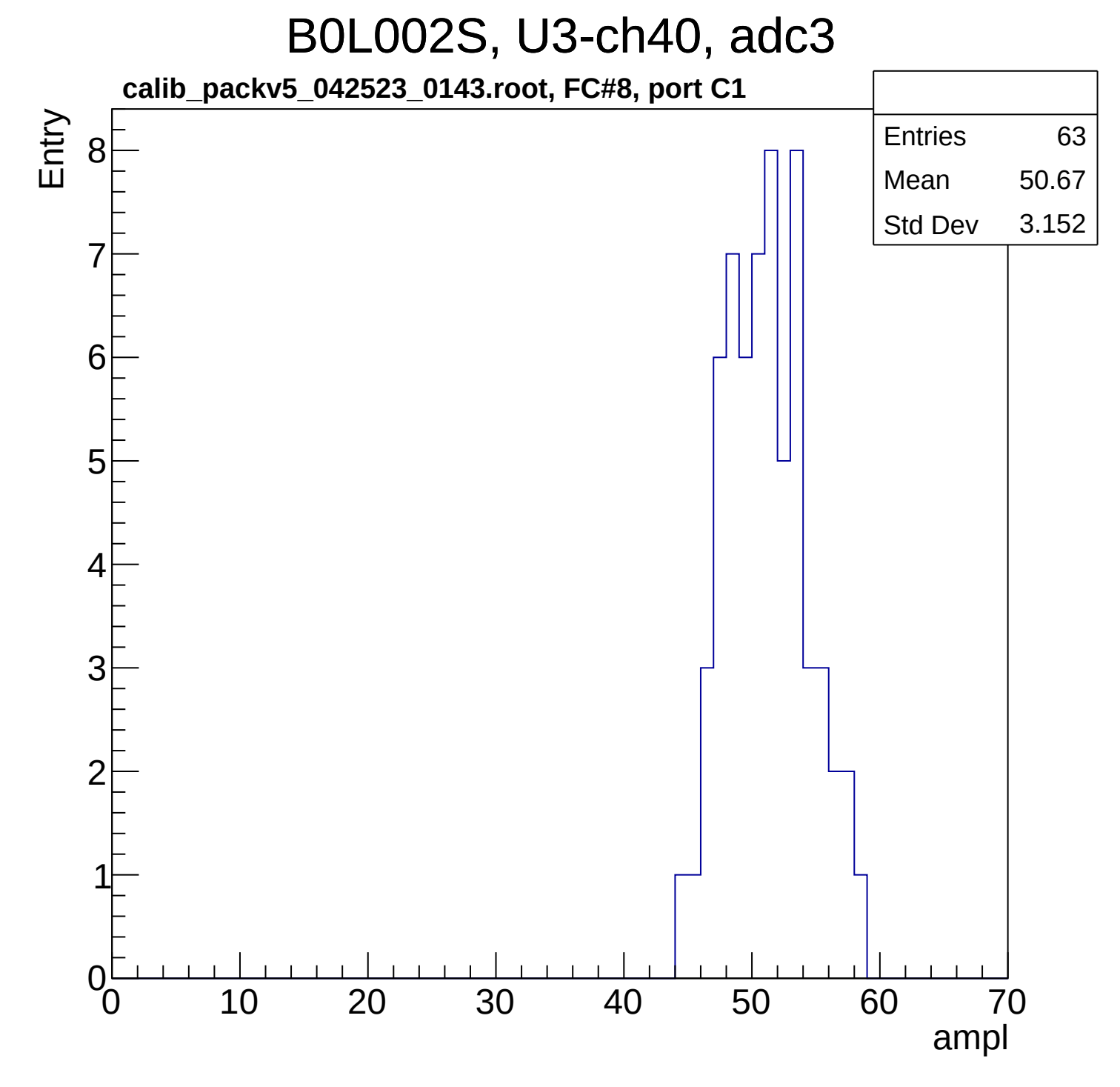
B0L002S, U3-ch40, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.67
Std Dev 3.152
Entry
ampl
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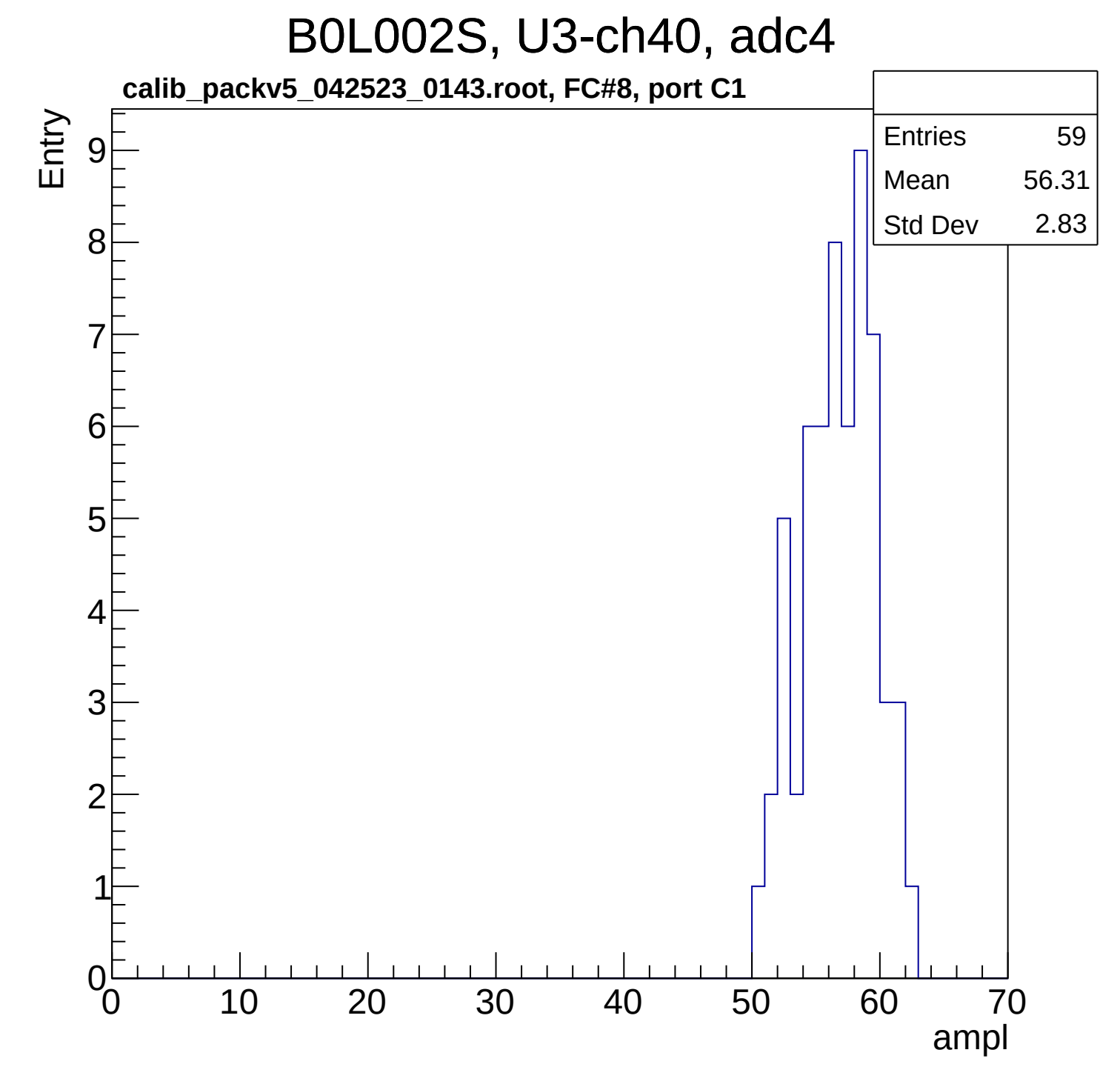
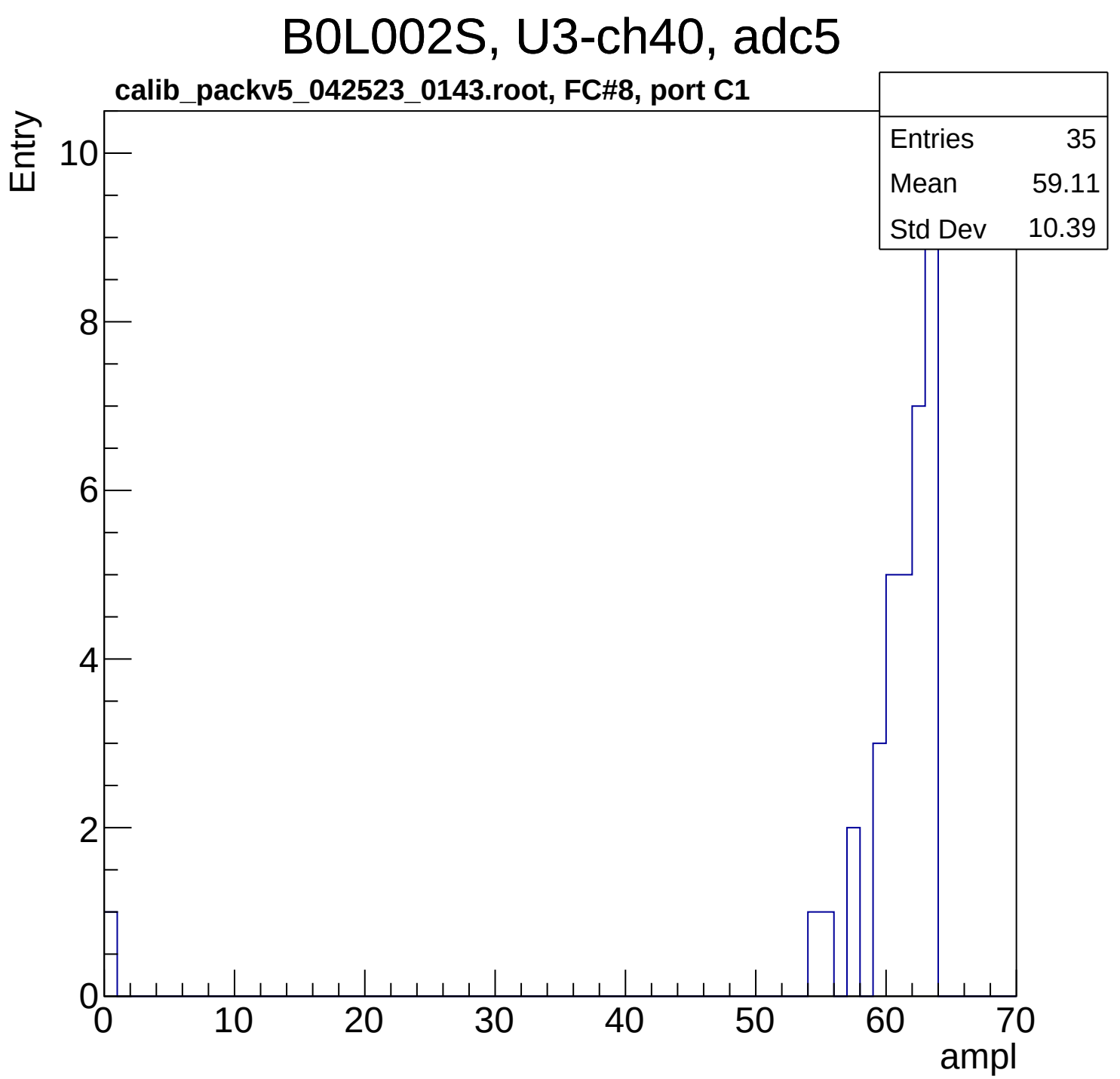
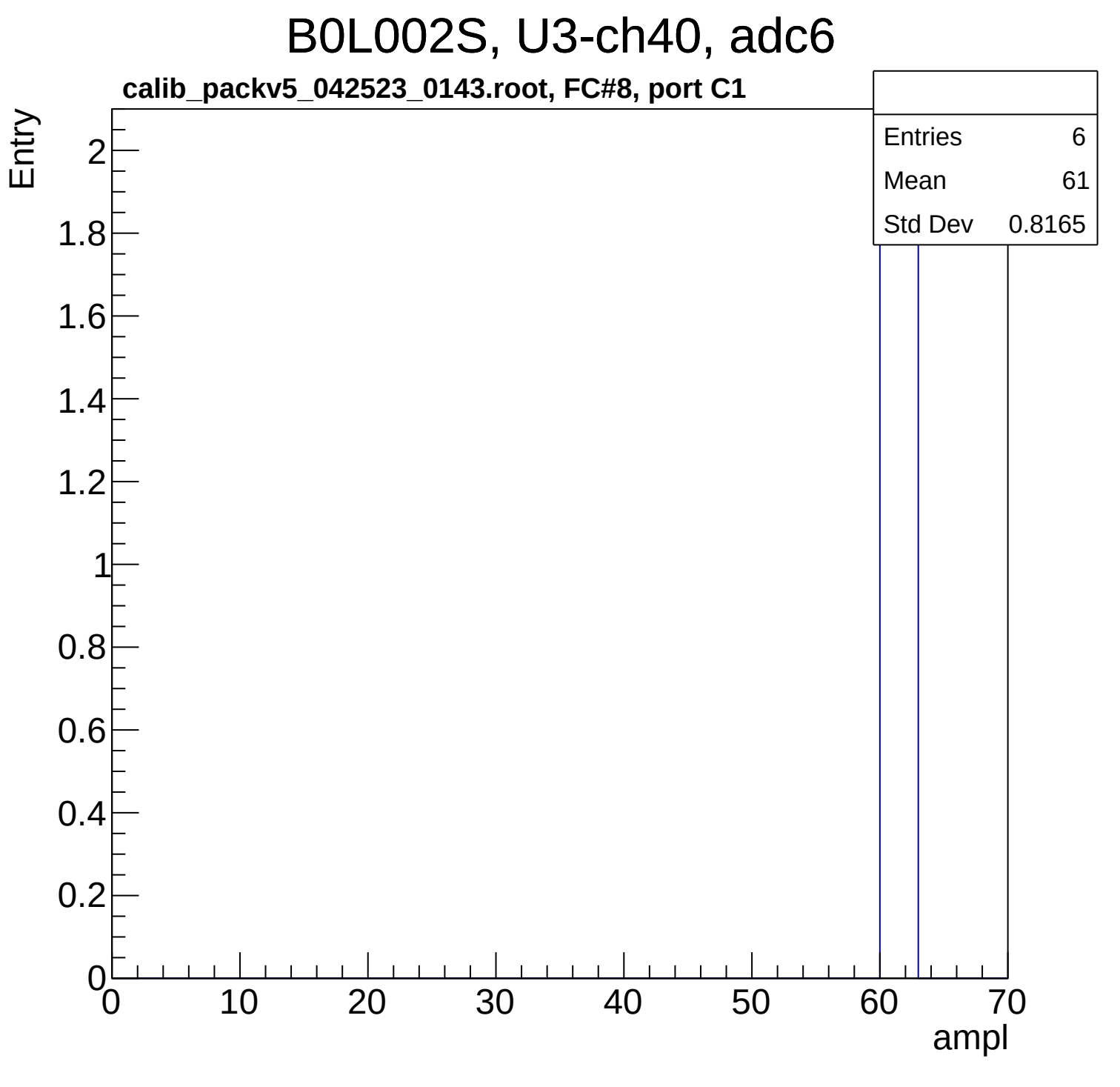
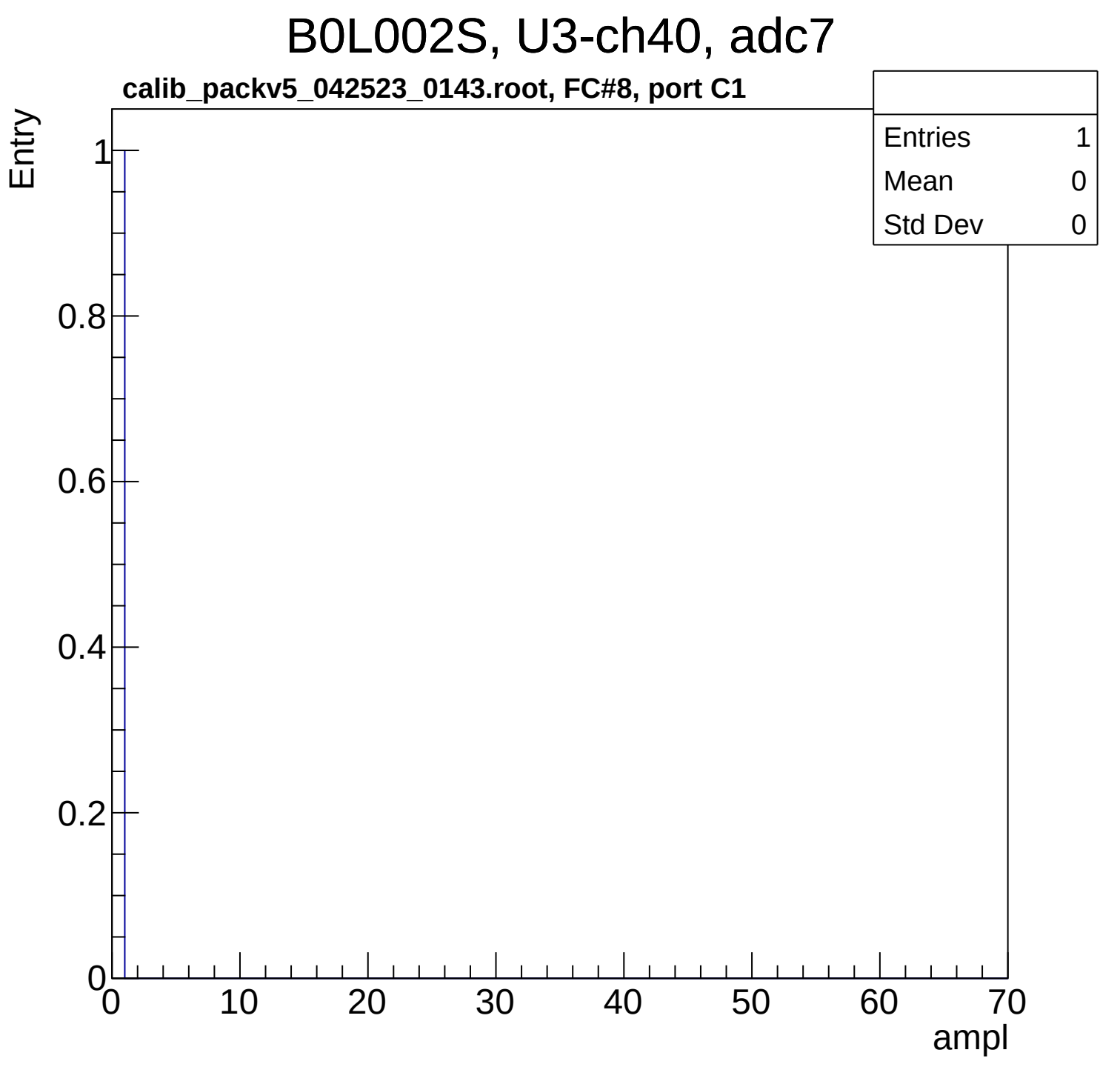
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch40, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.31
Std Dev 2.83
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch40, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.11
Std Dev 10.39
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch40, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch40, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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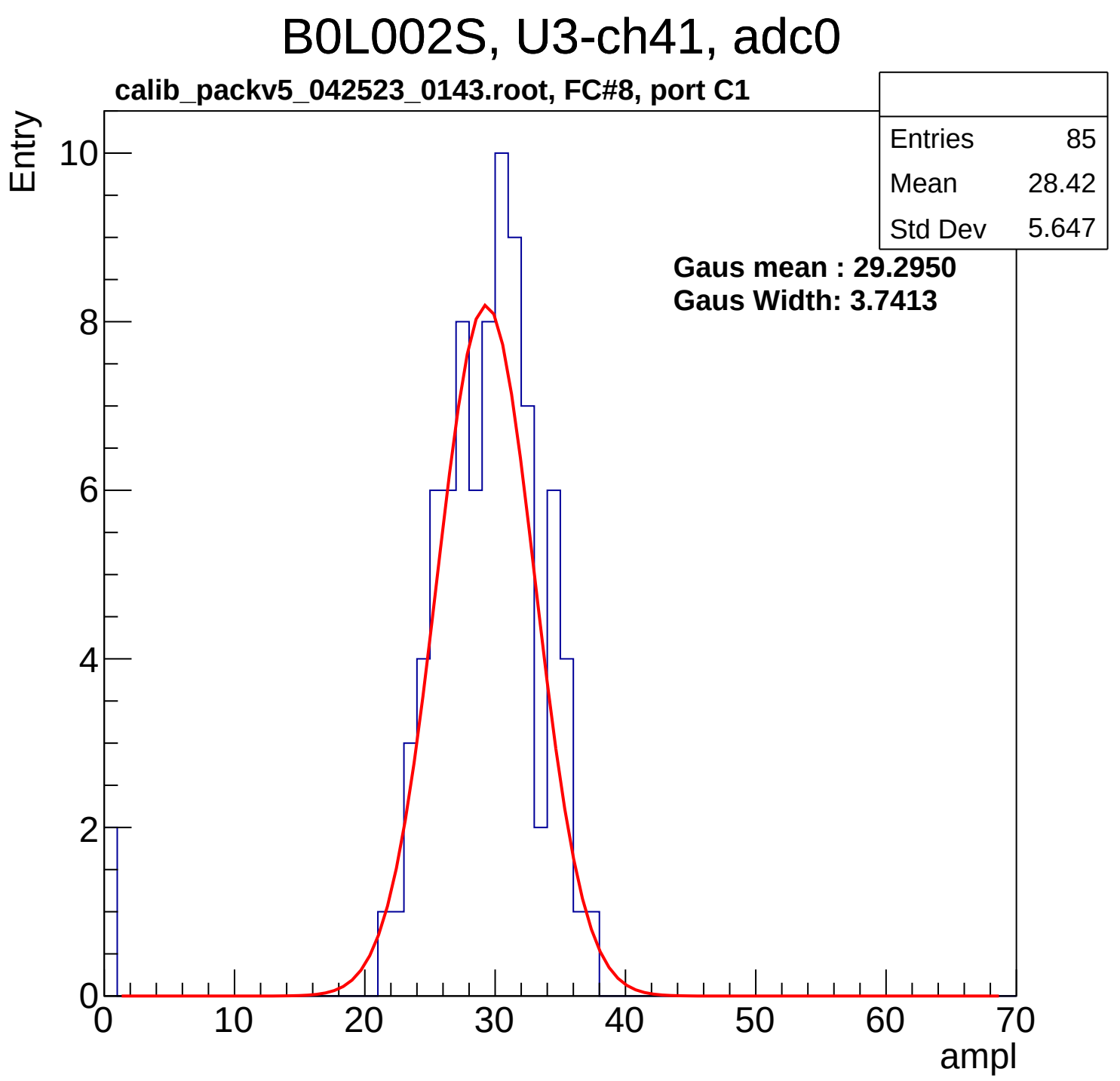
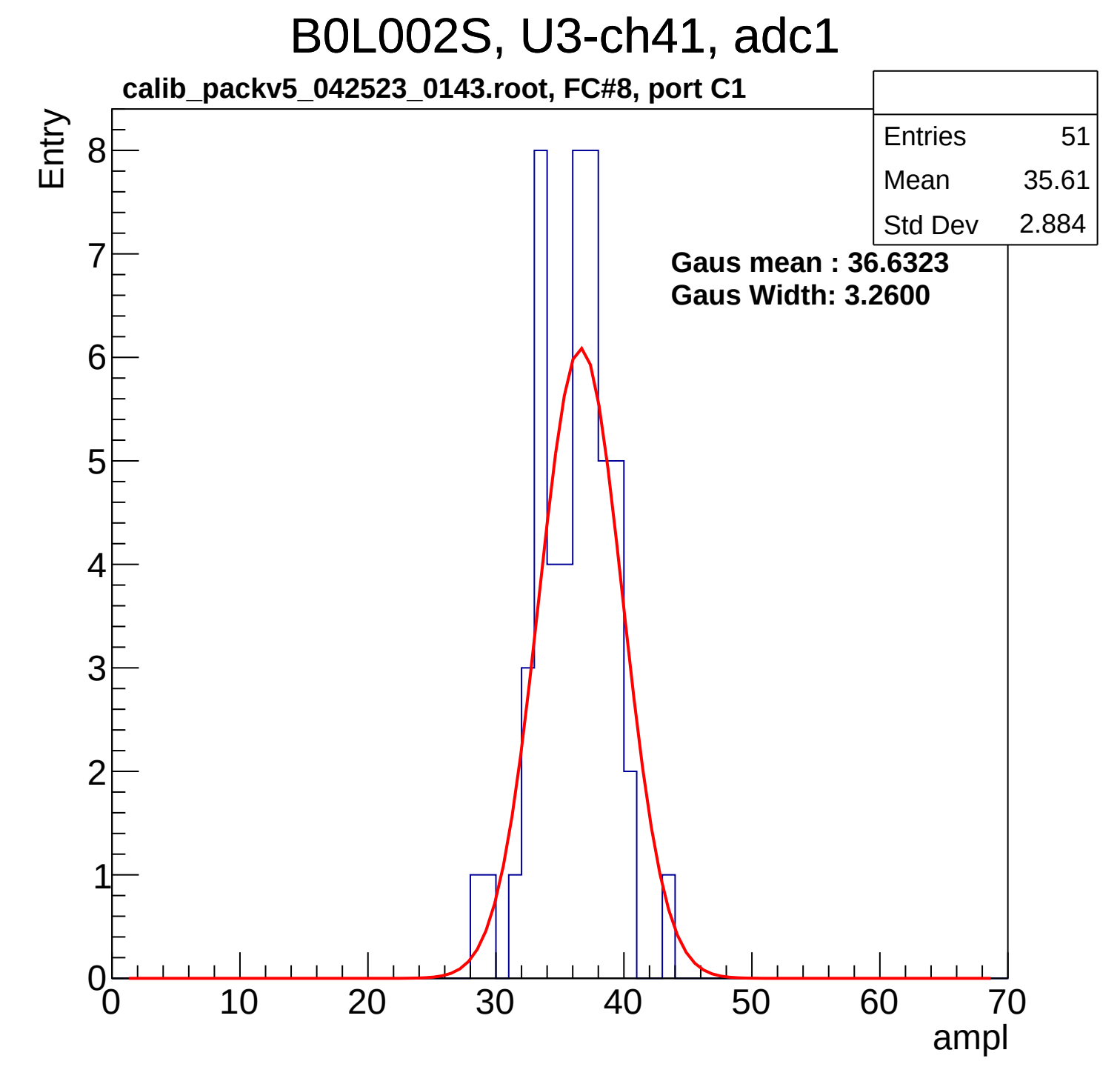
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch41, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 28.42
Std Dev 5.647
Gaus mean : 29.2950
Gaus Width: 3.7413
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch41, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 35.61
Std Dev 2.884
Gaus mean : 36.6323
Gaus Width: 3.2600
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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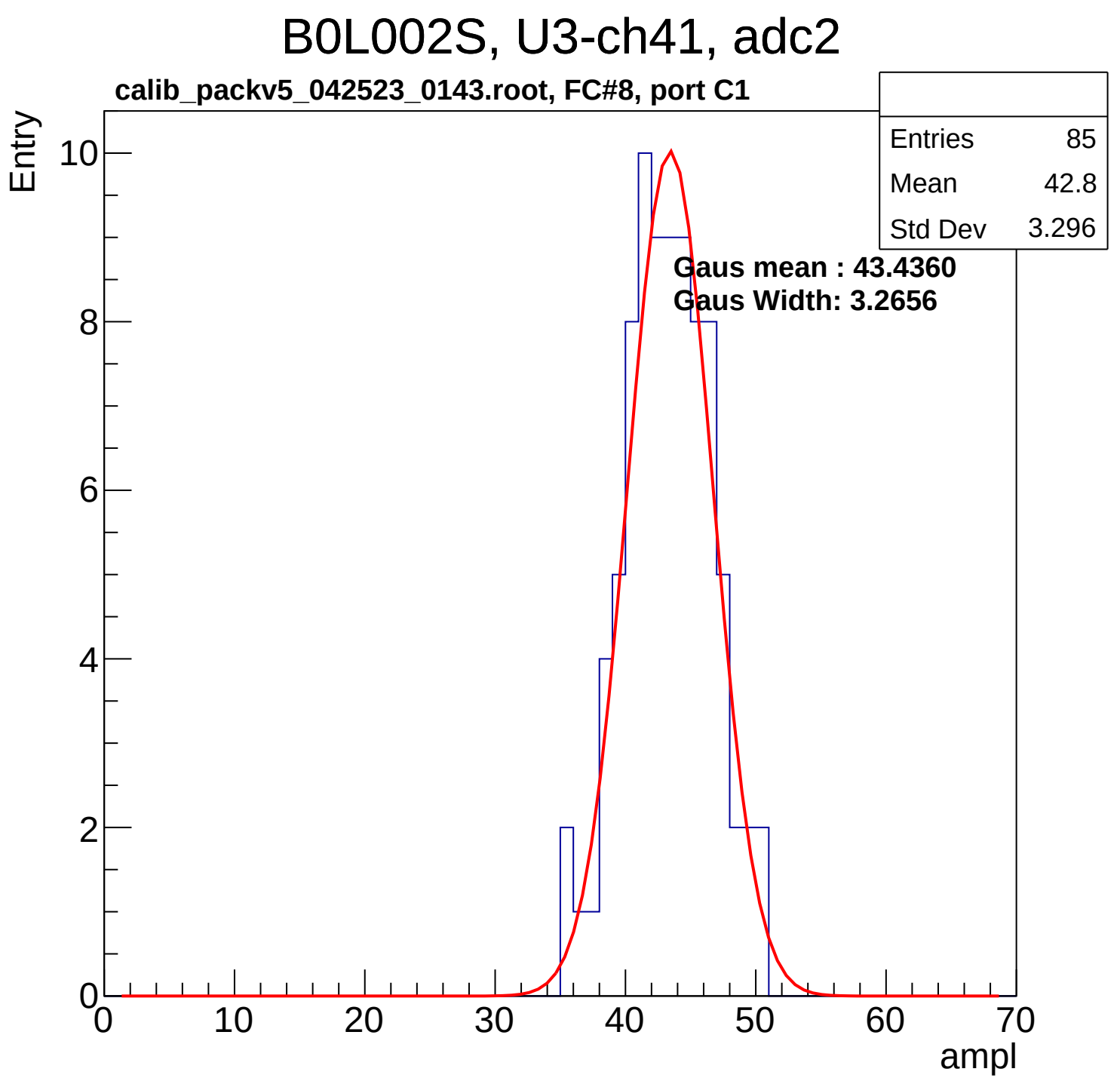
40
50
60
70


B0L002S, U3-ch41, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 42.8
Std Dev 3.296
Gaus mean : 43.4360
Gaus Width: 3.2656
Entry
ampl
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4
6
8
10
0
10
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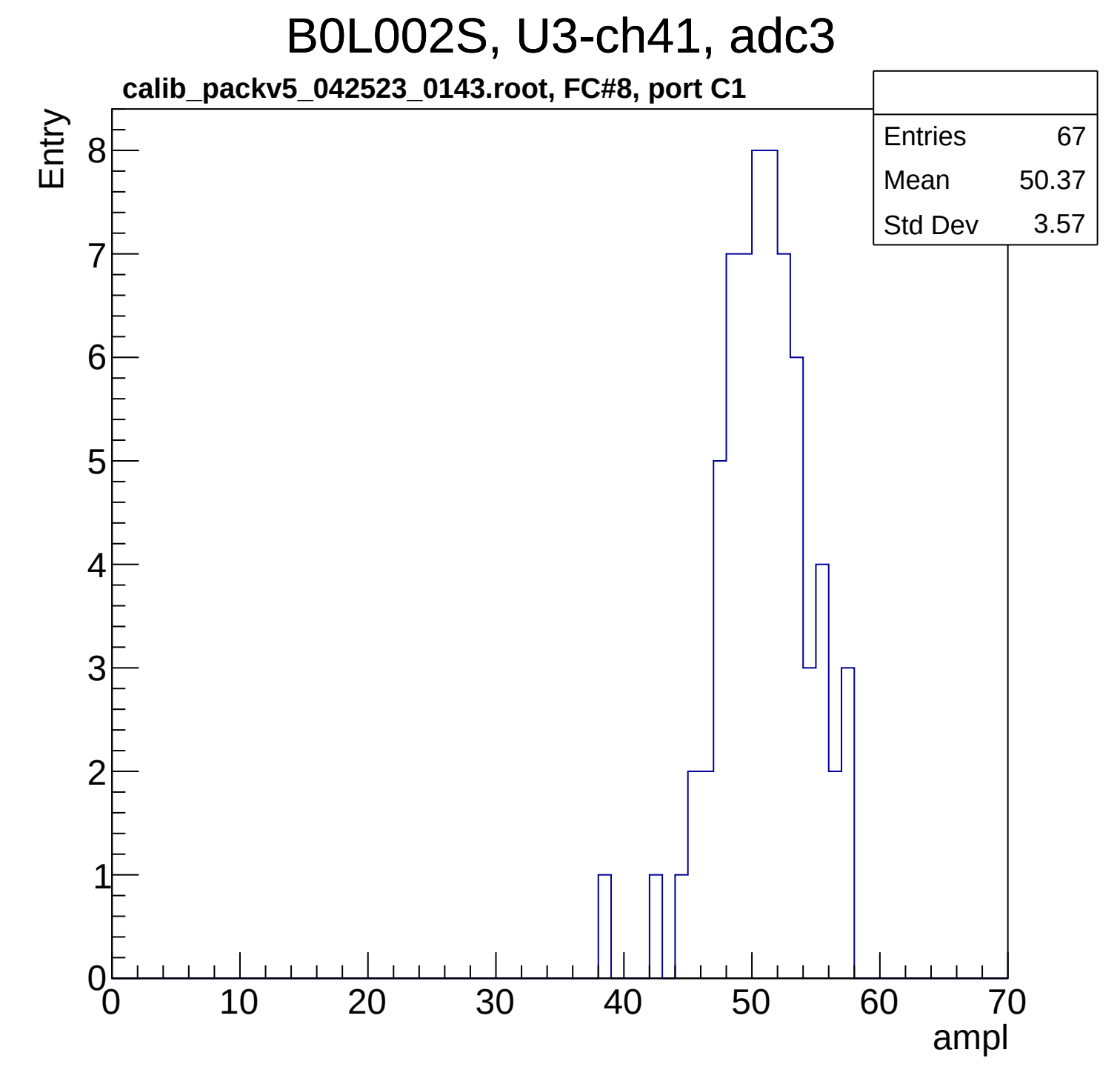
40
50
60
70


B0L002S, U3-ch41, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.37
Std Dev 3.57
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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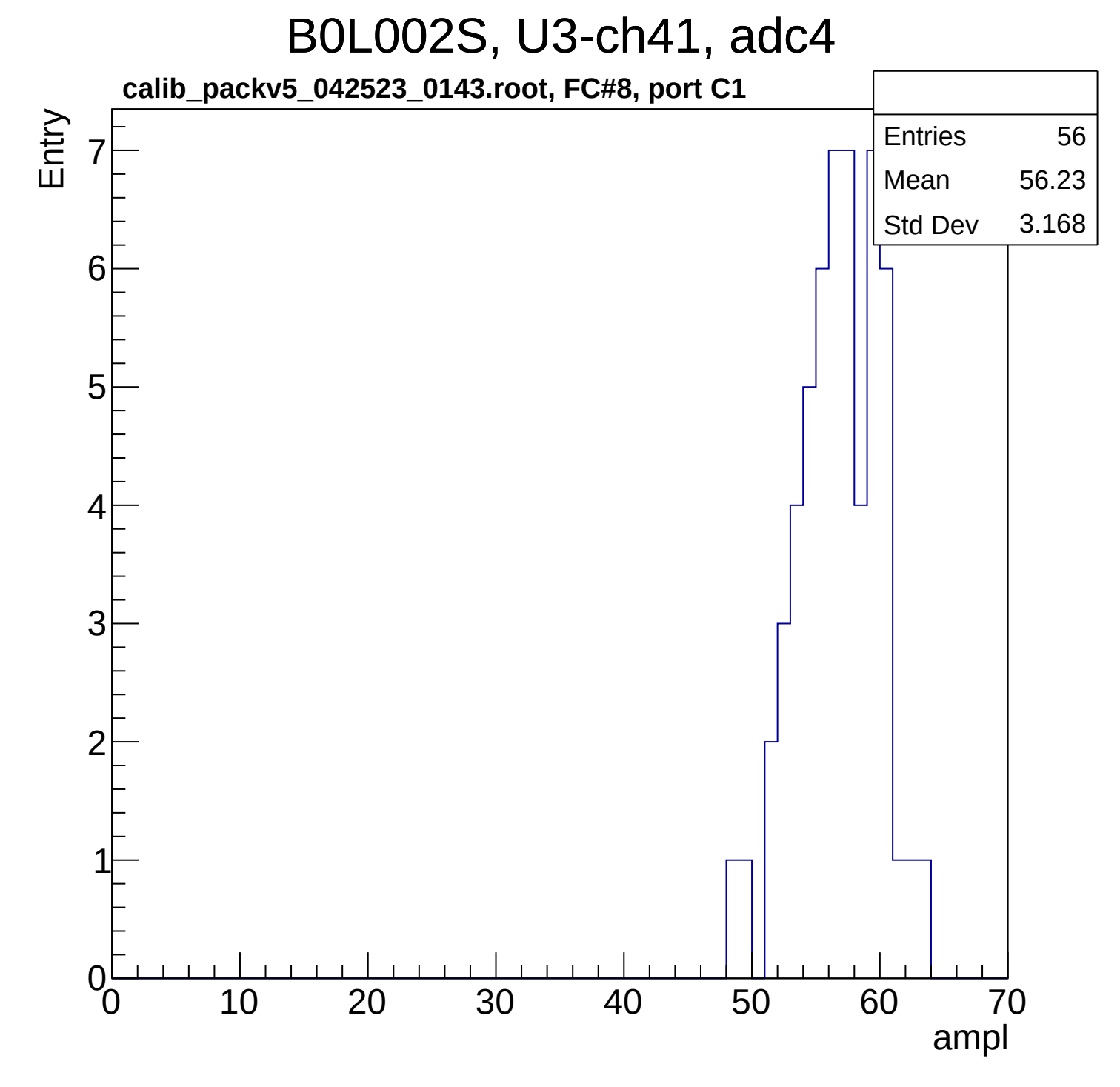
40
50
60
70


B0L002S, U3-ch41, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 56.23
Std Dev 3.168
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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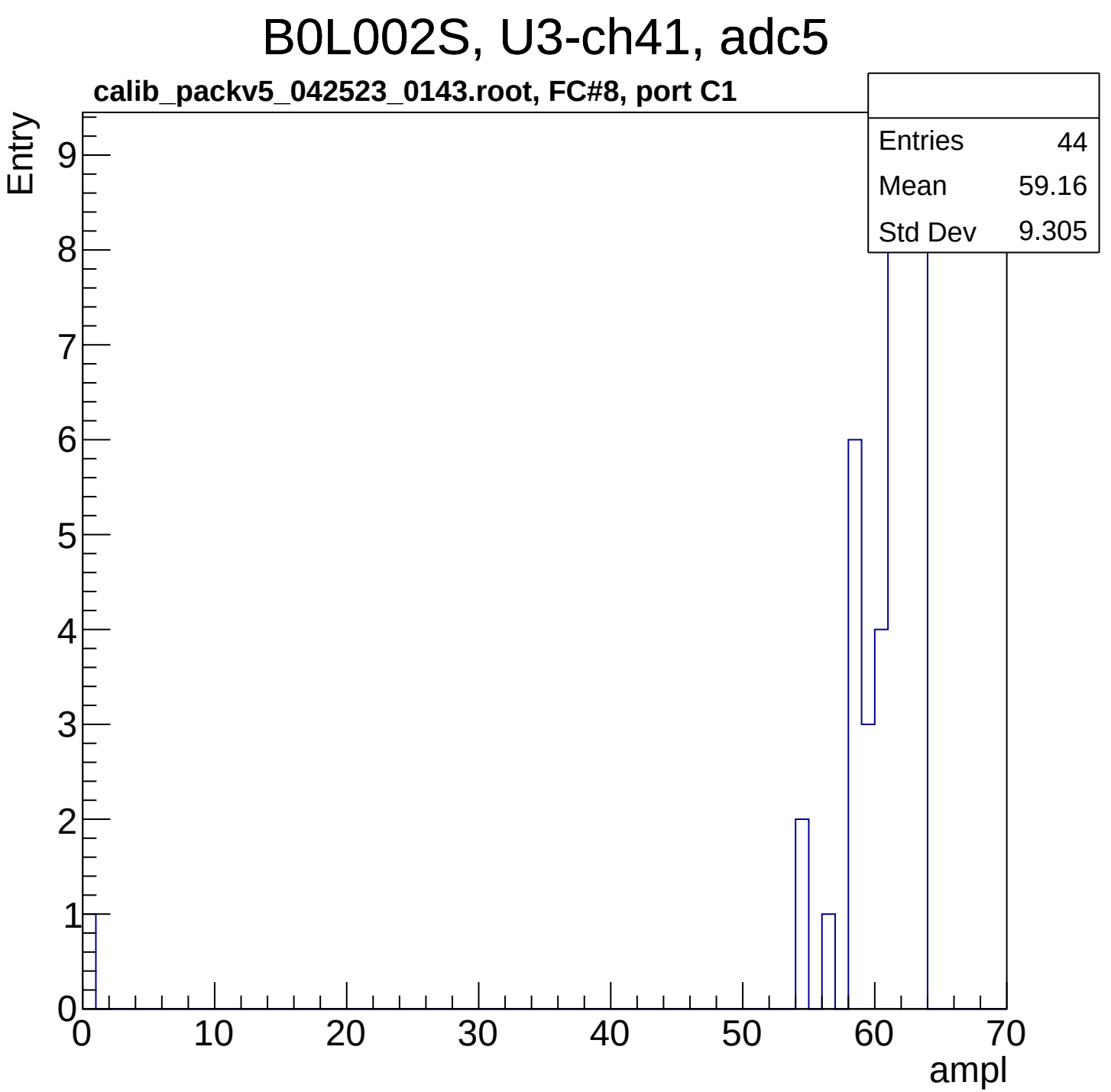
B0L002S, U3-ch41, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.16
Std Dev 9.305
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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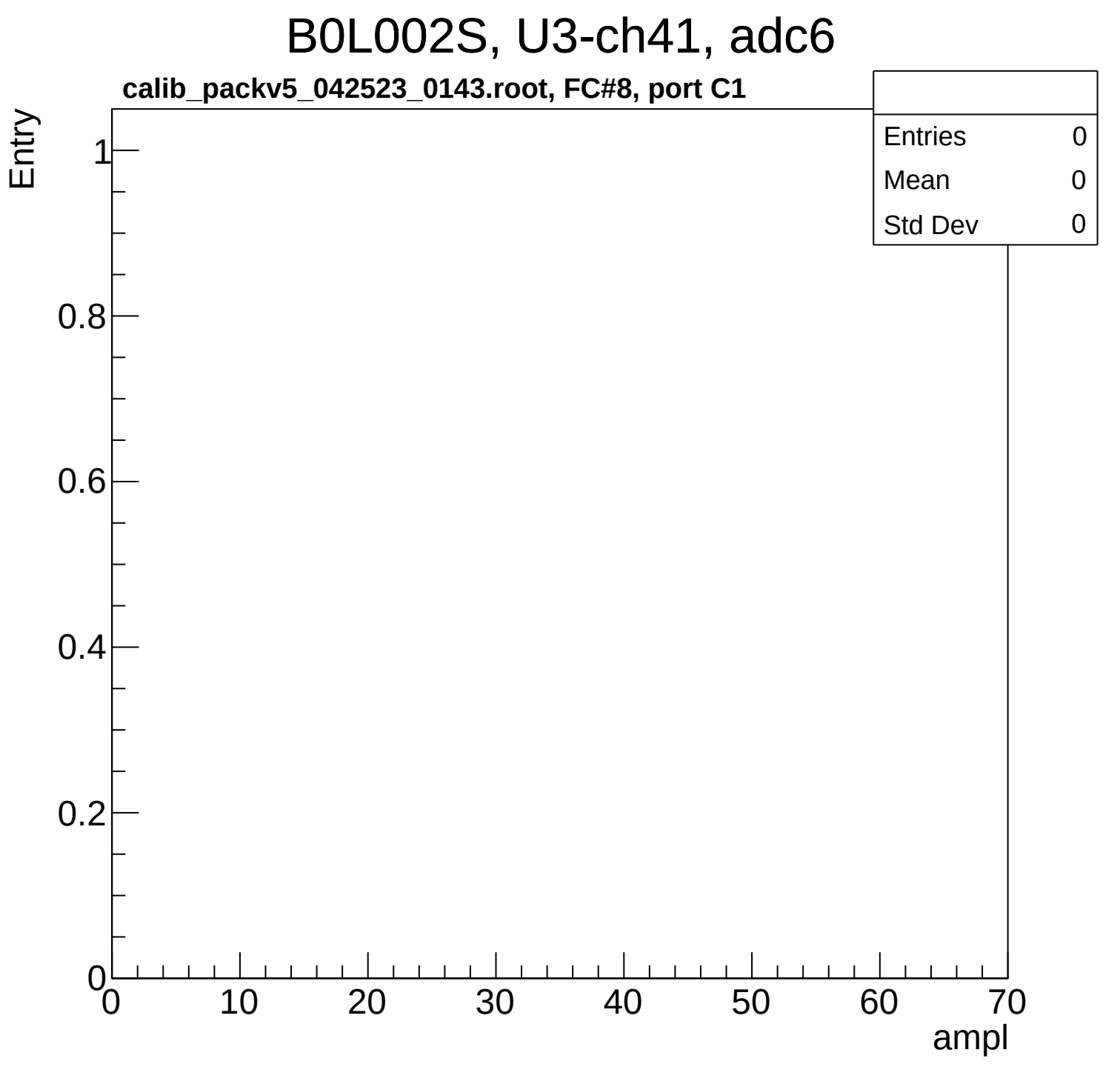
B0L002S, U3-ch41, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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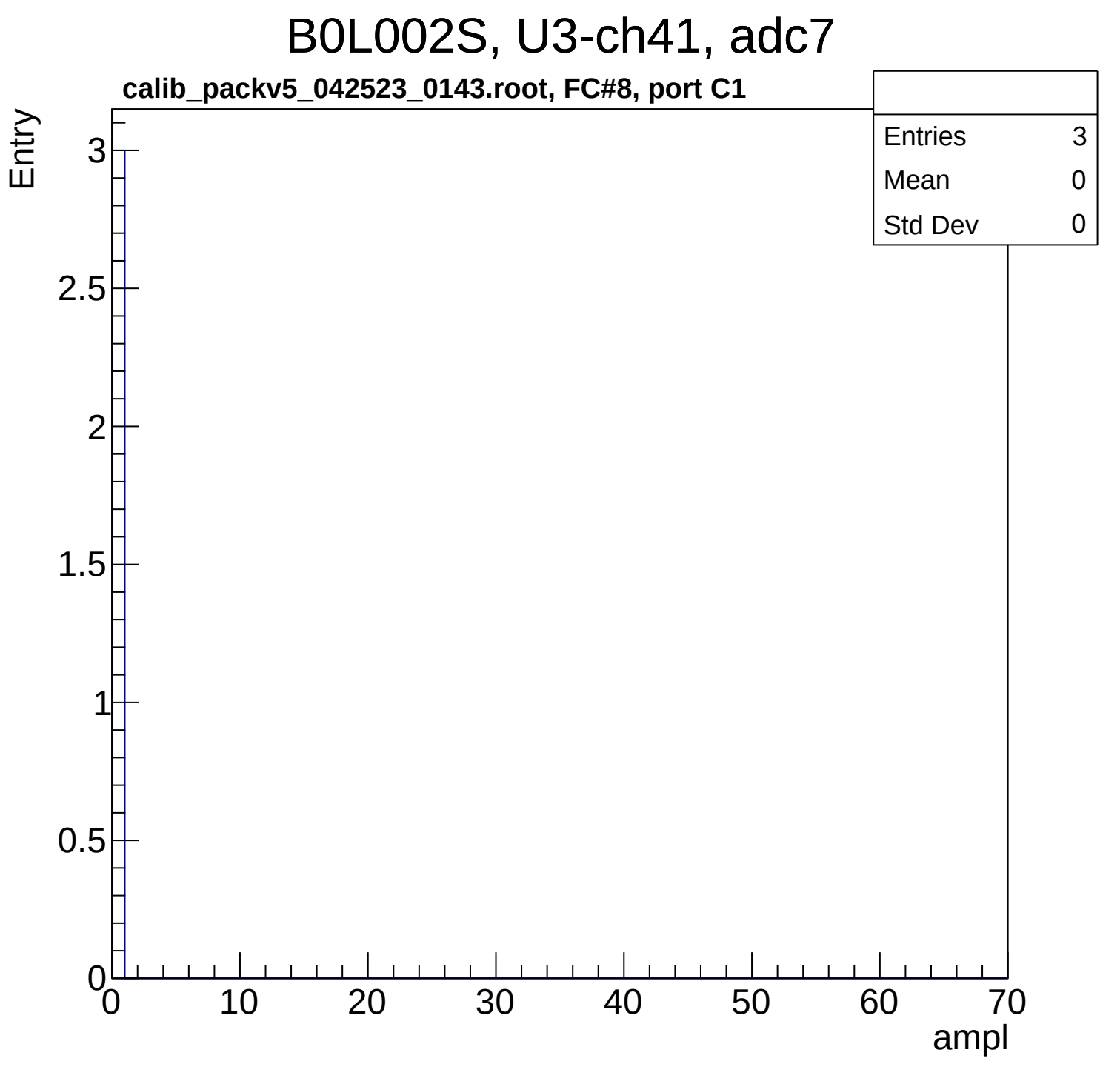
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch41, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
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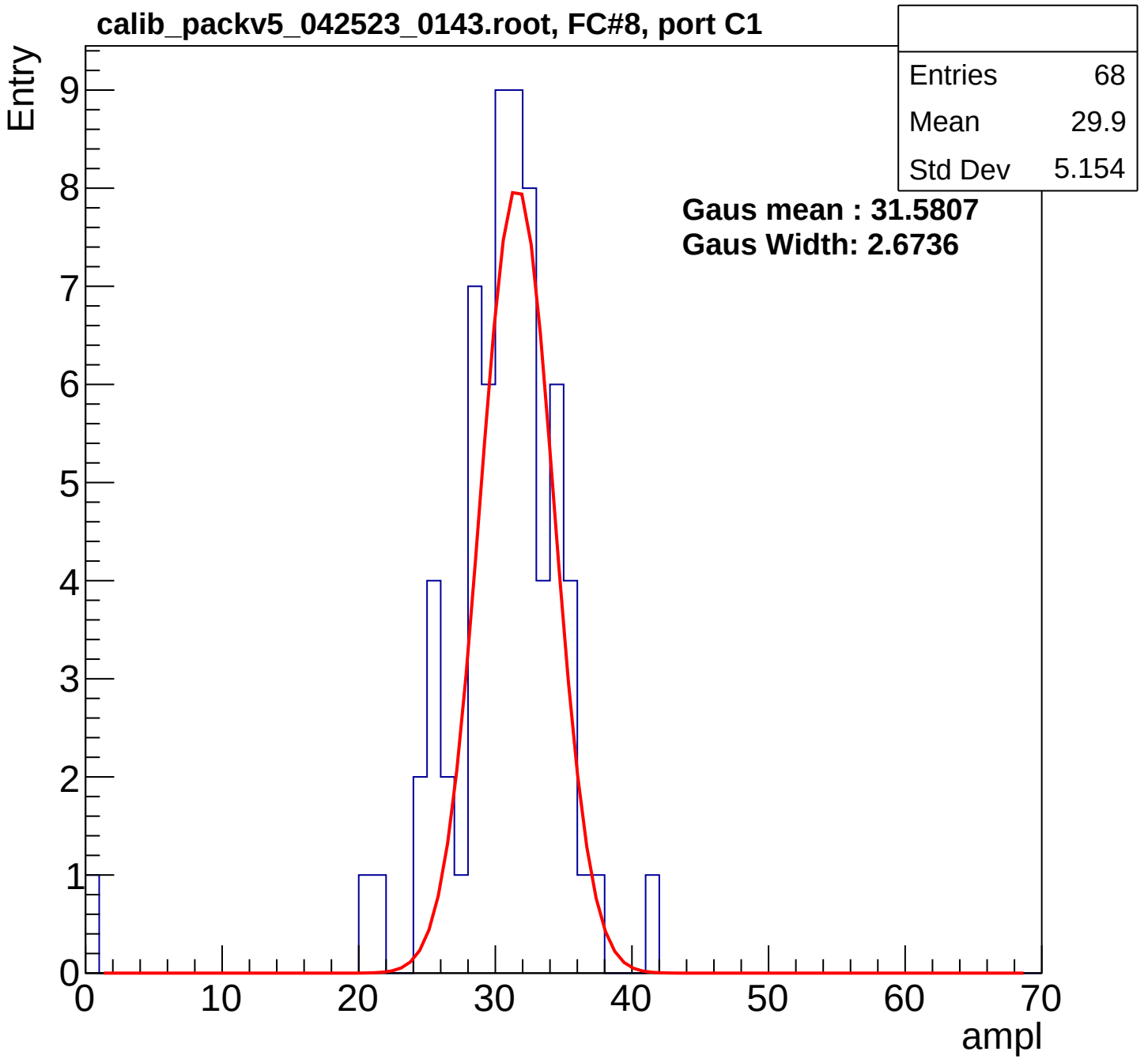
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch42, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.9
Std Dev 5.154
Gaus mean : 31.5807
Gaus Width: 2.6736
Entry
ampl
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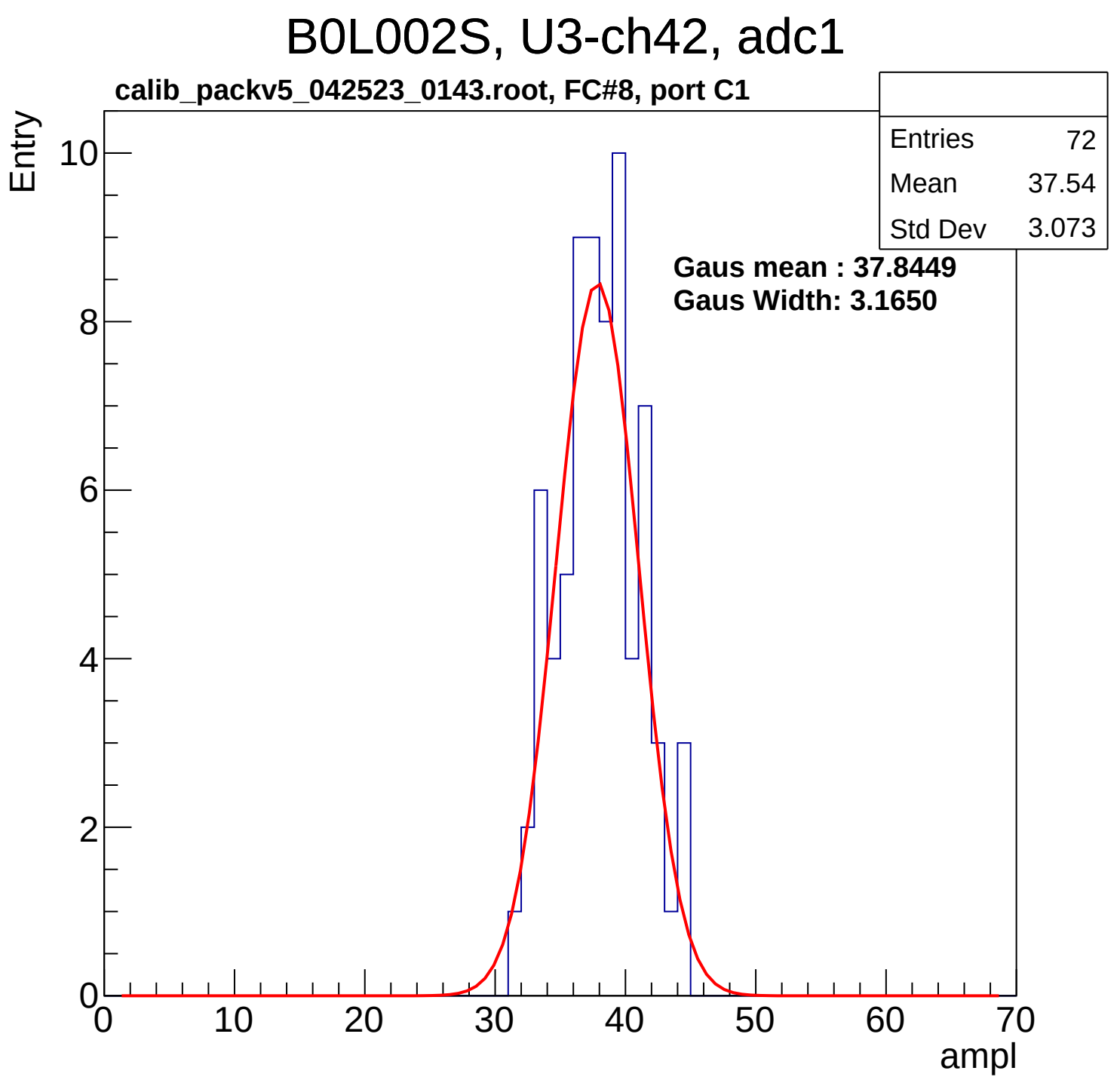
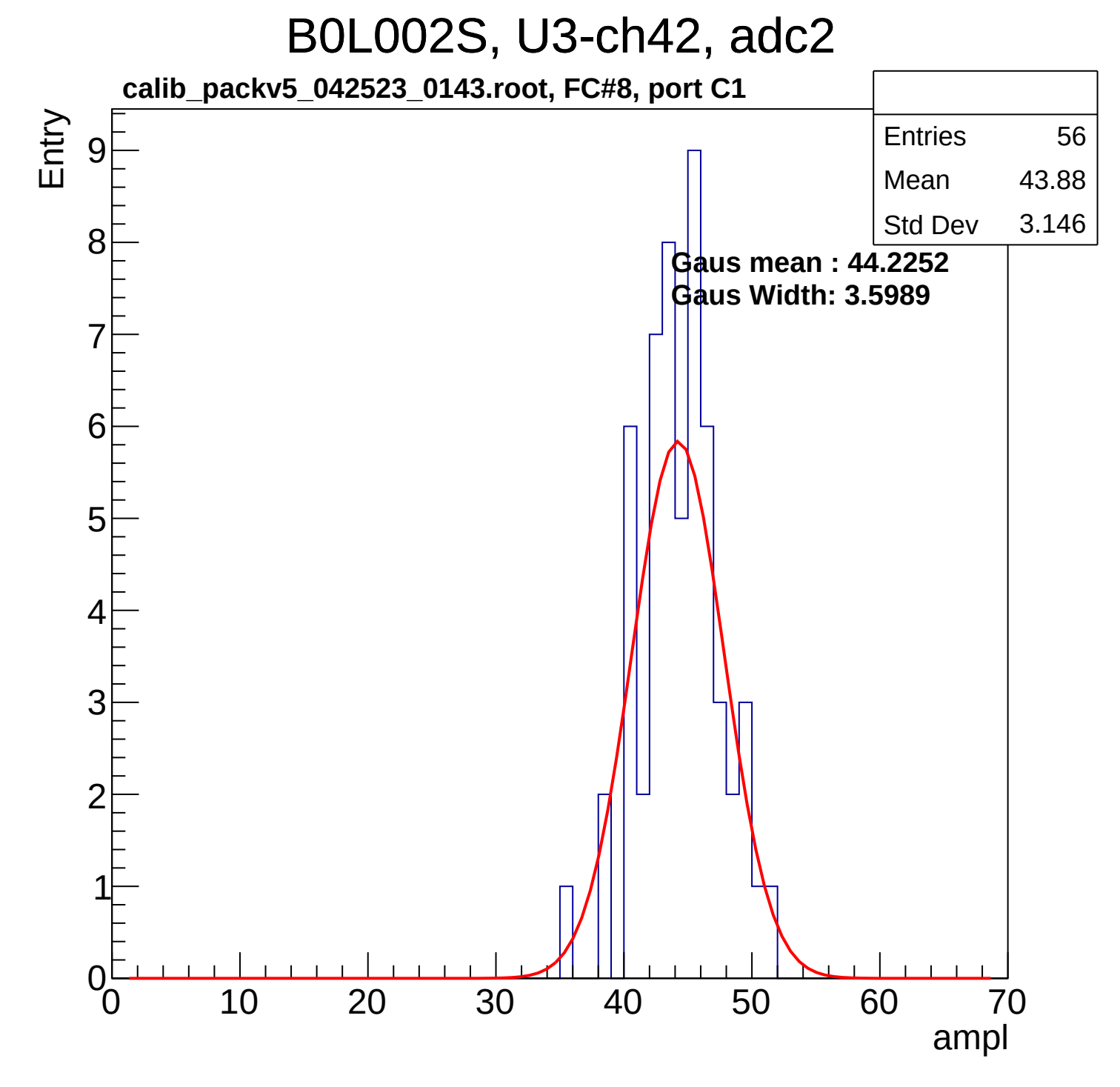
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch42, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.54
Std Dev 3.073
Gaus mean : 37.8449
Gaus Width: 3.1650
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch42, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 43.88
Std Dev 3.146
Gaus mean : 44.2252
Gaus Width: 3.5989
Entry
ampl
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2
3
4
5
6
7
8
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0
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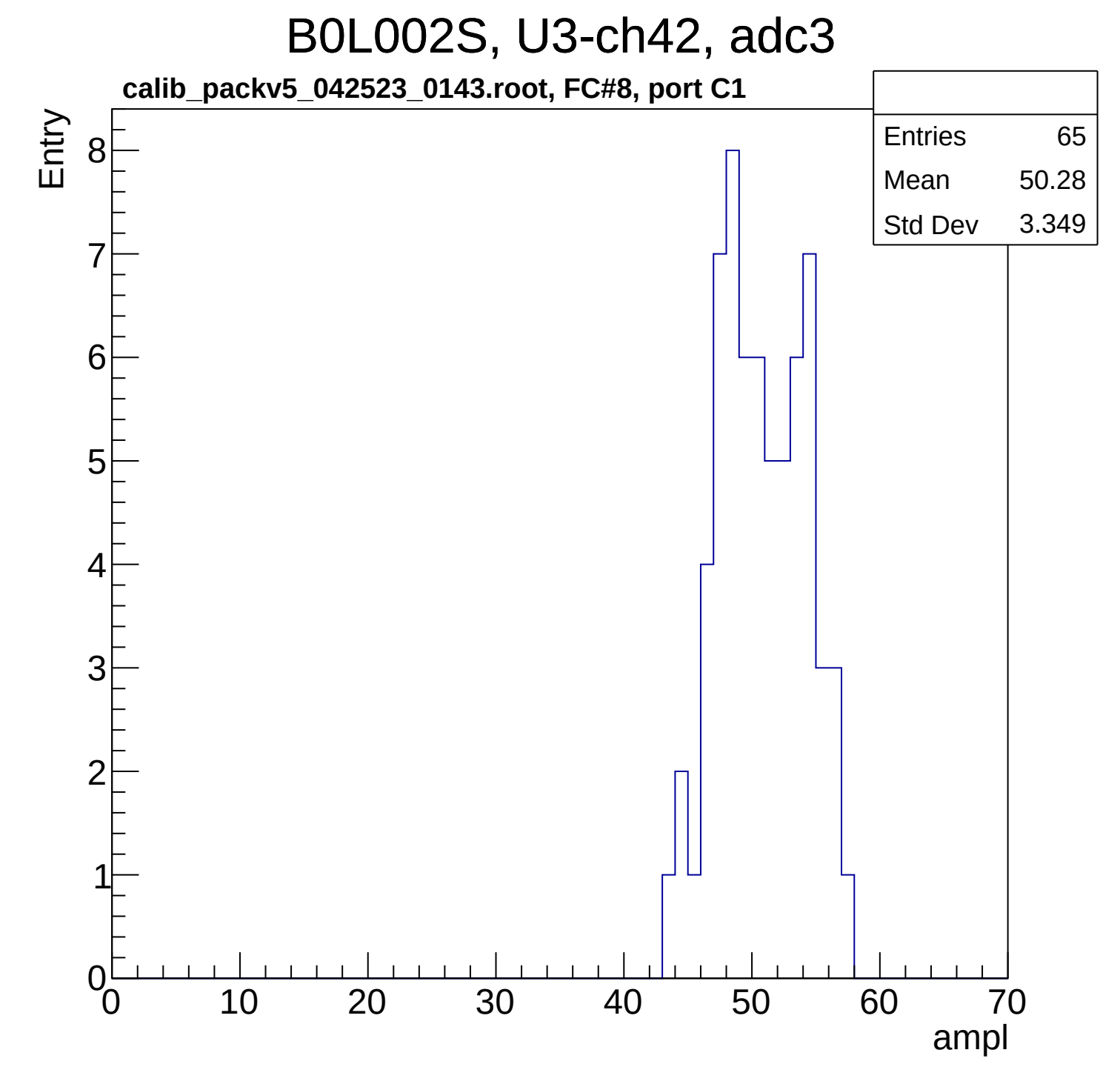
B0L002S, U3-ch42, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.28
Std Dev 3.349
Entry
ampl
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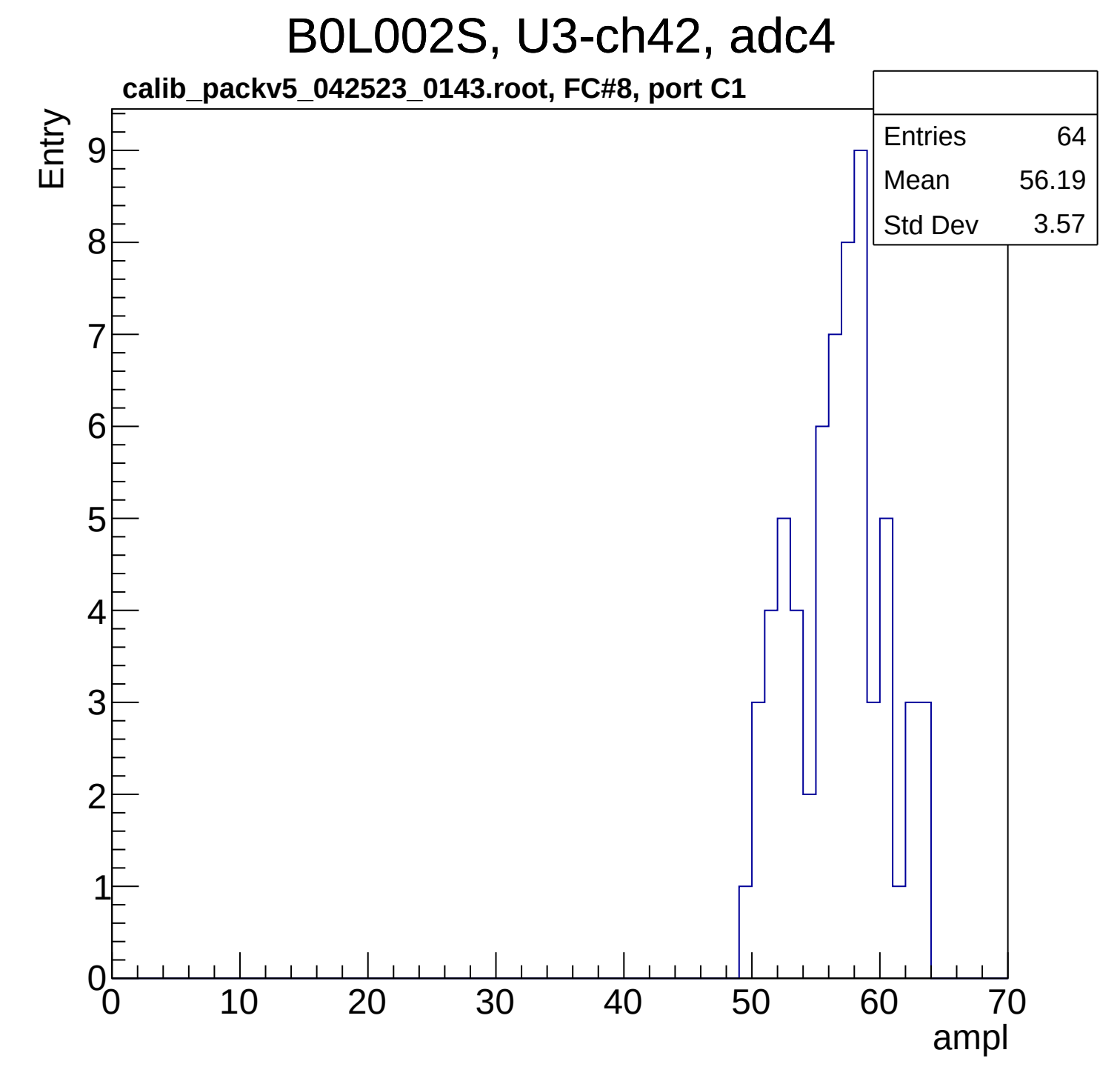
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch42, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.19
Std Dev 3.57
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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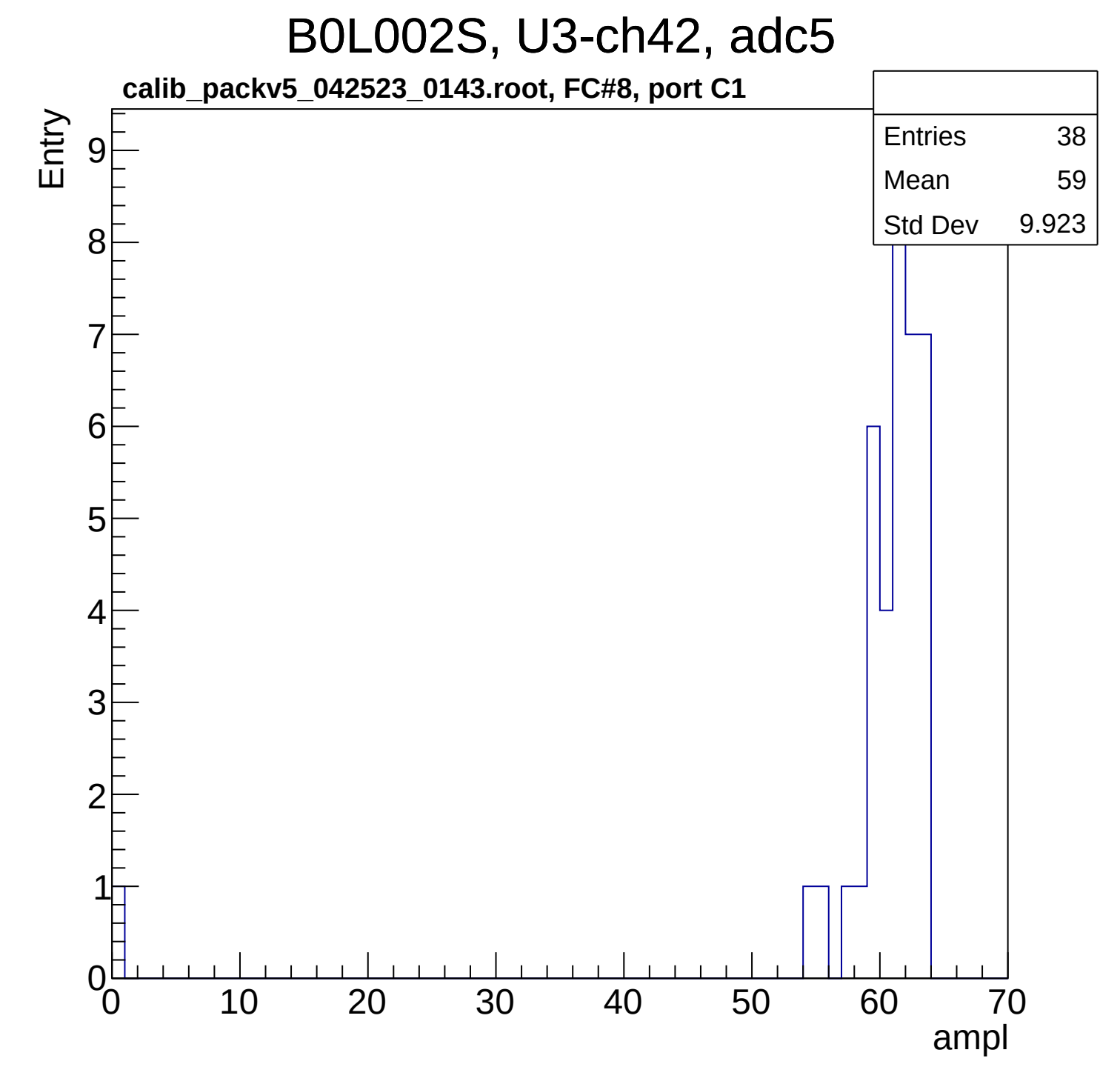
B0L002S, U3-ch42, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59
Std Dev 9.923
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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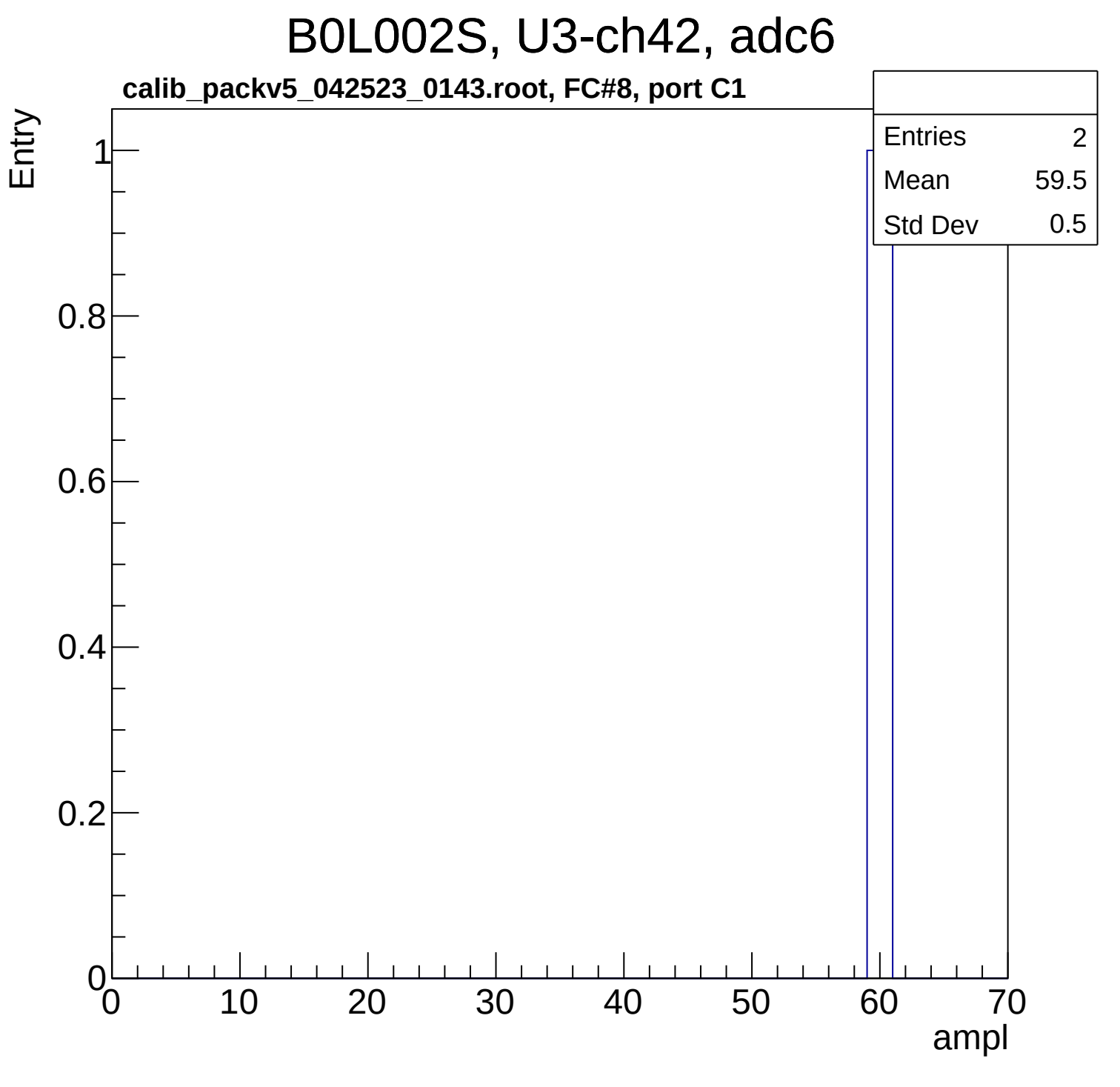
B0L002S, U3-ch42, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 59.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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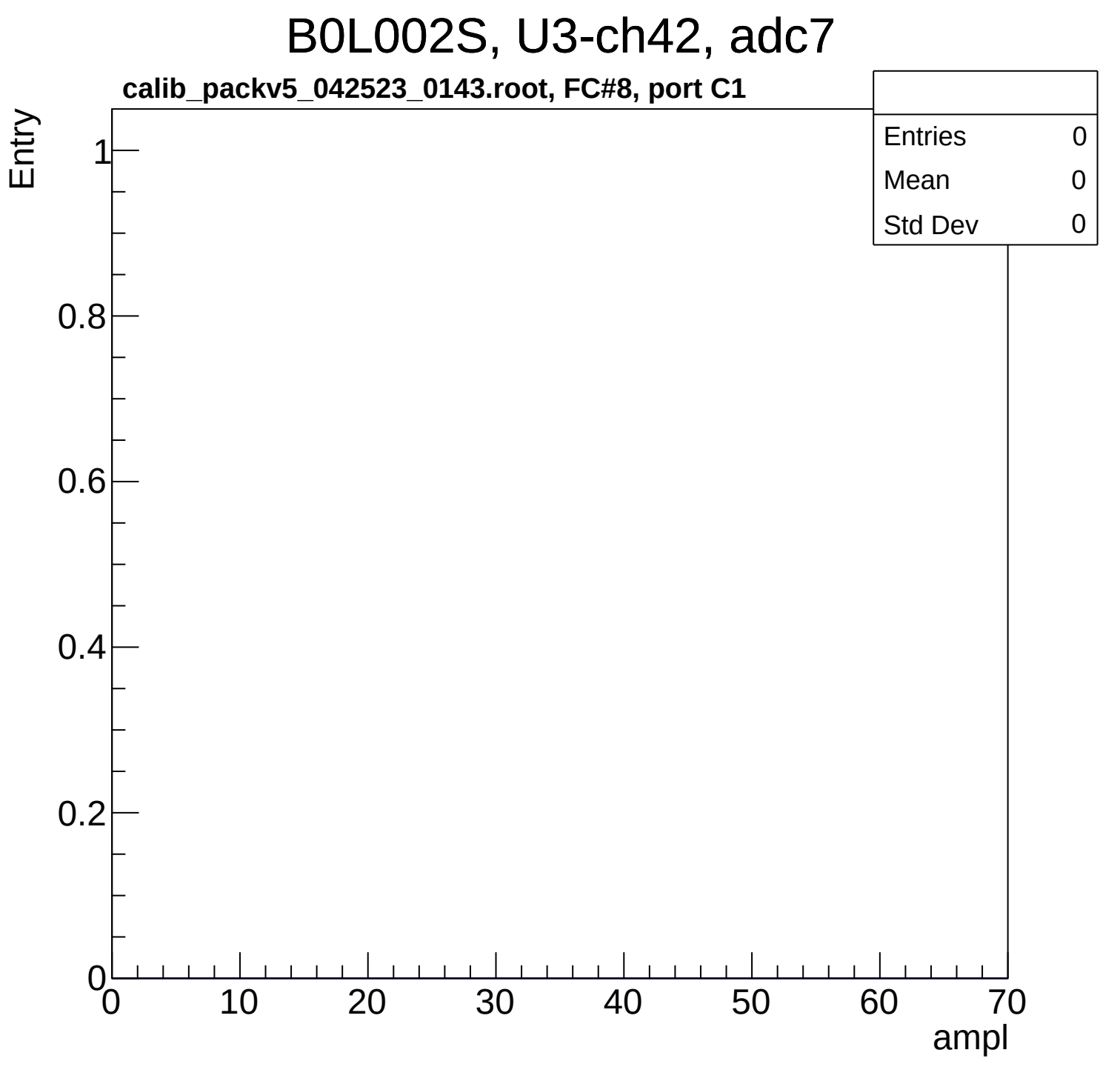
50
60
70
ampl


B0L002S, U3-ch42, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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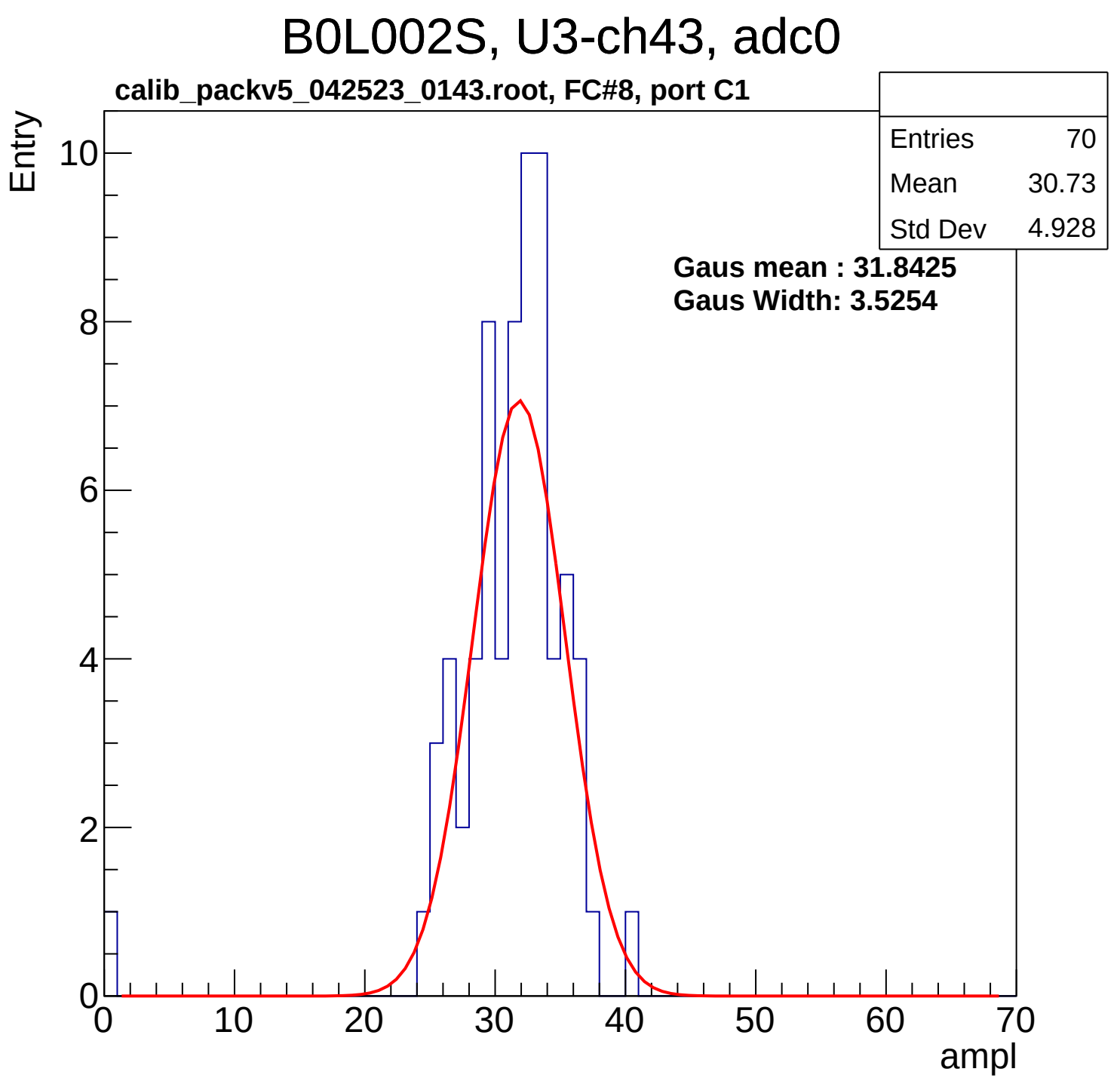
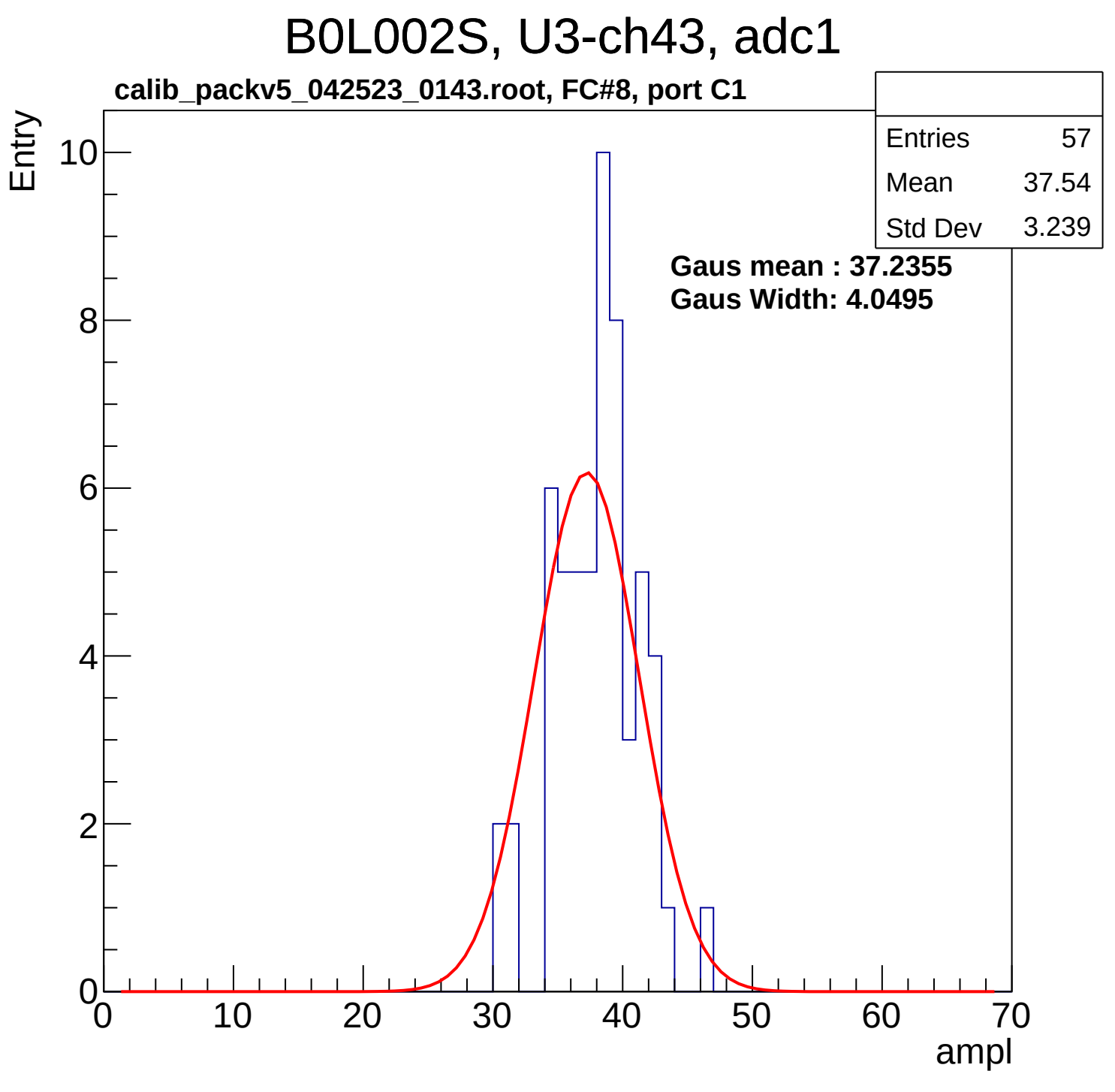
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch43, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 30.73
Std Dev 4.928
Gaus mean : 31.8425
Gaus Width: 3.5254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch43, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 37.54
Std Dev 3.239
Gaus mean : 37.2355
Gaus Width: 4.0495
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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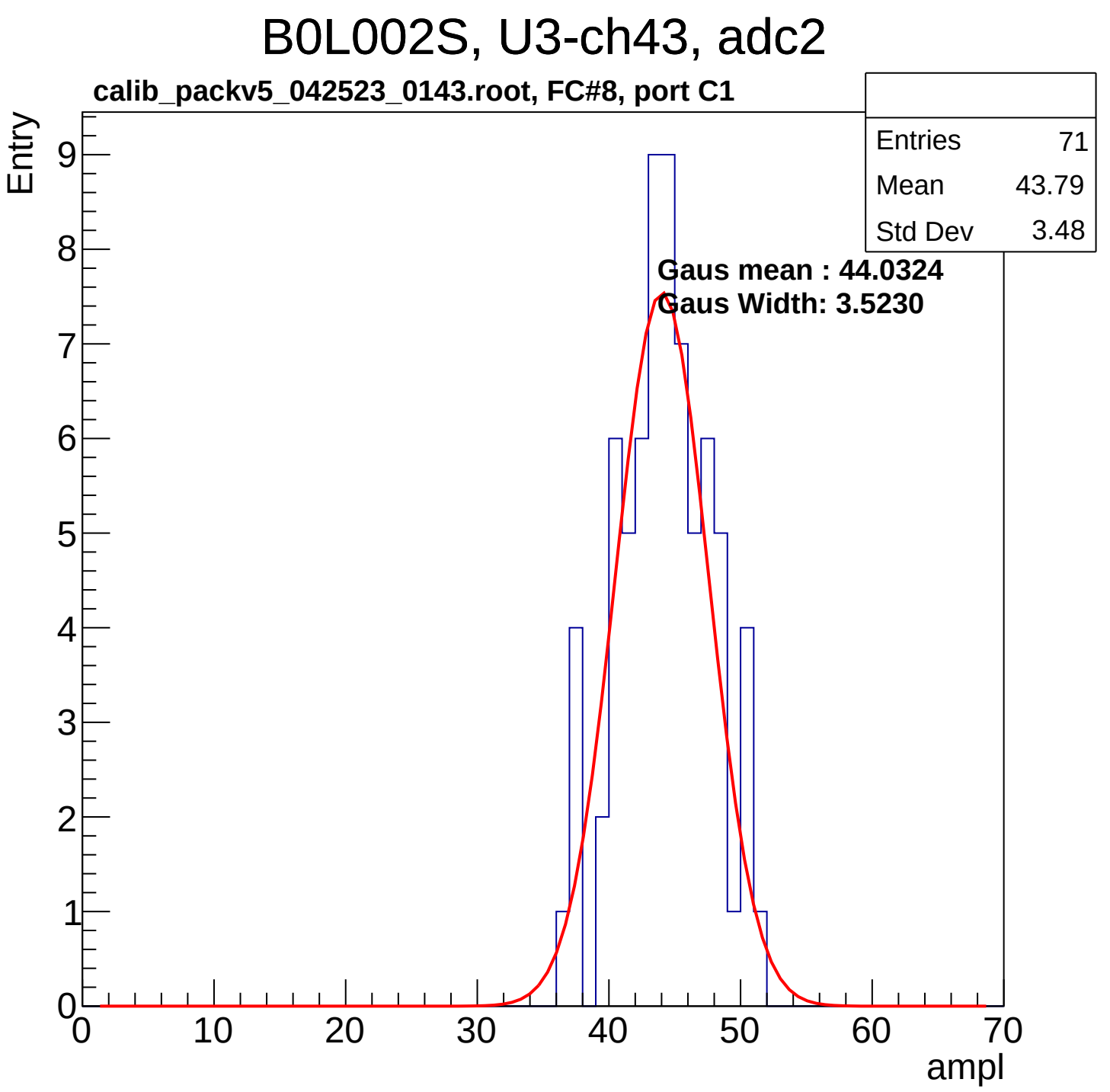
B0L002S, U3-ch43, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 43.79
Std Dev 3.48
Gaus mean : 44.0324
Gaus Width: 3.5230
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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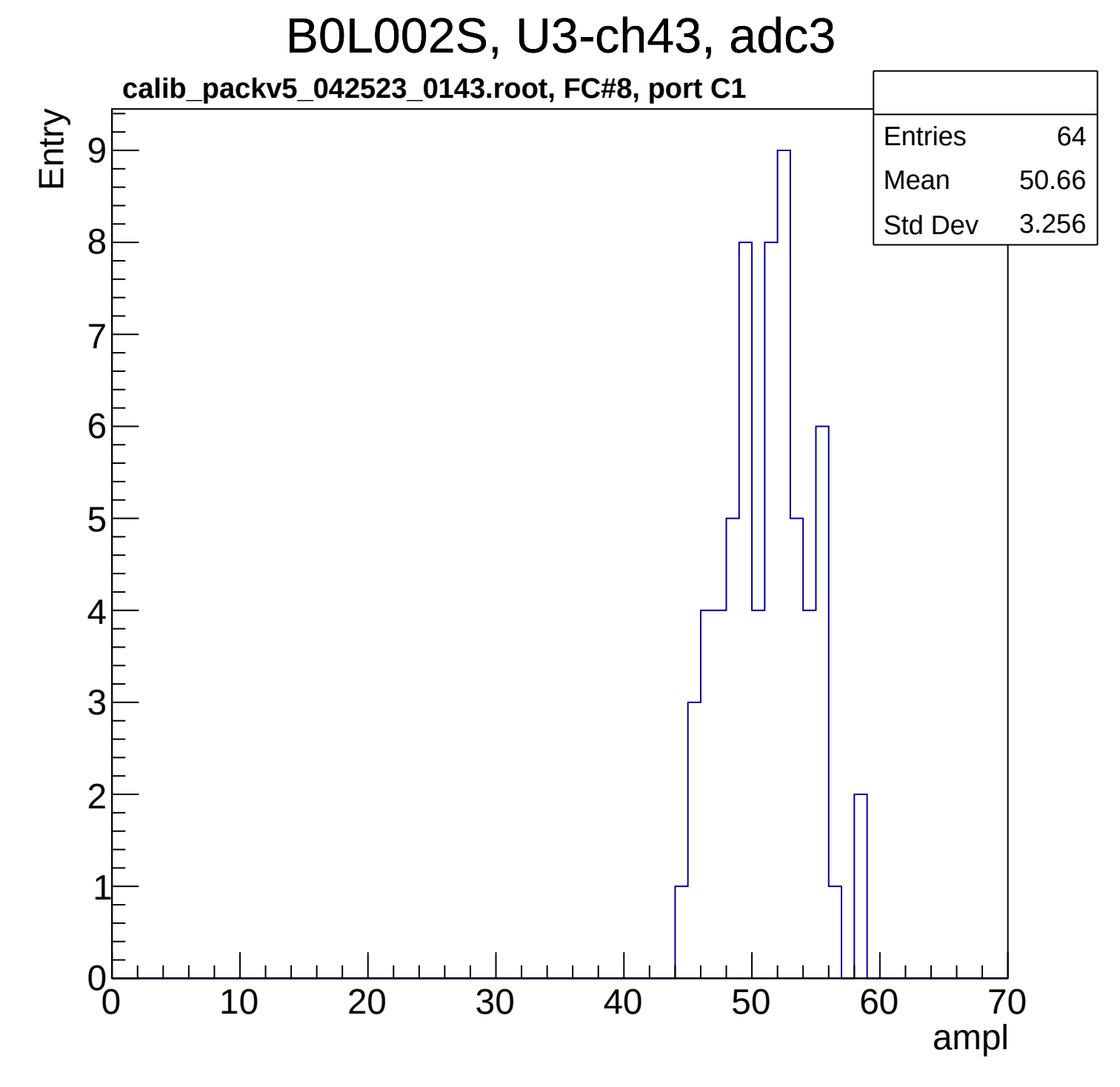
B0L002S, U3-ch43, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.66
Std Dev 3.256
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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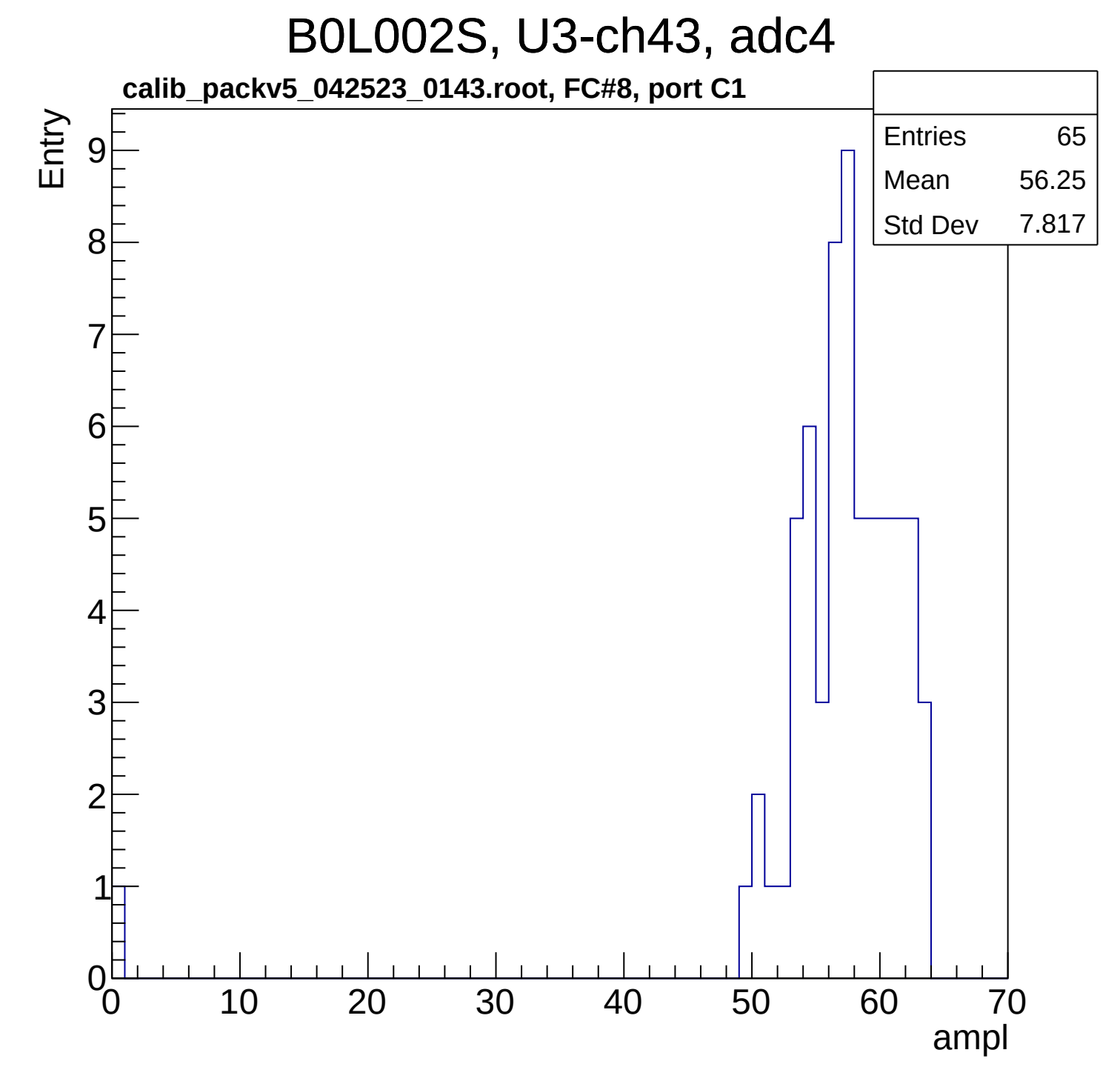
B0L002S, U3-ch43, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.25
Std Dev 7.817
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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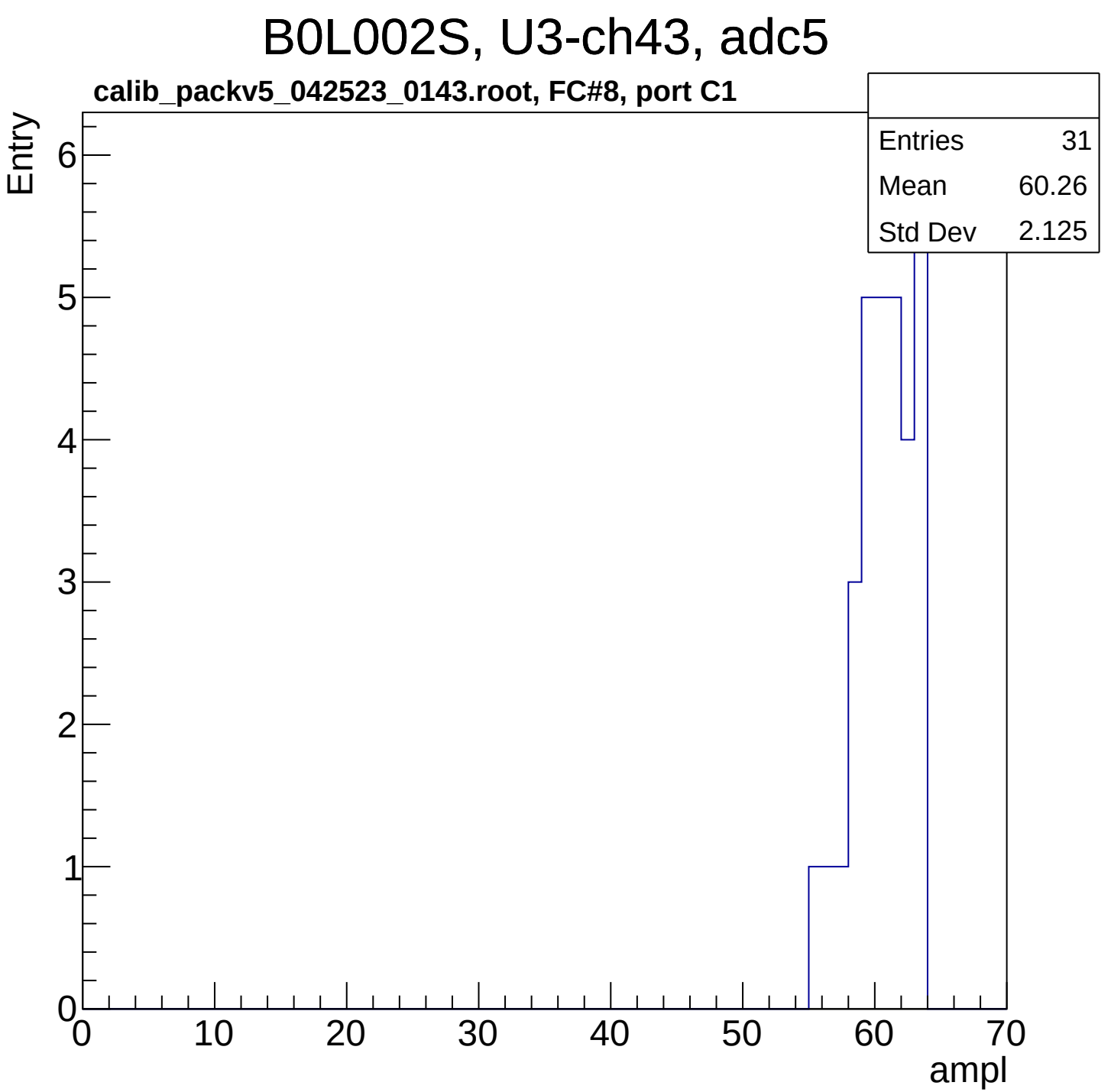
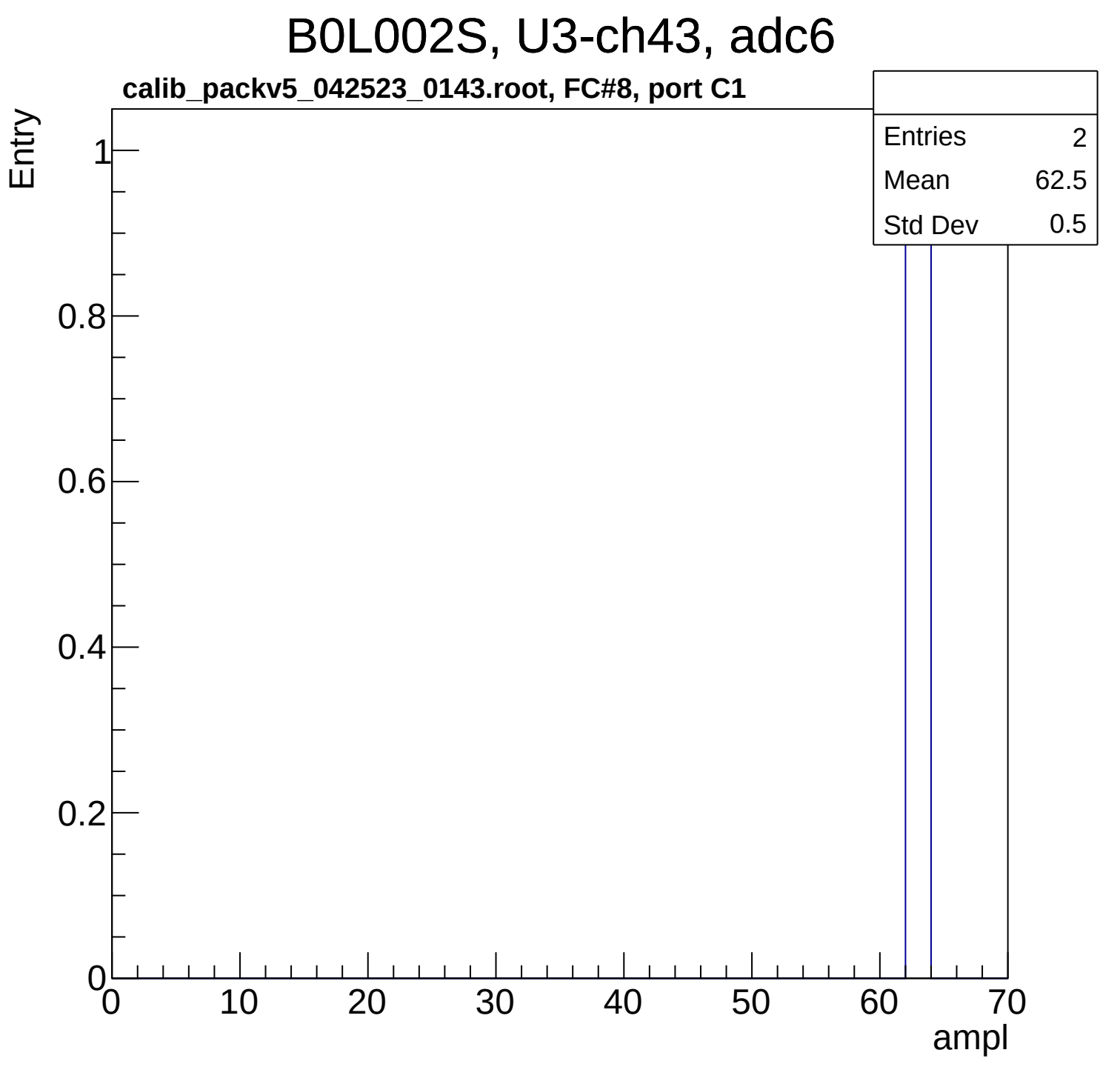
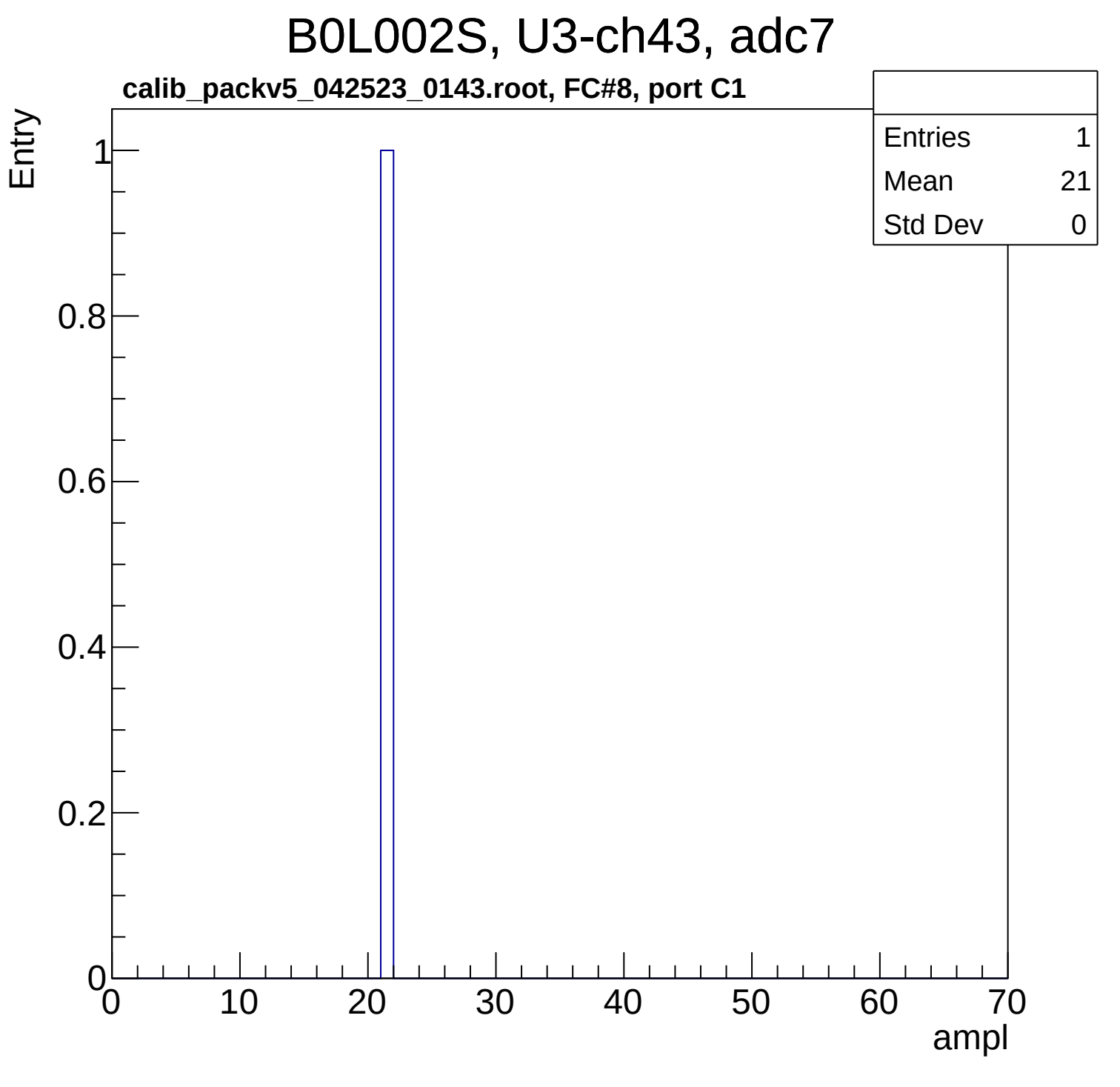

B0L002S, U3-ch43, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 60.26
Std Dev 2.125
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch43, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch43, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
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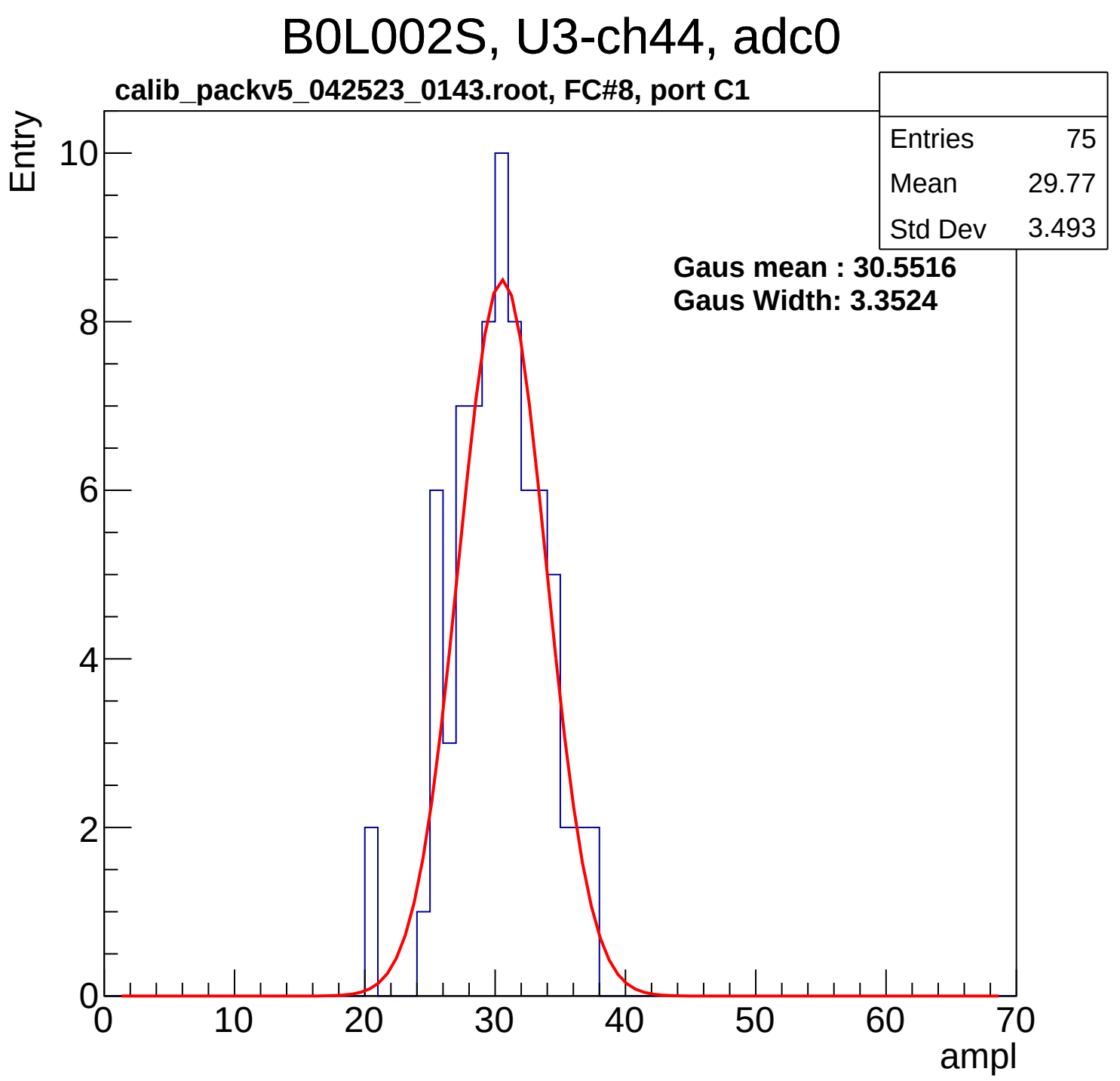
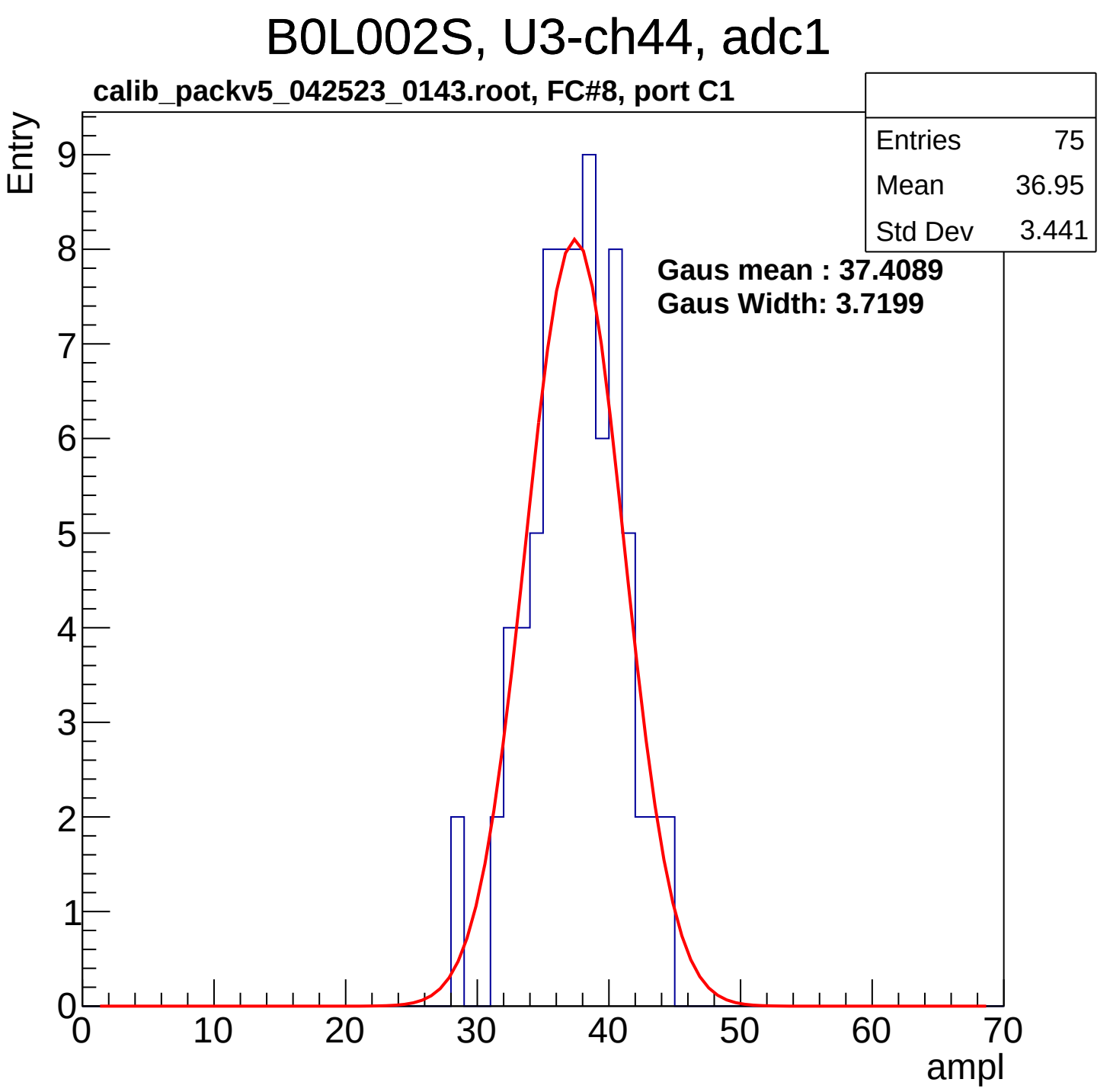
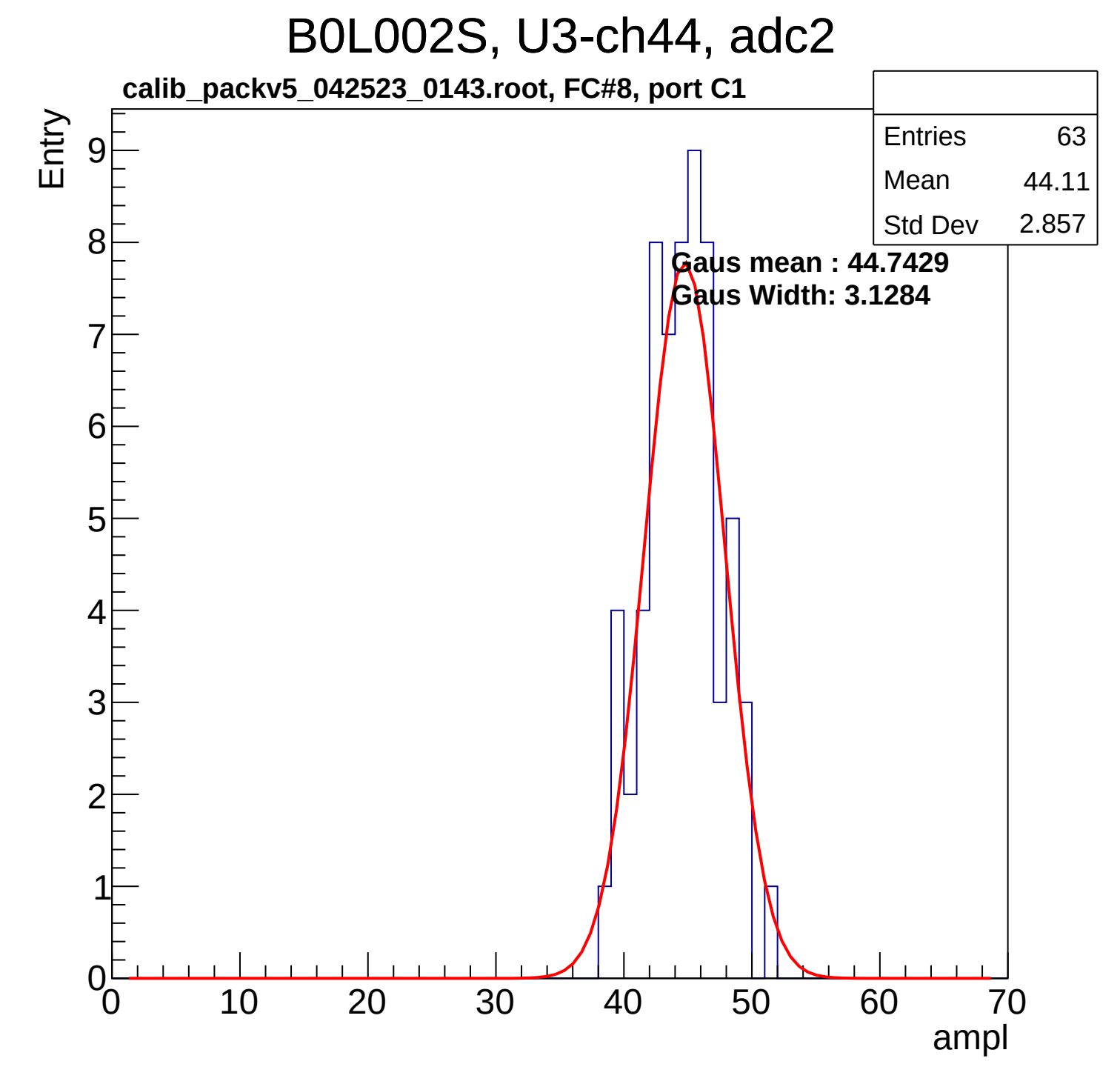
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch44, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.77
Std Dev 3.493
Gaus mean : 30.5516
Gaus Width: 3.3524
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch44, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 36.95
Std Dev 3.441
Gaus mean : 37.4089
Gaus Width: 3.7199
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch44, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.11
Std Dev 2.857
Gaus mean : 44.7429
Gaus Width: 3.1284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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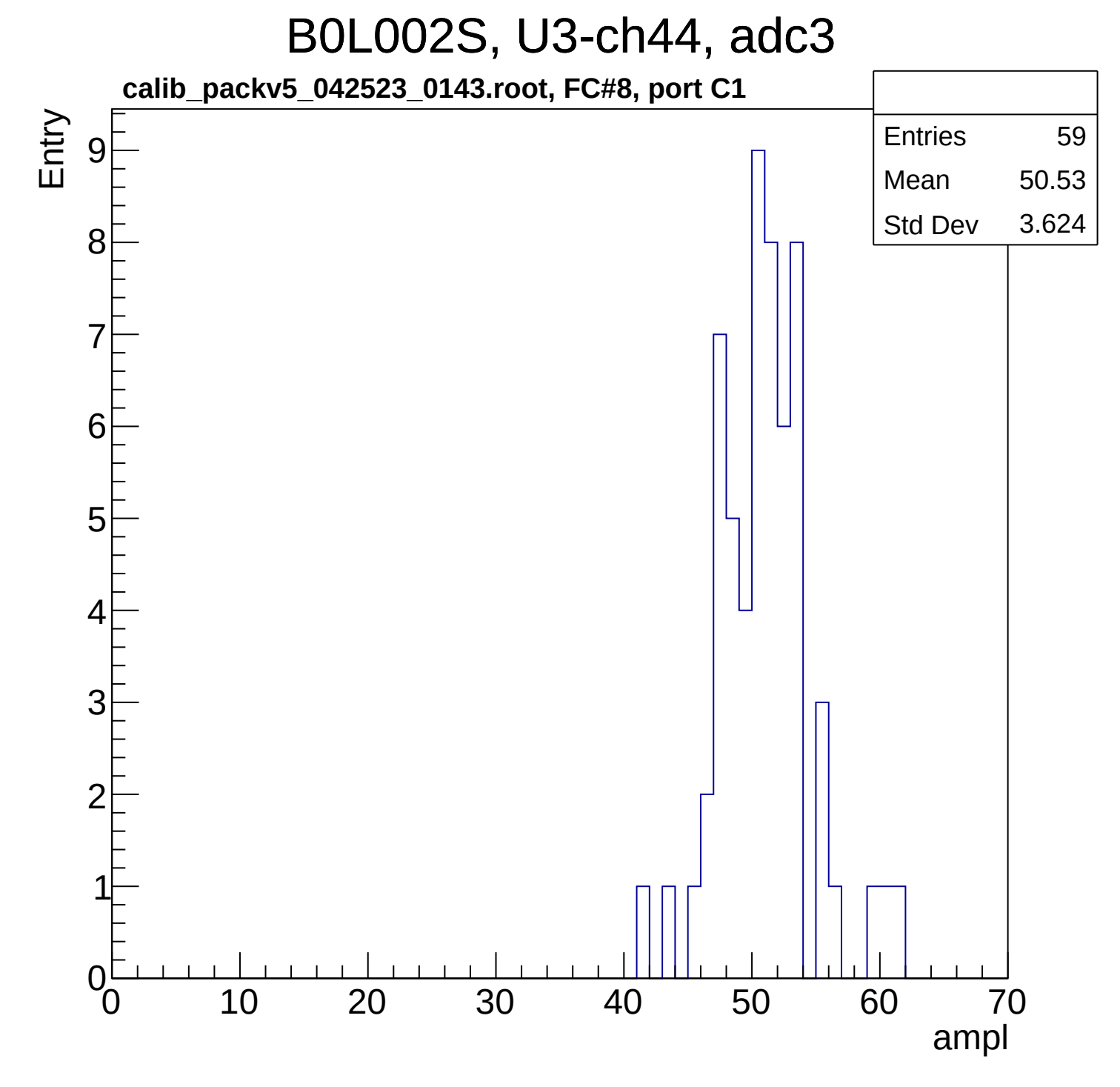
B0L002S, U3-ch44, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.53
Std Dev 3.624
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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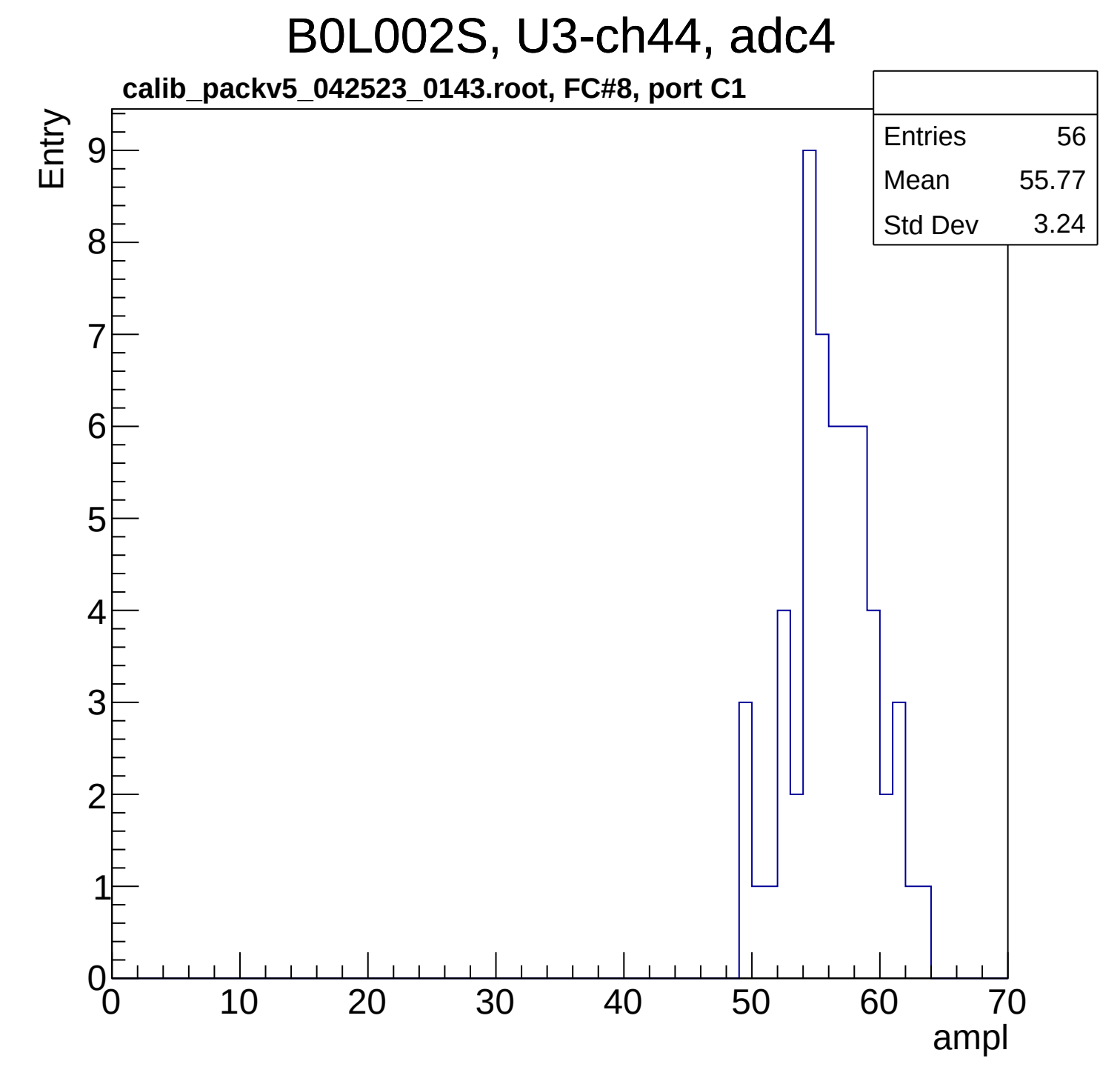
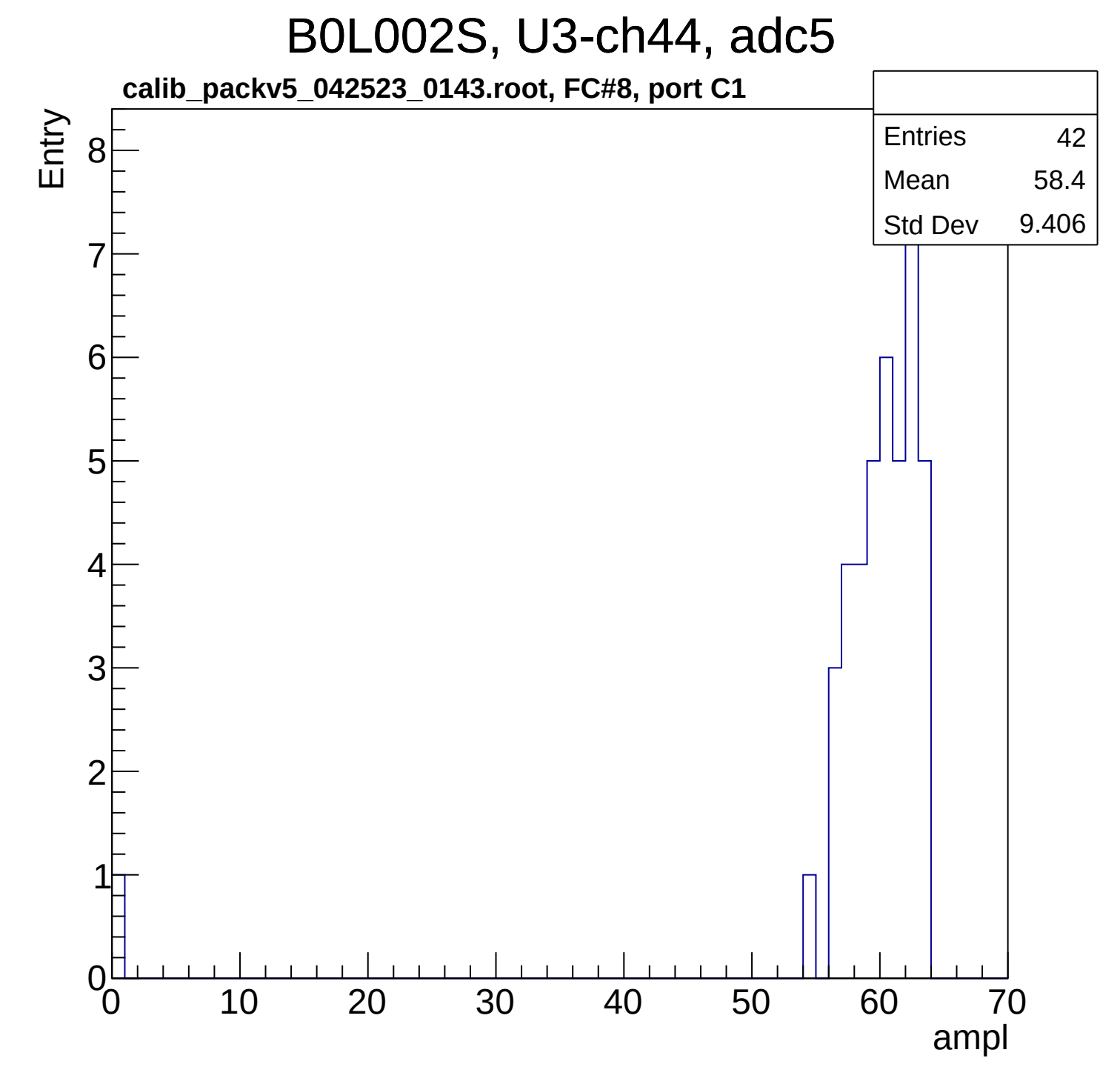

B0L002S, U3-ch44, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.77
Std Dev 3.24
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch44, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.4
Std Dev 9.406
Entry
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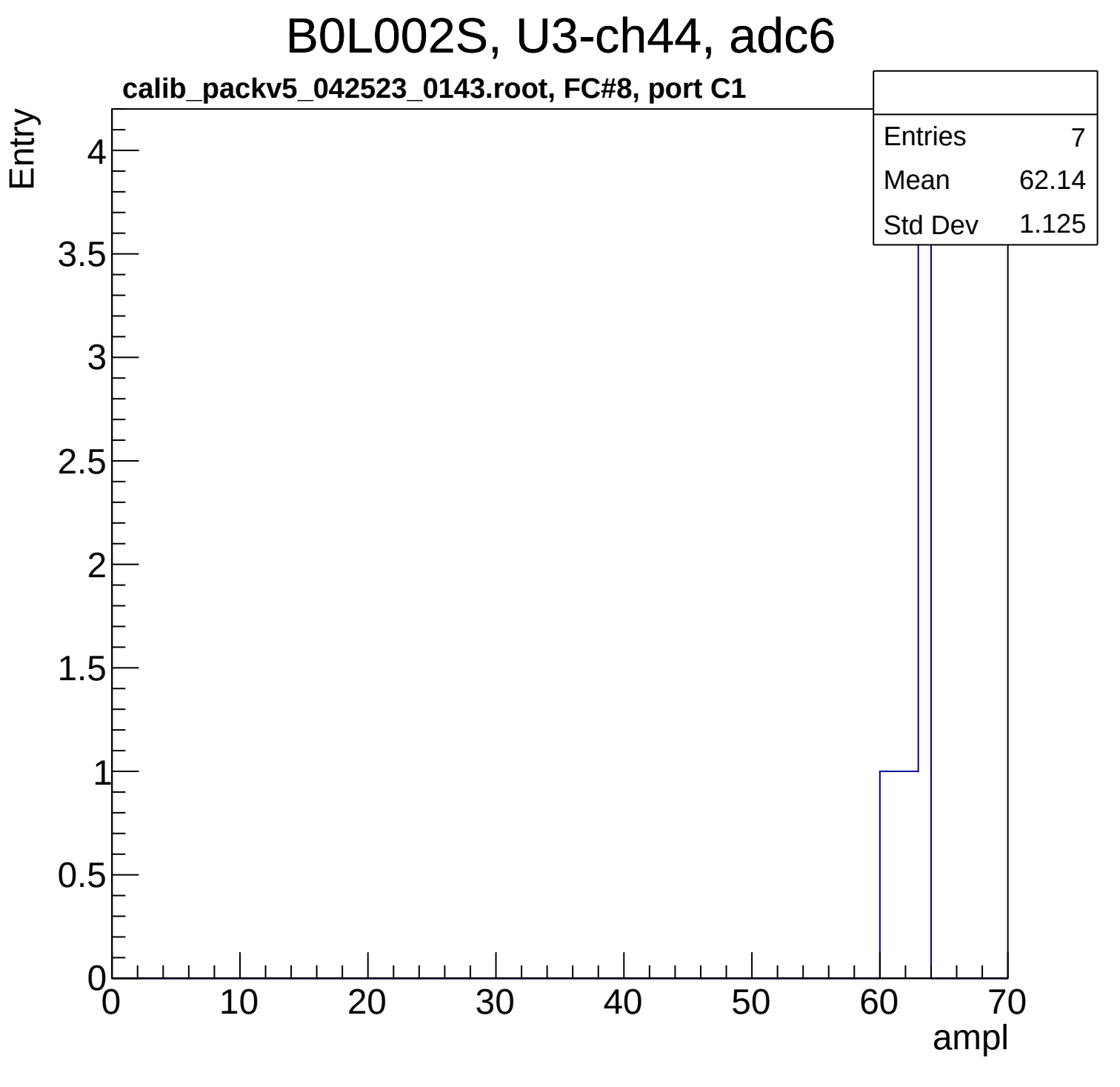
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch44, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.14
Std Dev 1.125
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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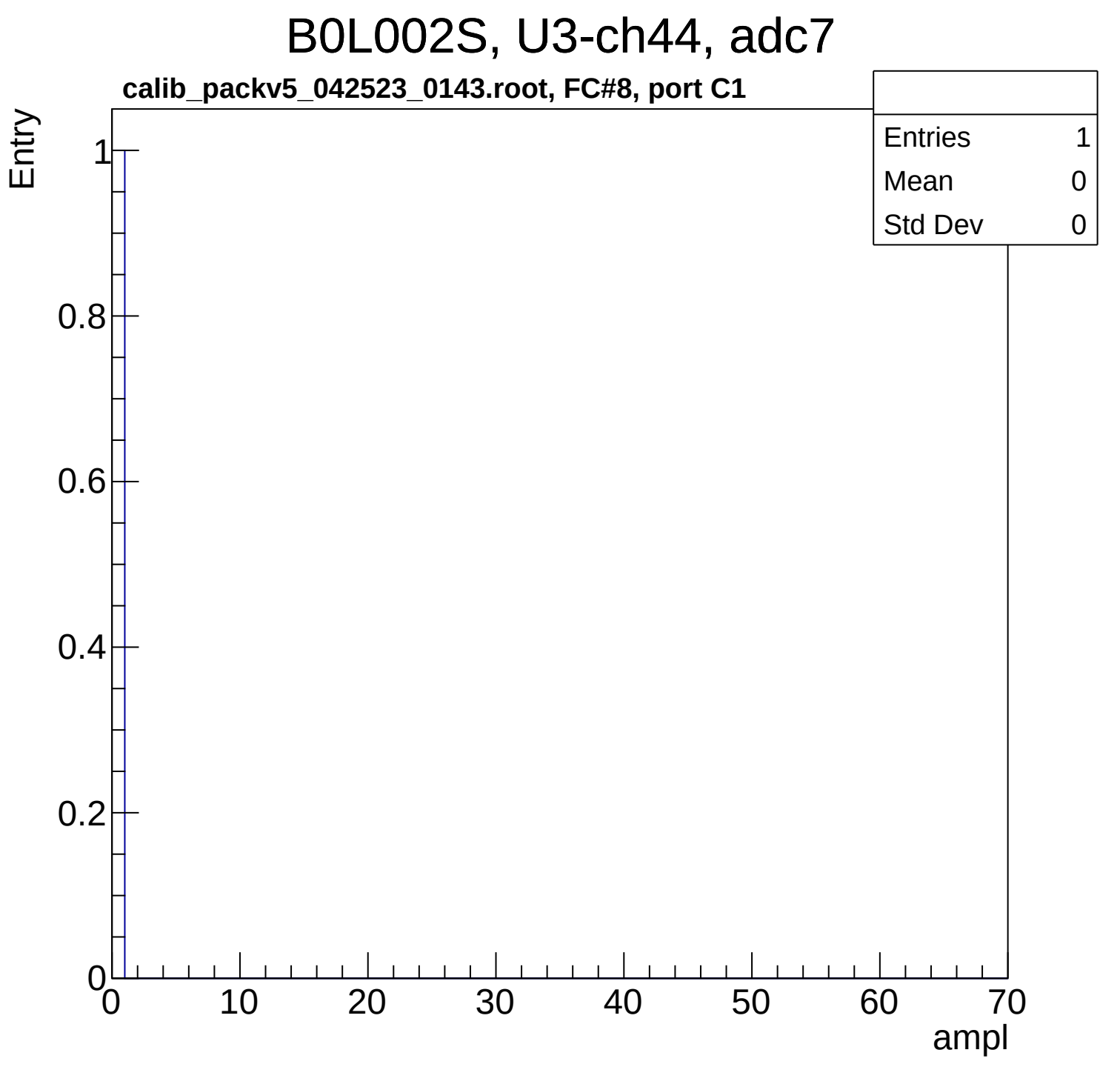
60
70


B0L002S, U3-ch44, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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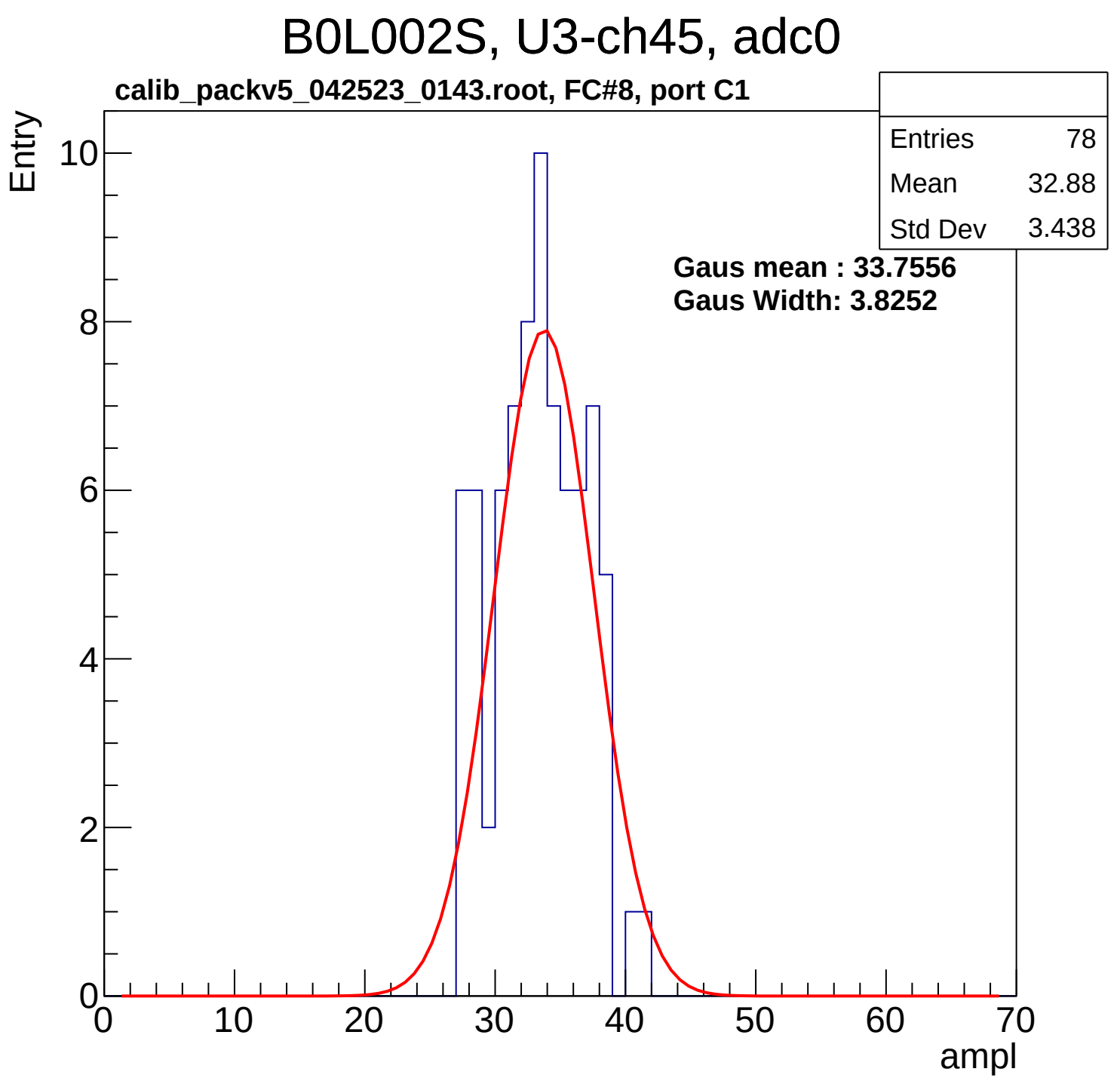
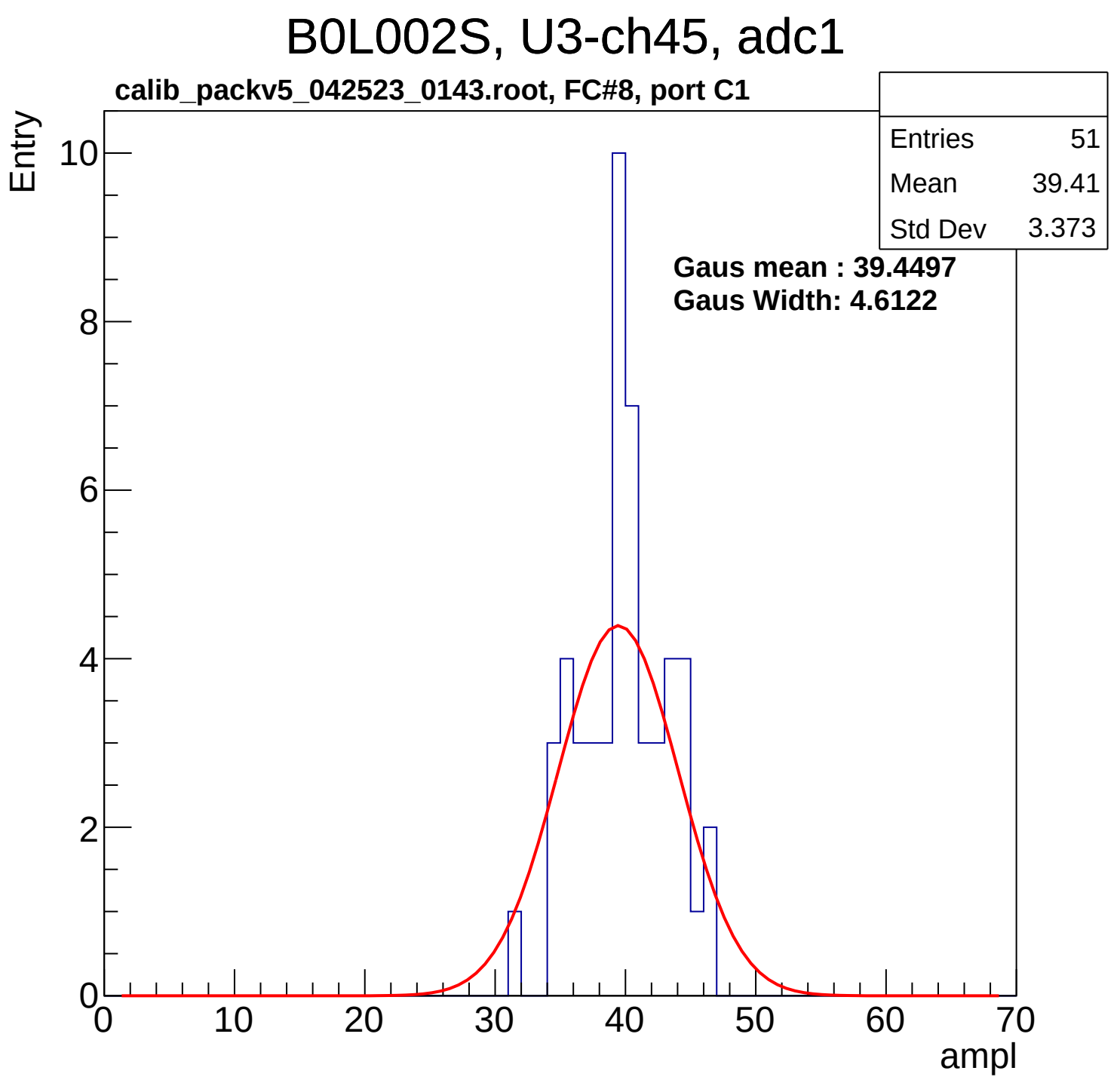
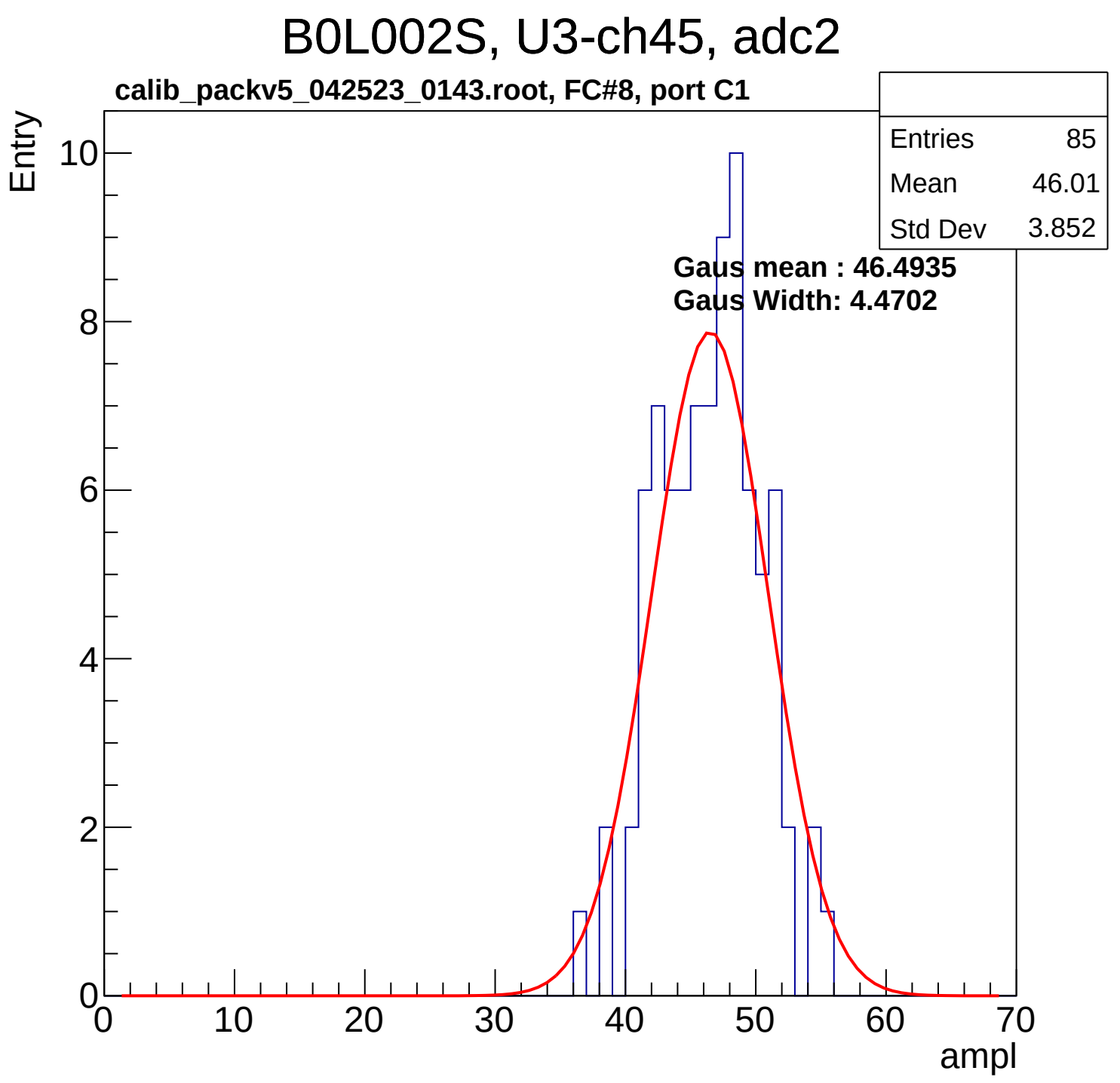
50
60
70
ampl


B0L002S, U3-ch45, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 32.88
Std Dev 3.438
Gaus mean : 33.7556
Gaus Width: 3.8252
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch45, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 39.41
Std Dev 3.373
Gaus mean : 39.4497
Gaus Width: 4.6122
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch45, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 46.01
Std Dev 3.852
Gaus mean : 46.4935
Gaus Width: 4.4702
Entry
ampl
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2
4
6
8
10
0
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20
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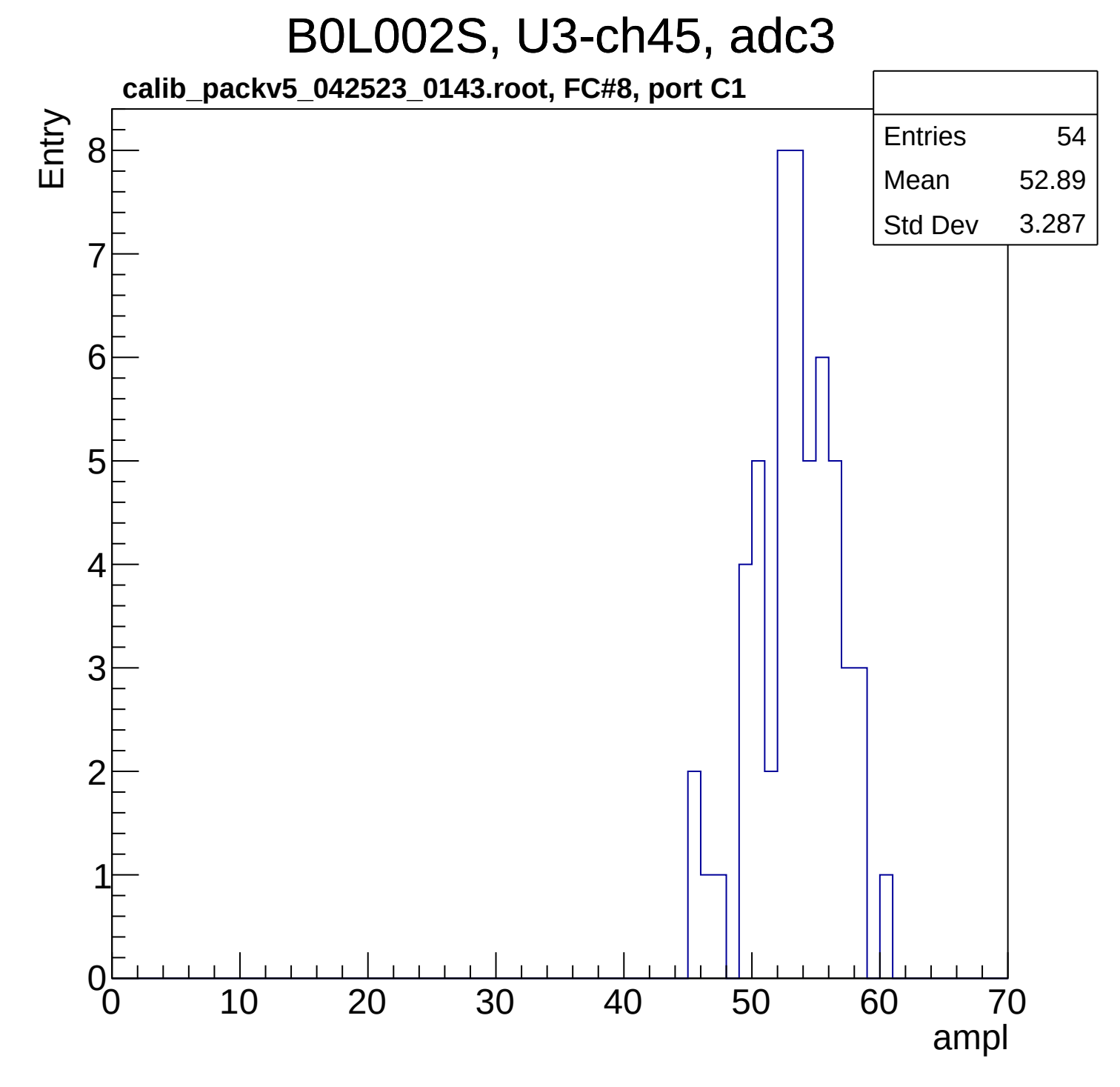
60
70


B0L002S, U3-ch45, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 52.89
Std Dev 3.287
Entry
ampl
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6
7
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0
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60
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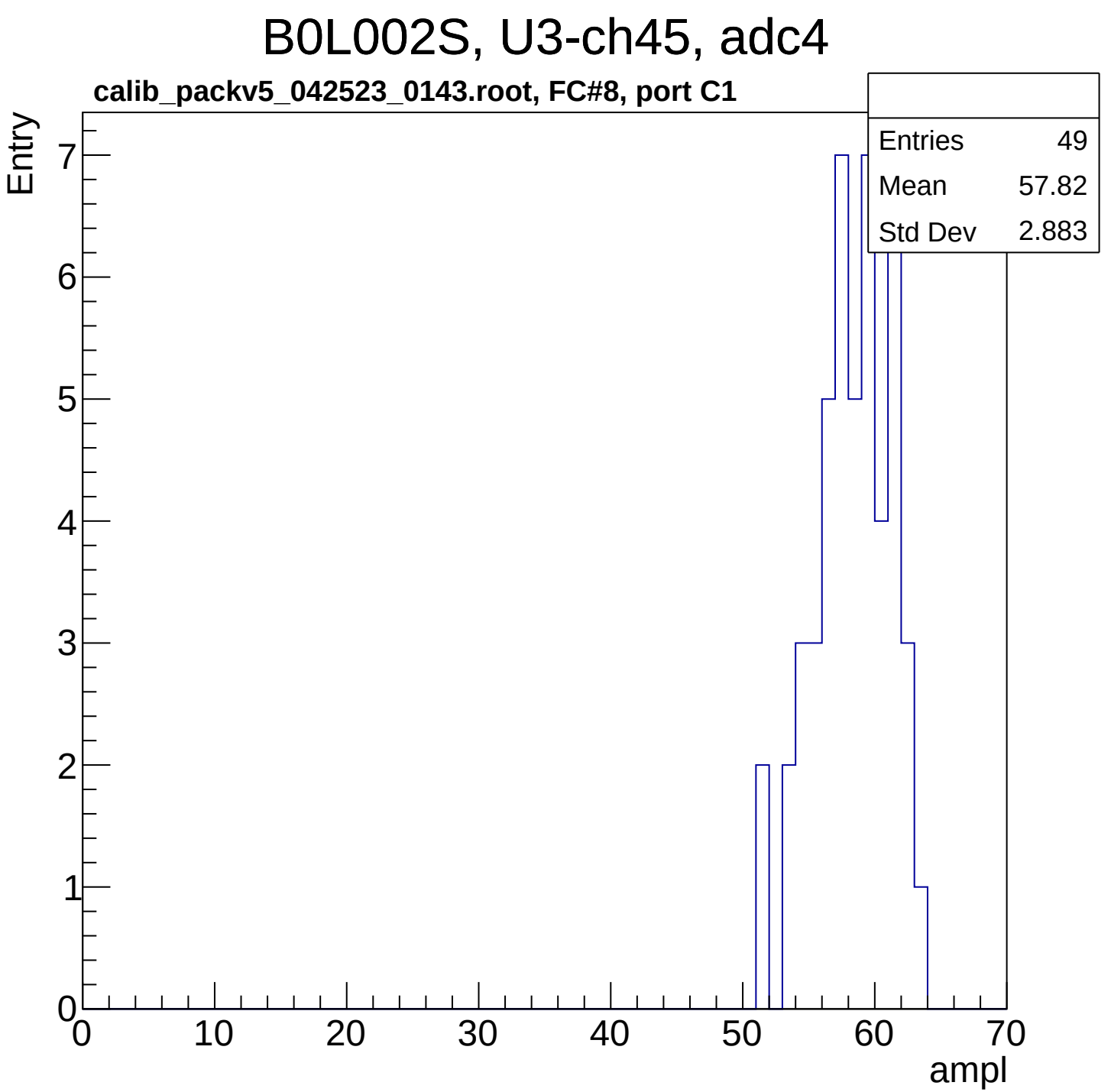
B0L002S, U3-ch45, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 57.82
Std Dev 2.883
Entry
ampl
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1
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3
4
5
6
7
0
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20
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40
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60
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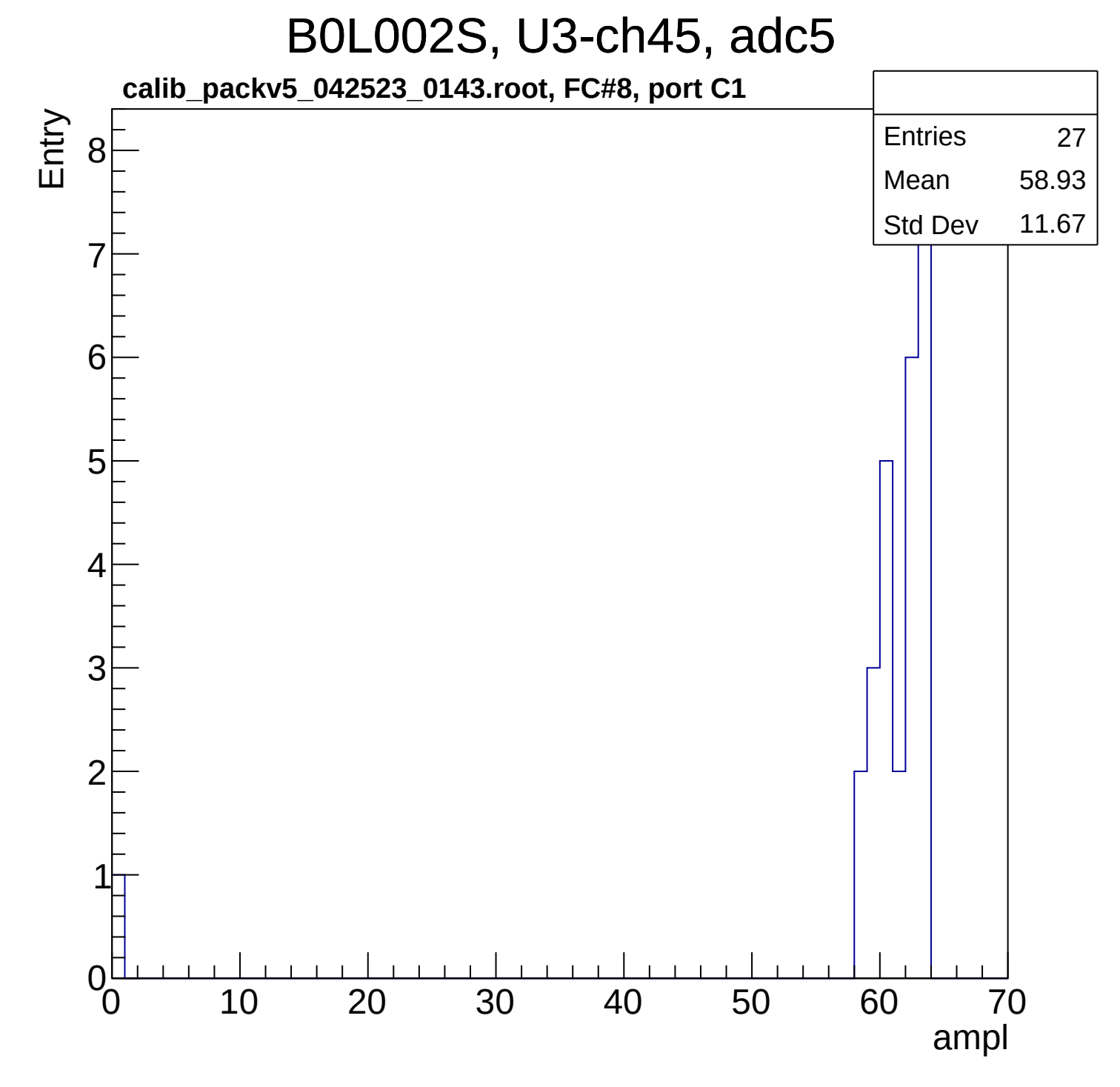
B0L002S, U3-ch45, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.93
Std Dev 11.67
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
50
60
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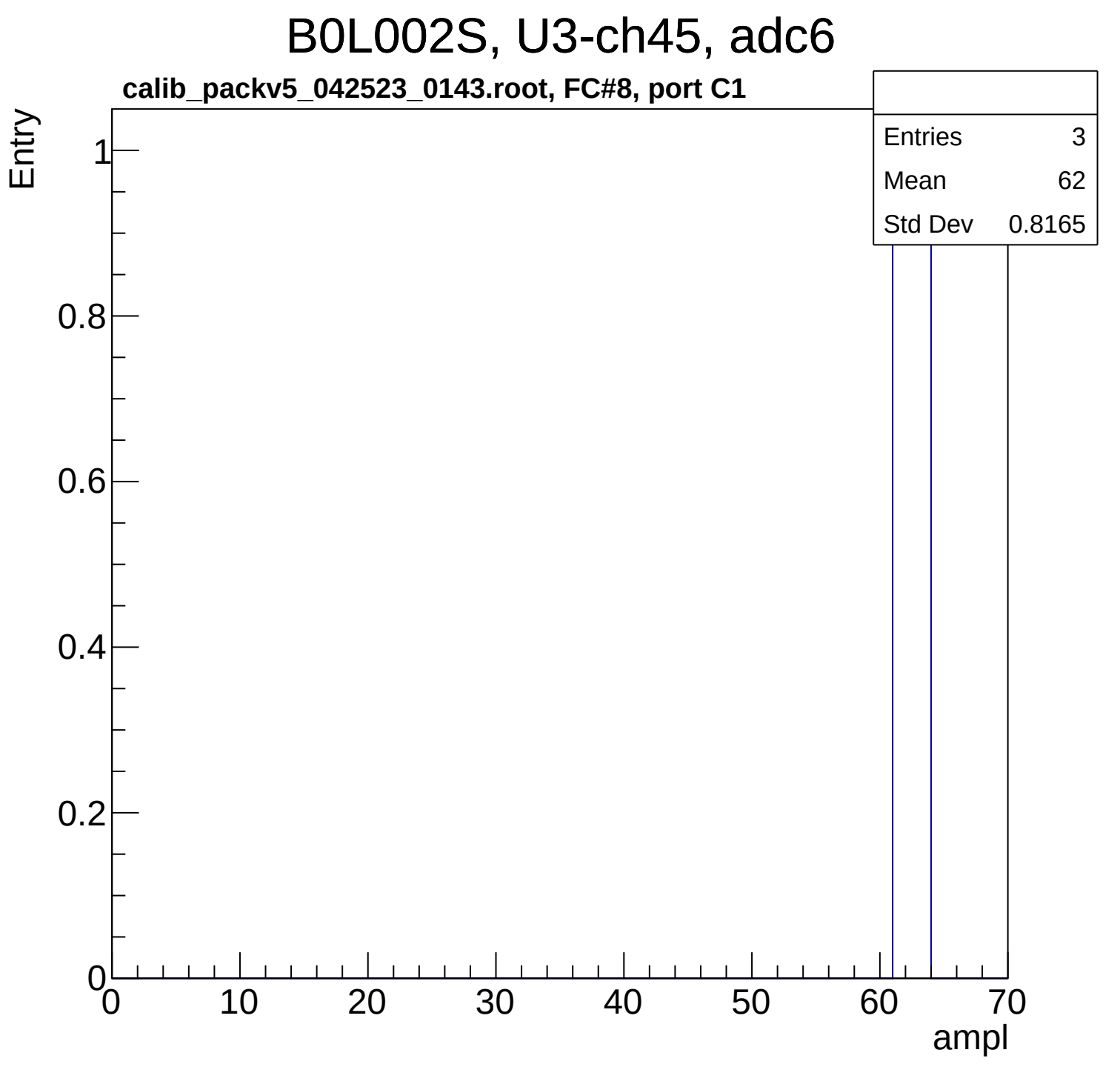
B0L002S, U3-ch45, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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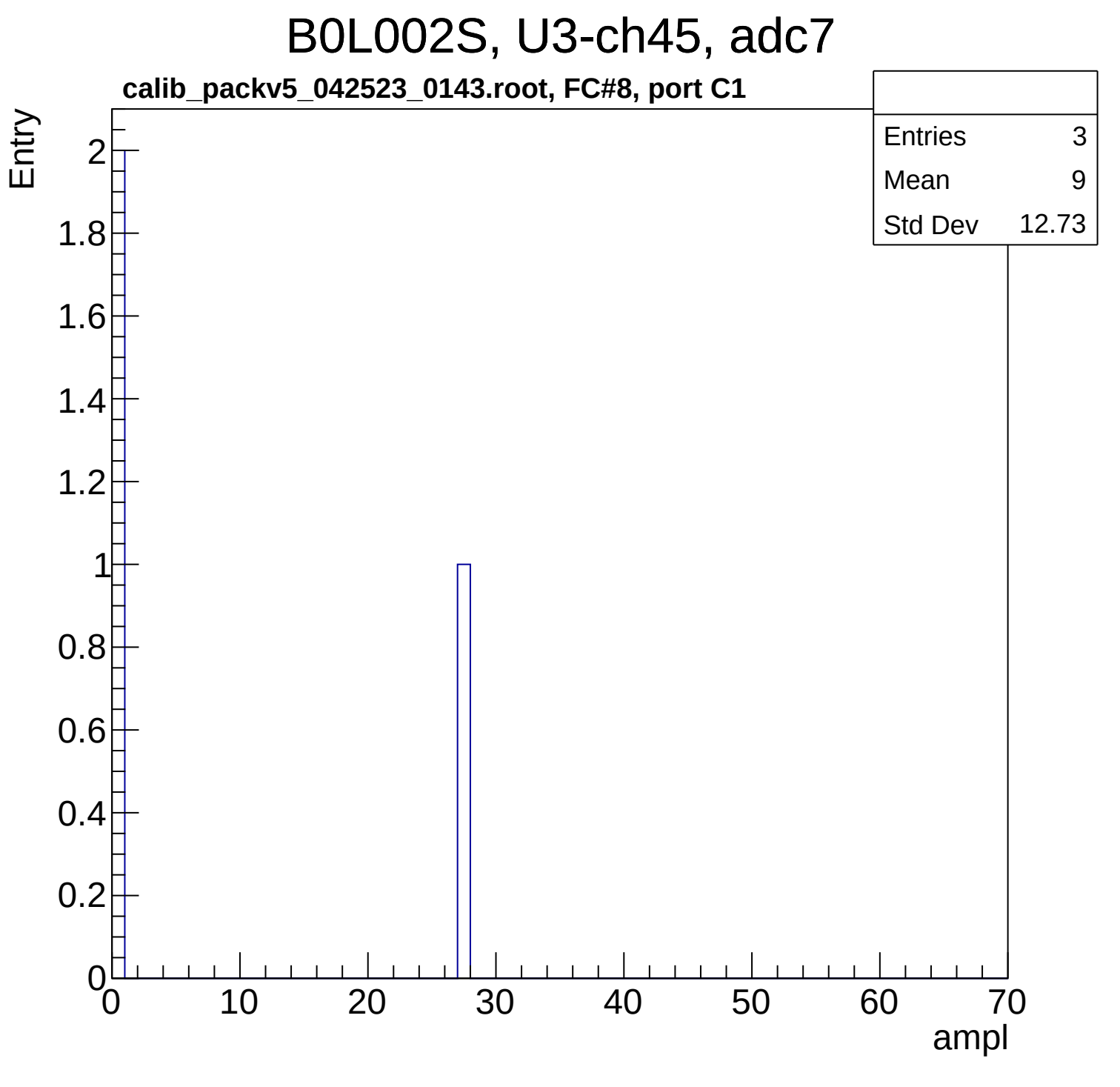
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch45, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 9
Std Dev 12.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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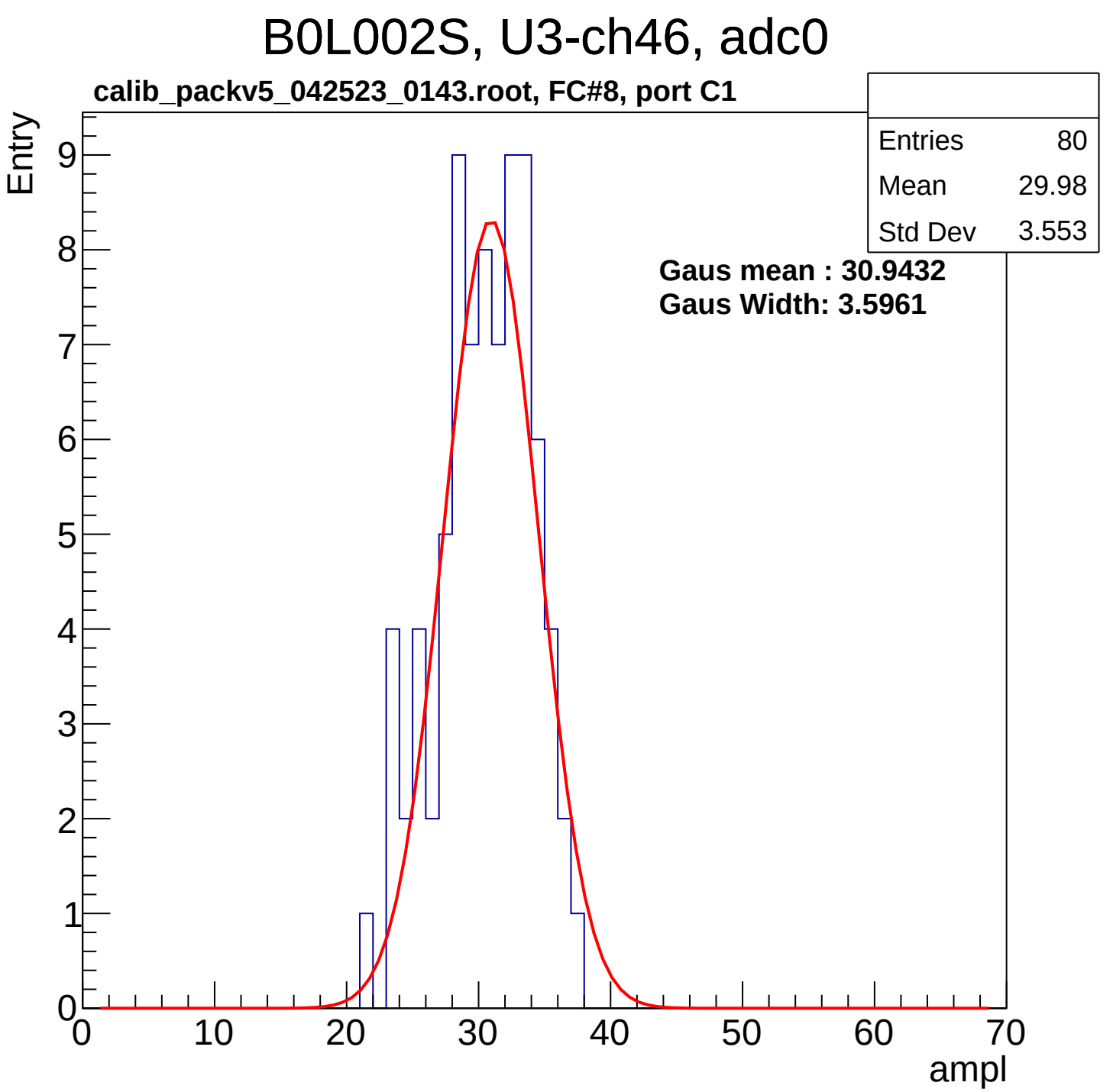
B0L002S, U3-ch46, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 29.98
Std Dev 3.553
Gaus mean : 30.9432
Gaus Width: 3.5961
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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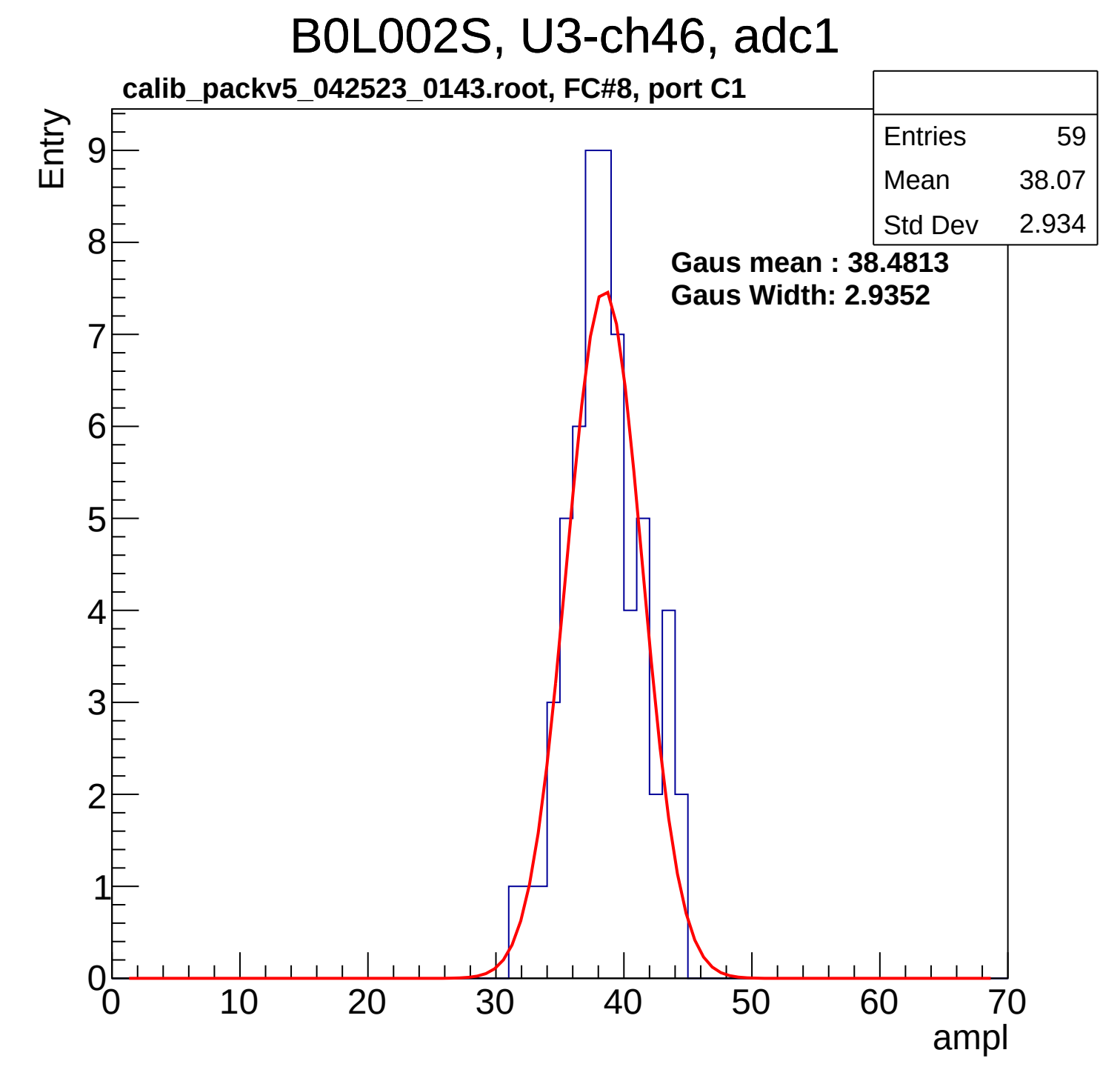
B0L002S, U3-ch46, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 38.07
Std Dev 2.934
Gaus mean : 38.4813
Gaus Width: 2.9352
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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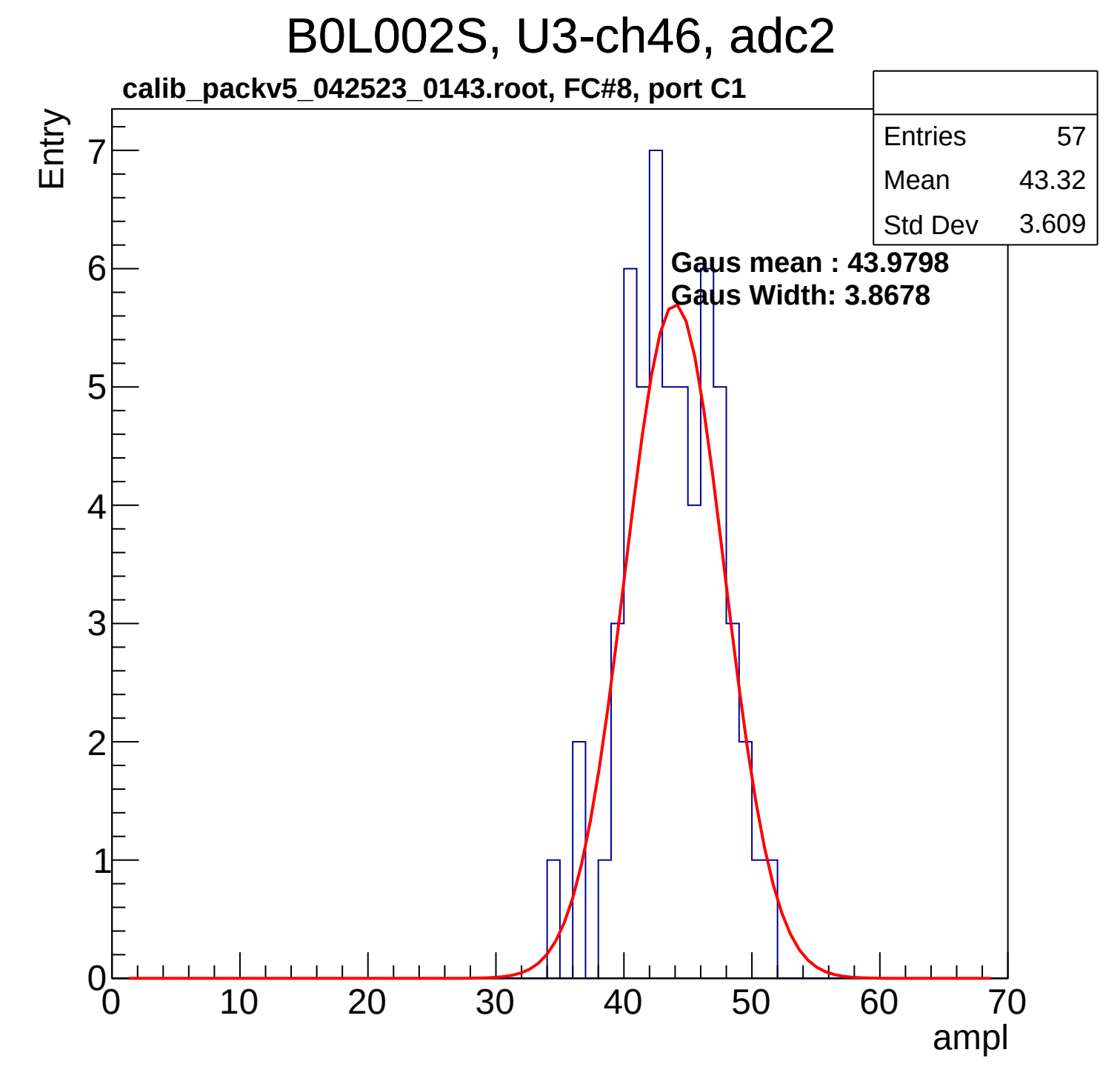
B0L002S, U3-ch46, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 43.32
Std Dev 3.609
Gaus mean : 43.9798
Gaus Width: 3.8678
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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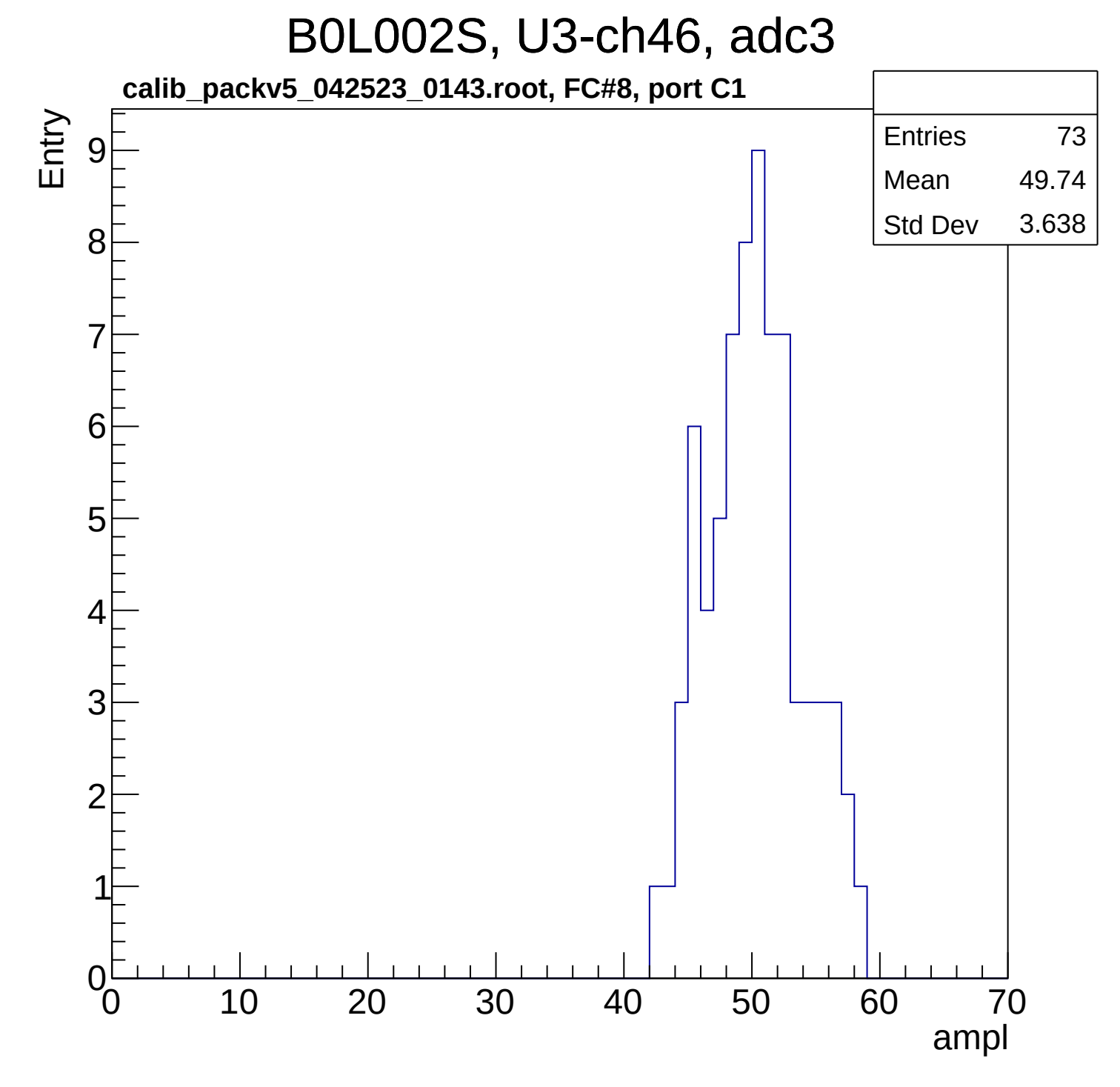
B0L002S, U3-ch46, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 49.74
Std Dev 3.638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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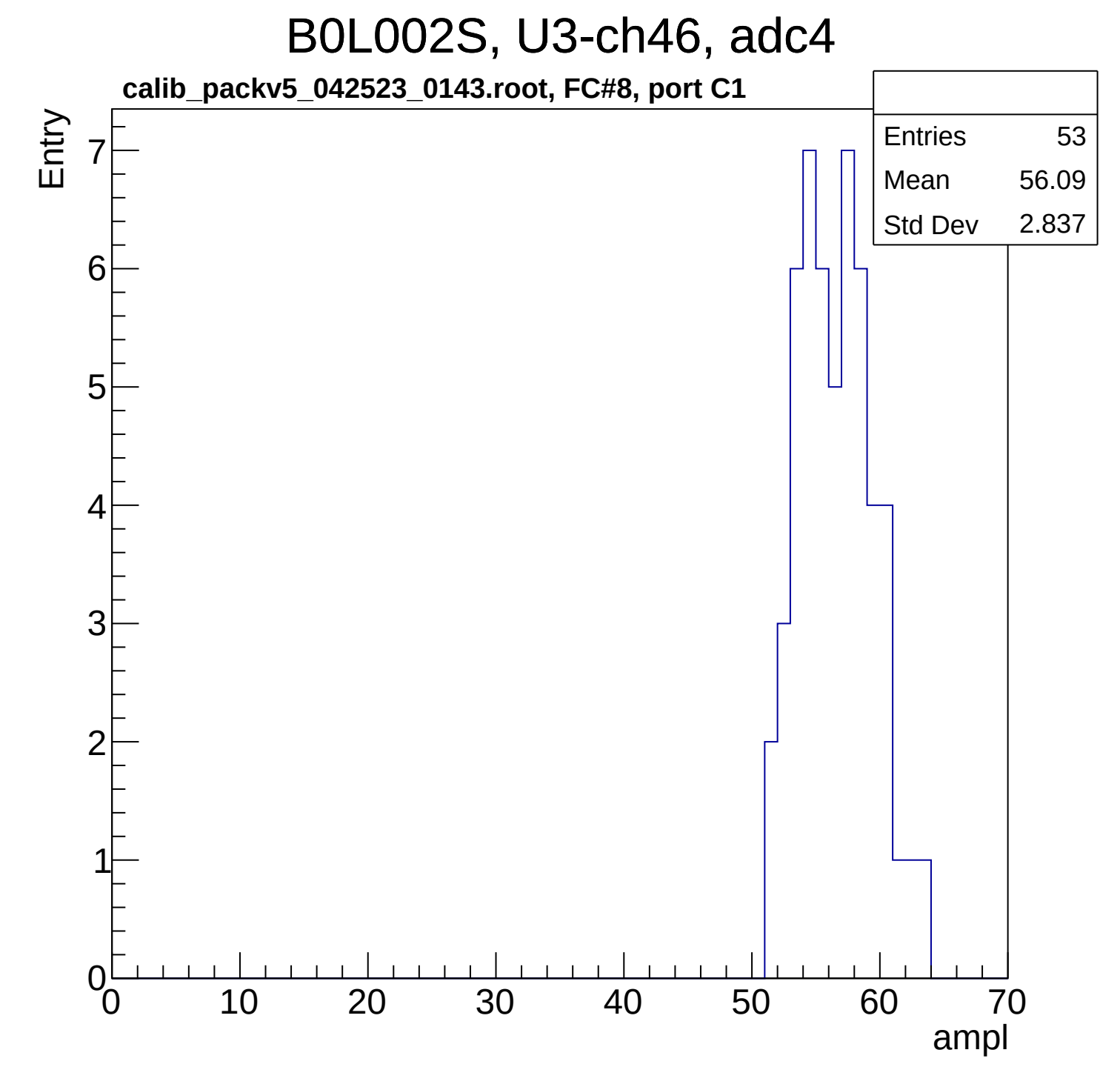
B0L002S, U3-ch46, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.09
Std Dev 2.837
Entry
ampl
0
1
2
3
4
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6
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0
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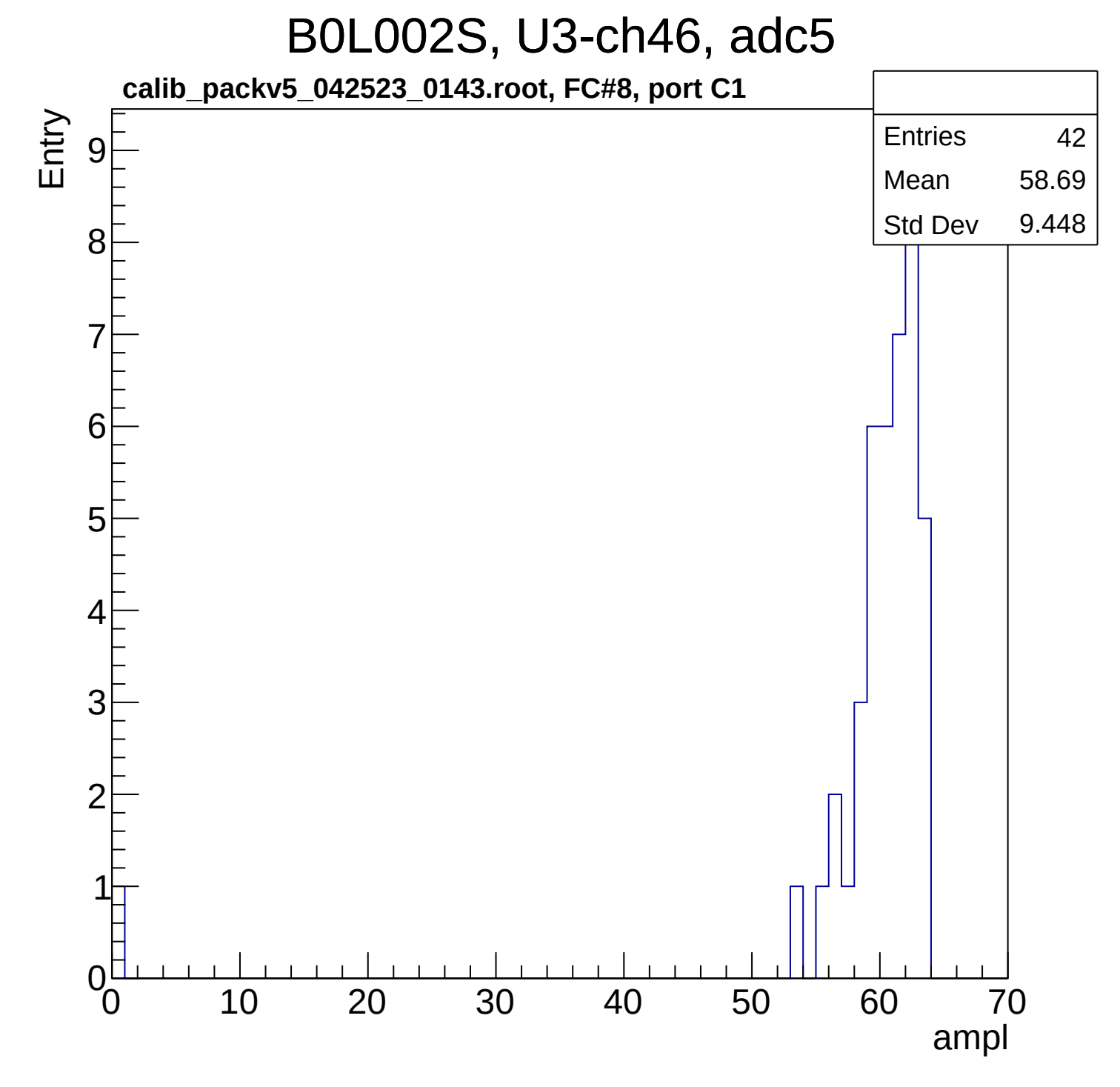
B0L002S, U3-ch46, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.69
Std Dev 9.448
Entry
ampl
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3
4
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6
7
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9
0
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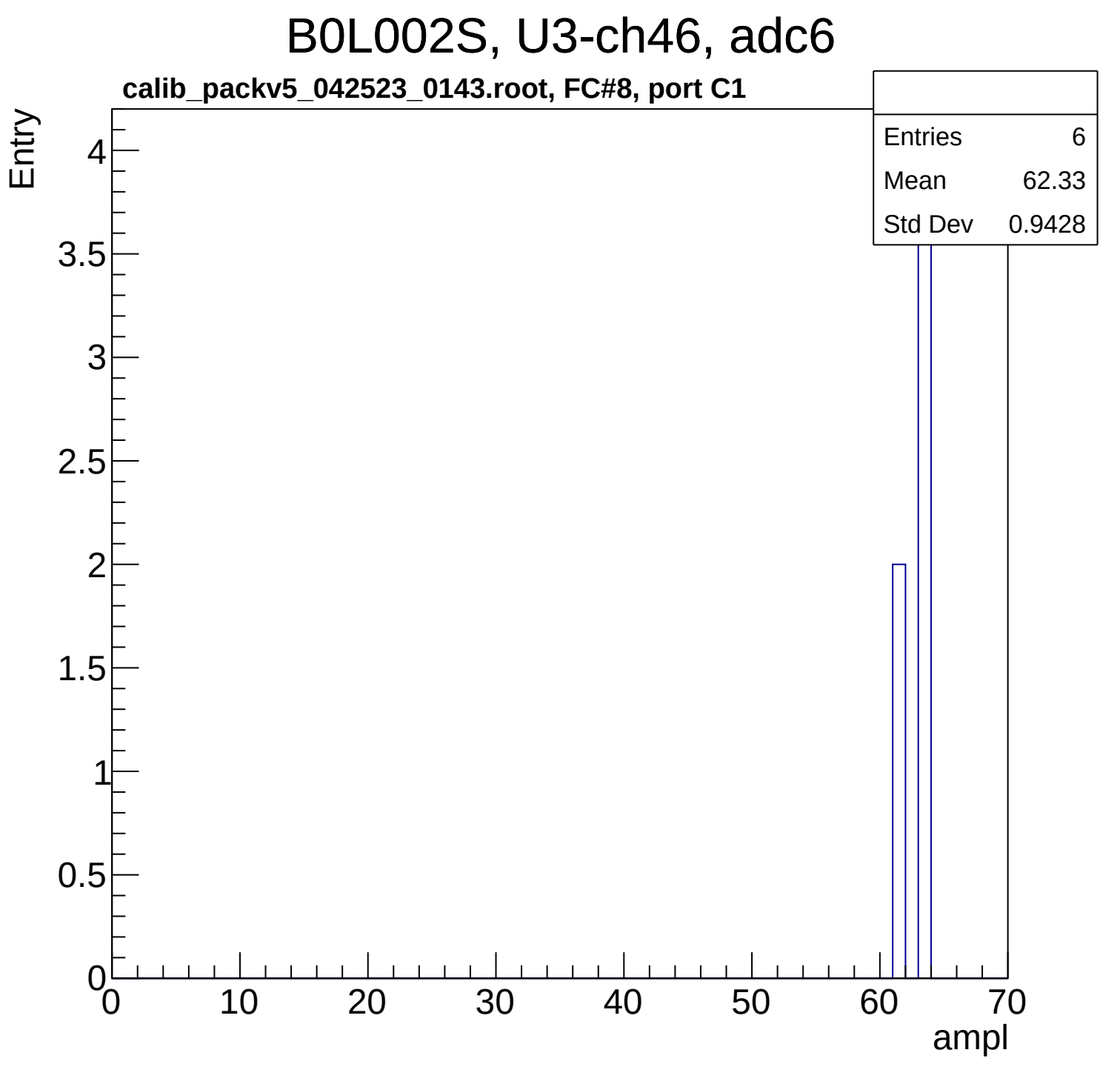
B0L002S, U3-ch46, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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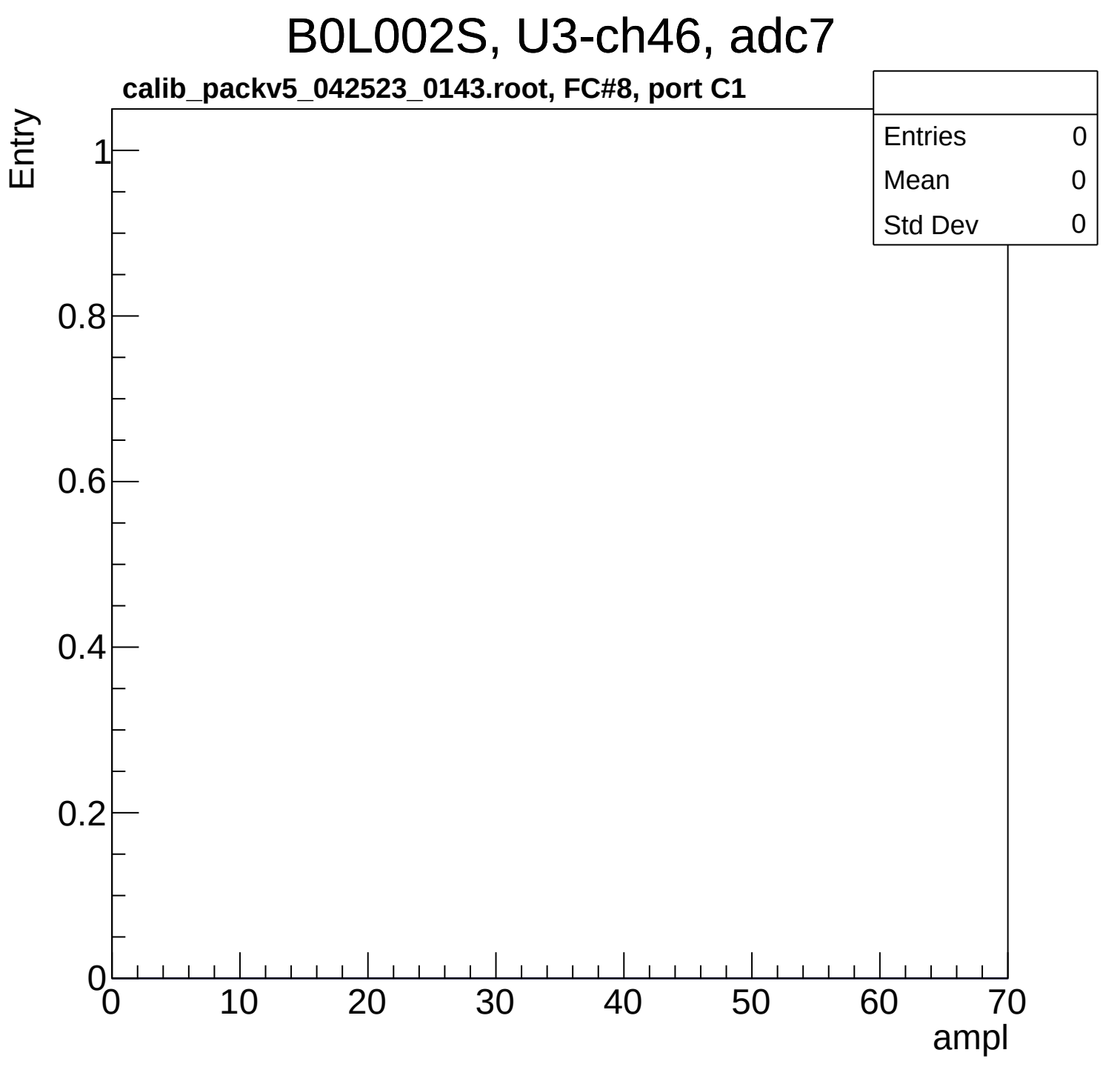
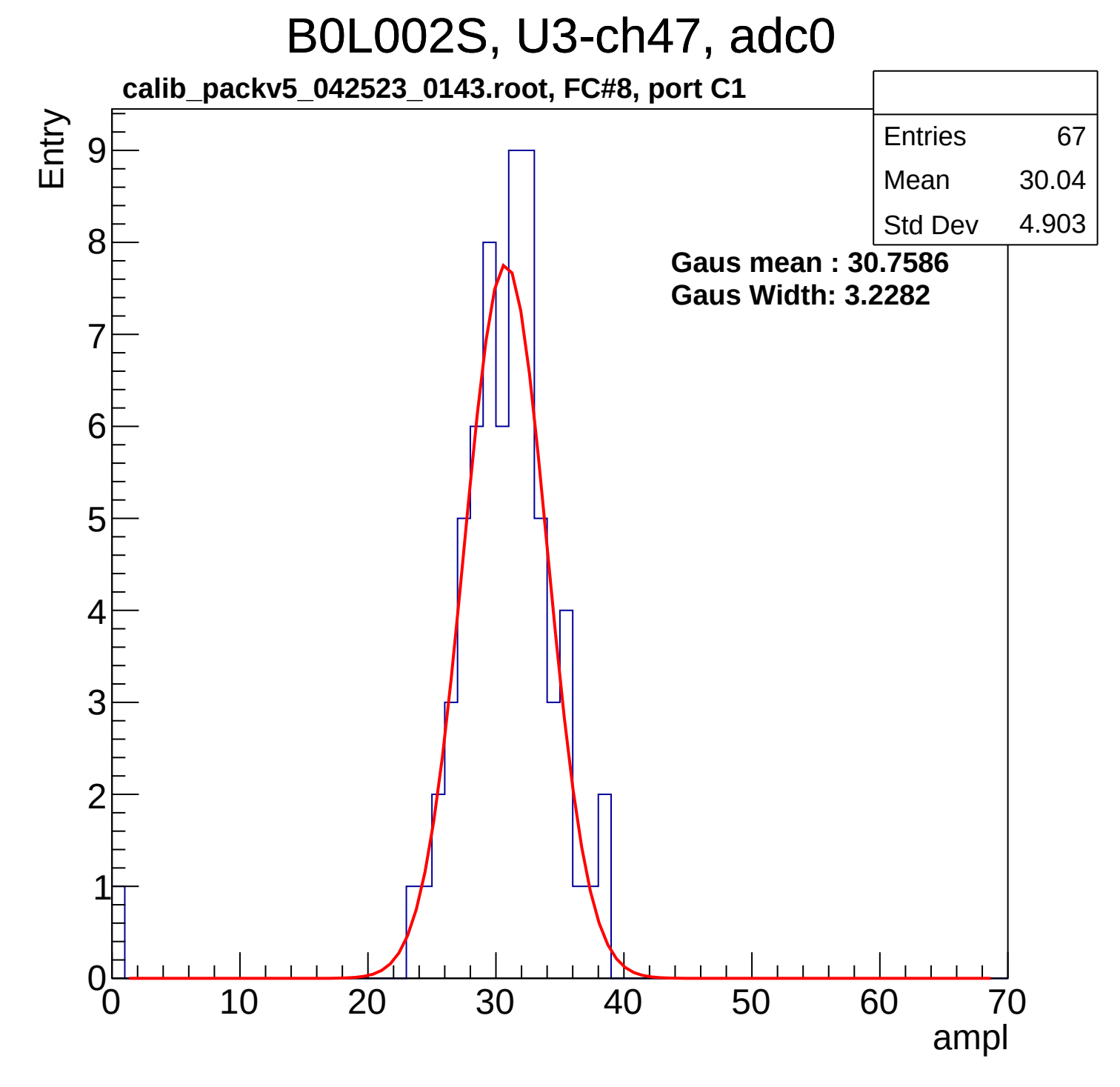

B0L002S, U3-ch46, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch47, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.04
Std Dev 4.903
Gaus mean : 30.7586
Gaus Width: 3.2282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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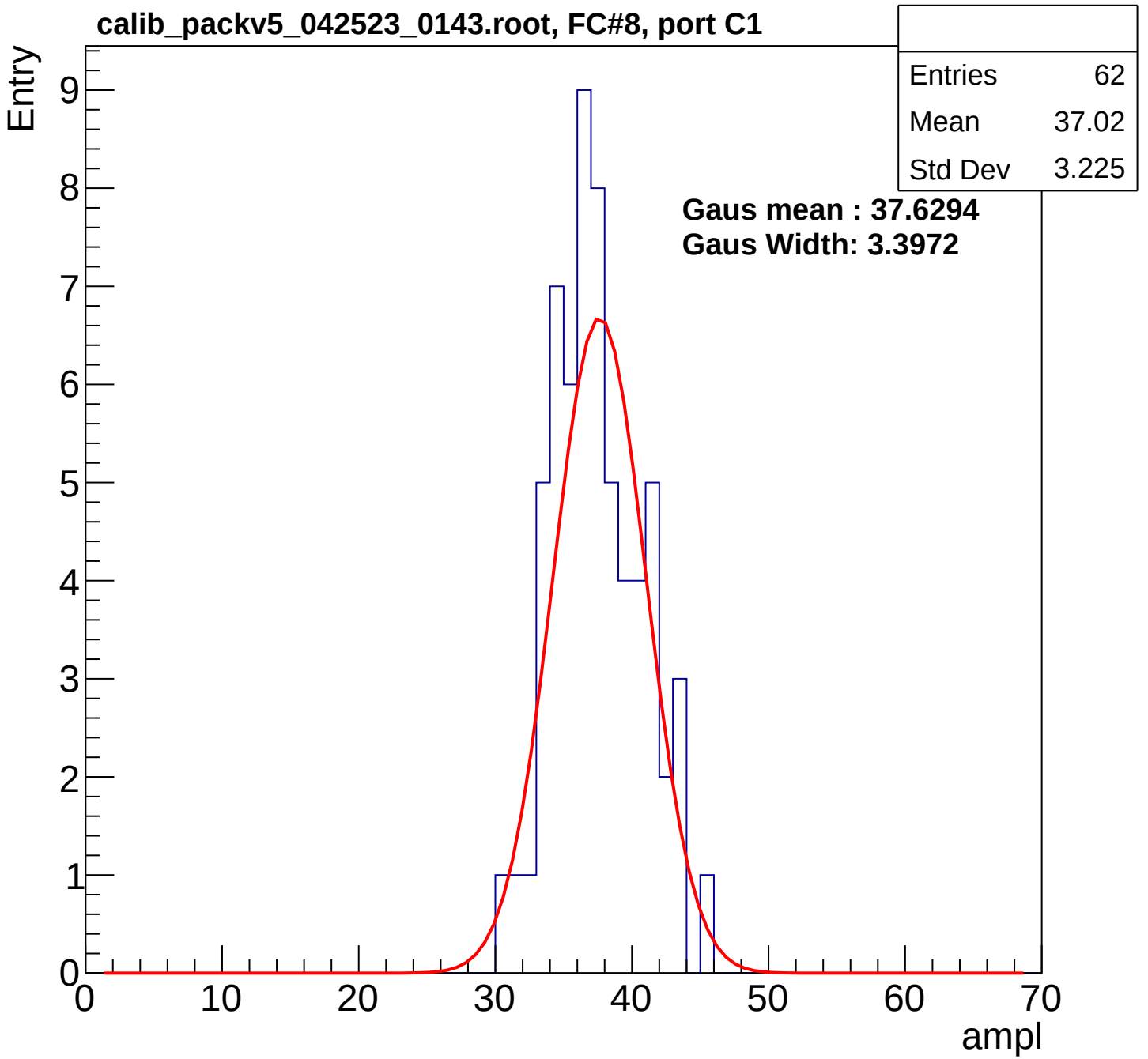
B0L002S, U3-ch47, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.02
Std Dev 3.225
Gaus mean : 37.6294
Gaus Width: 3.3972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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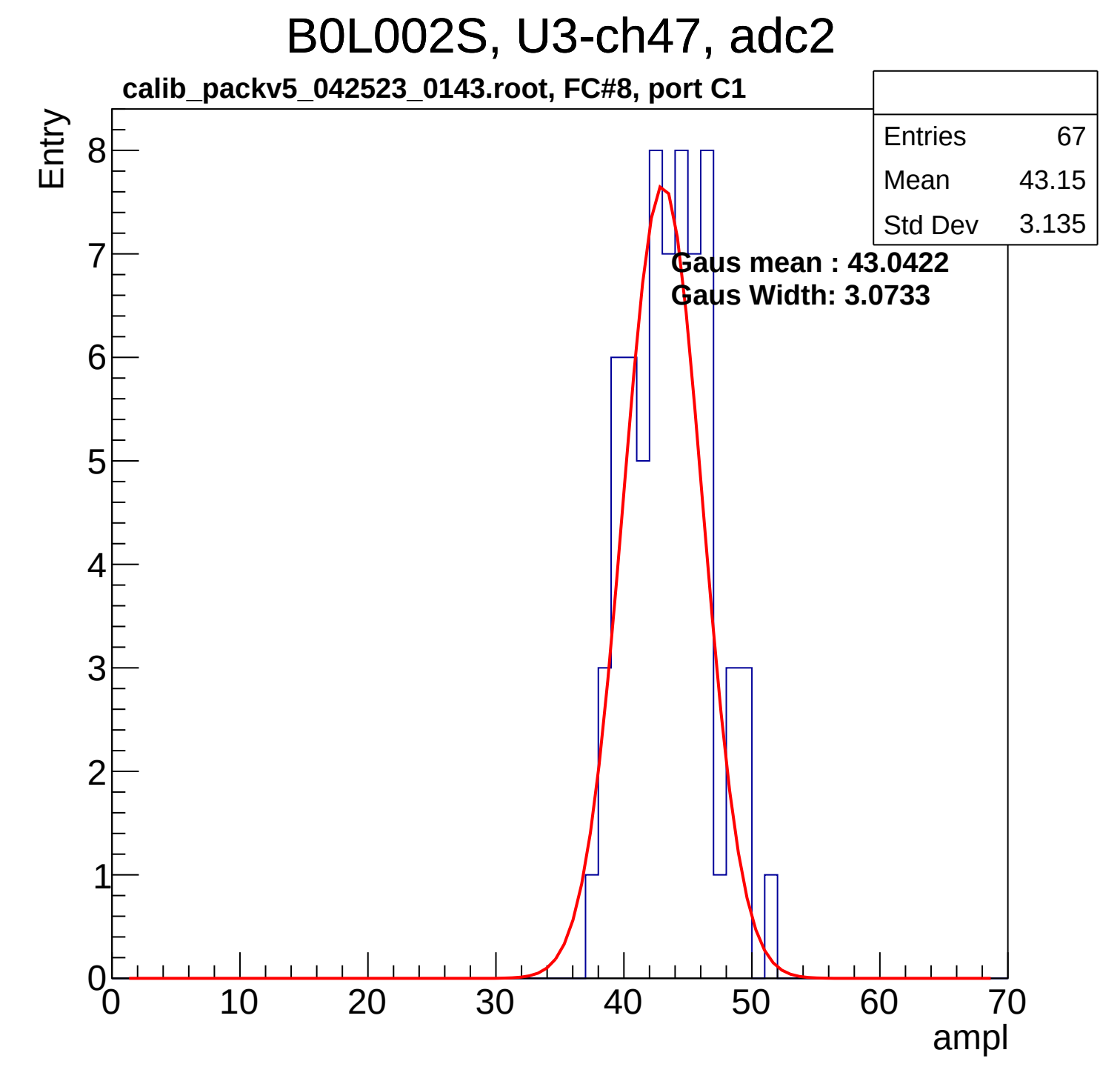
B0L002S, U3-ch47, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.15
Std Dev 3.135
Gaus mean : 43.0422
Gaus Width: 3.0733
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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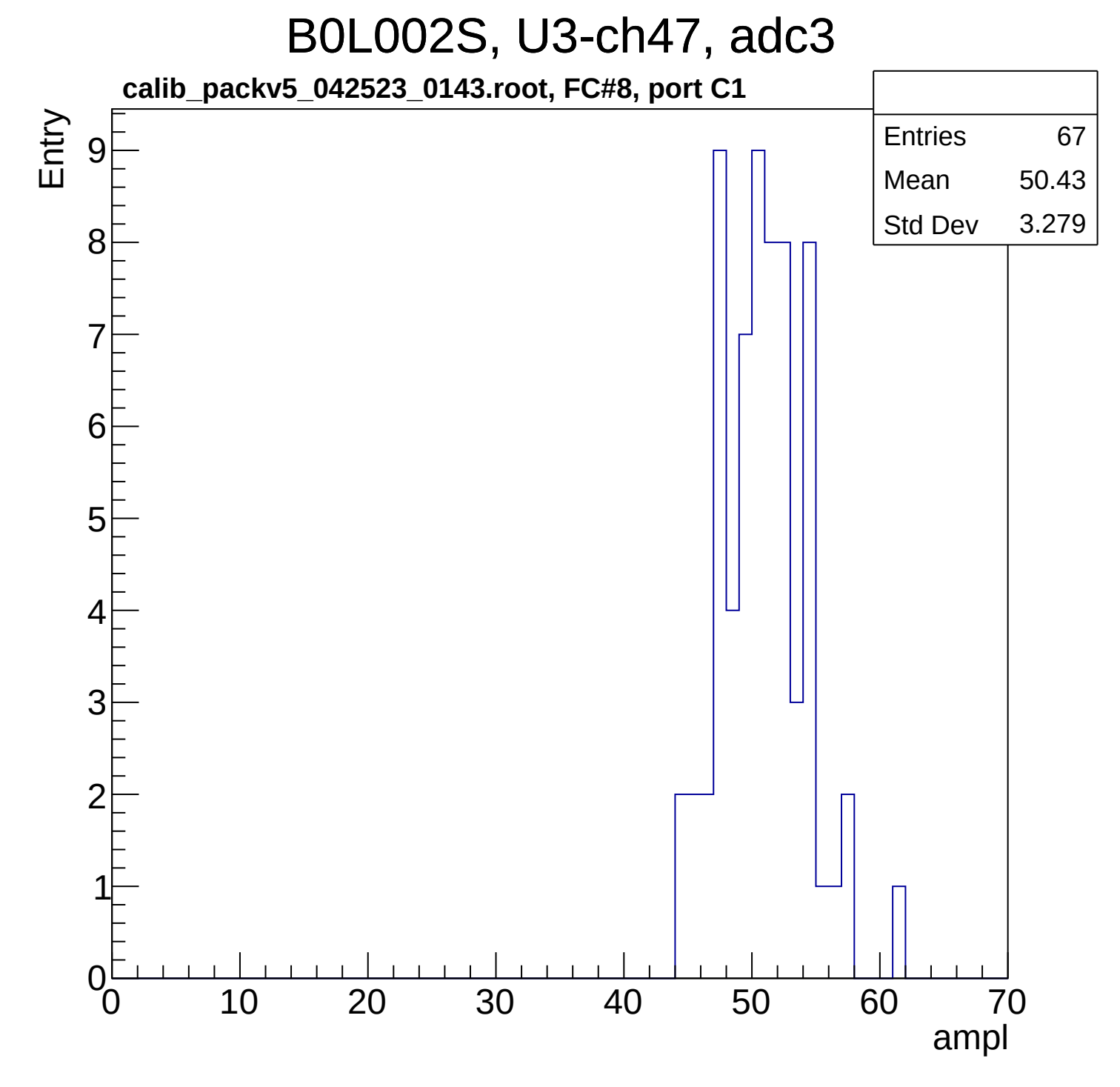
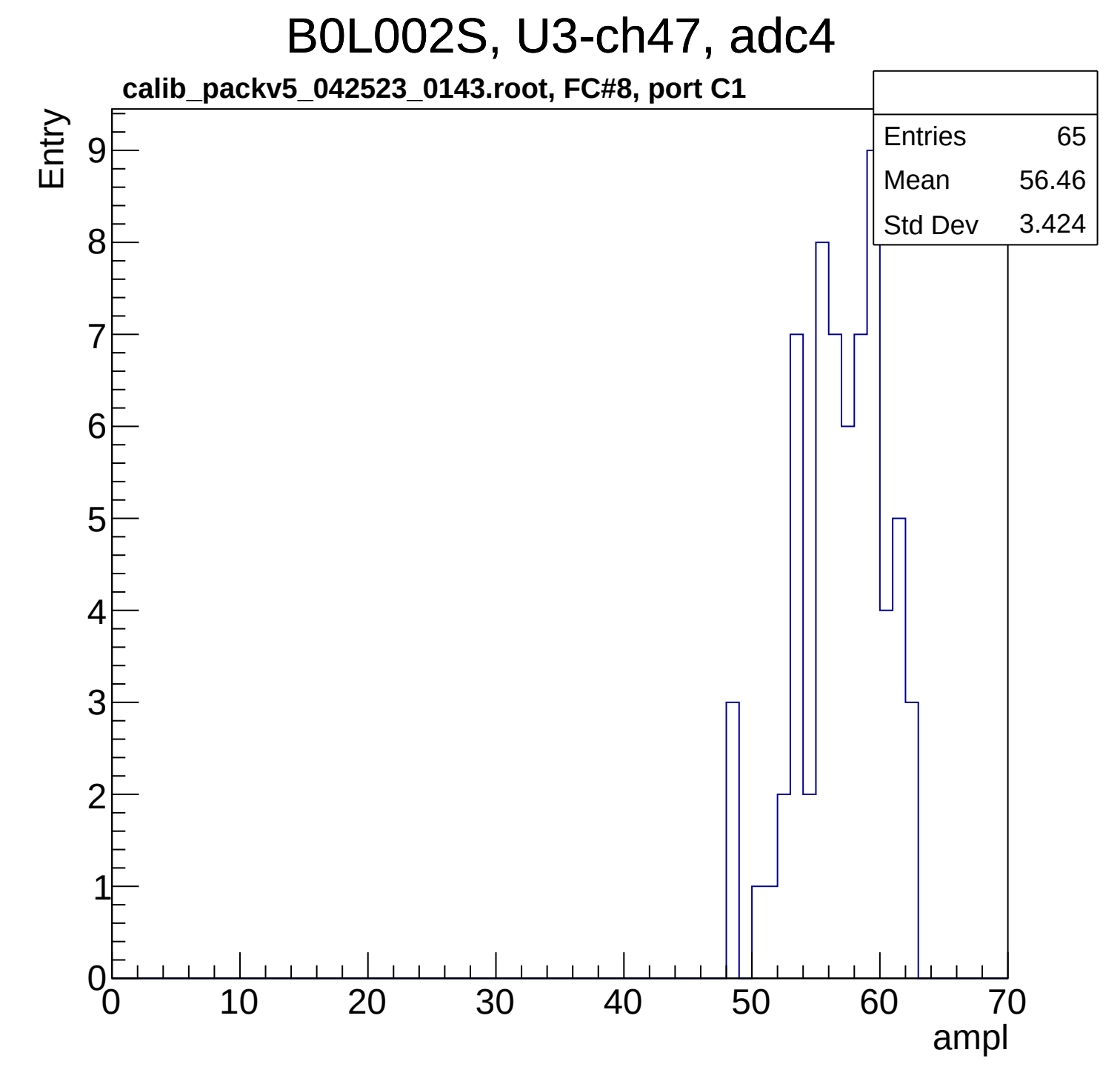
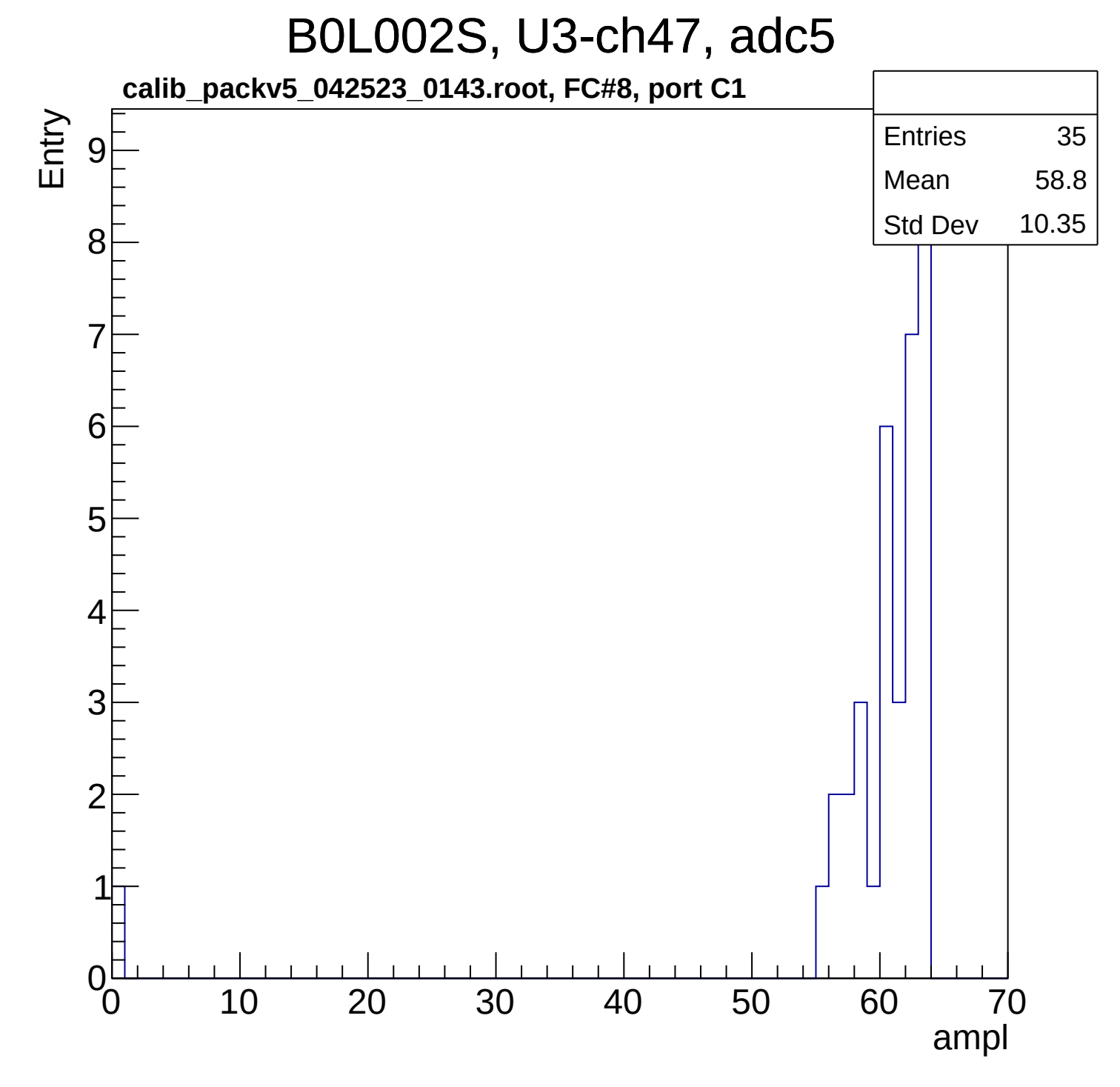
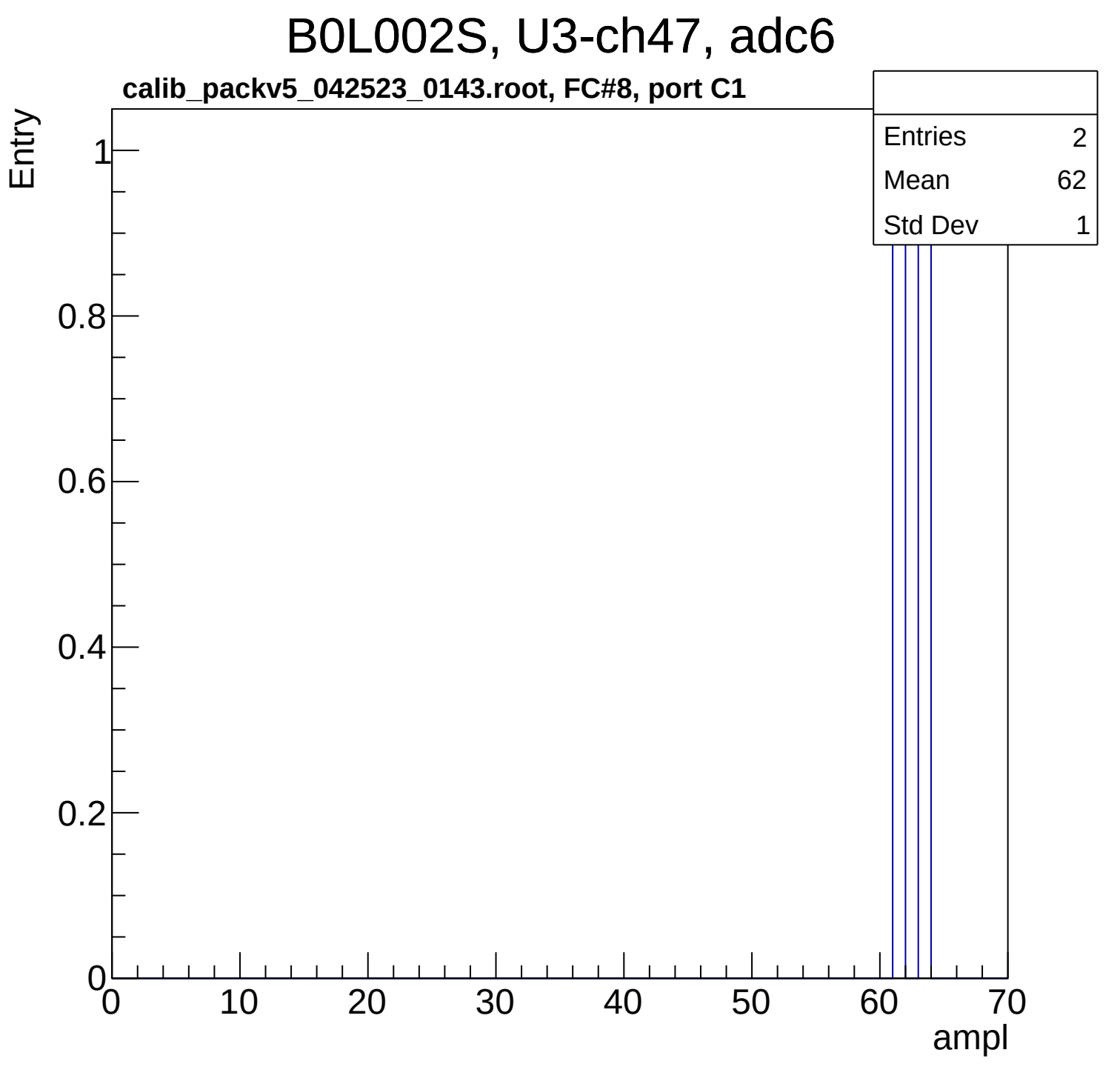
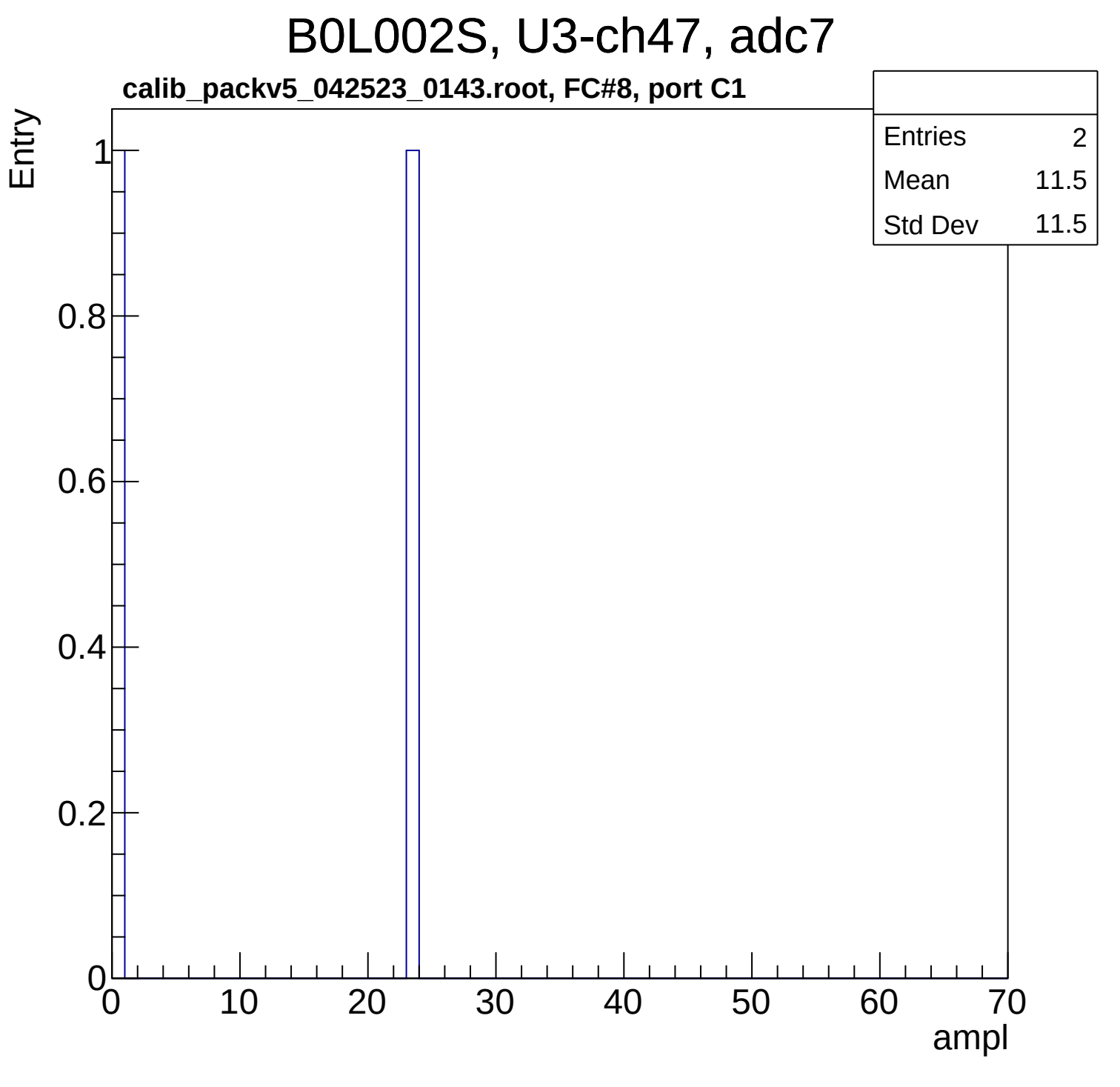
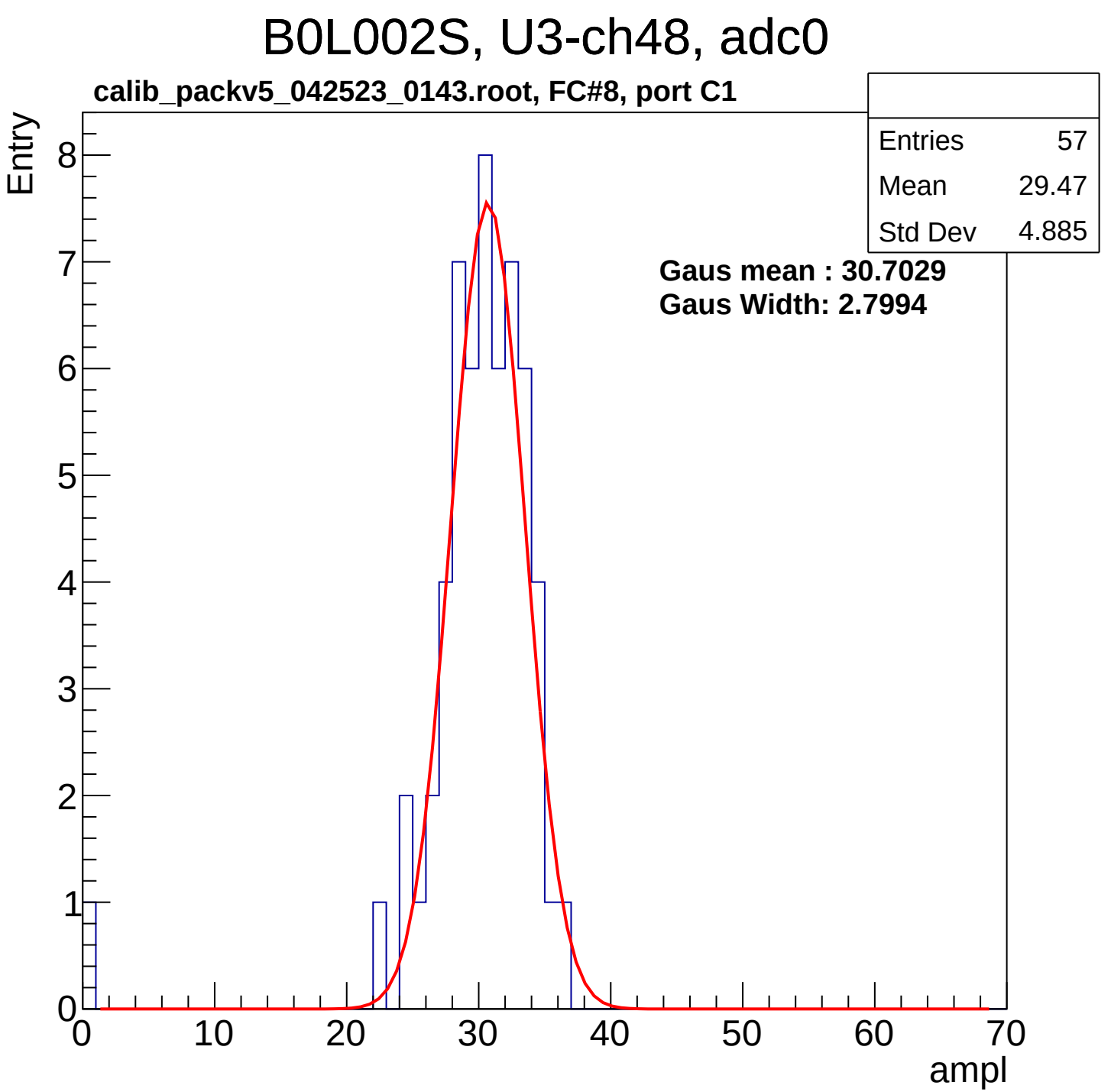

B0L002S, U3-ch47, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.43
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

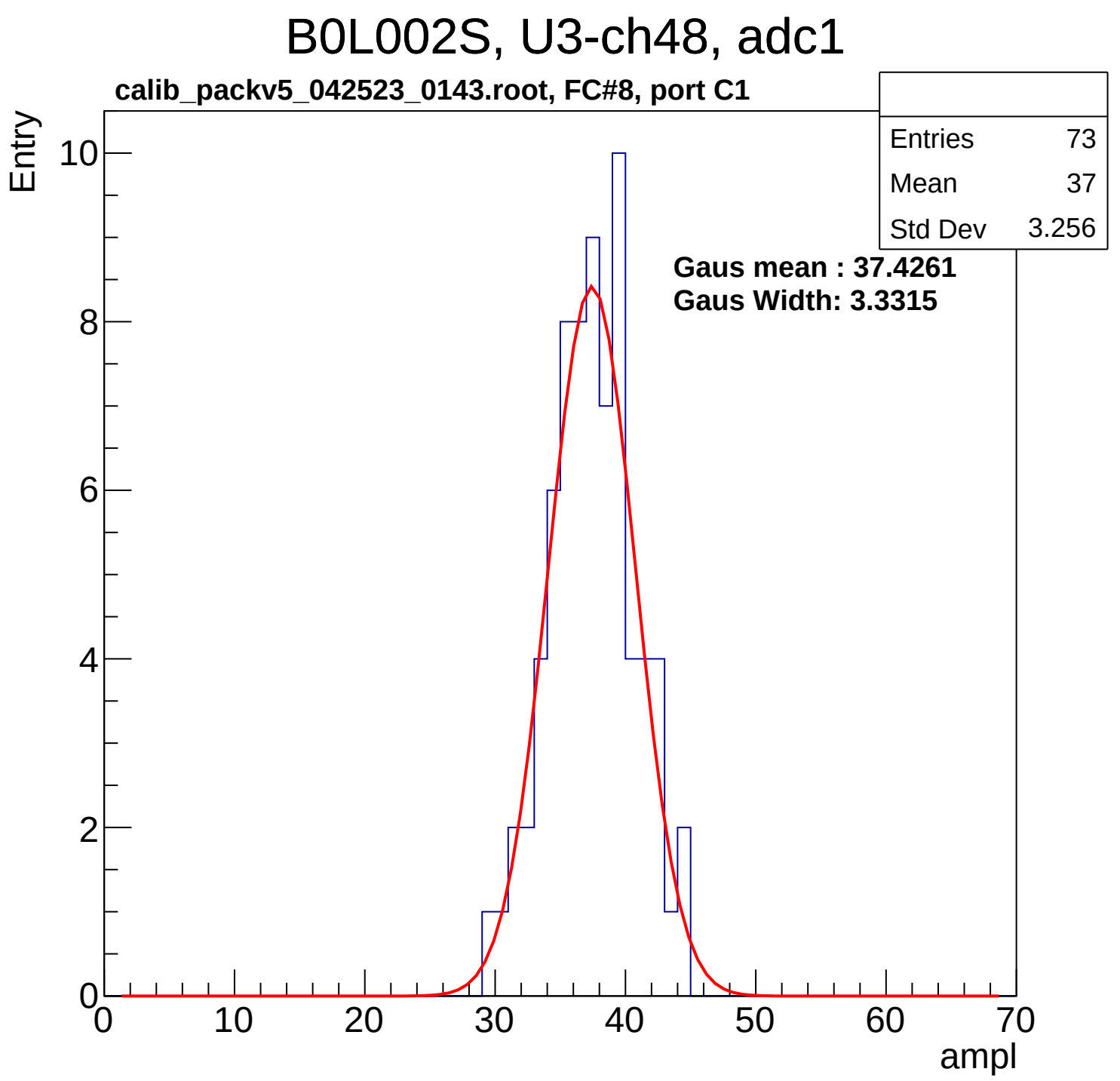

B0L002S, U3-ch47, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.46
Std Dev 3.424
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

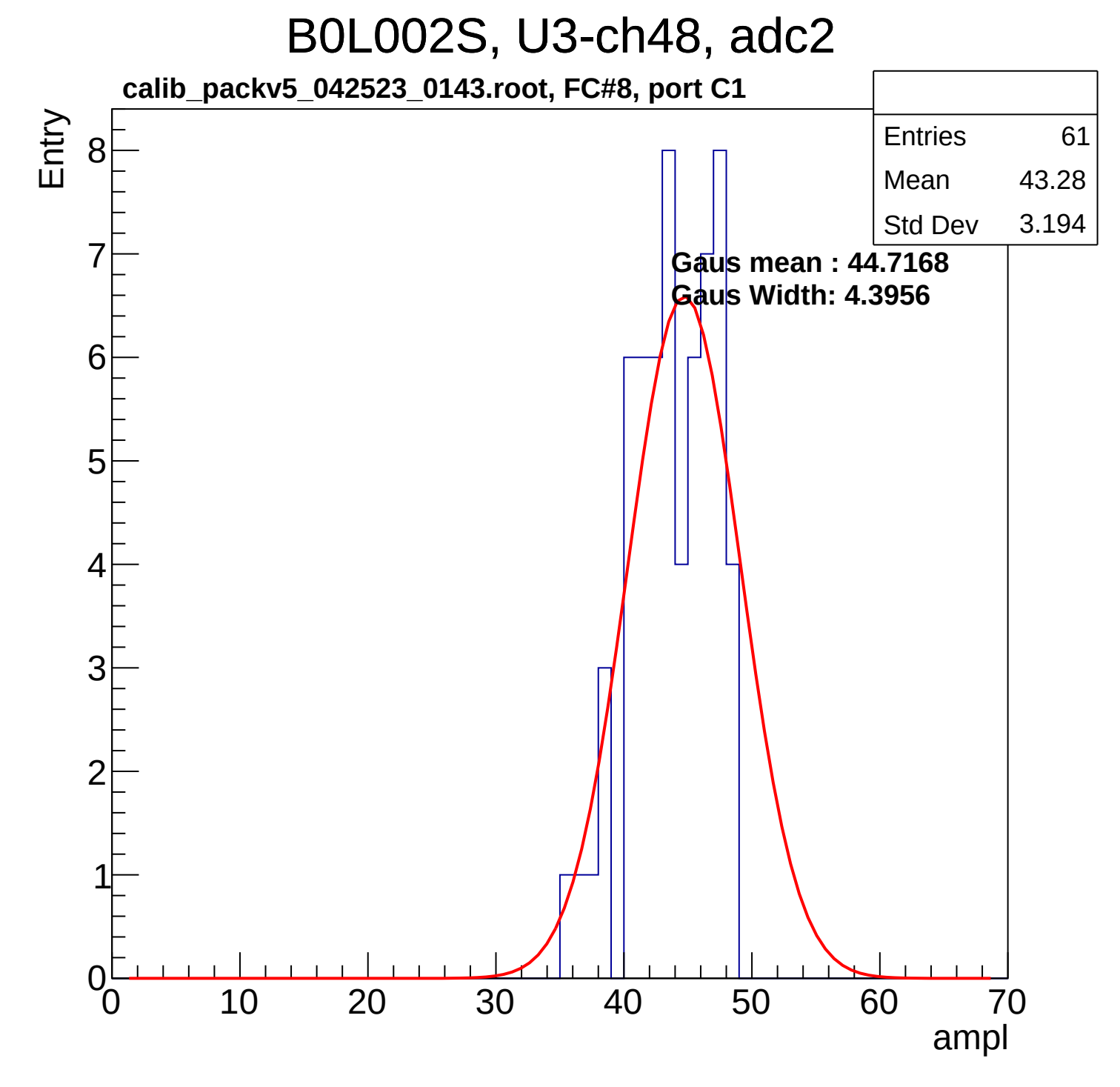

B0L002S, U3-ch47, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 58.8
Std Dev 10.35
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

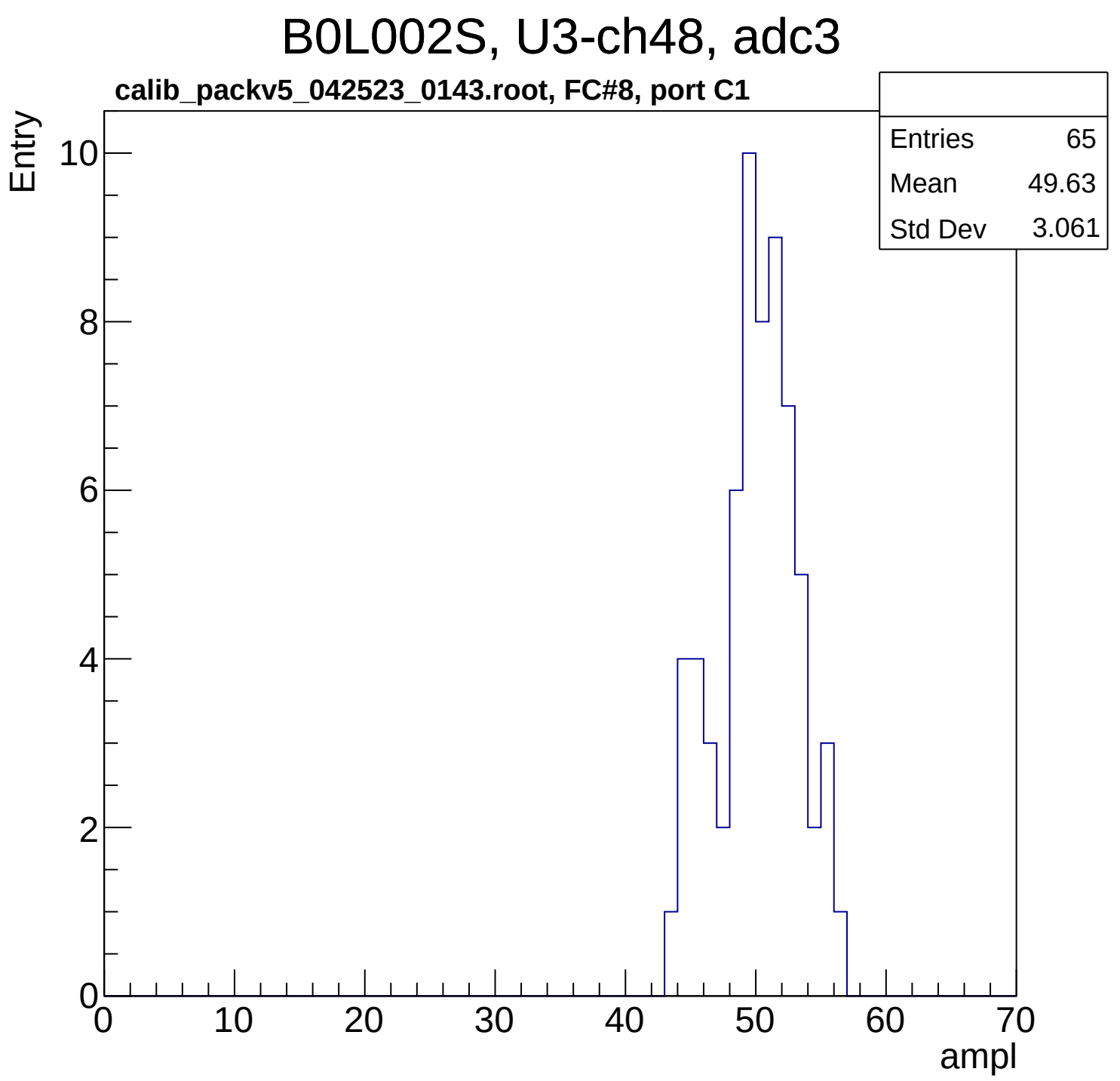

B0L002S, U3-ch47, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

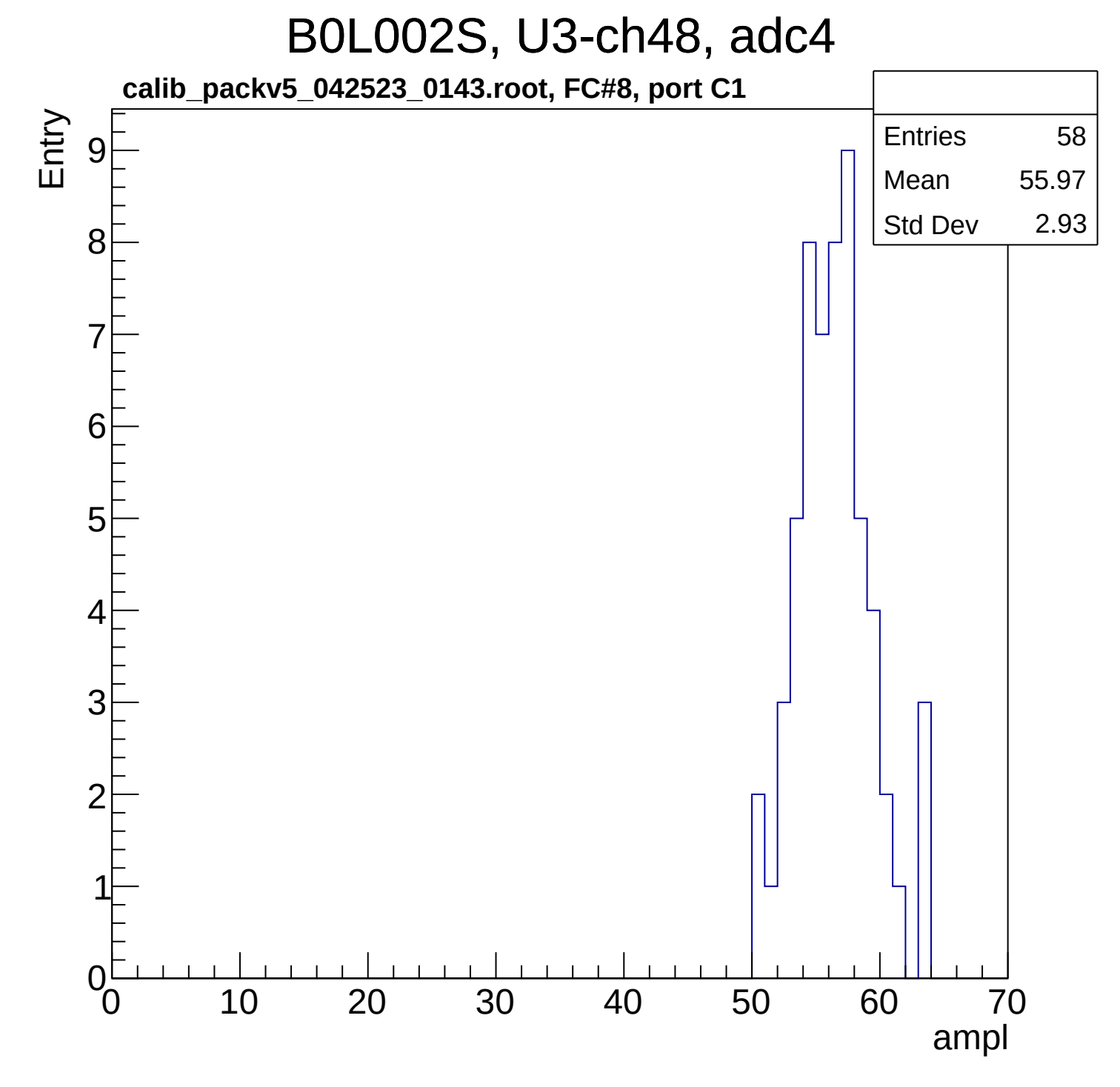

B0L002S, U3-ch47, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch48, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 29.47
Std Dev 4.885
Gaus mean : 30.7029
Gaus Width: 2.7994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch48, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37
Std Dev 3.256
Gaus mean : 37.4261
Gaus Width: 3.3315
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch48, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.28
Std Dev 3.194
Gaus mean : 44.7168
Gaus Width: 4.3956
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch48, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 49.63
Std Dev 3.061
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch48, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 55.97
Std Dev 2.93
Entry
ampl
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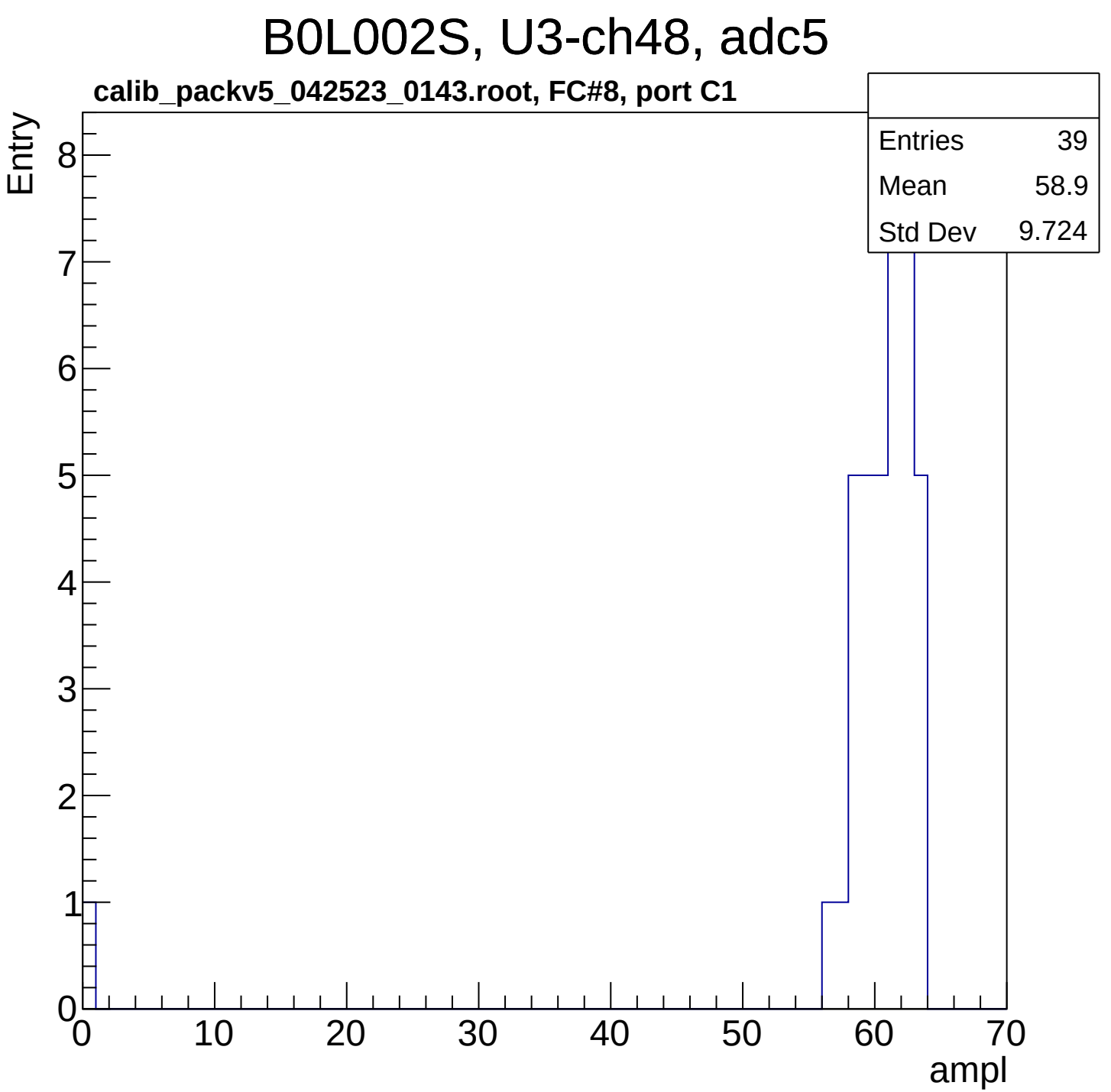
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch48, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.9
Std Dev 9.724
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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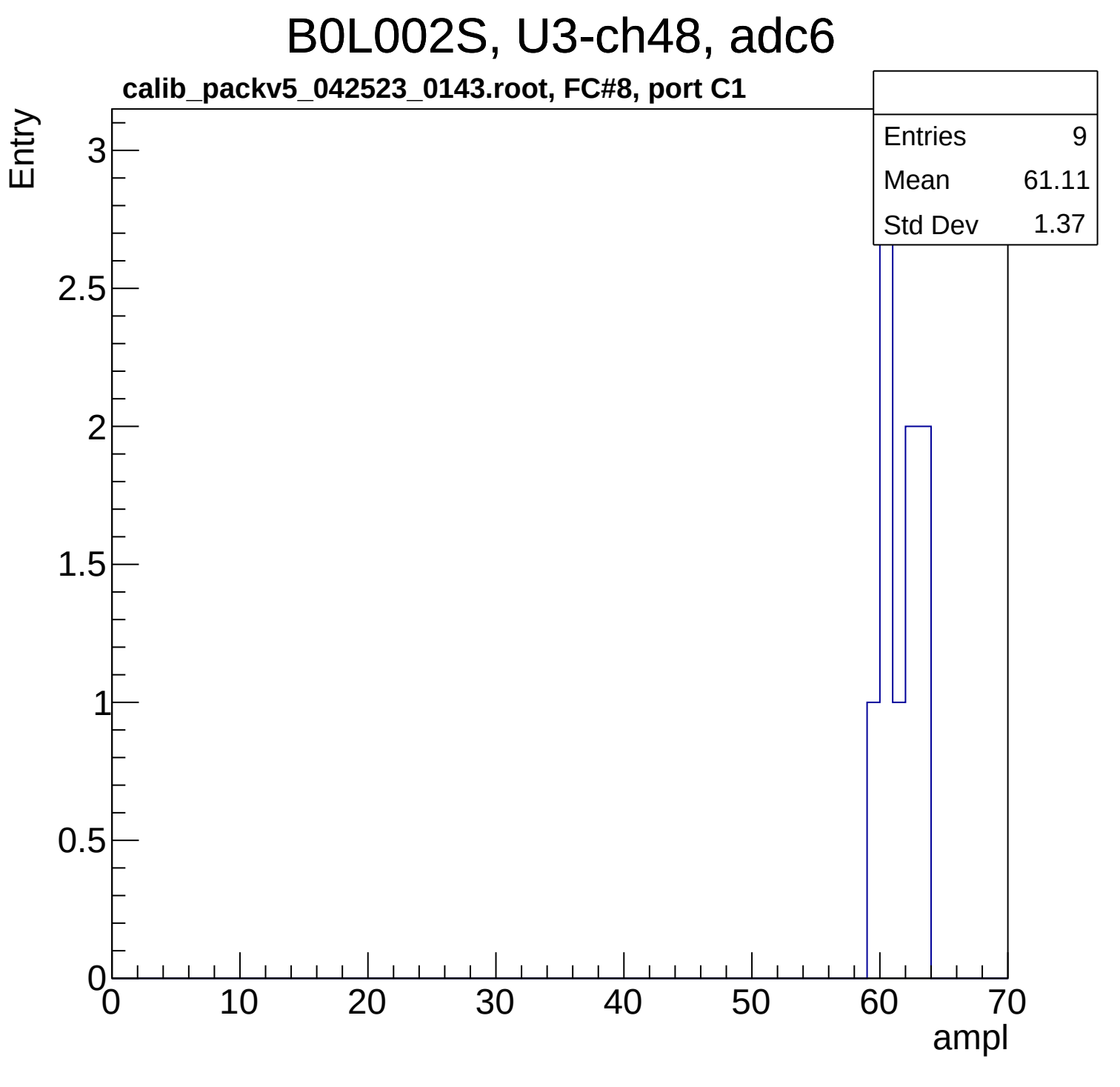
B0L002S, U3-ch48, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 61.11
Std Dev 1.37
Entry
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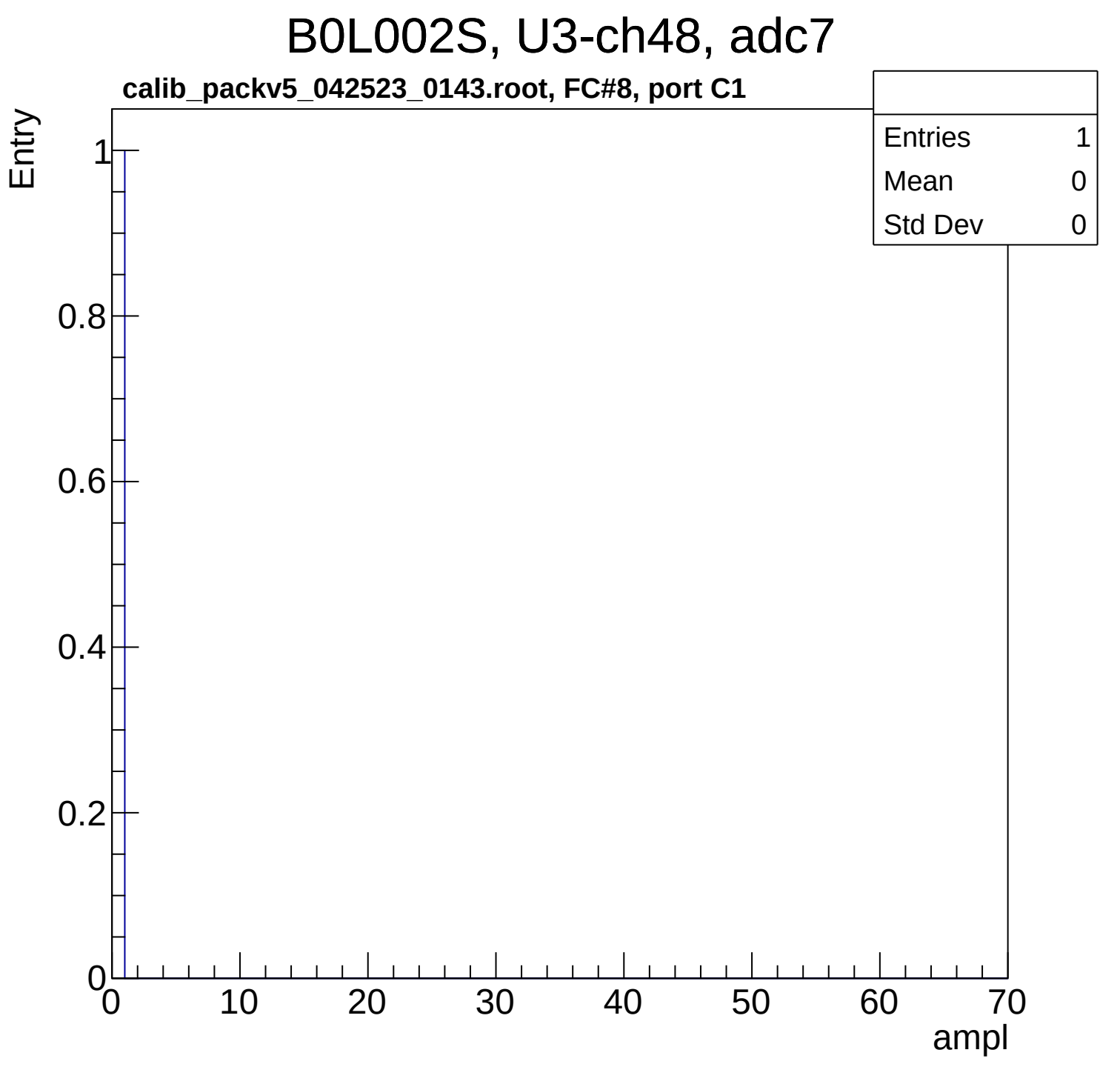
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch48, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
20
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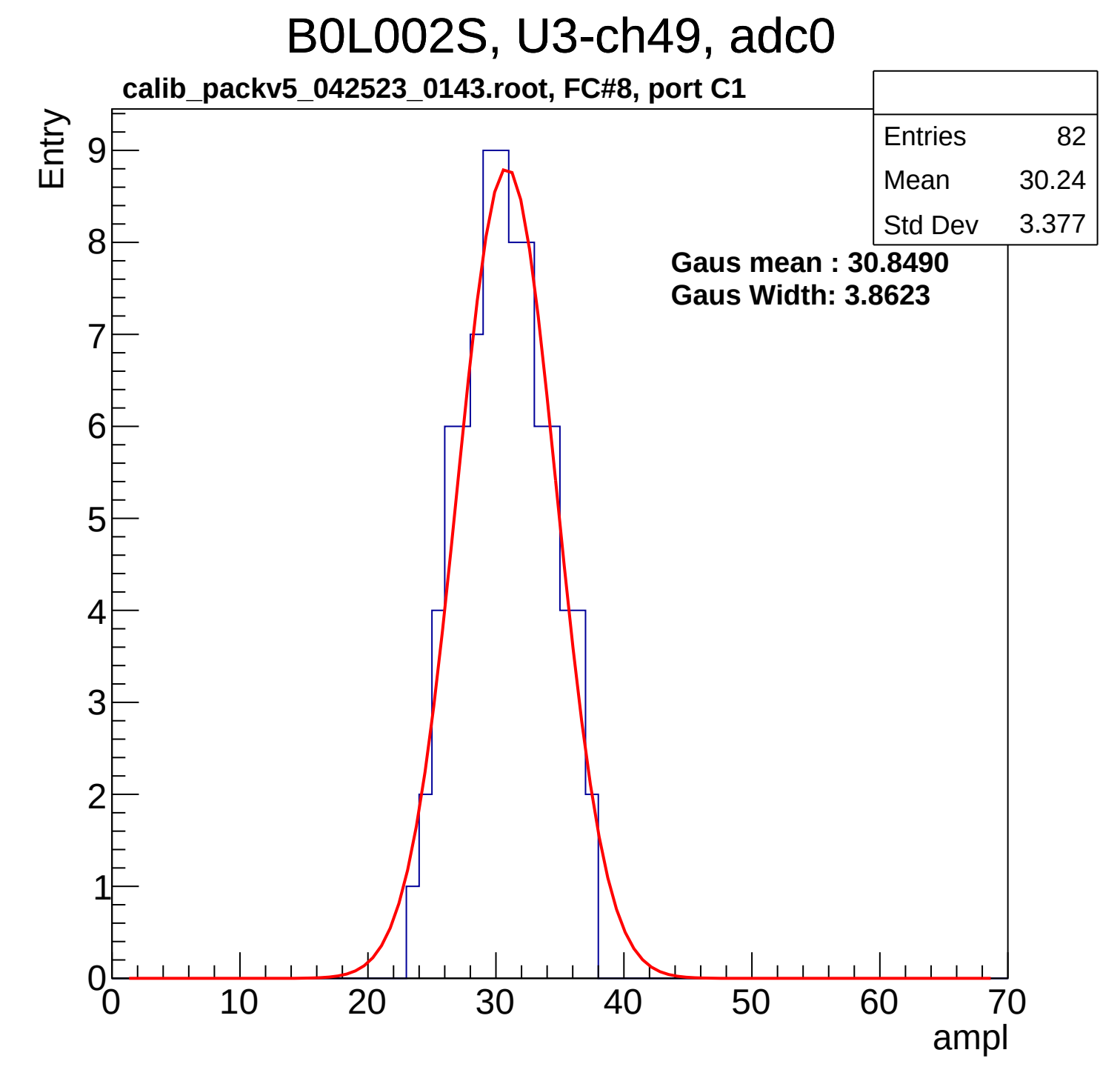
60
70
ampl


B0L002S, U3-ch49, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 30.24
Std Dev 3.377
Gaus mean : 30.8490
Gaus Width: 3.8623
Entry
ampl
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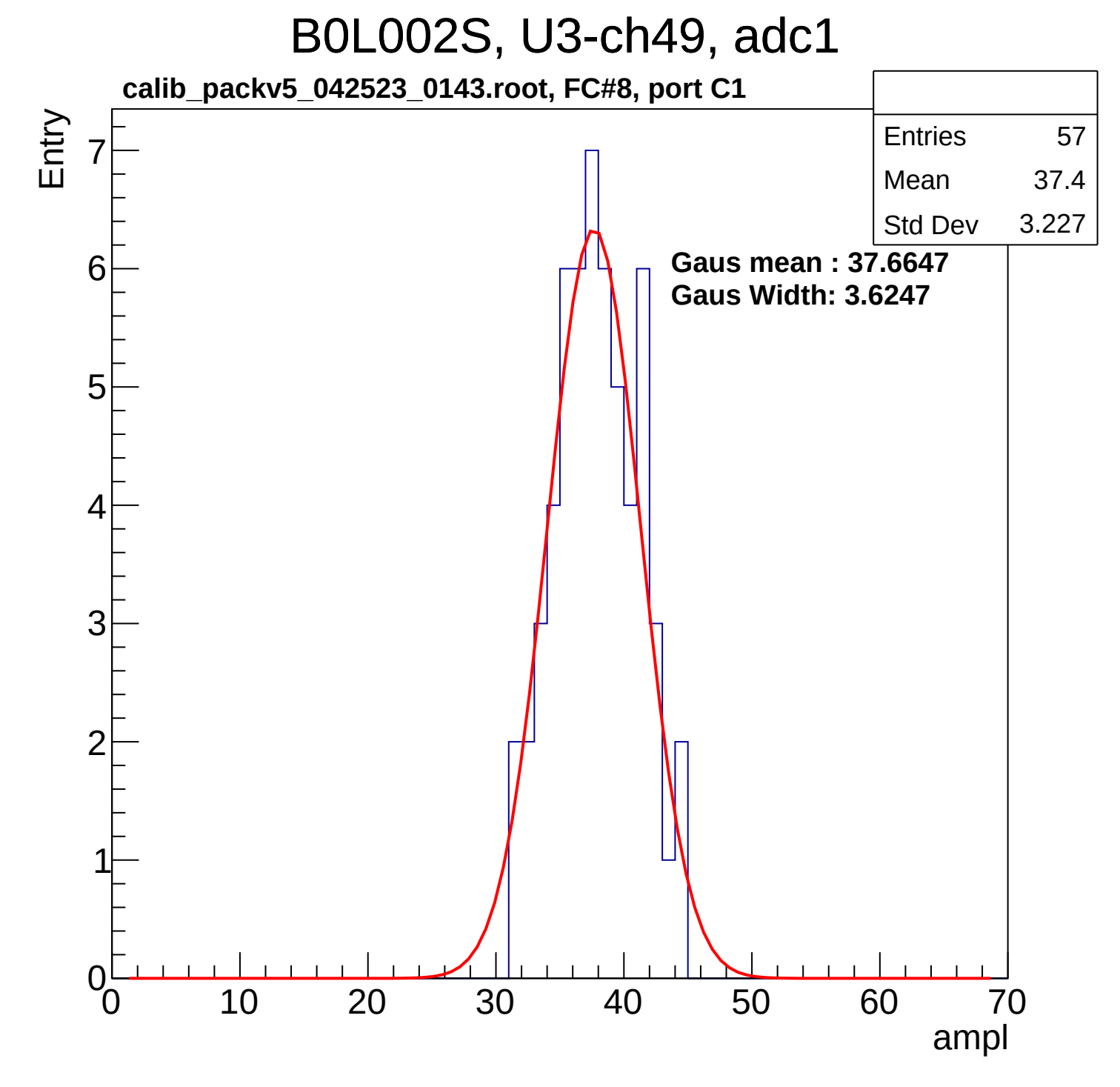
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch49, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 37.4
Std Dev 3.227
Gaus mean : 37.6647
Gaus Width: 3.6247
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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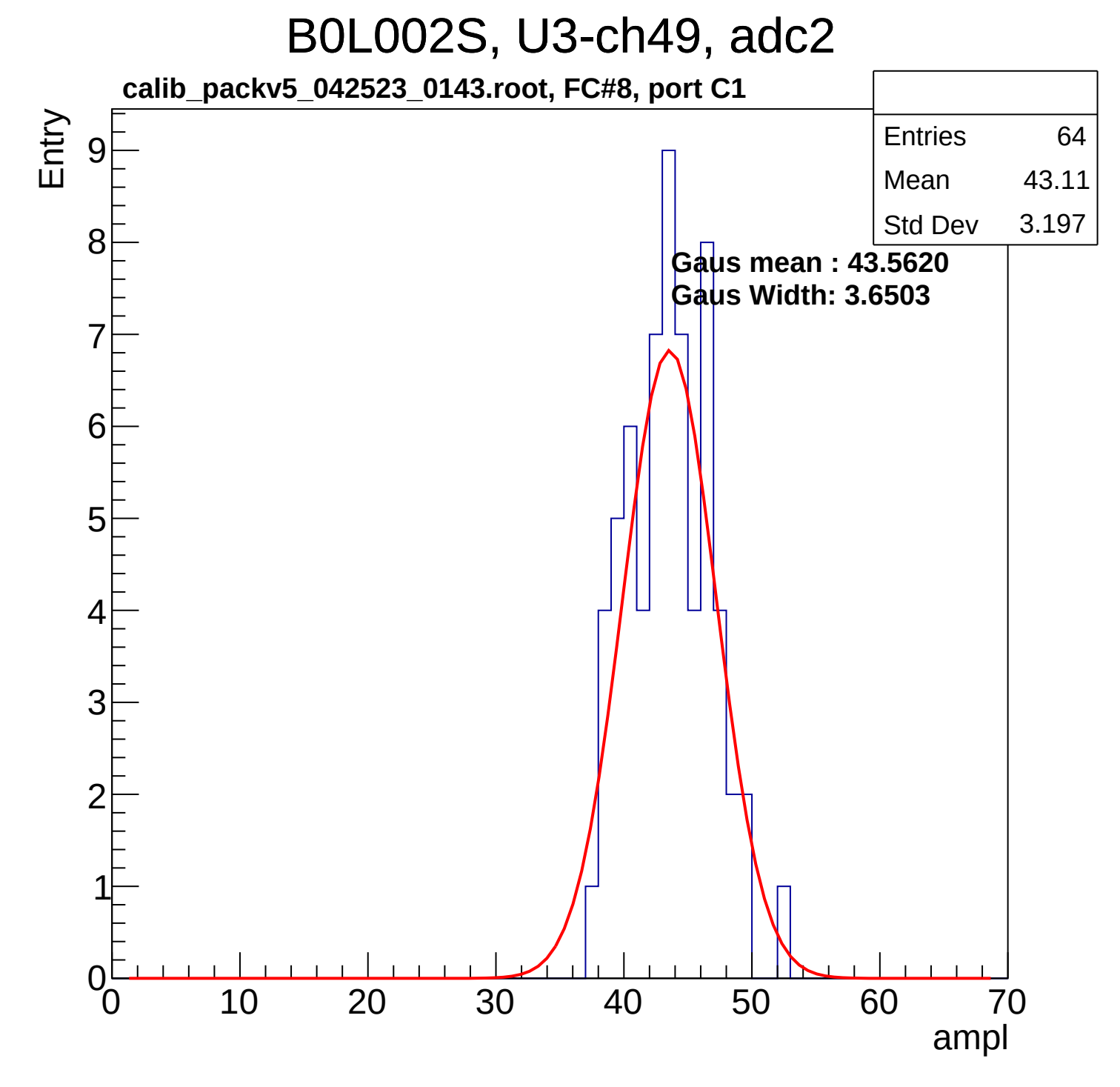
B0L002S, U3-ch49, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 43.11
Std Dev 3.197
Gaus mean : 43.5620
Gaus Width: 3.6503
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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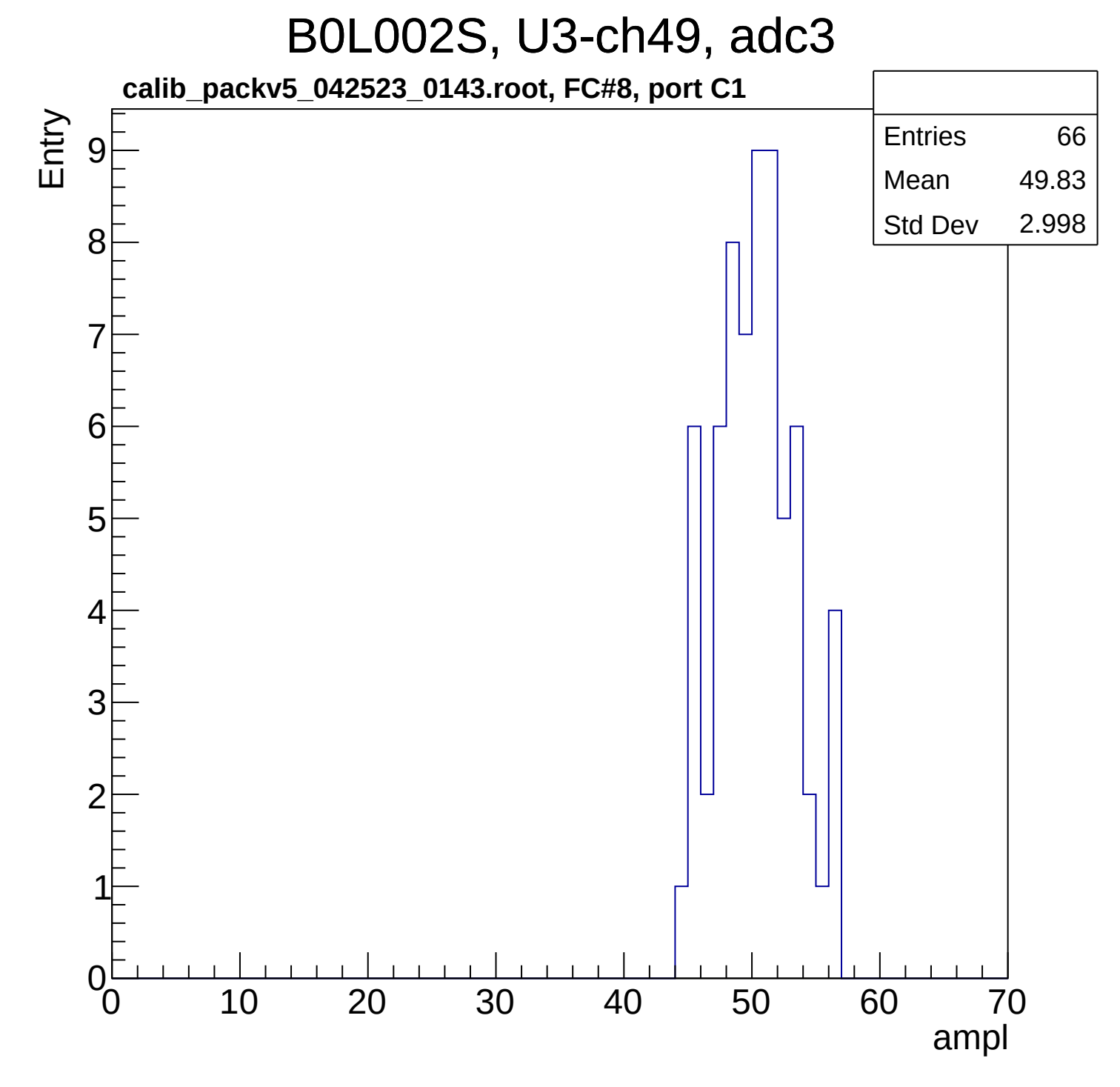
B0L002S, U3-ch49, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.83
Std Dev 2.998
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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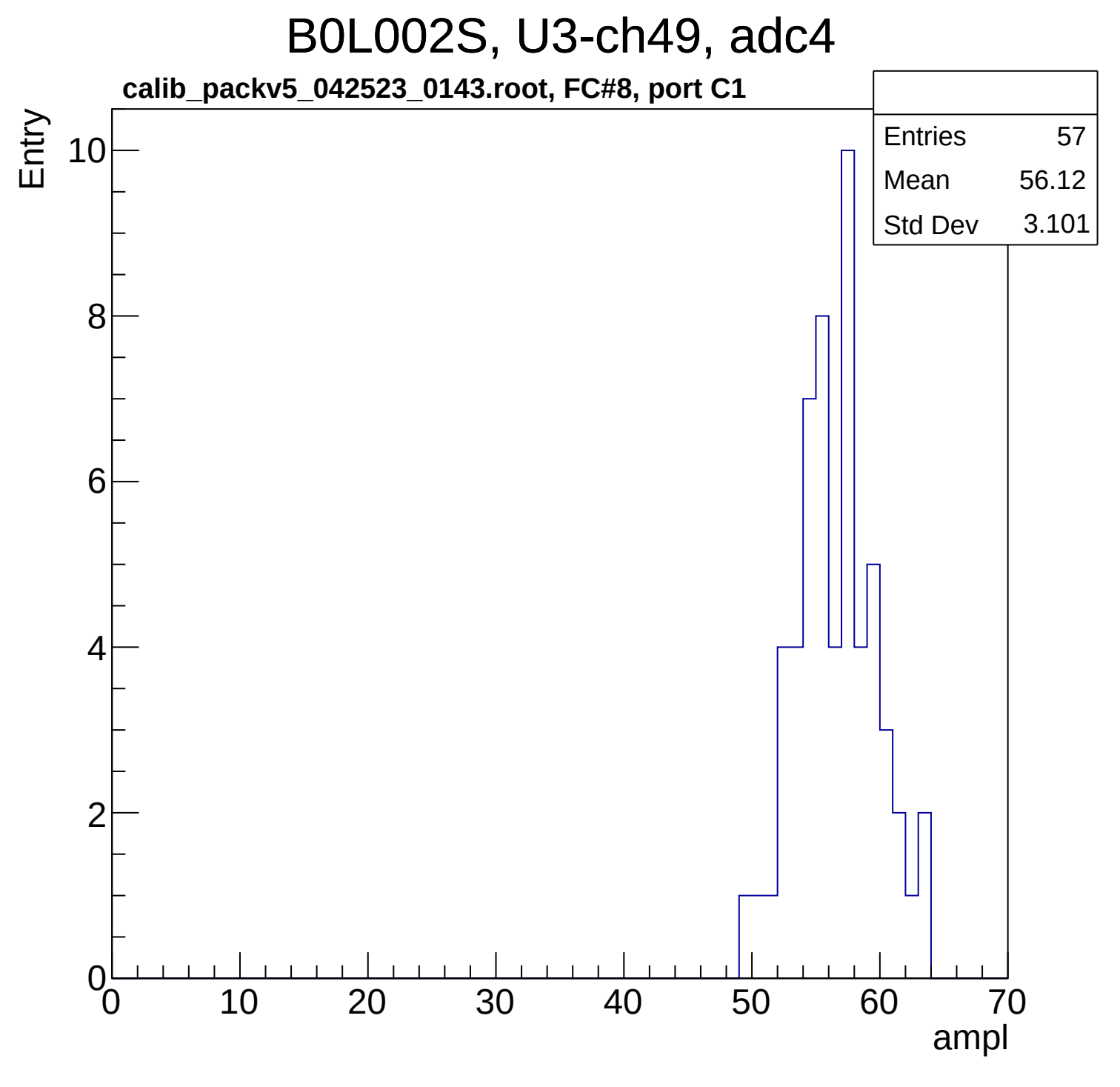
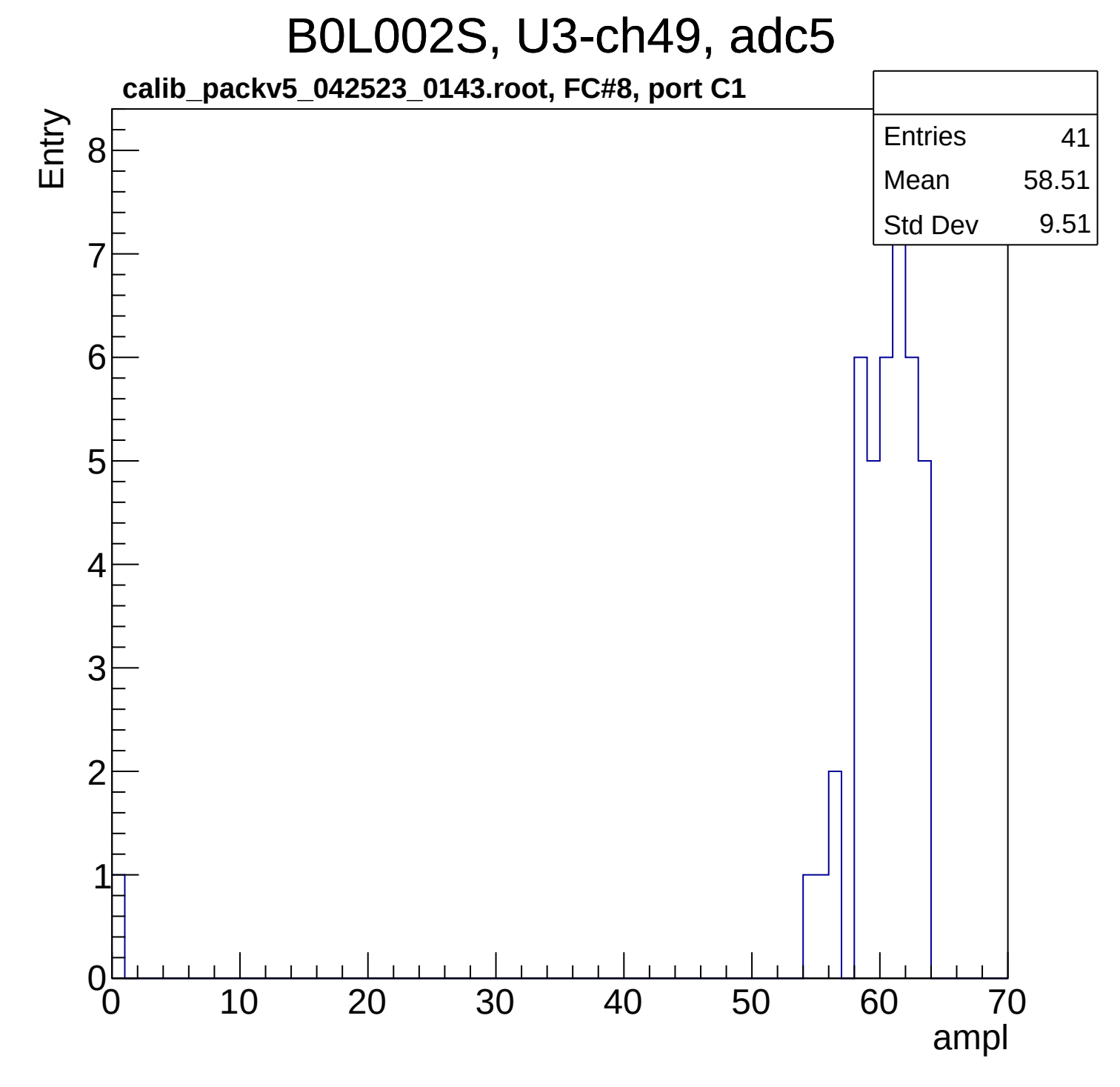
40
50
60
70


B0L002S, U3-ch49, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.12
Std Dev 3.101
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch49, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.51
Std Dev 9.51
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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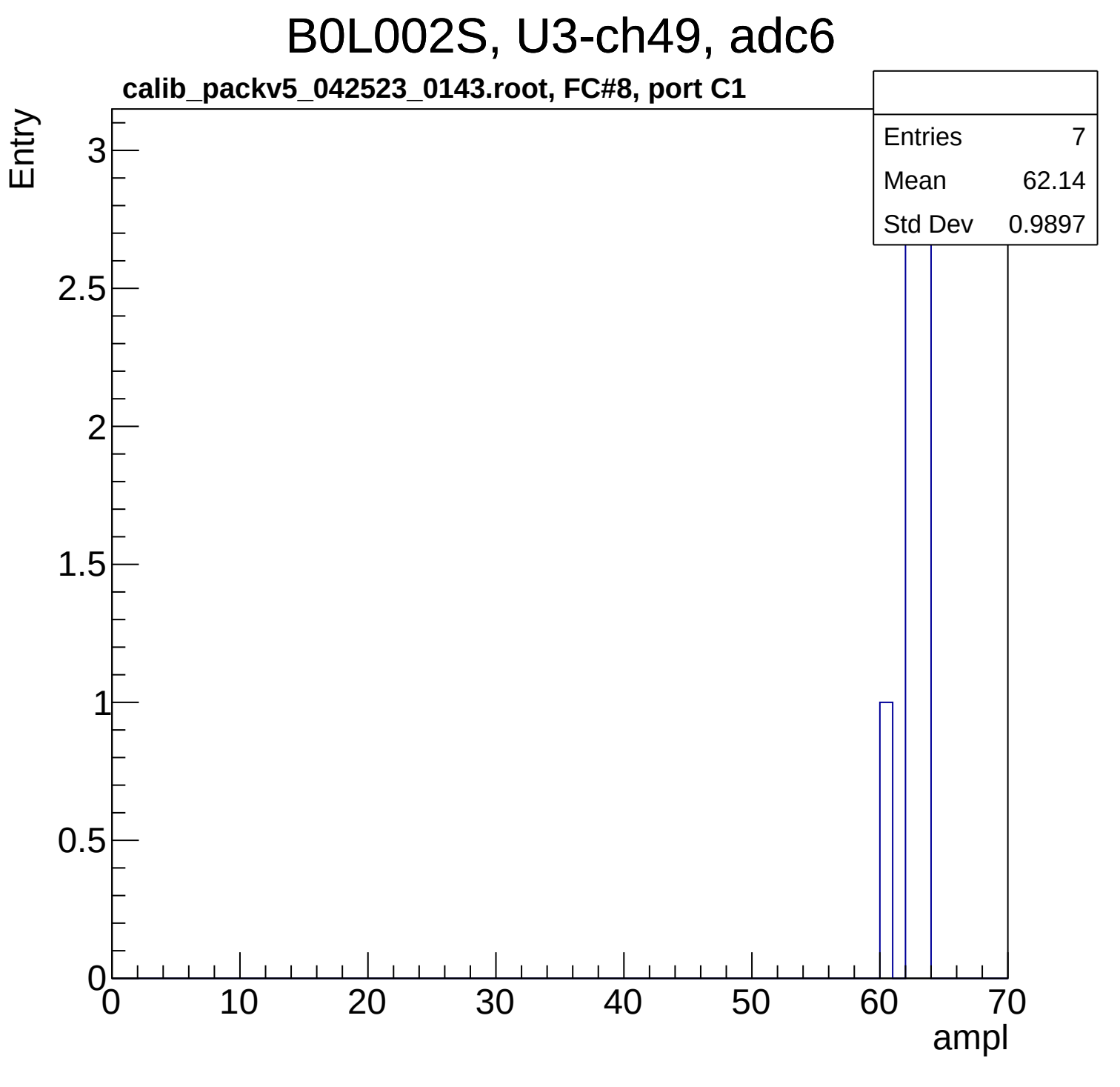
40
50
60
70


B0L002S, U3-ch49, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
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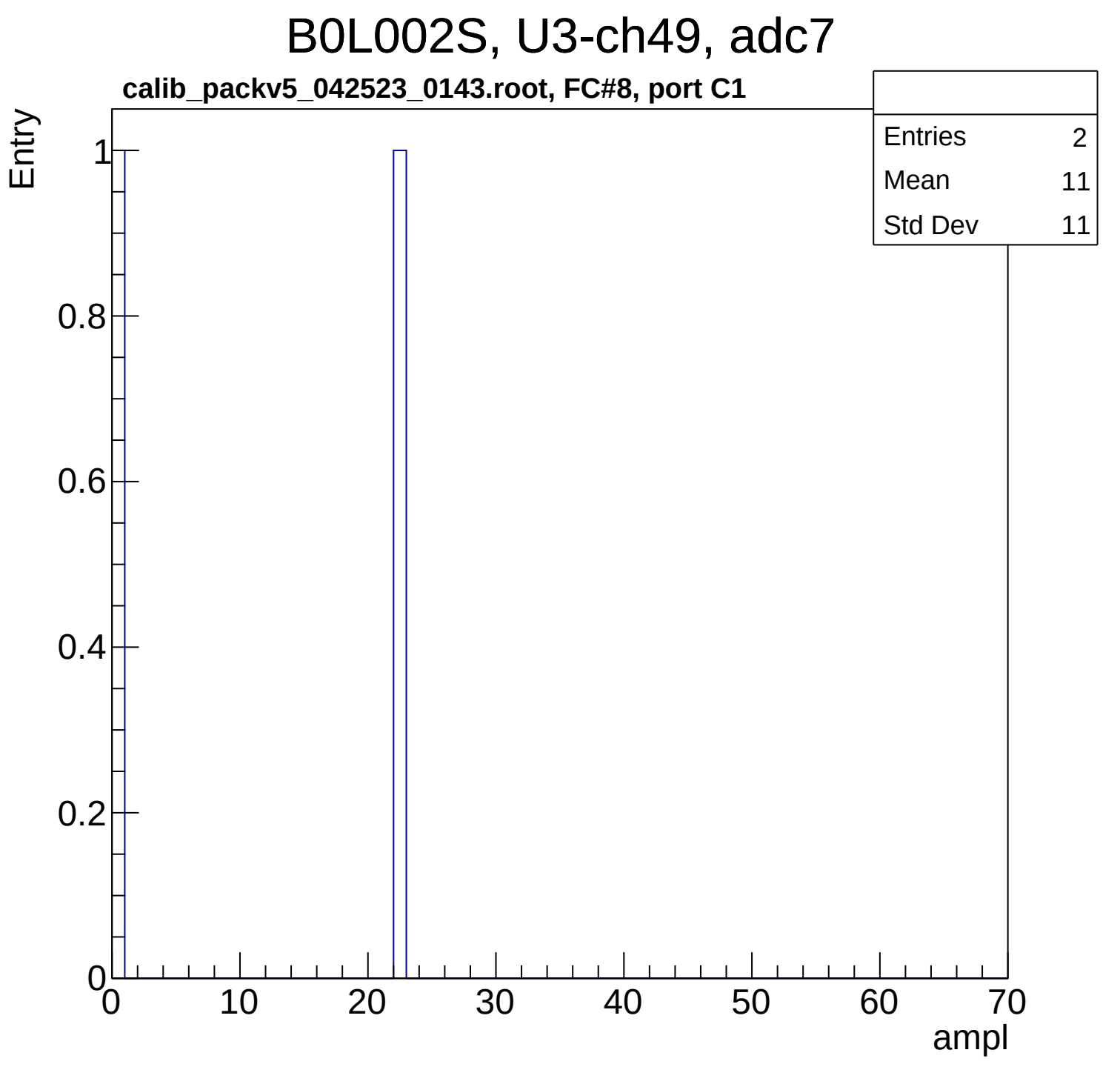
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch49, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
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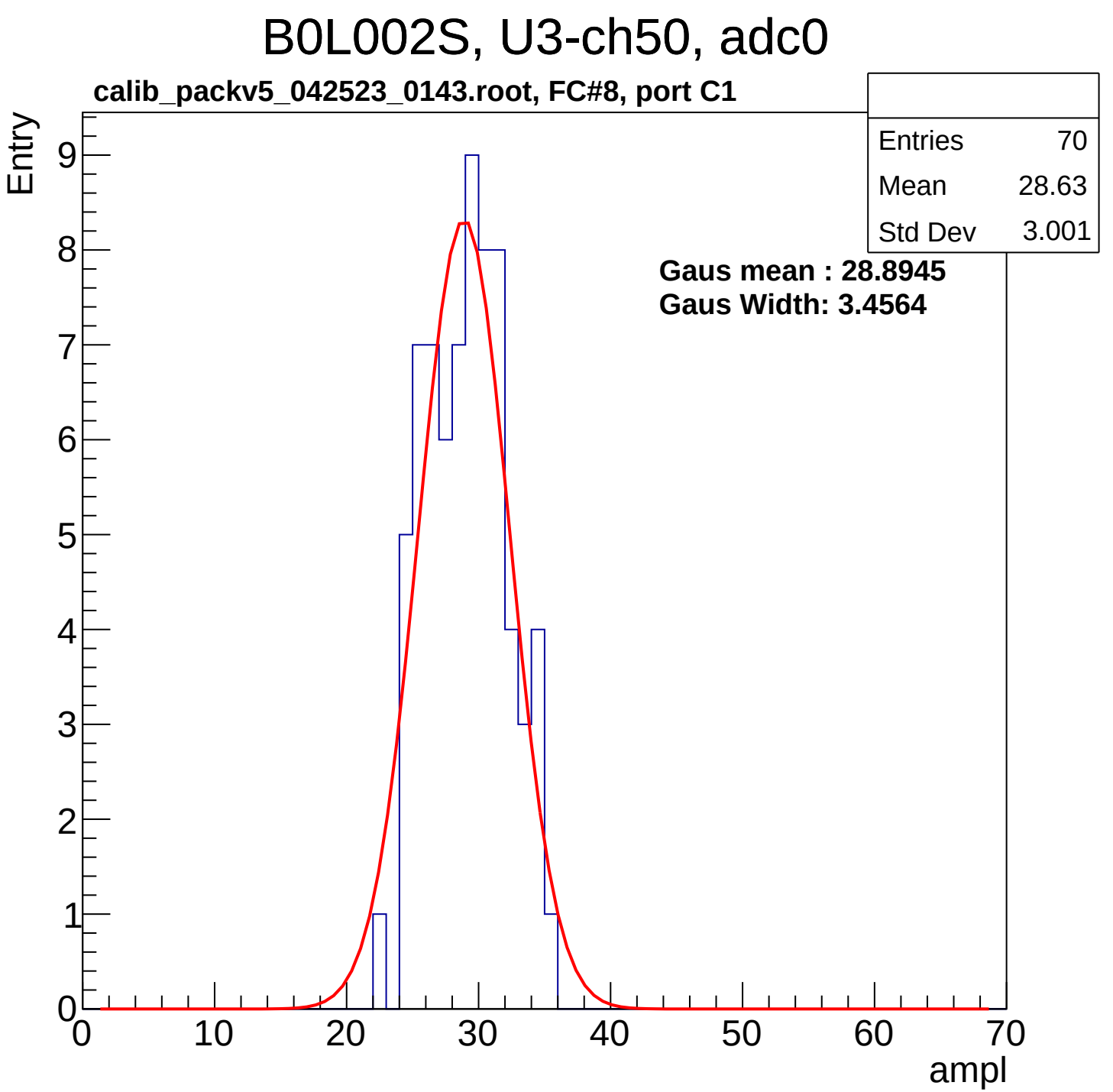
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch50, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 28.63
Std Dev 3.001
Gaus mean : 28.8945
Gaus Width: 3.4564
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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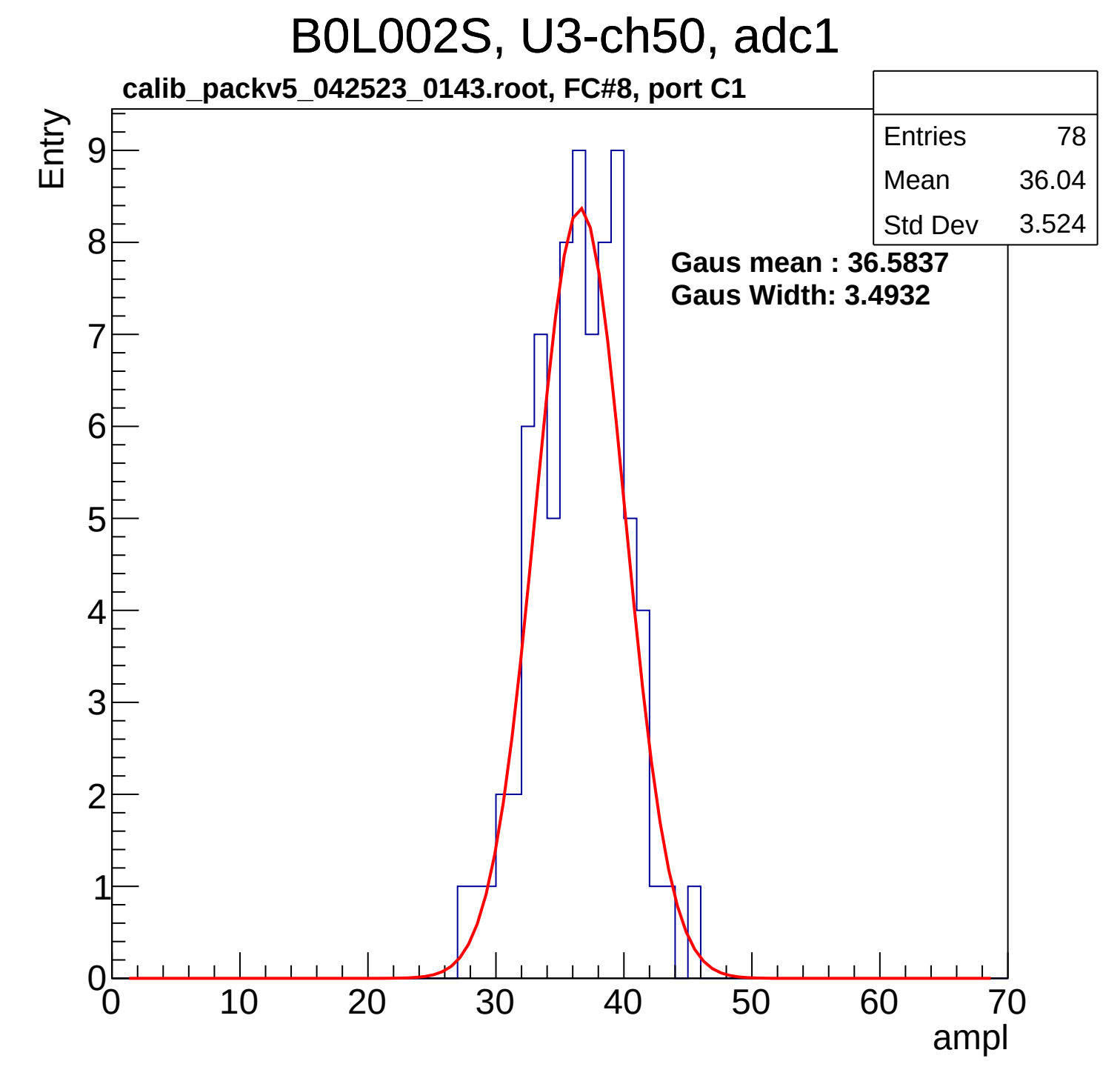
B0L002S, U3-ch50, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 36.04
Std Dev 3.524
Gaus mean : 36.5837
Gaus Width: 3.4932
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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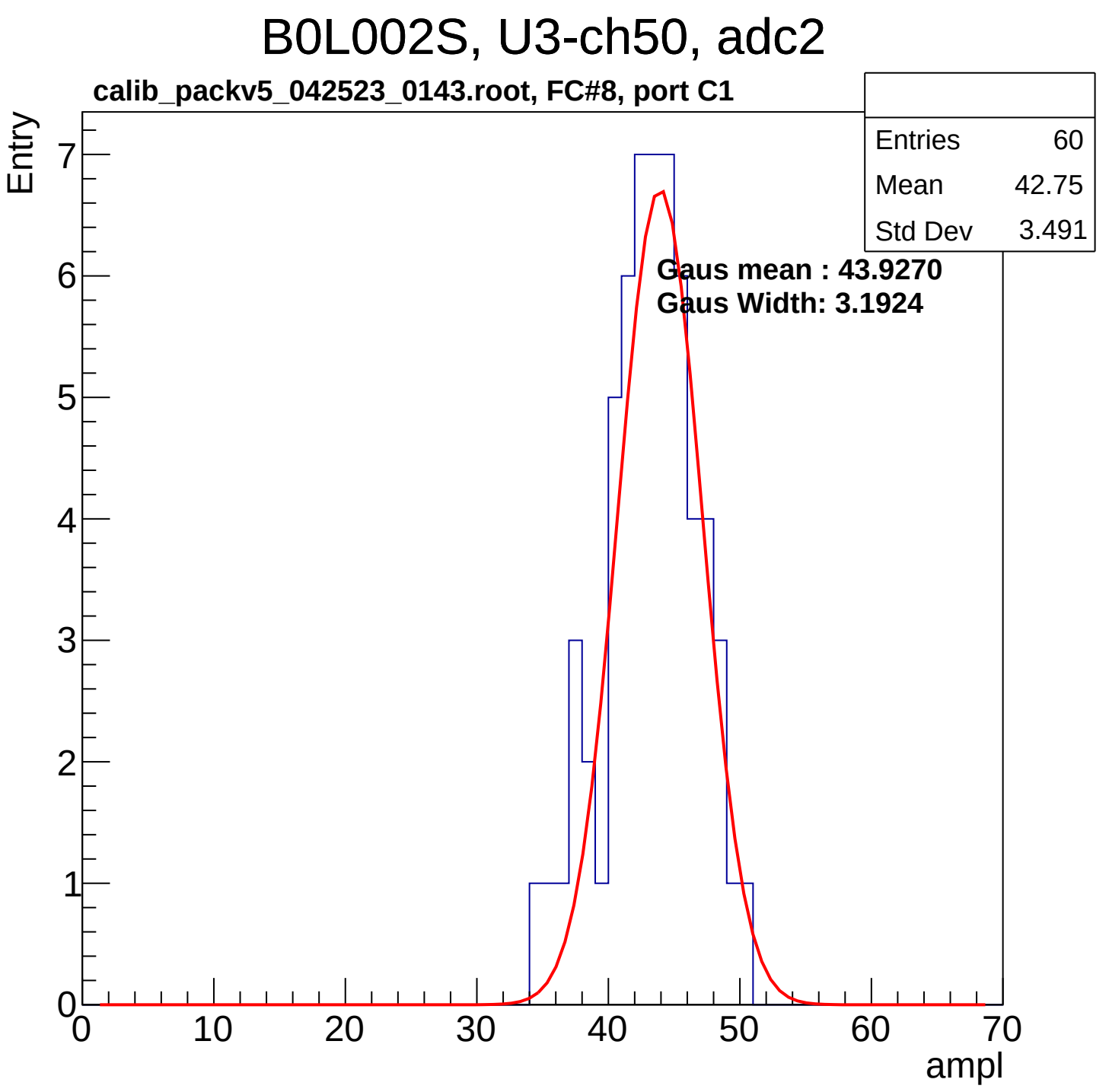
B0L002S, U3-ch50, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 42.75
Std Dev 3.491
Gaus mean : 43.9270
Gaus Width: 3.1924
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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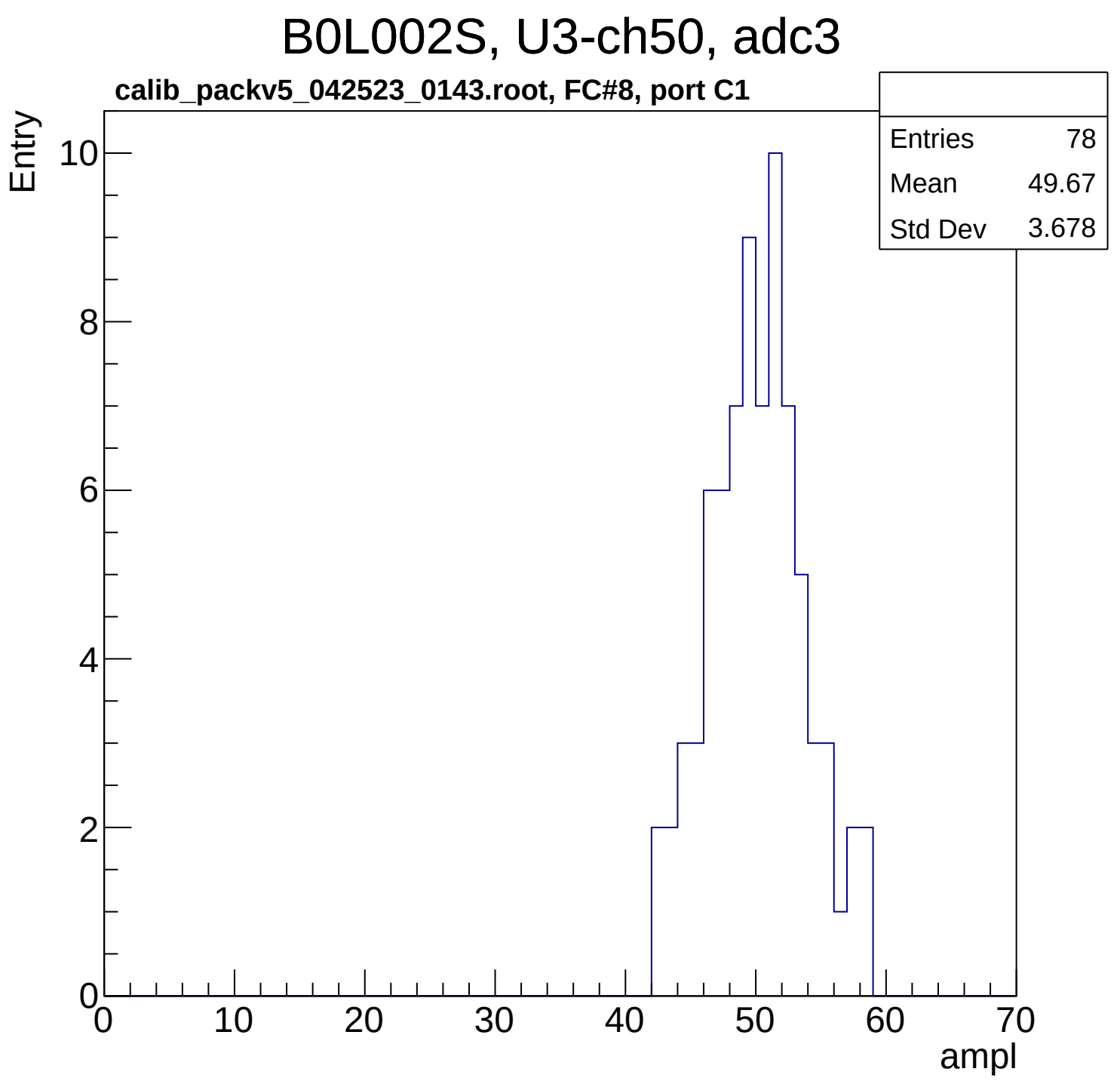
B0L002S, U3-ch50, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 49.67
Std Dev 3.678
Entry
ampl
0
2
4
6
8
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0
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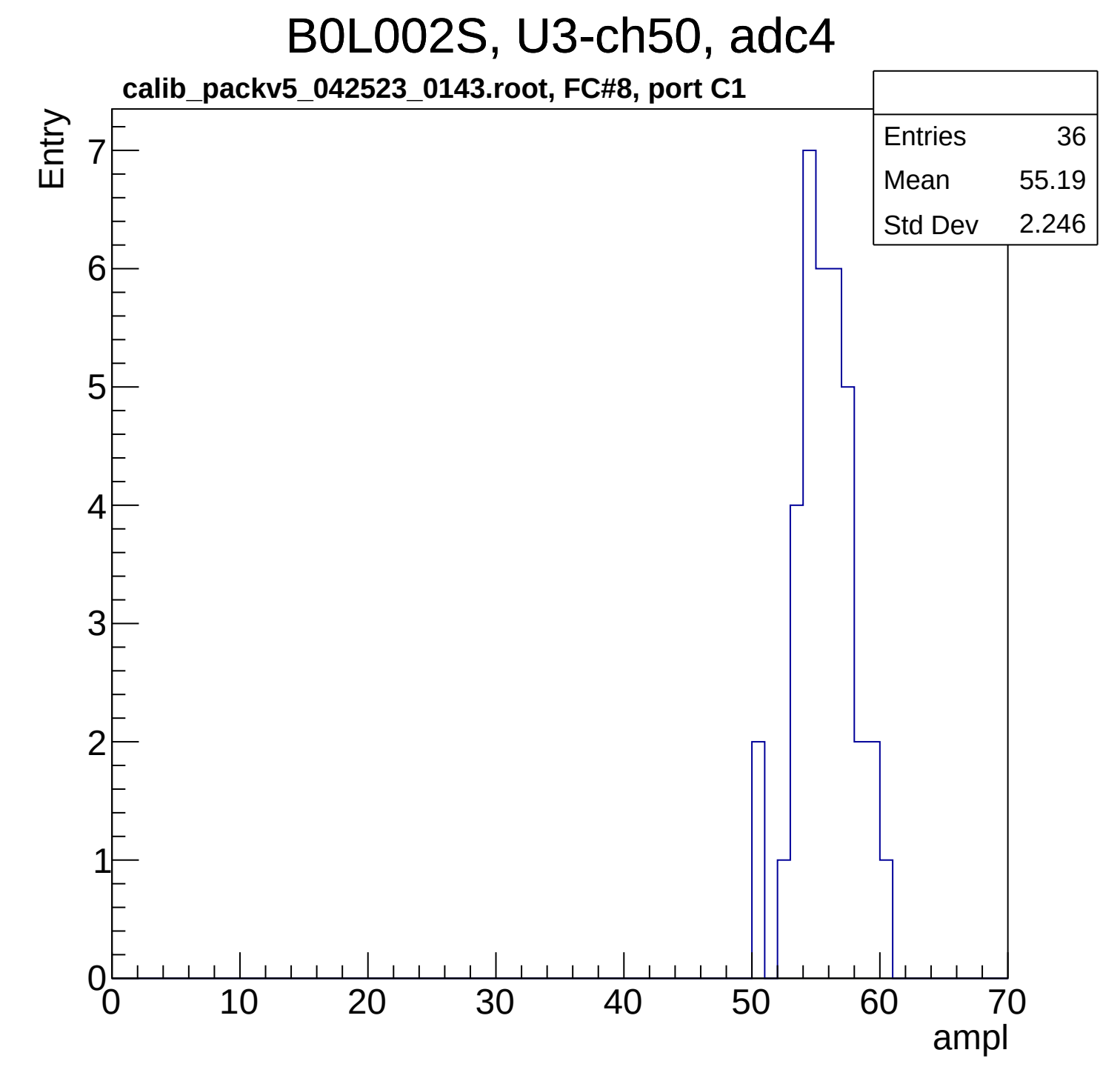
30
40
50
60
70


B0L002S, U3-ch50, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 55.19
Std Dev 2.246
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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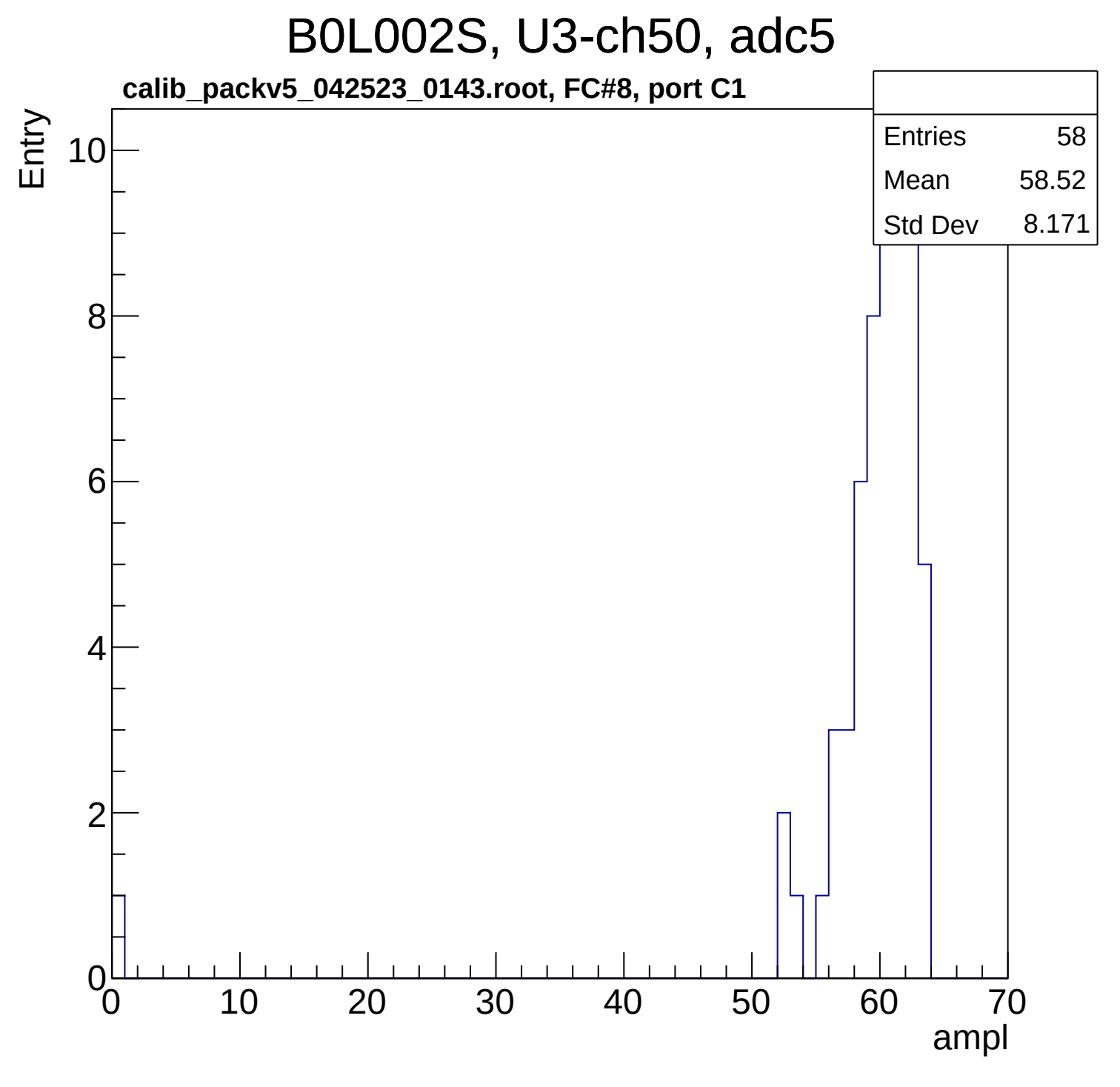
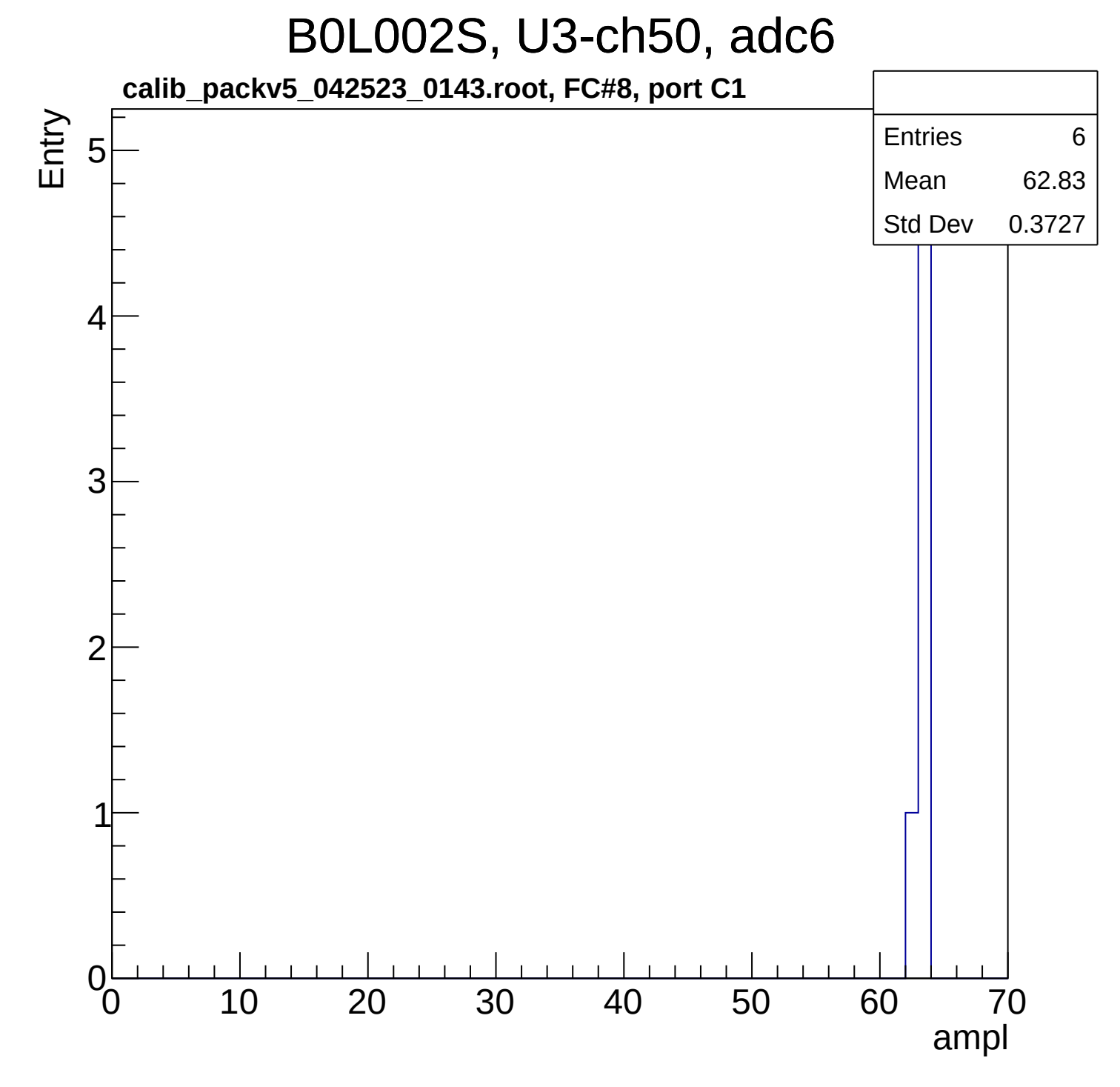
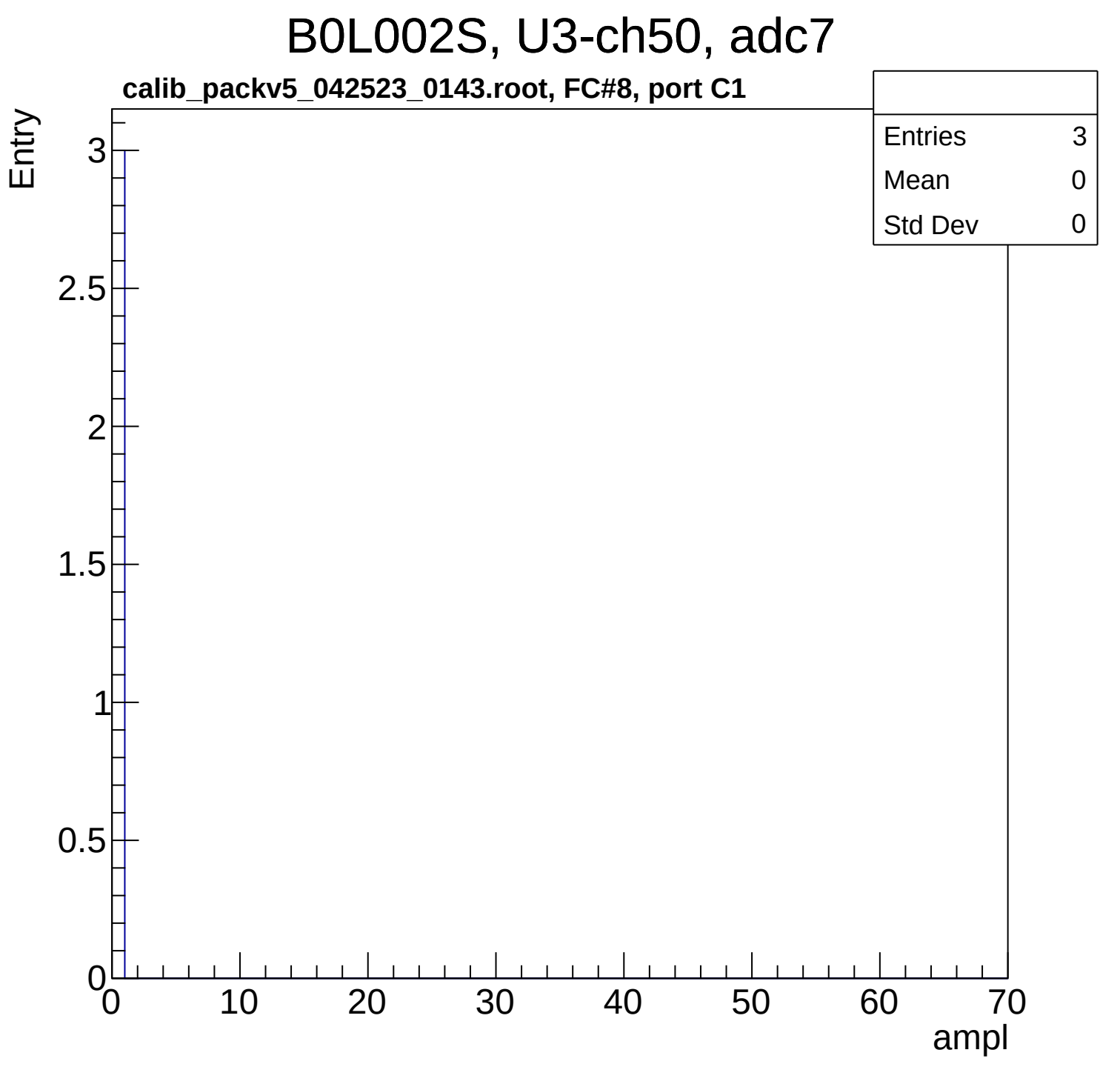
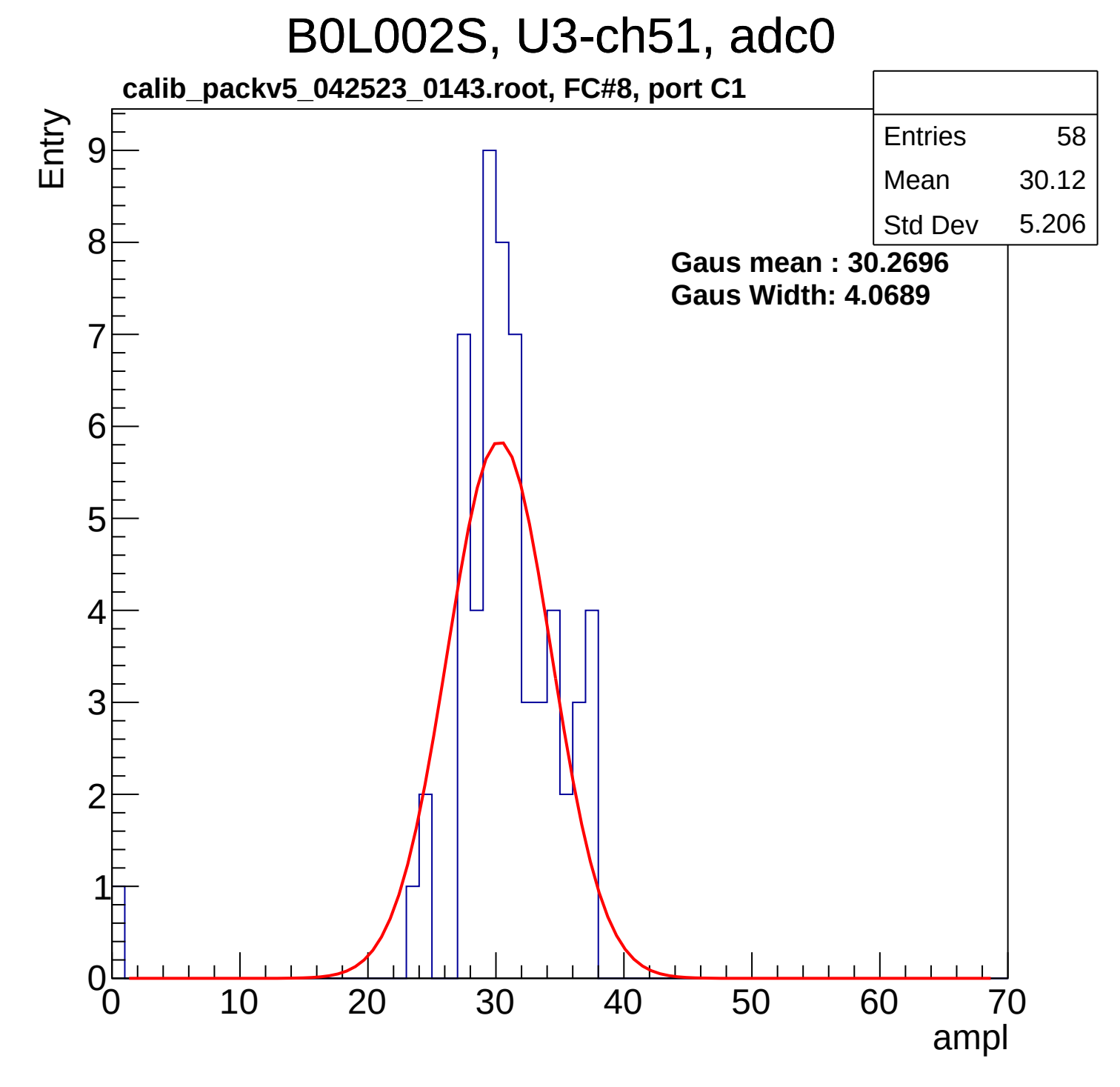
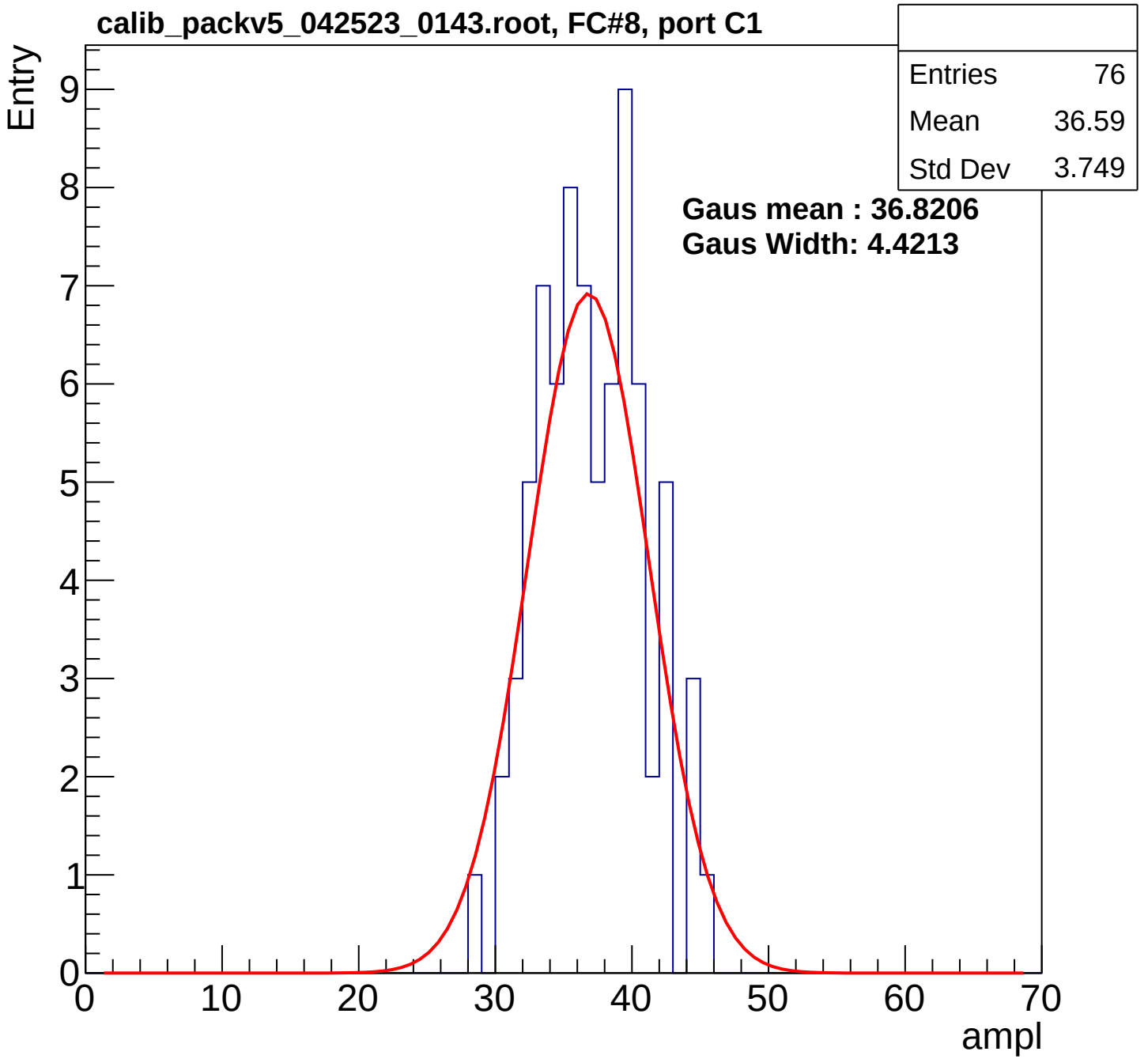

B0L002S, U3-ch50, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 58.52
Std Dev 8.171
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch50, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 62.83
Std Dev 0.3727
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L002S, U3-ch50, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch51, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 30.12
Std Dev 5.206
Gaus mean : 30.2696
Gaus Width: 4.0689
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch51, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 36.59
Std Dev 3.749
Gaus mean : 36.8206
Gaus Width: 4.4213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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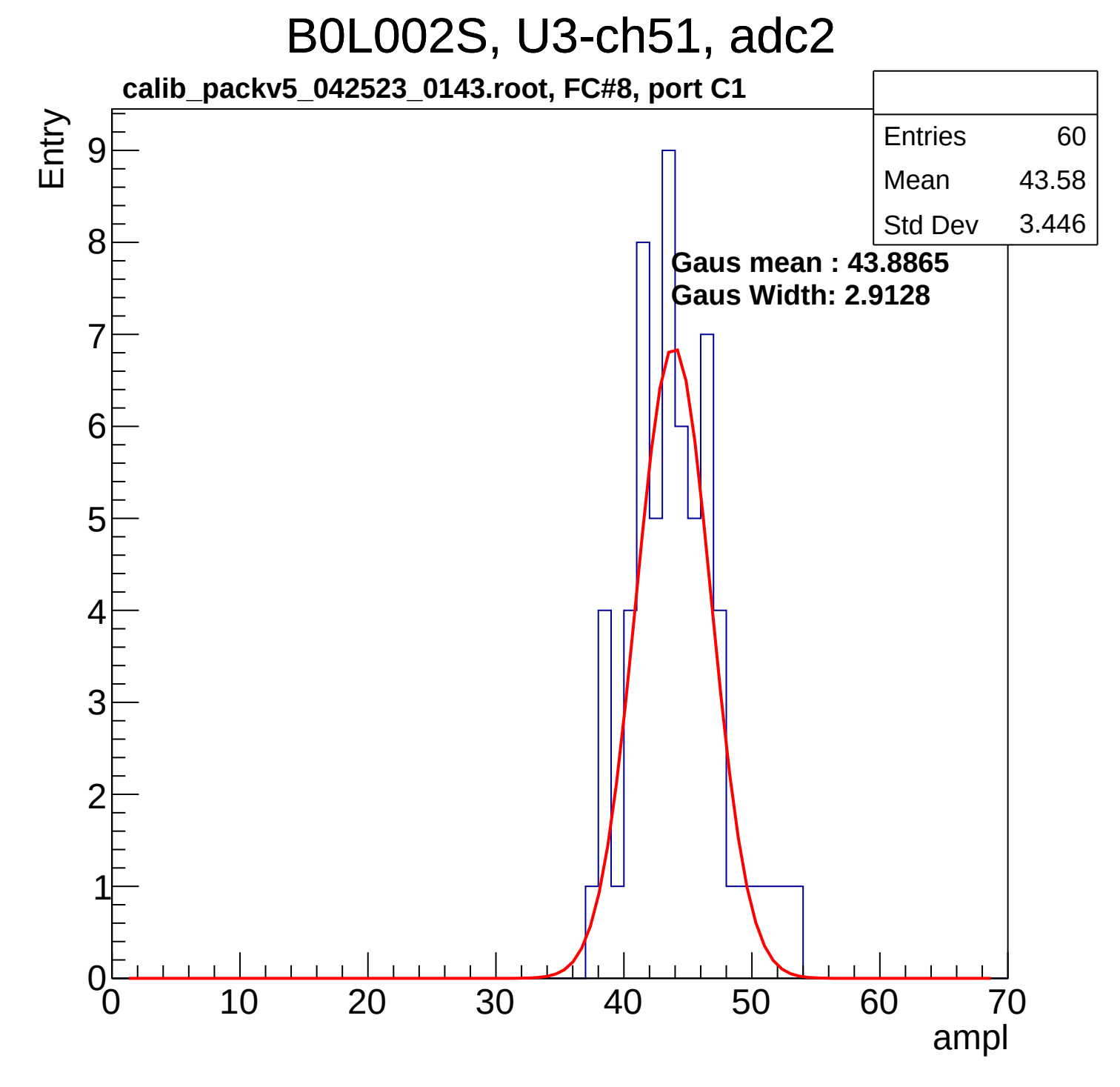
B0L002S, U3-ch51, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 43.58
Std Dev 3.446
Gaus mean : 43.8865
Gaus Width: 2.9128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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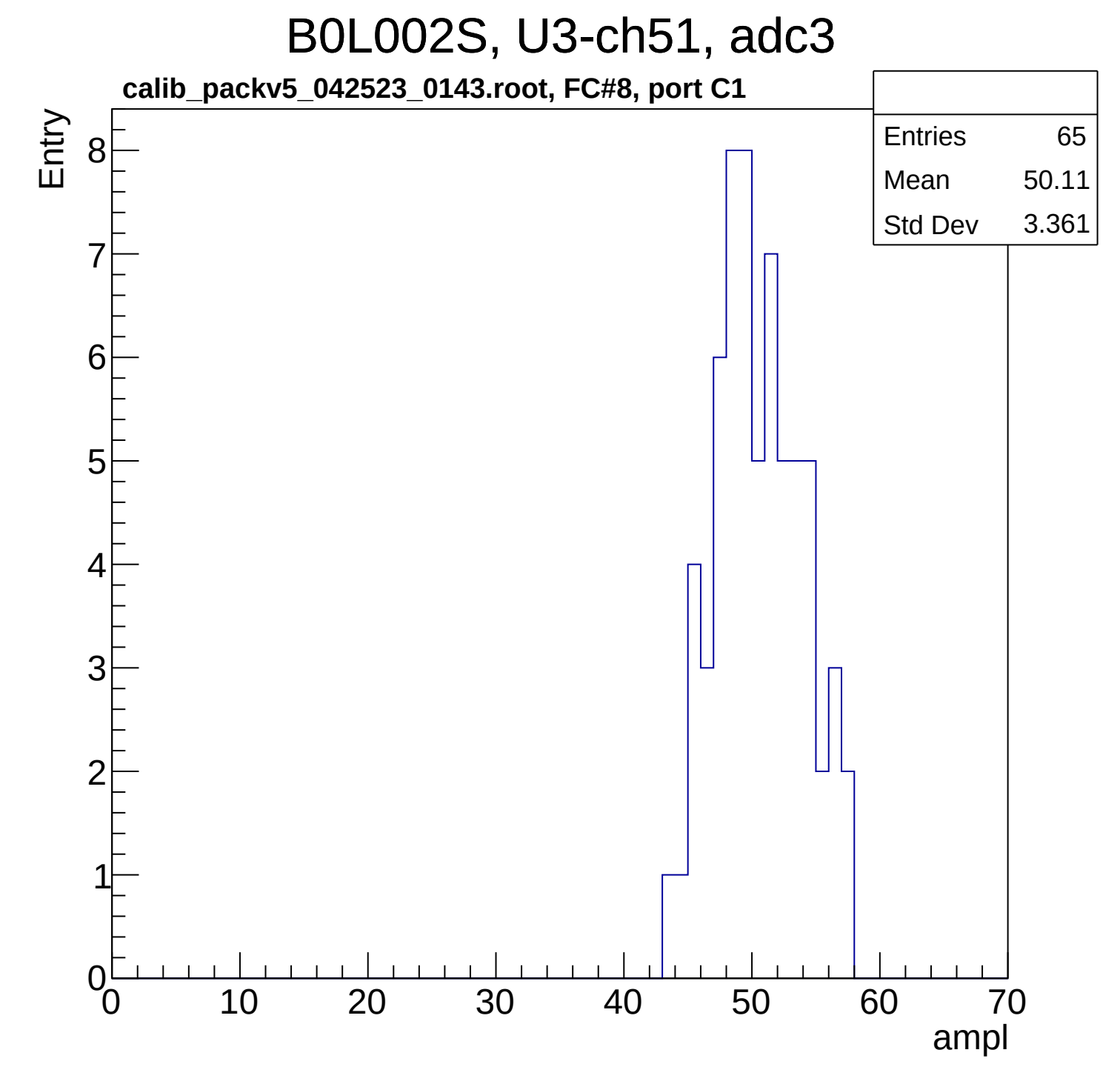
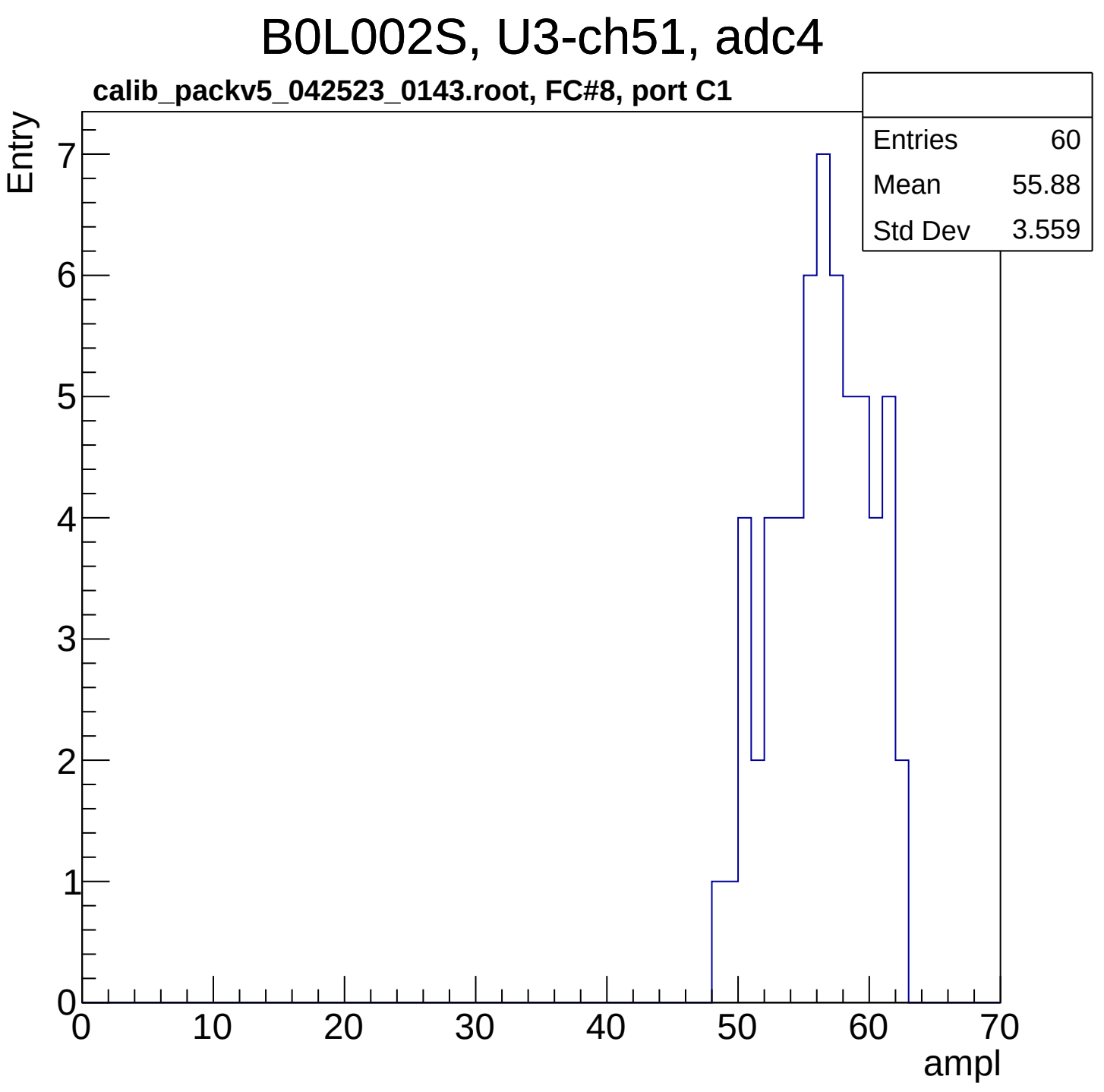
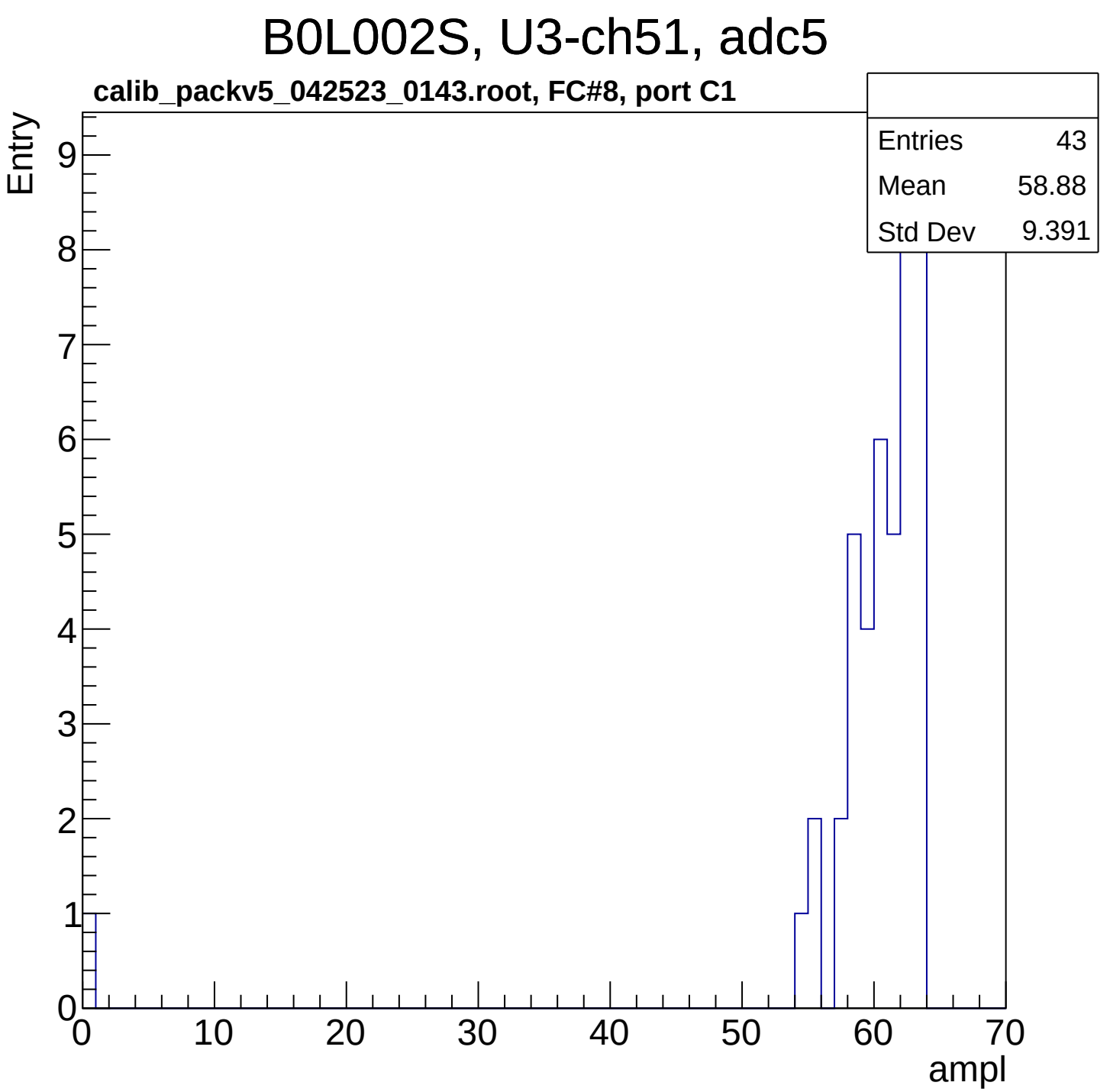

B0L002S, U3-ch51, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.11
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch51, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.88
Std Dev 3.559
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch51, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 58.88
Std Dev 9.391
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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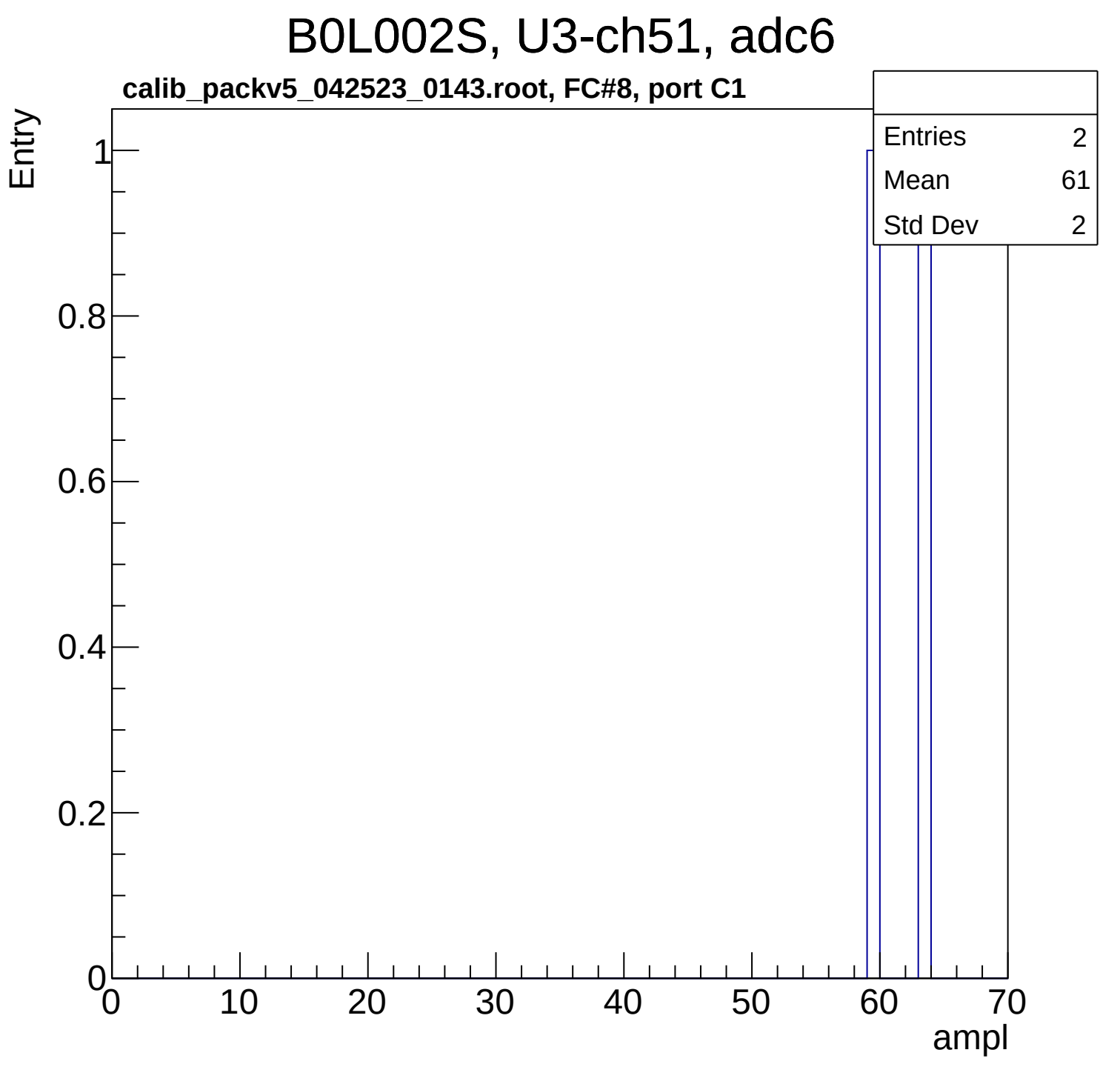
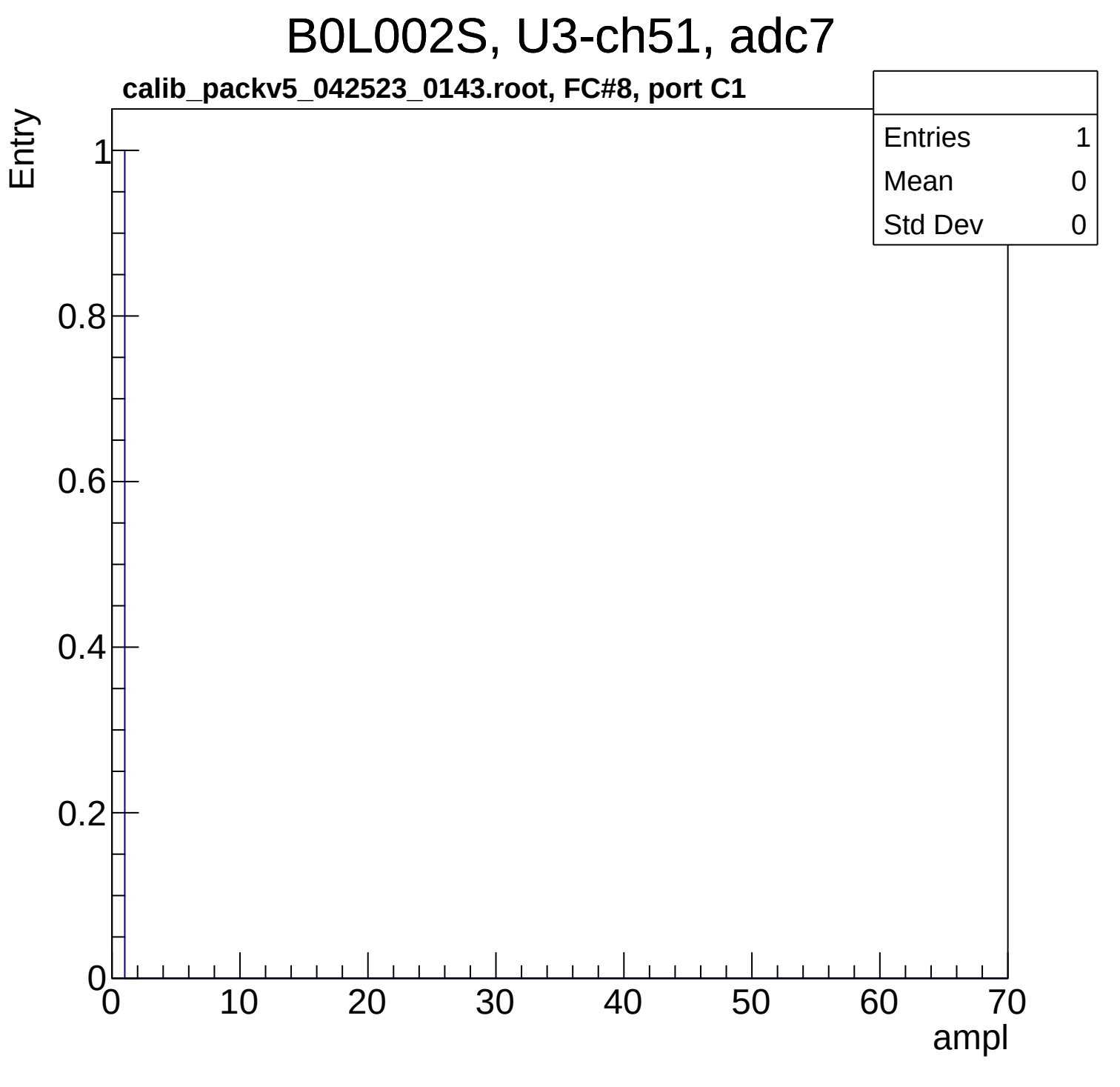
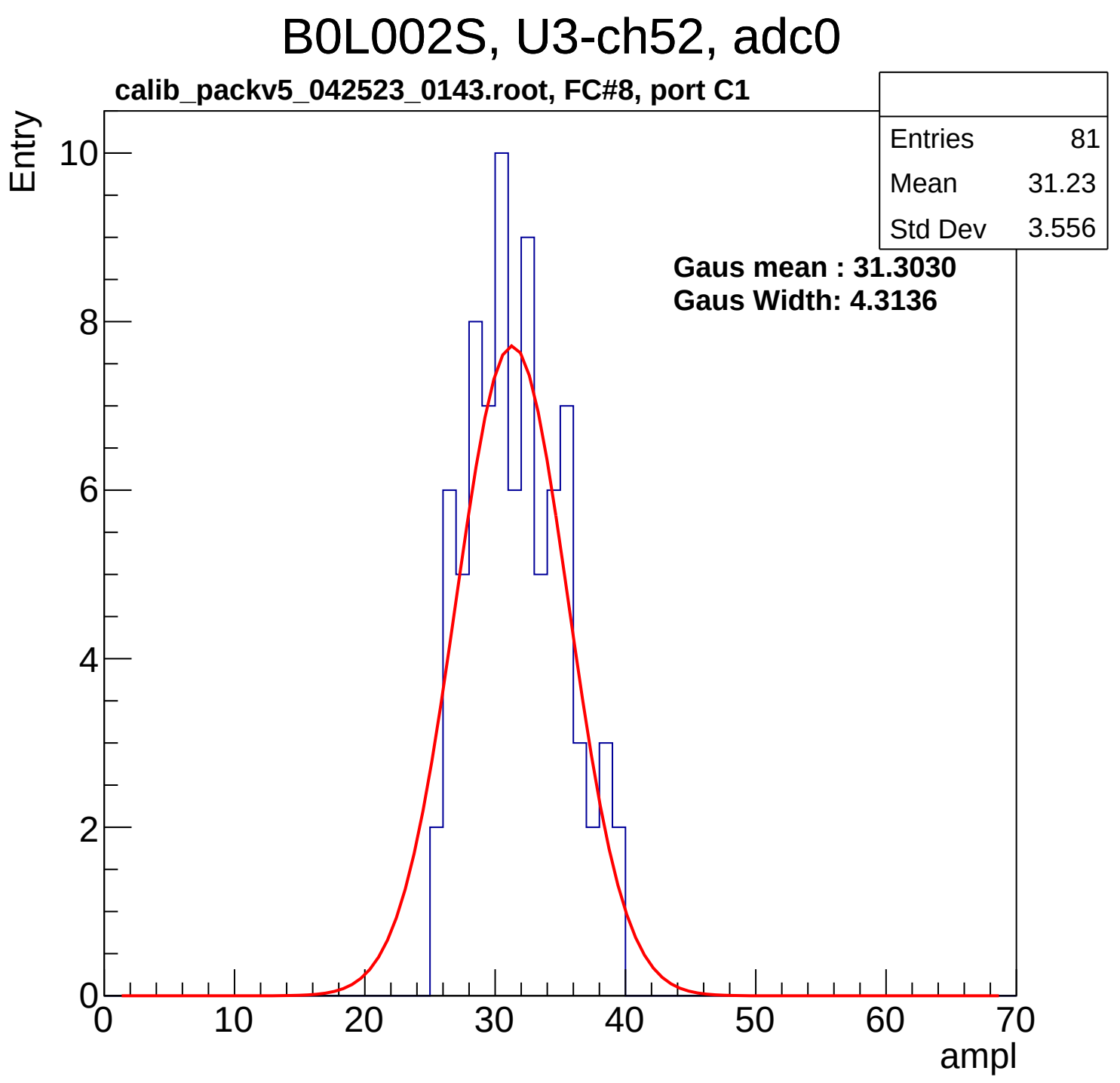
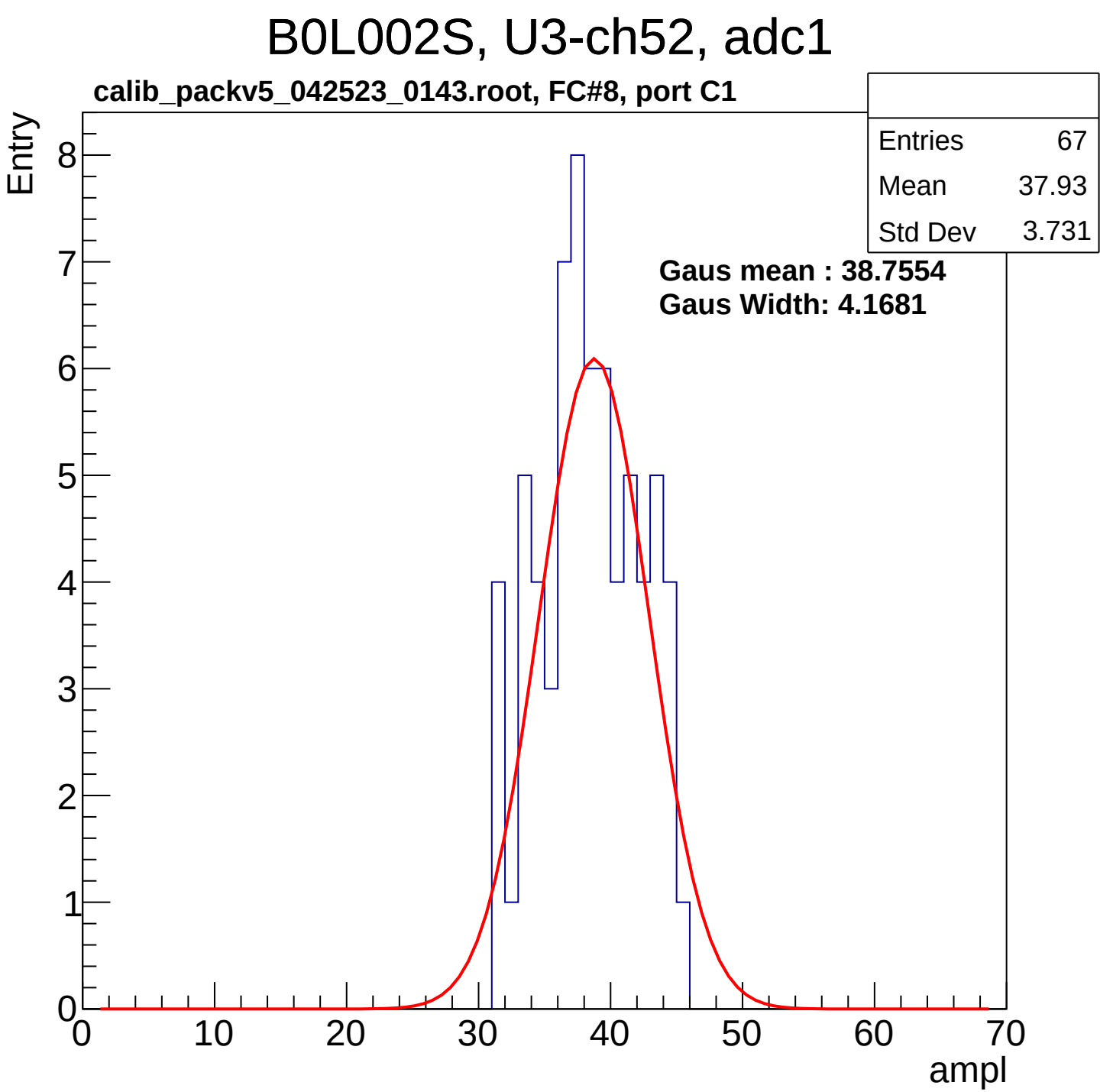
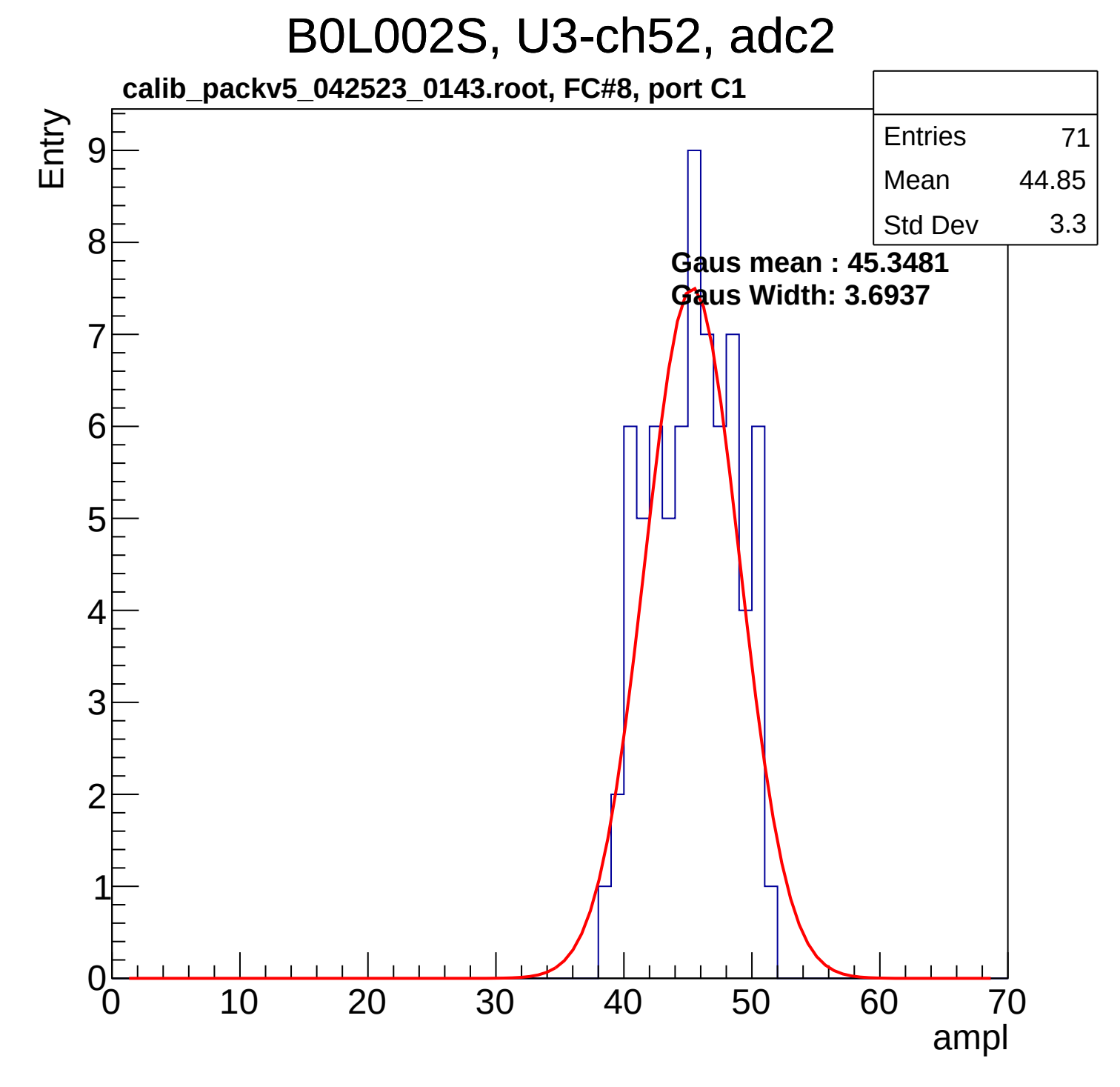

B0L002S, U3-ch51, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch51, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch52, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 31.23
Std Dev 3.556
Gaus mean : 31.3030
Gaus Width: 4.3136
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch52, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.93
Std Dev 3.731
Gaus mean : 38.7554
Gaus Width: 4.1681
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch52, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 44.85
Std Dev 3.3
Gaus mean : 45.3481
Gaus Width: 3.6937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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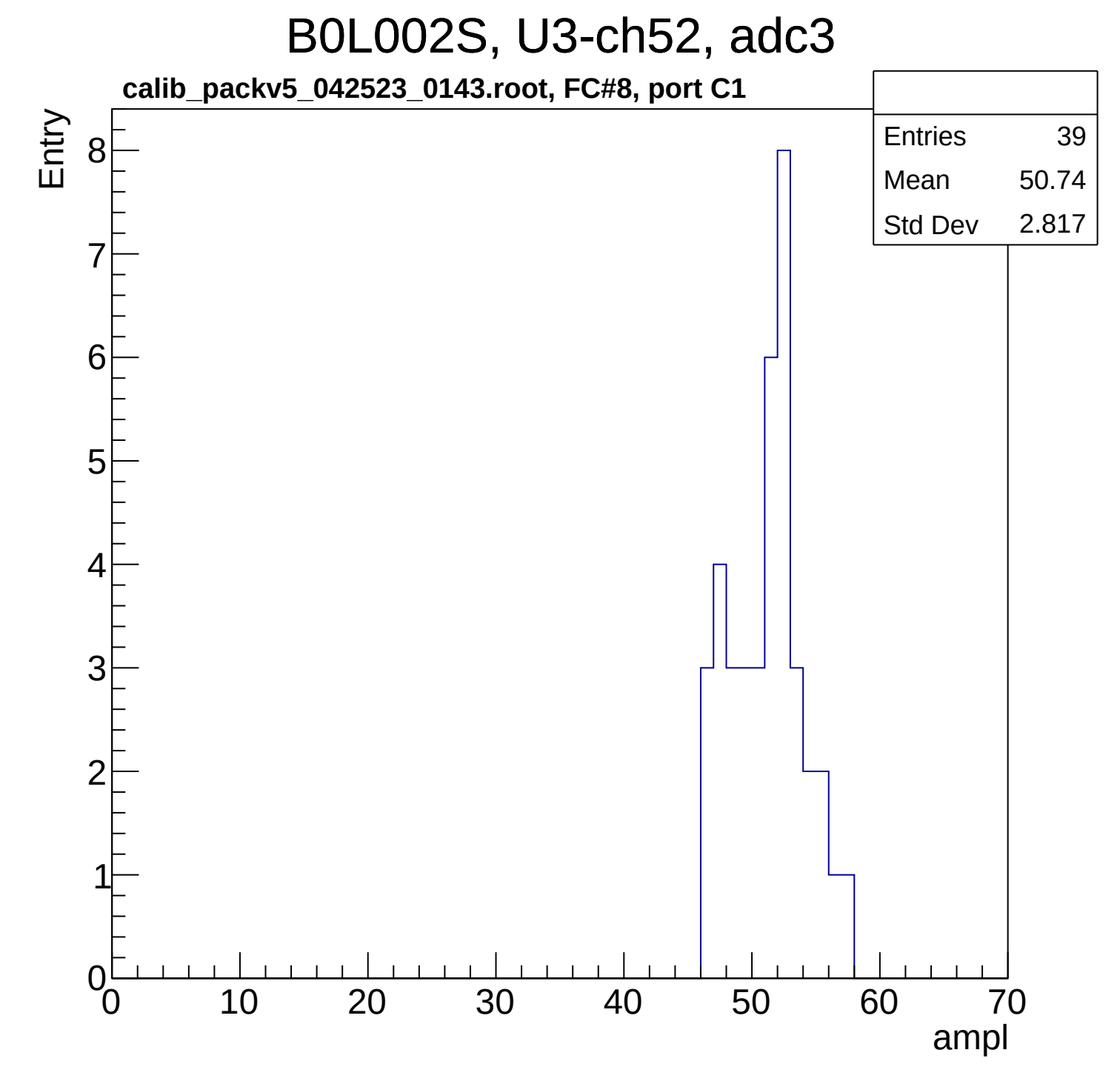
B0L002S, U3-ch52, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 50.74
Std Dev 2.817
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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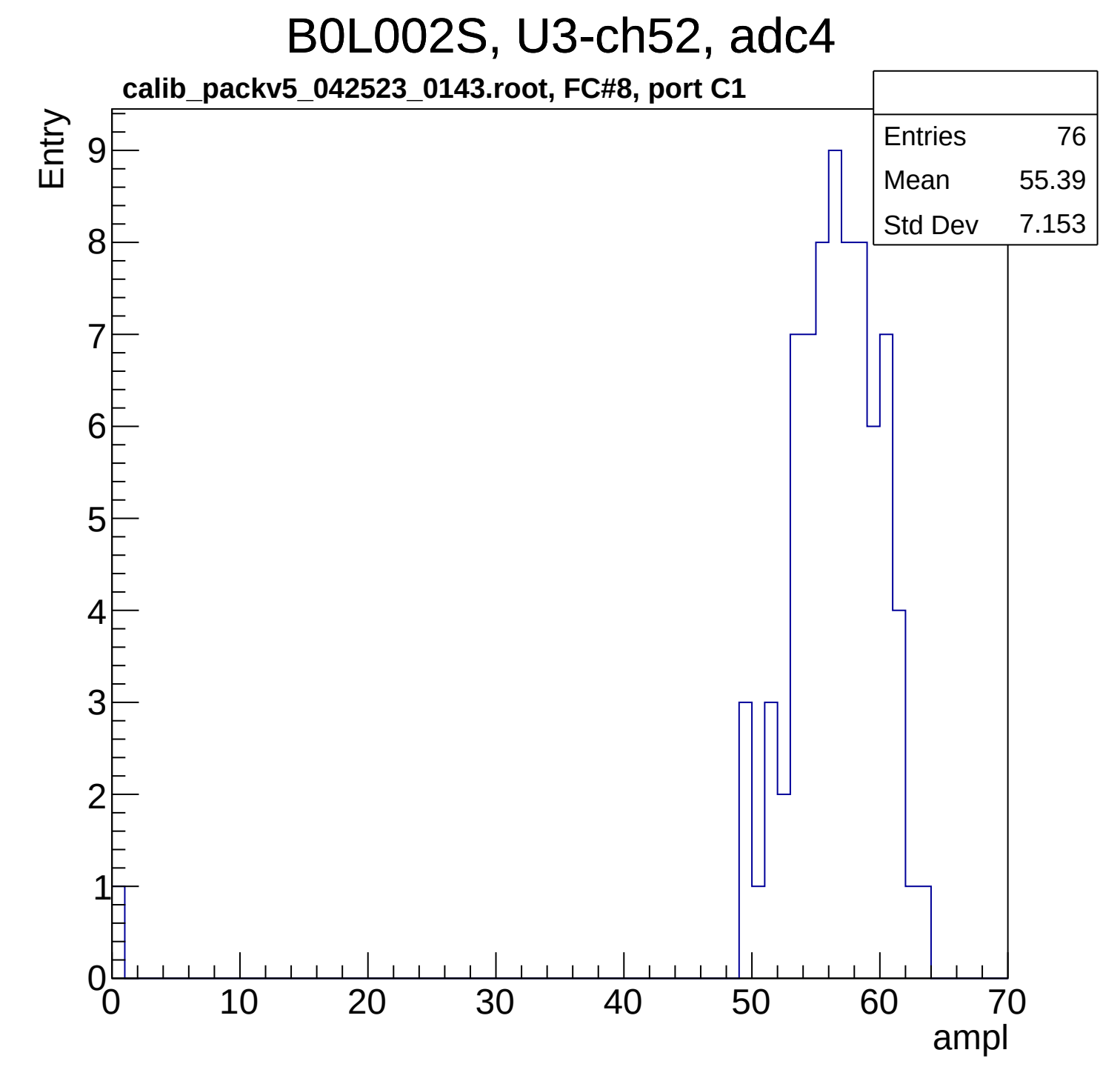
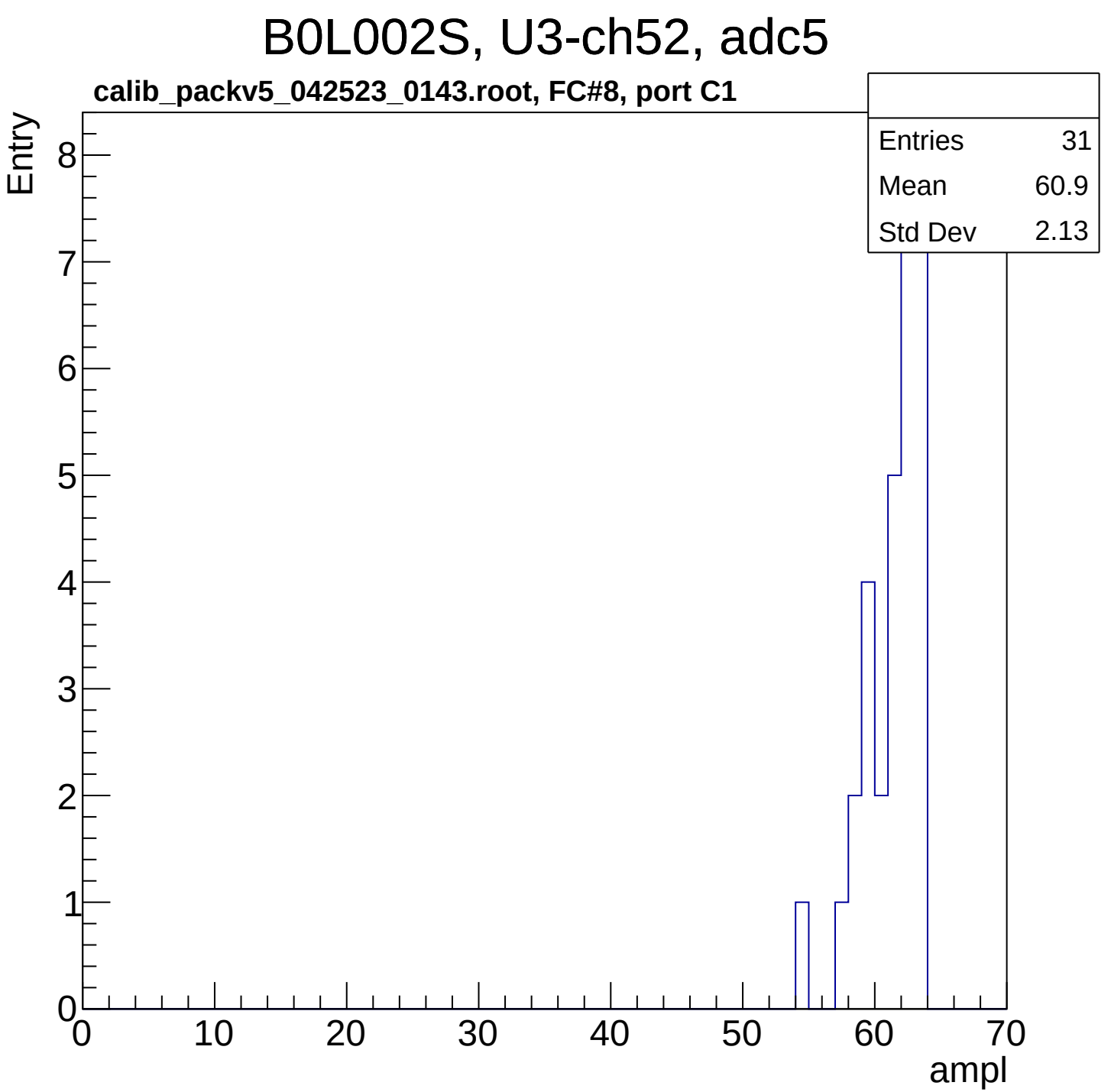

B0L002S, U3-ch52, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 55.39
Std Dev 7.153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch52, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 60.9
Std Dev 2.13
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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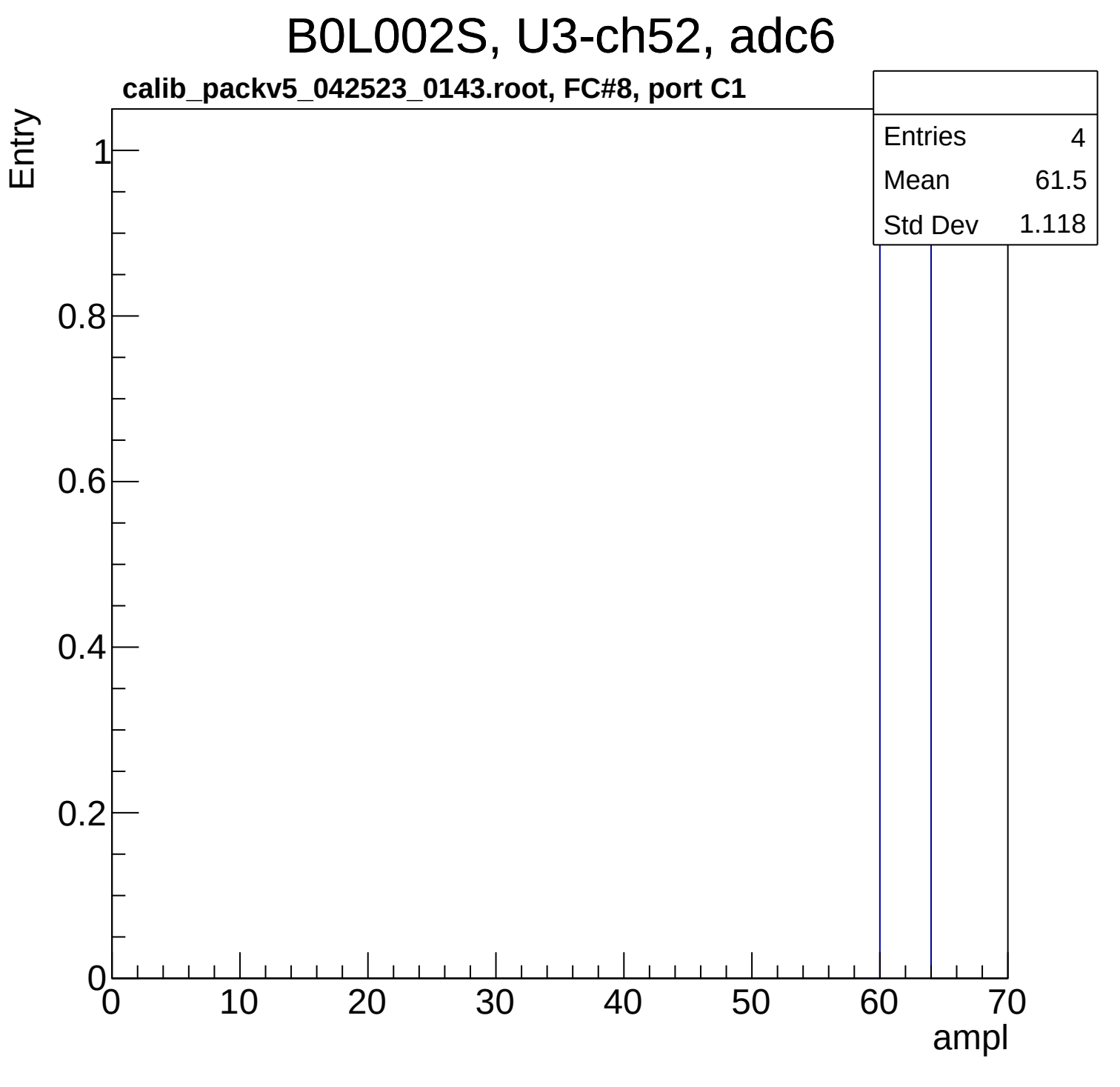
B0L002S, U3-ch52, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.5
Std Dev 1.118
Entry
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0.6
0.4
0.2
0
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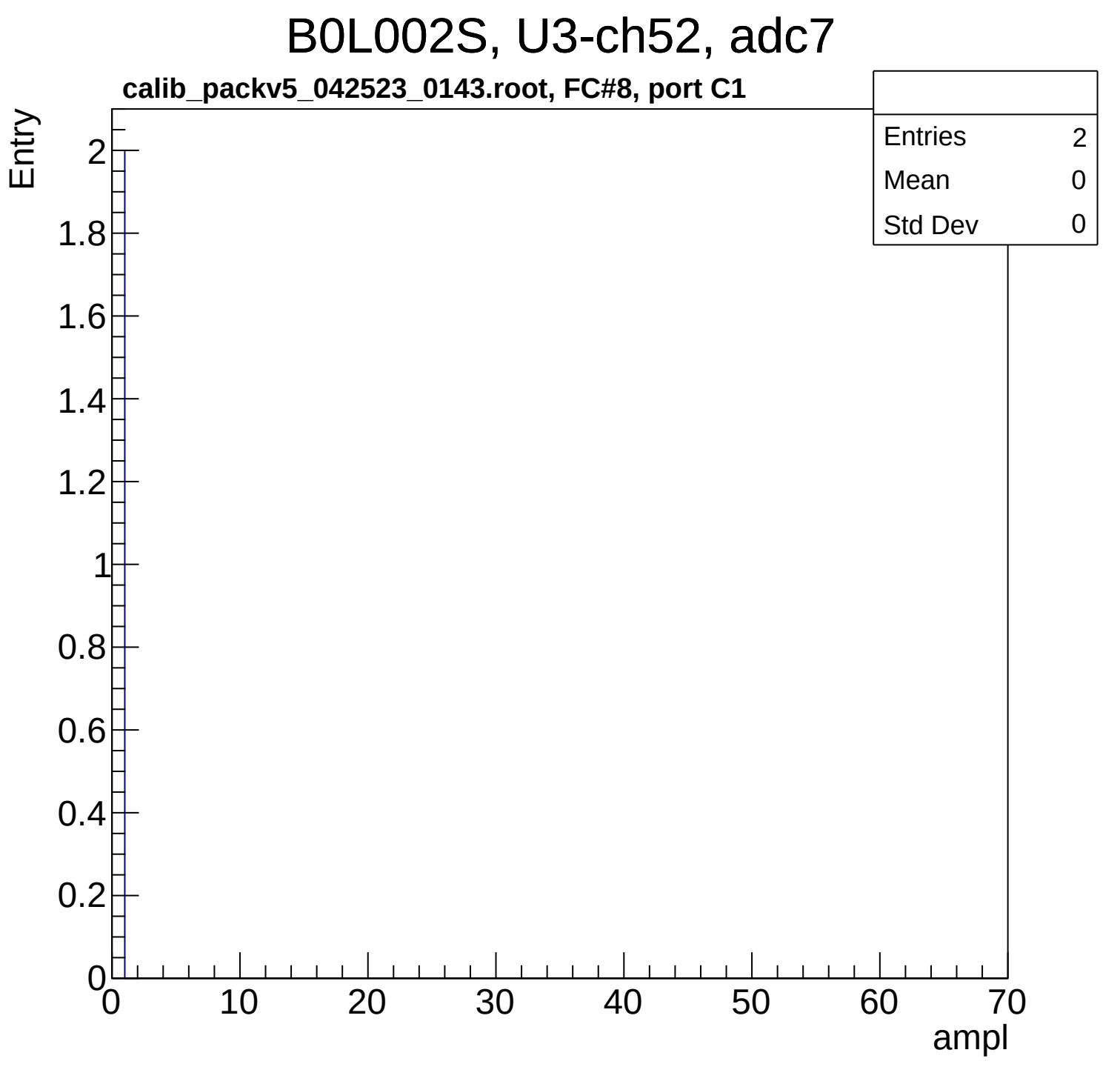
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch52, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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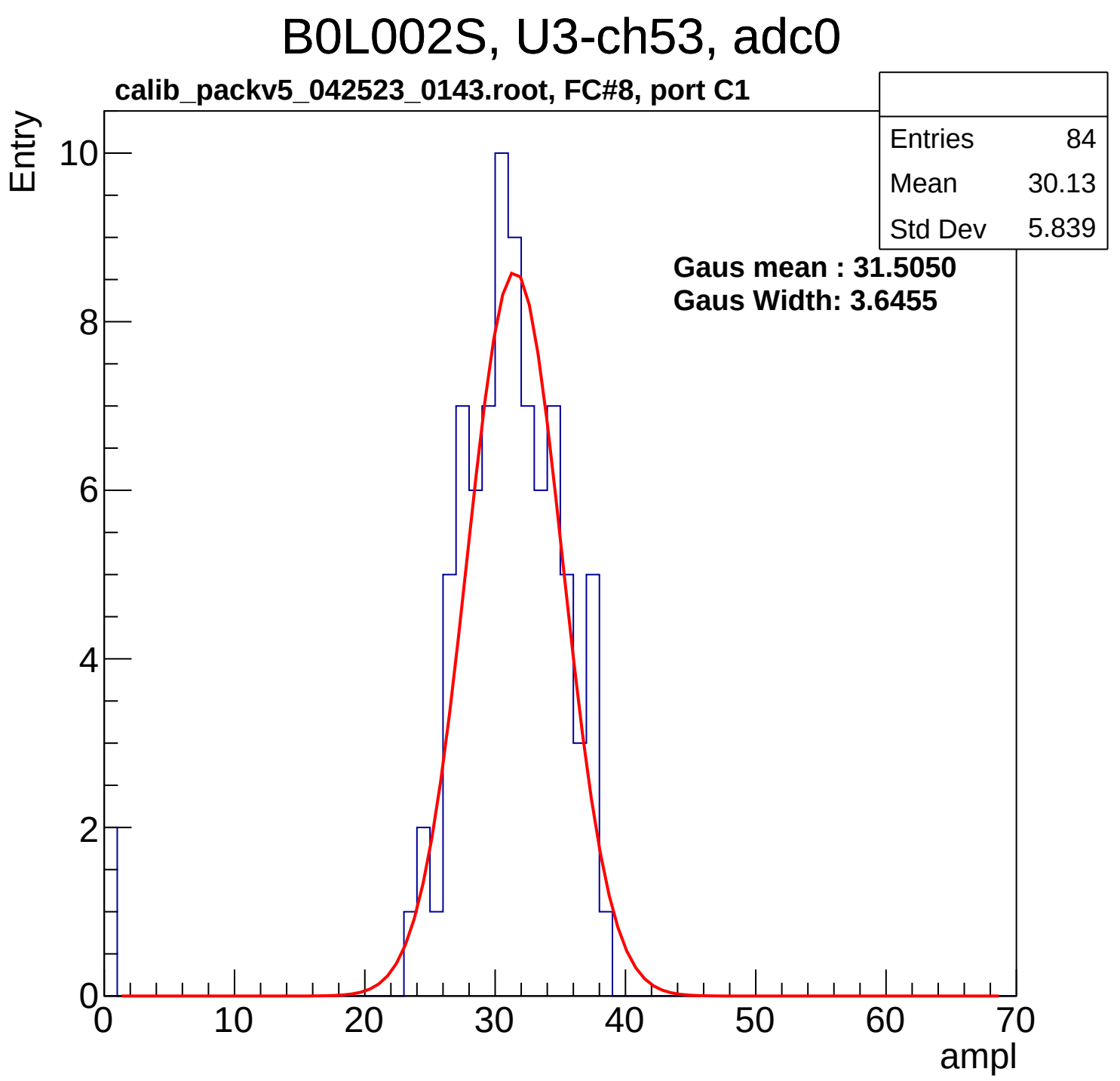
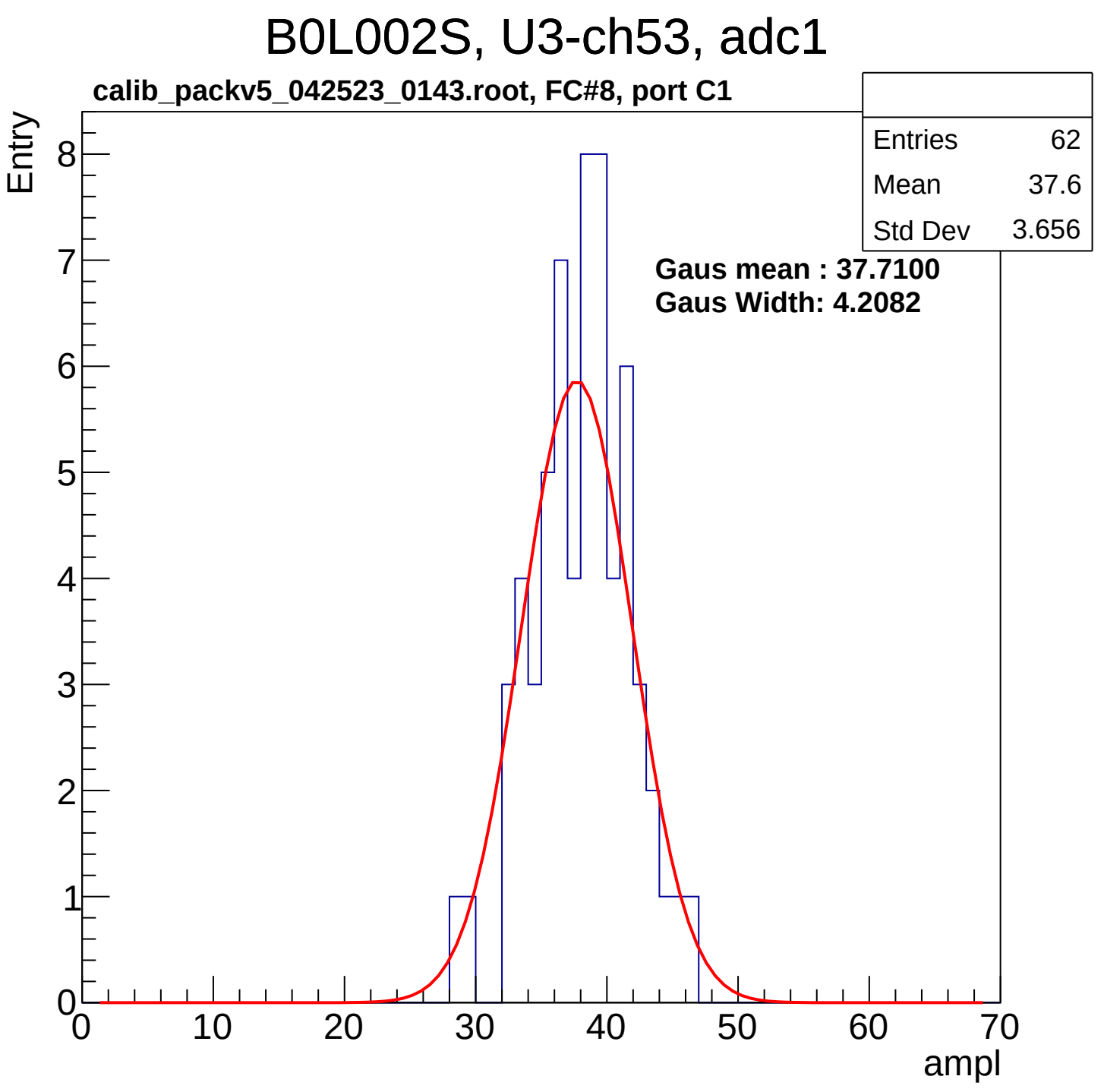
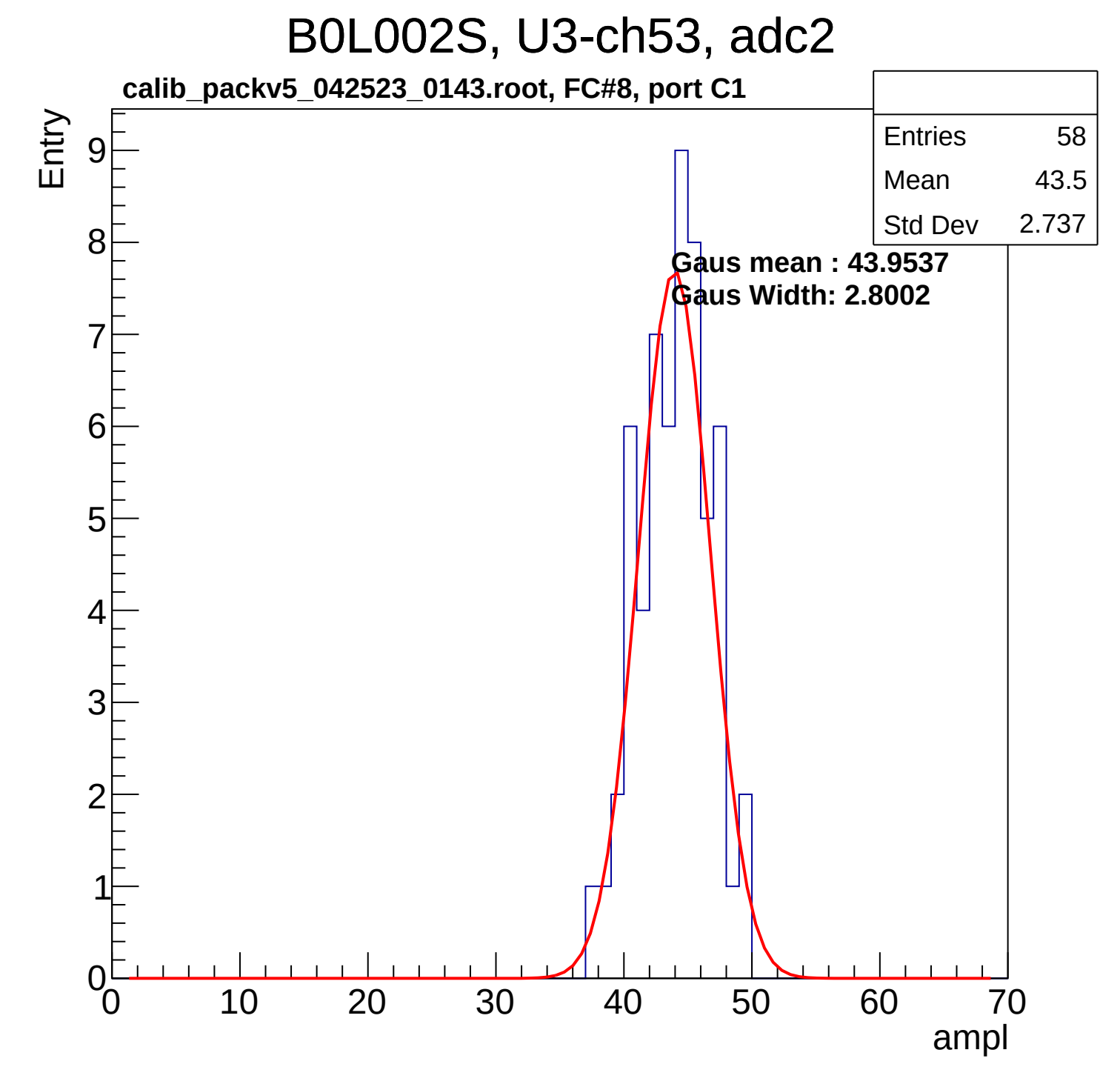

B0L002S, U3-ch53, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 30.13
Std Dev 5.839
Gaus mean : 31.5050
Gaus Width: 3.6455
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch53, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.6
Std Dev 3.656
Gaus mean : 37.7100
Gaus Width: 4.2082
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch53, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.5
Std Dev 2.737
Gaus mean : 43.9537
Gaus Width: 2.8002
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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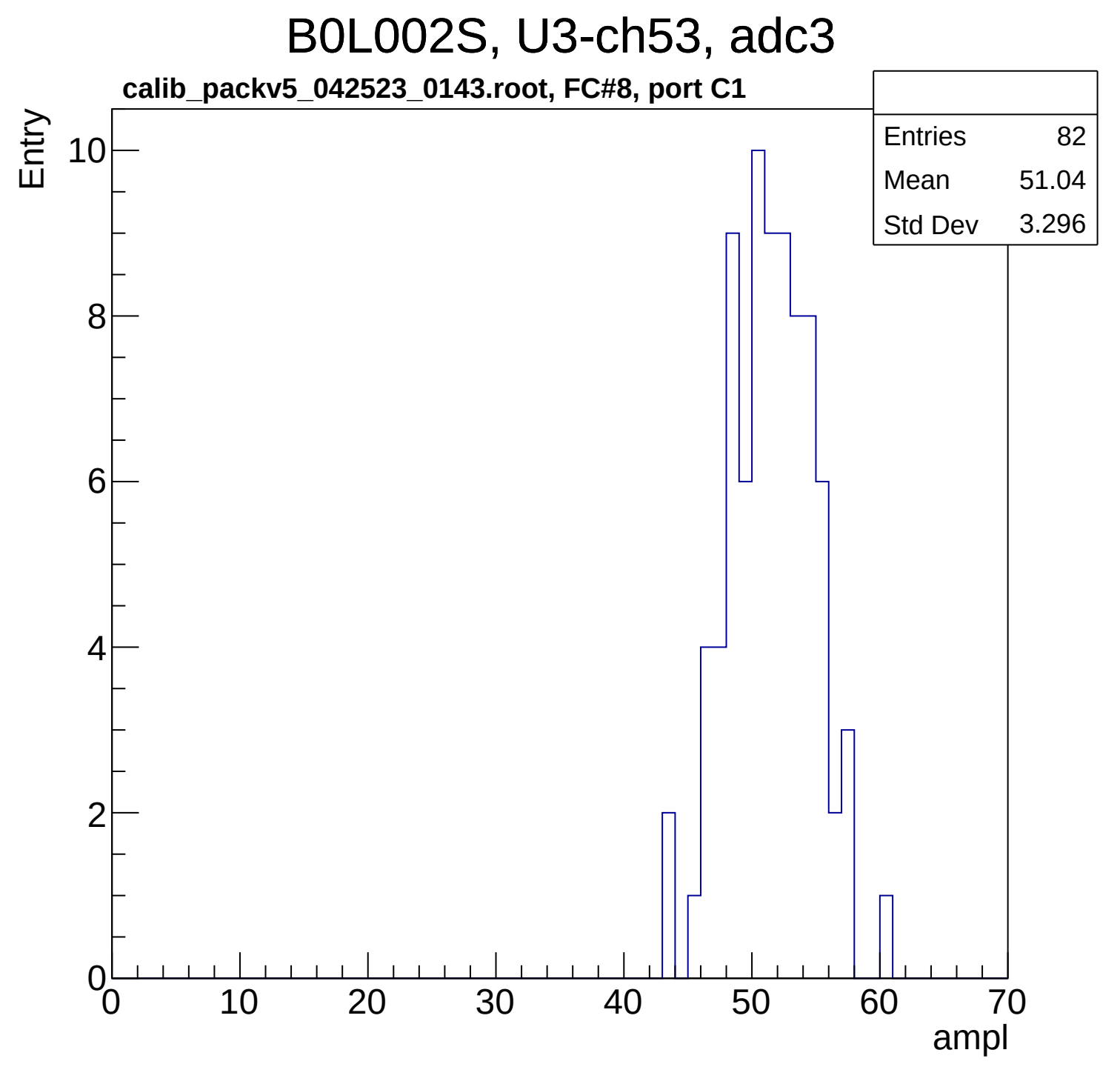
60
70


B0L002S, U3-ch53, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 51.04
Std Dev 3.296
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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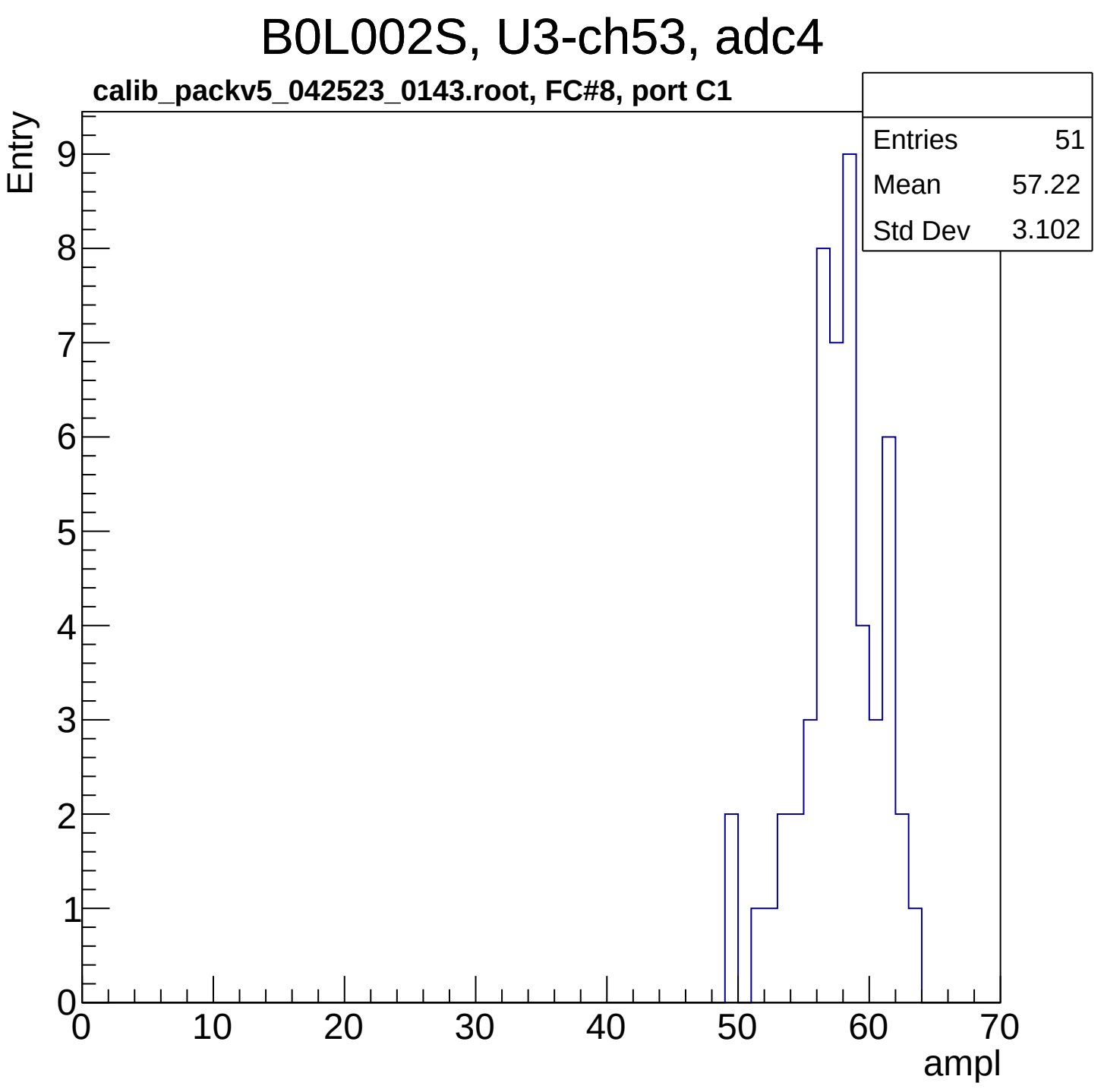
B0L002S, U3-ch53, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.22
Std Dev 3.102
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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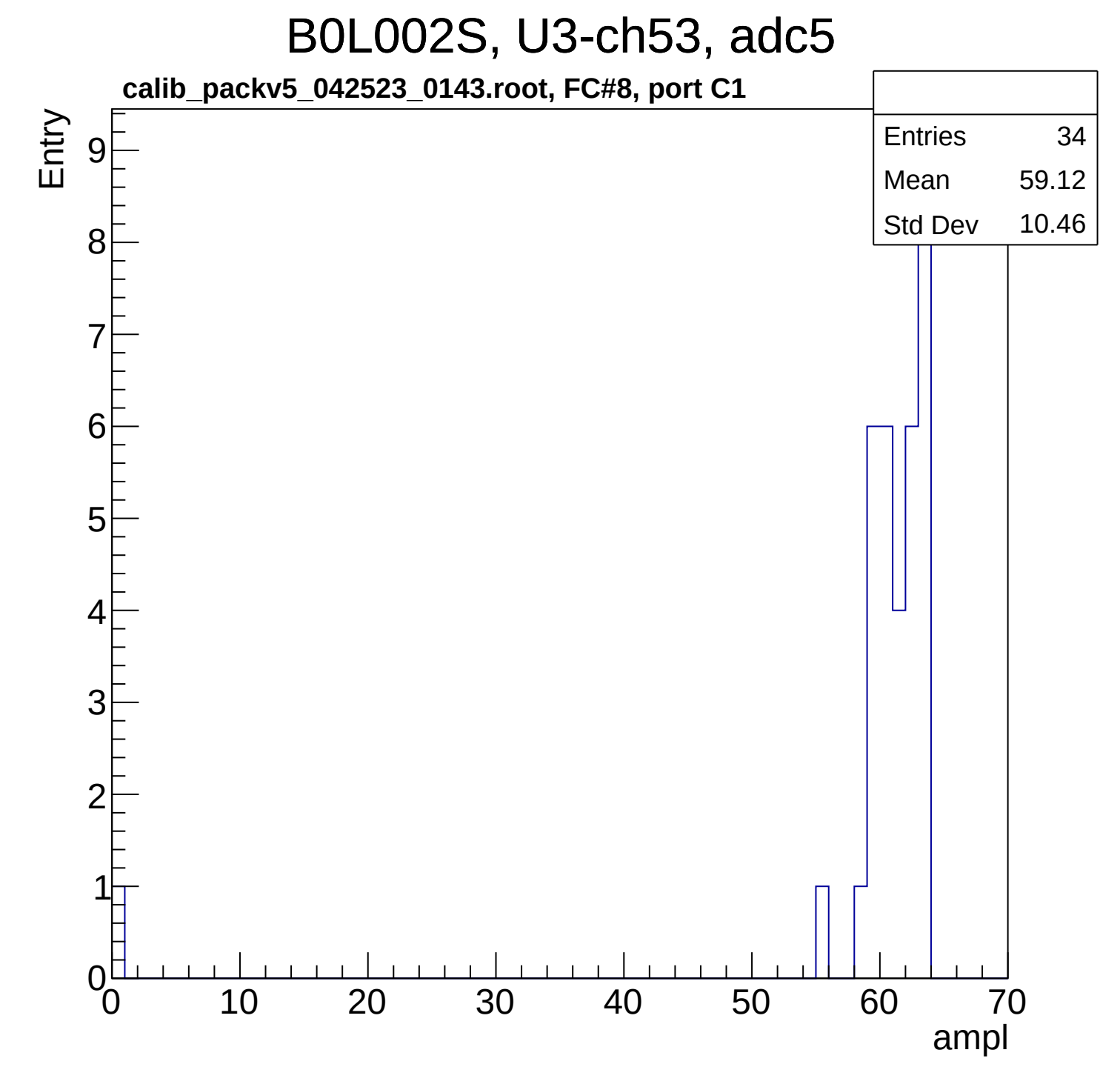
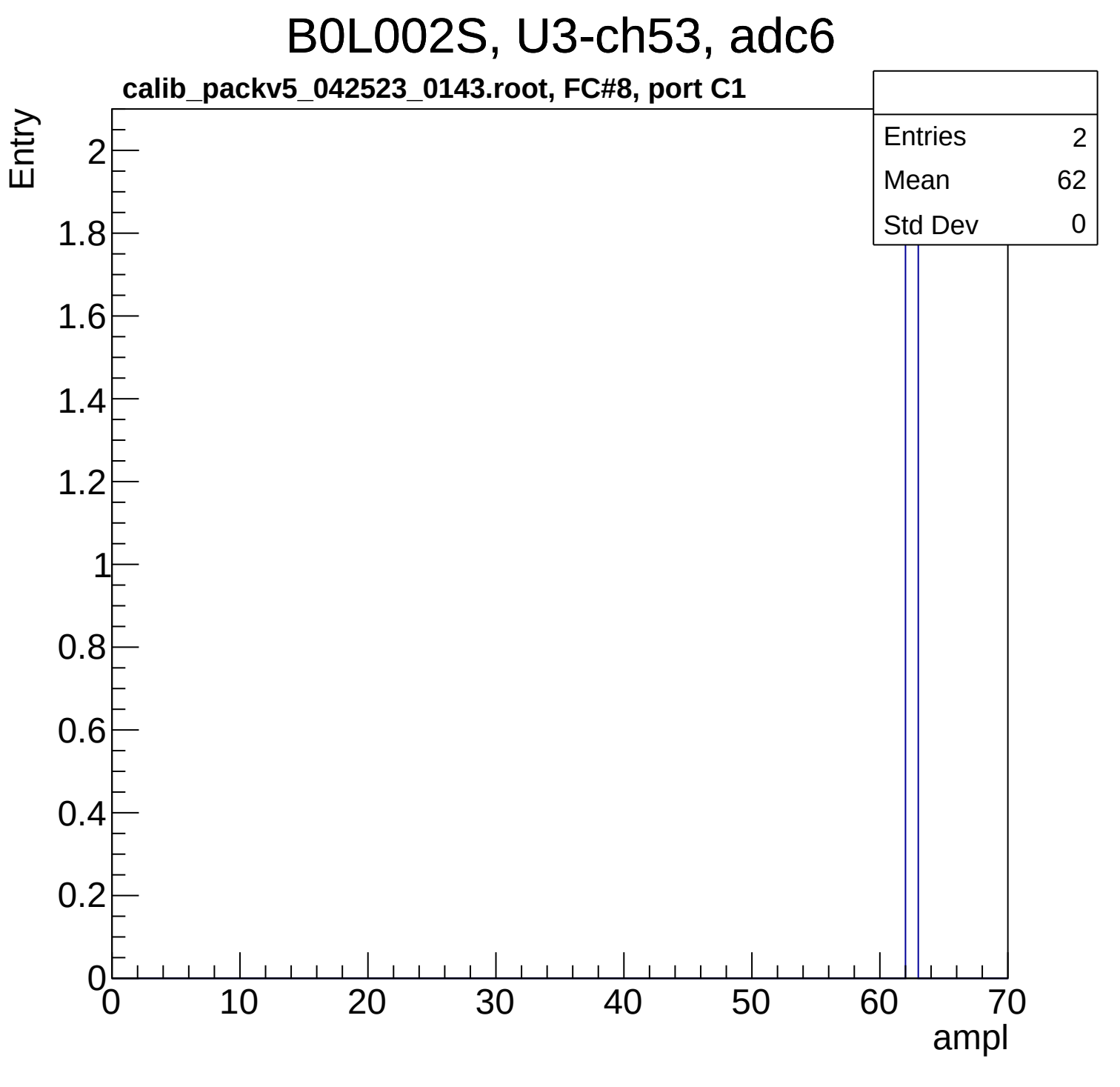
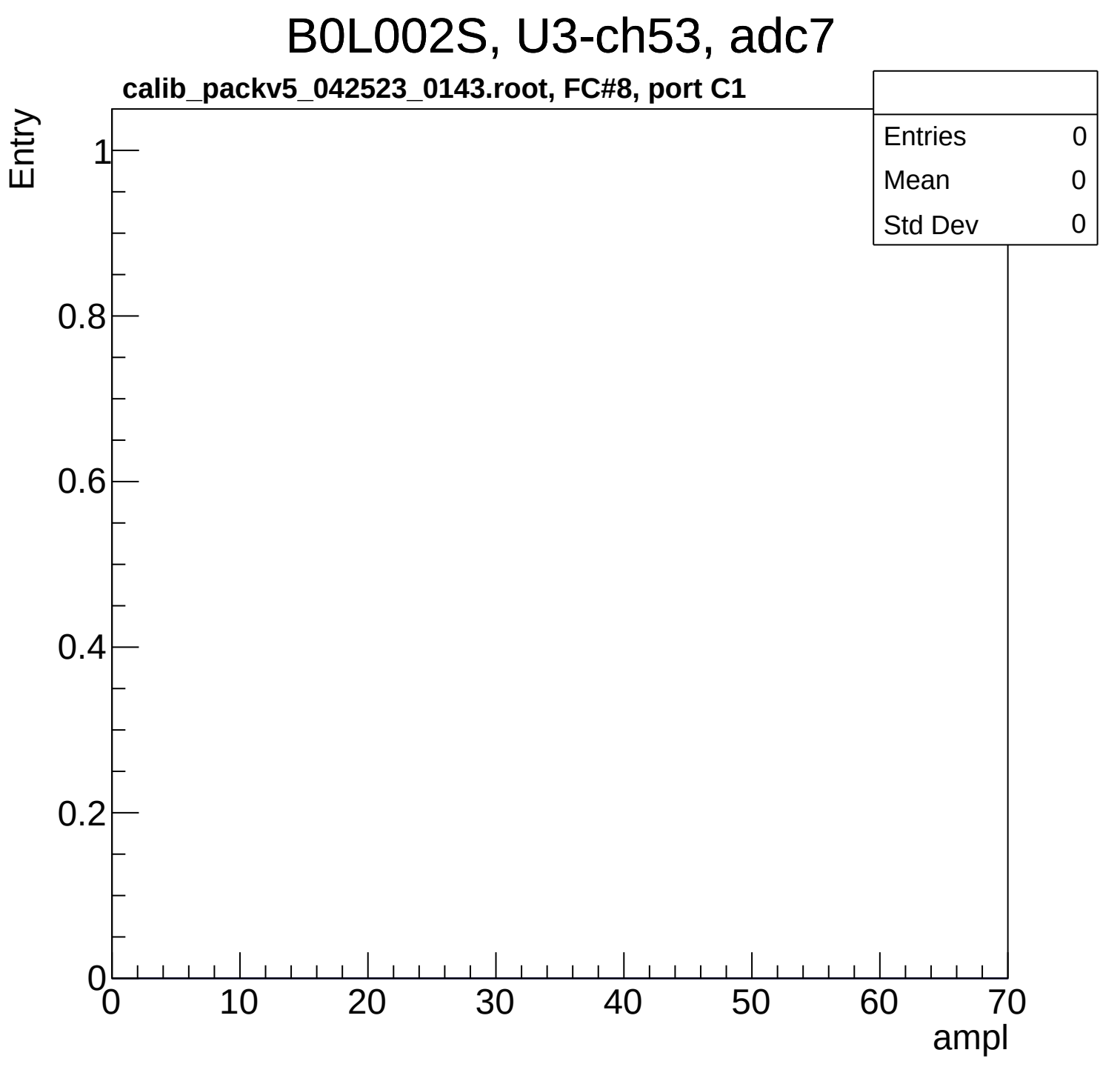

B0L002S, U3-ch53, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.12
Std Dev 10.46
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch53, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch53, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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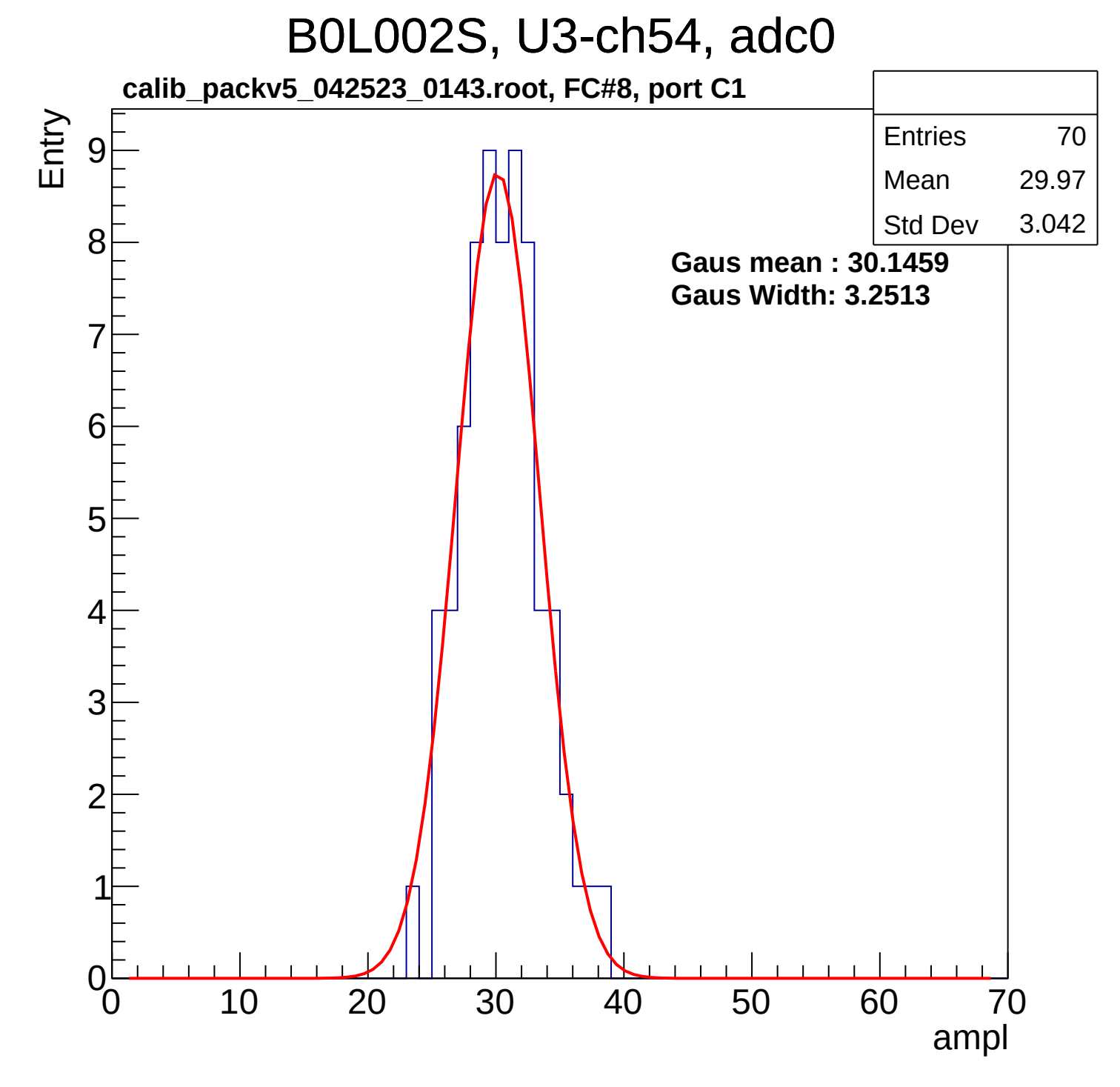
50
60
70
ampl


B0L002S, U3-ch54, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 29.97
Std Dev 3.042
Gaus mean : 30.1459
Gaus Width: 3.2513
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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50
60
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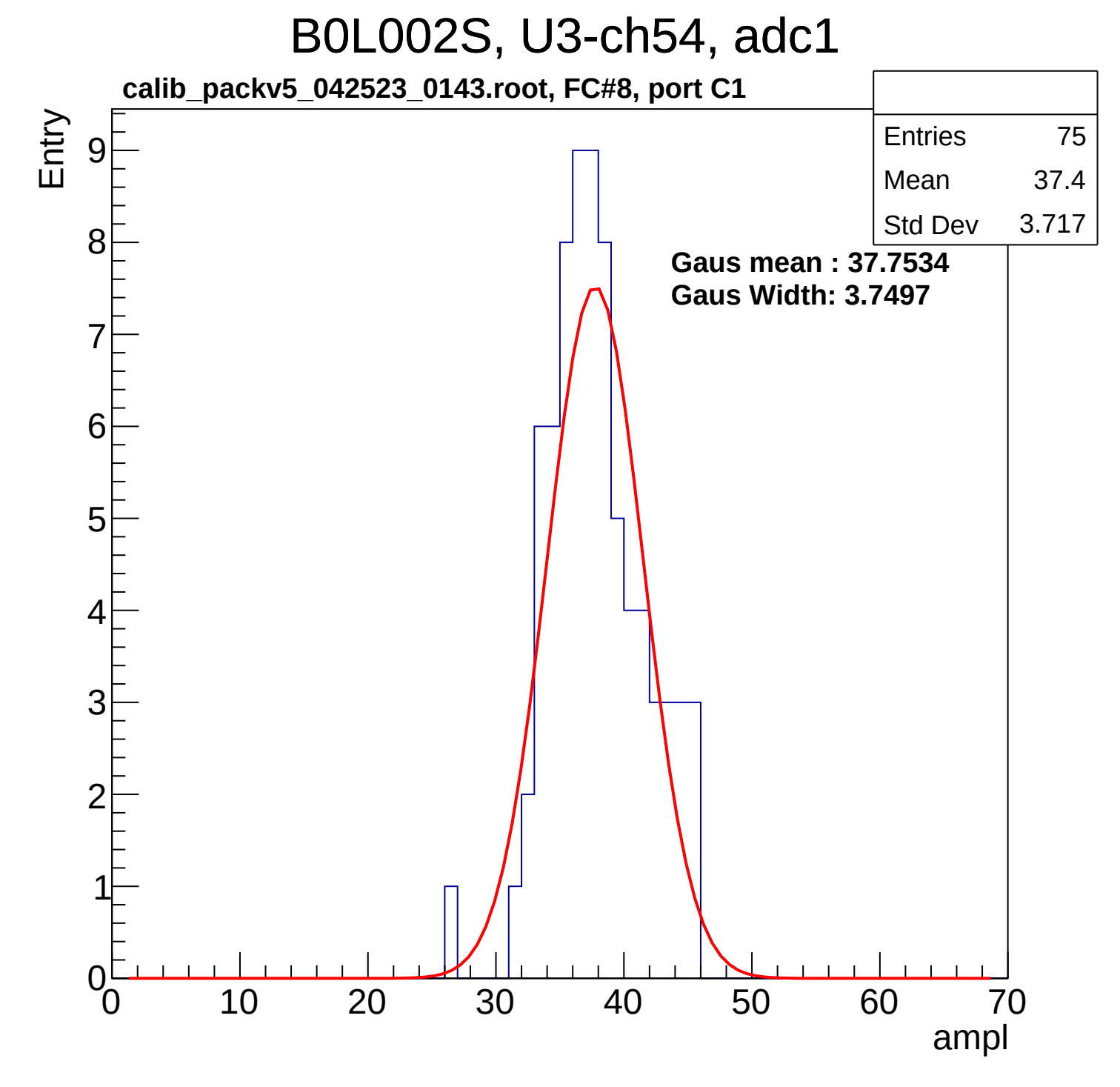
B0L002S, U3-ch54, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.4
Std Dev 3.717
Gaus mean : 37.7534
Gaus Width: 3.7497
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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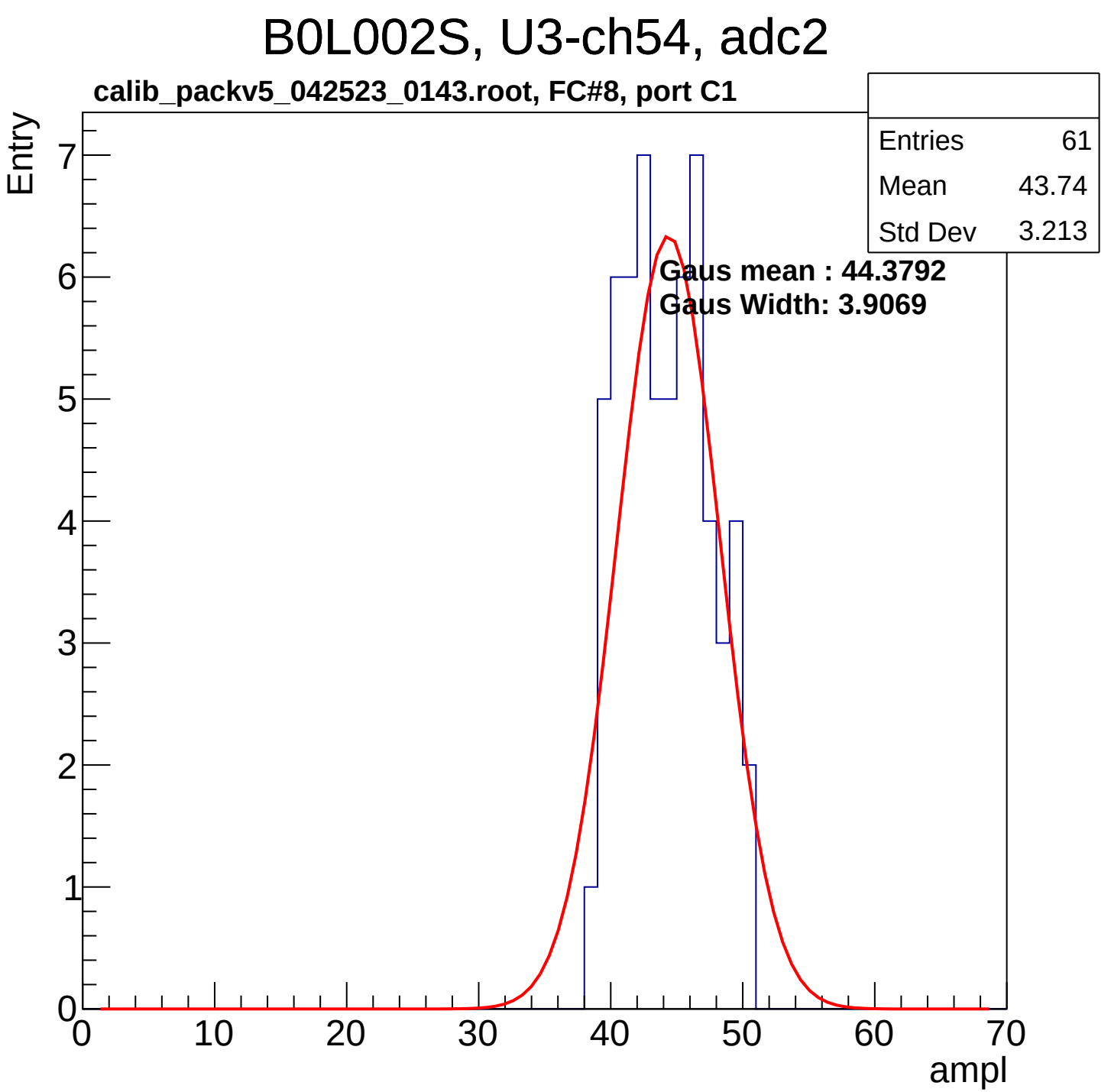
B0L002S, U3-ch54, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.74
Std Dev 3.213
Gaus mean : 44.3792
Gaus Width: 3.9069
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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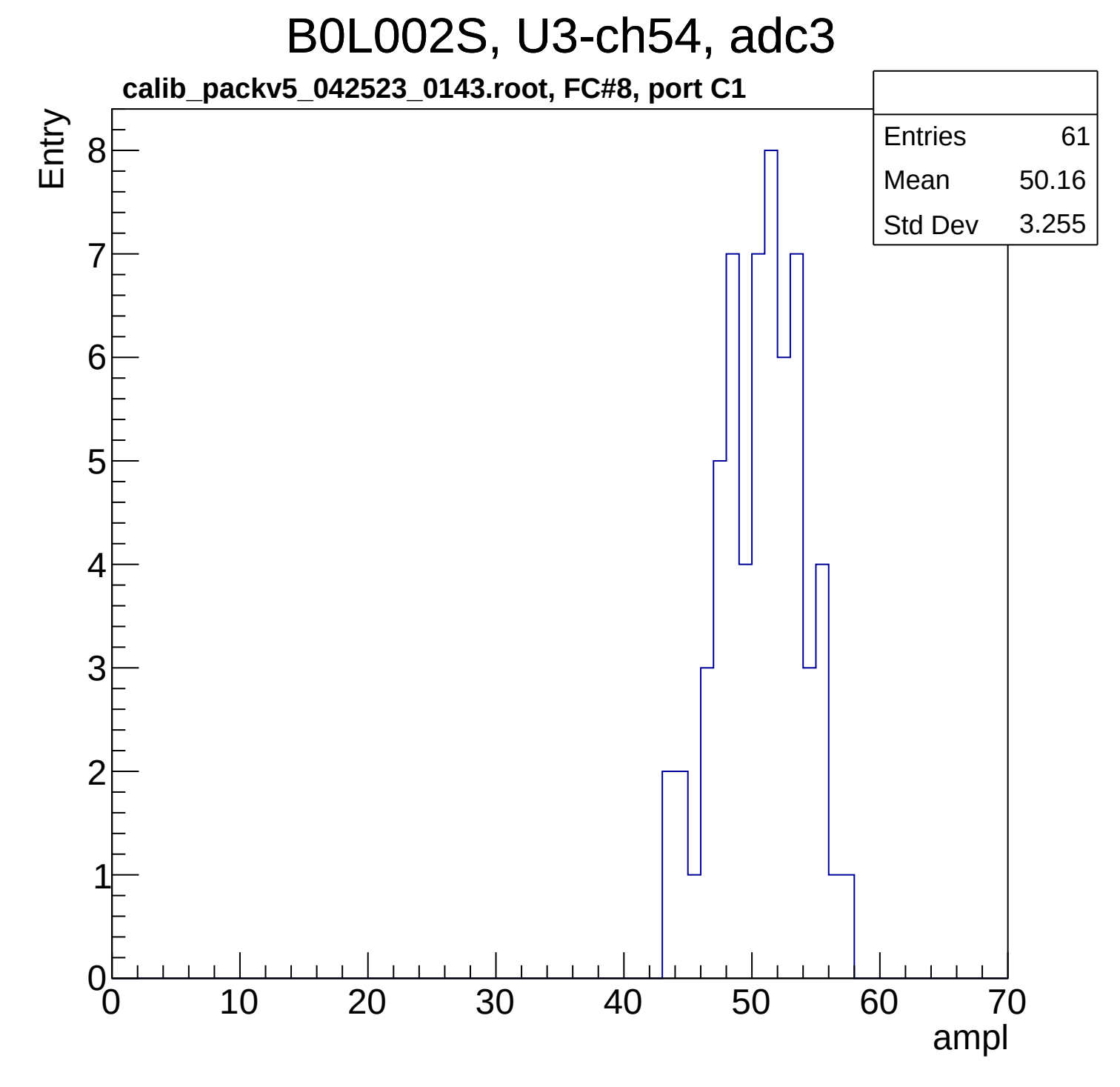
B0L002S, U3-ch54, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 50.16
Std Dev 3.255
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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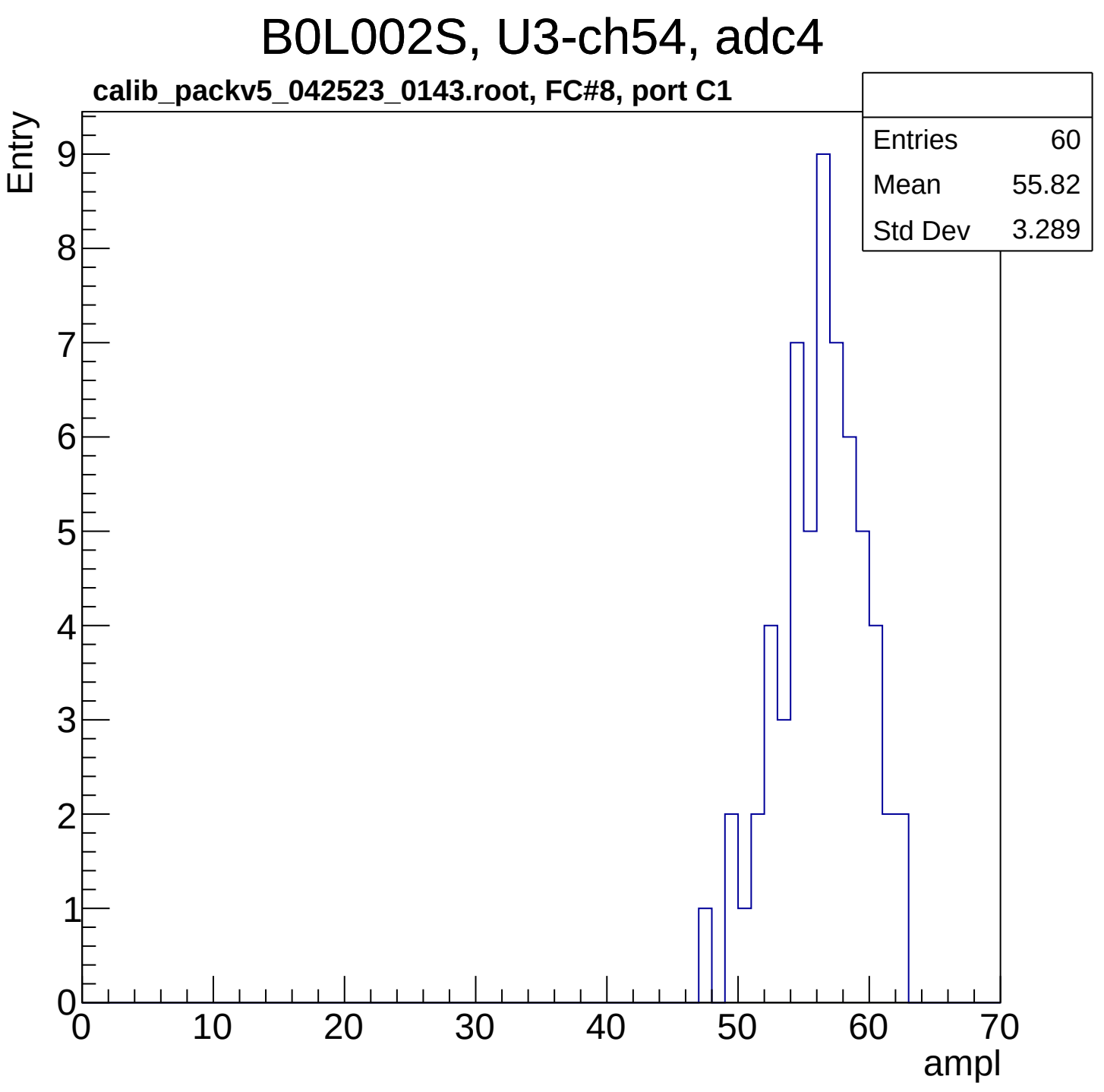
B0L002S, U3-ch54, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.82
Std Dev 3.289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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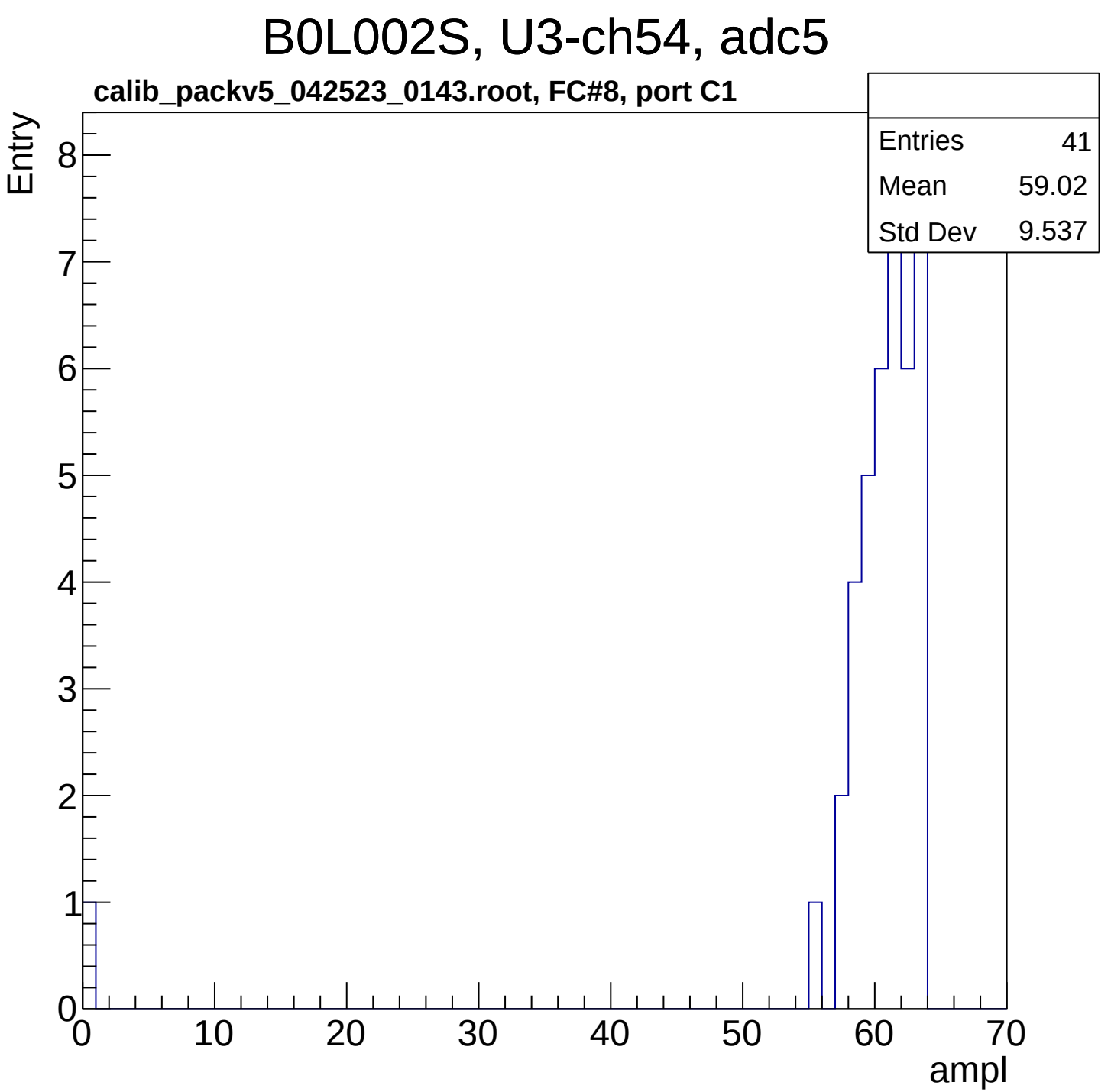
B0L002S, U3-ch54, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.02
Std Dev 9.537
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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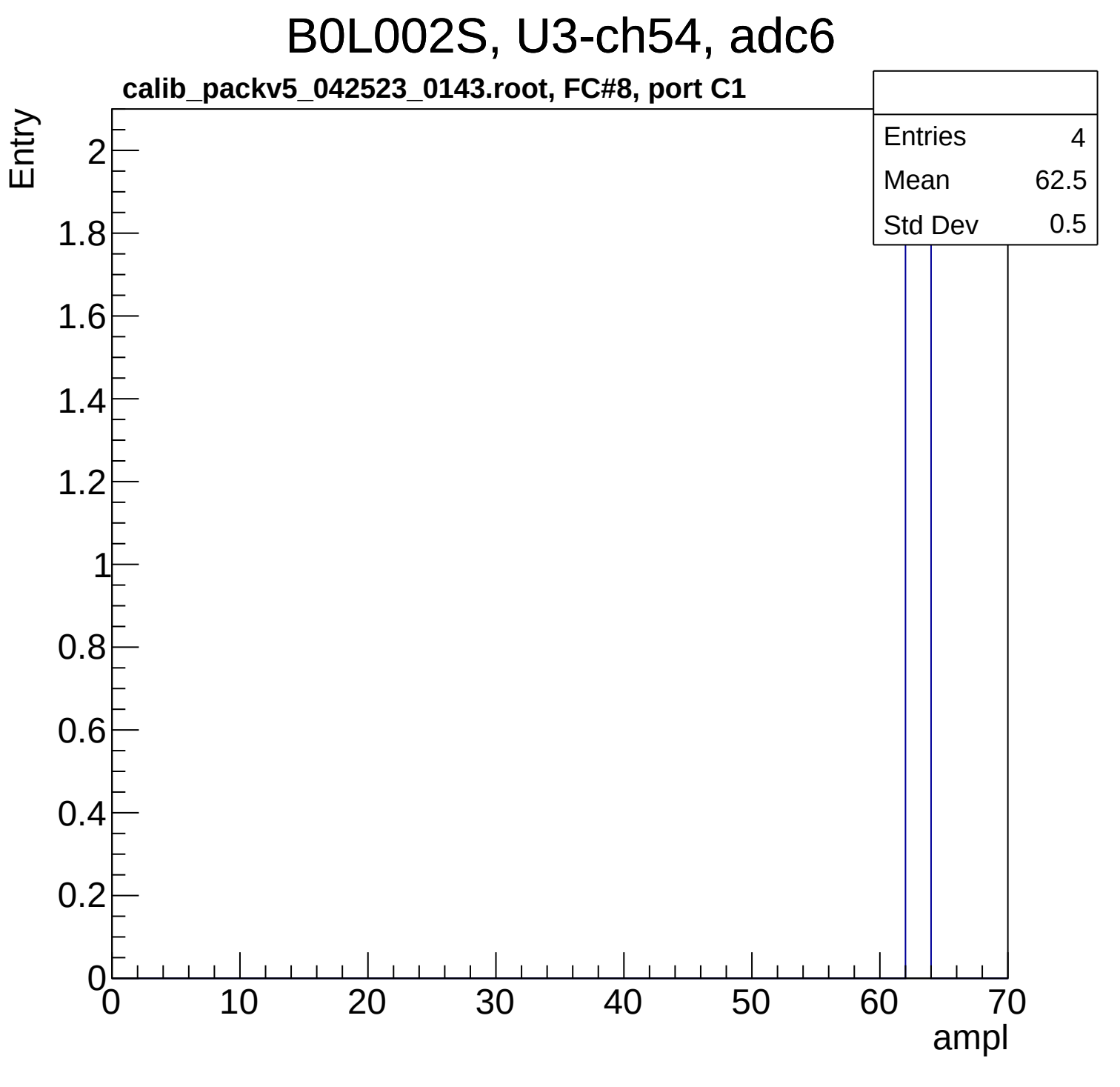
B0L002S, U3-ch54, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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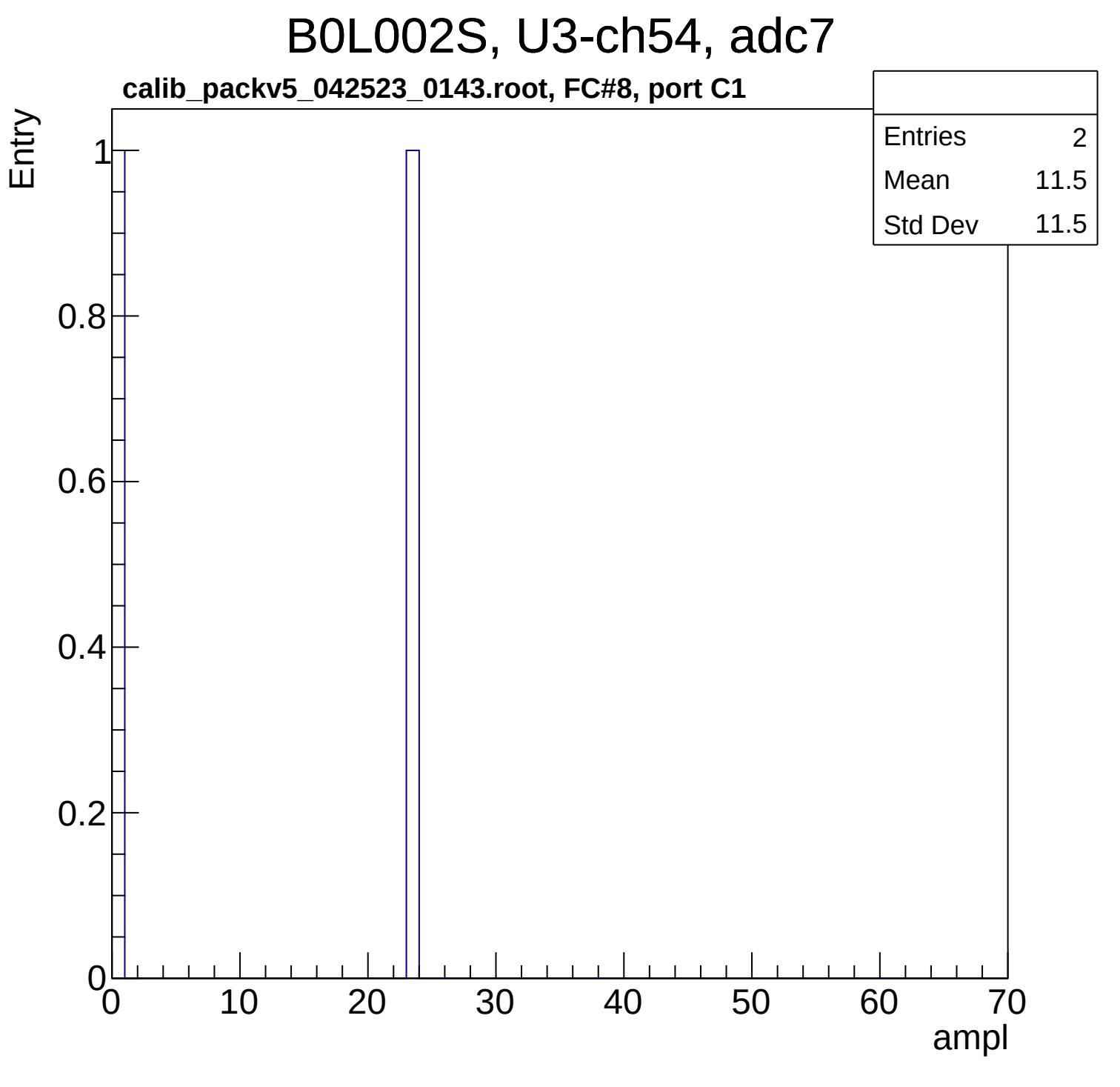
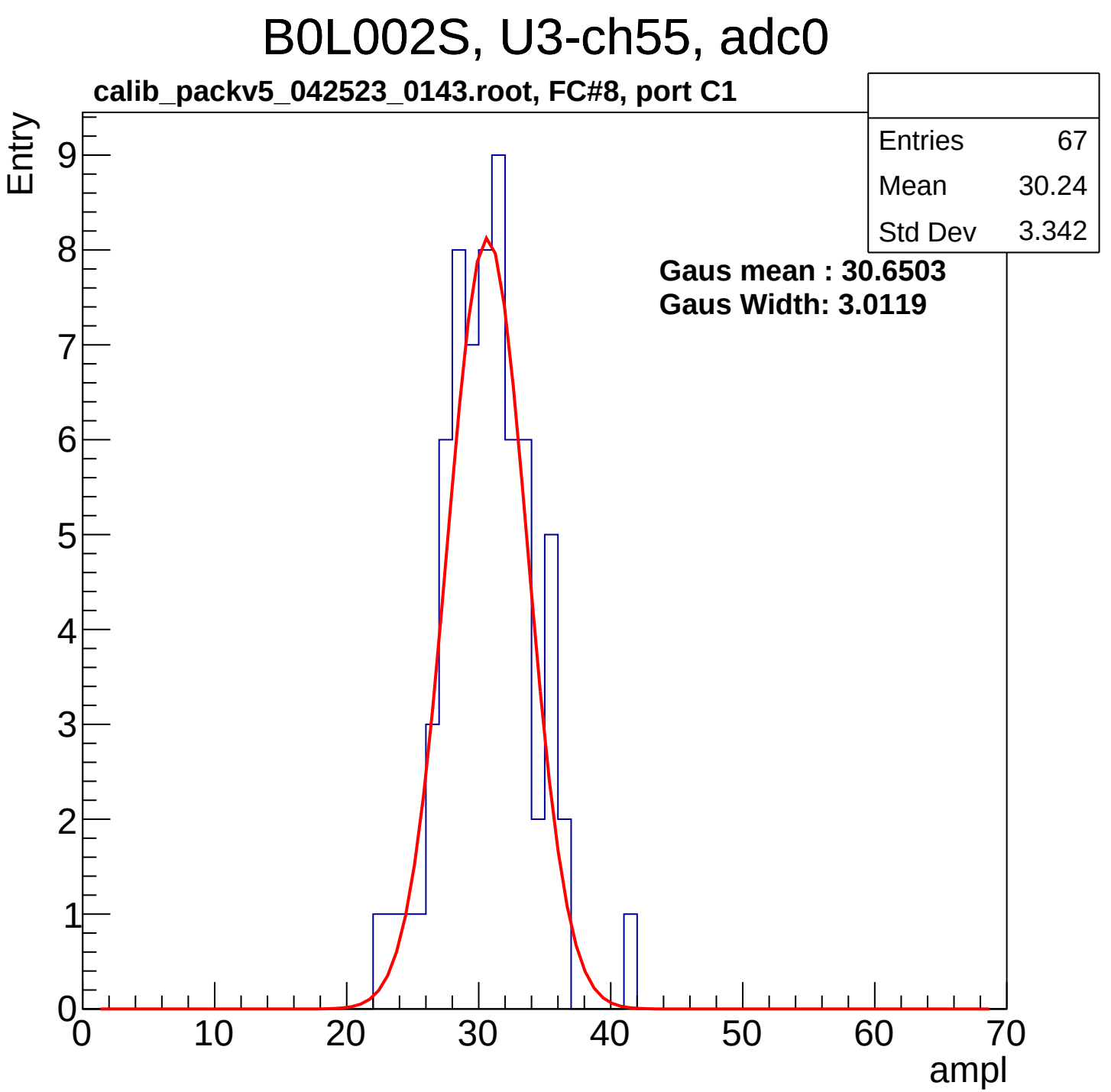

B0L002S, U3-ch54, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch55, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.24
Std Dev 3.342
Gaus mean : 30.6503
Gaus Width: 3.0119
Entry
ampl
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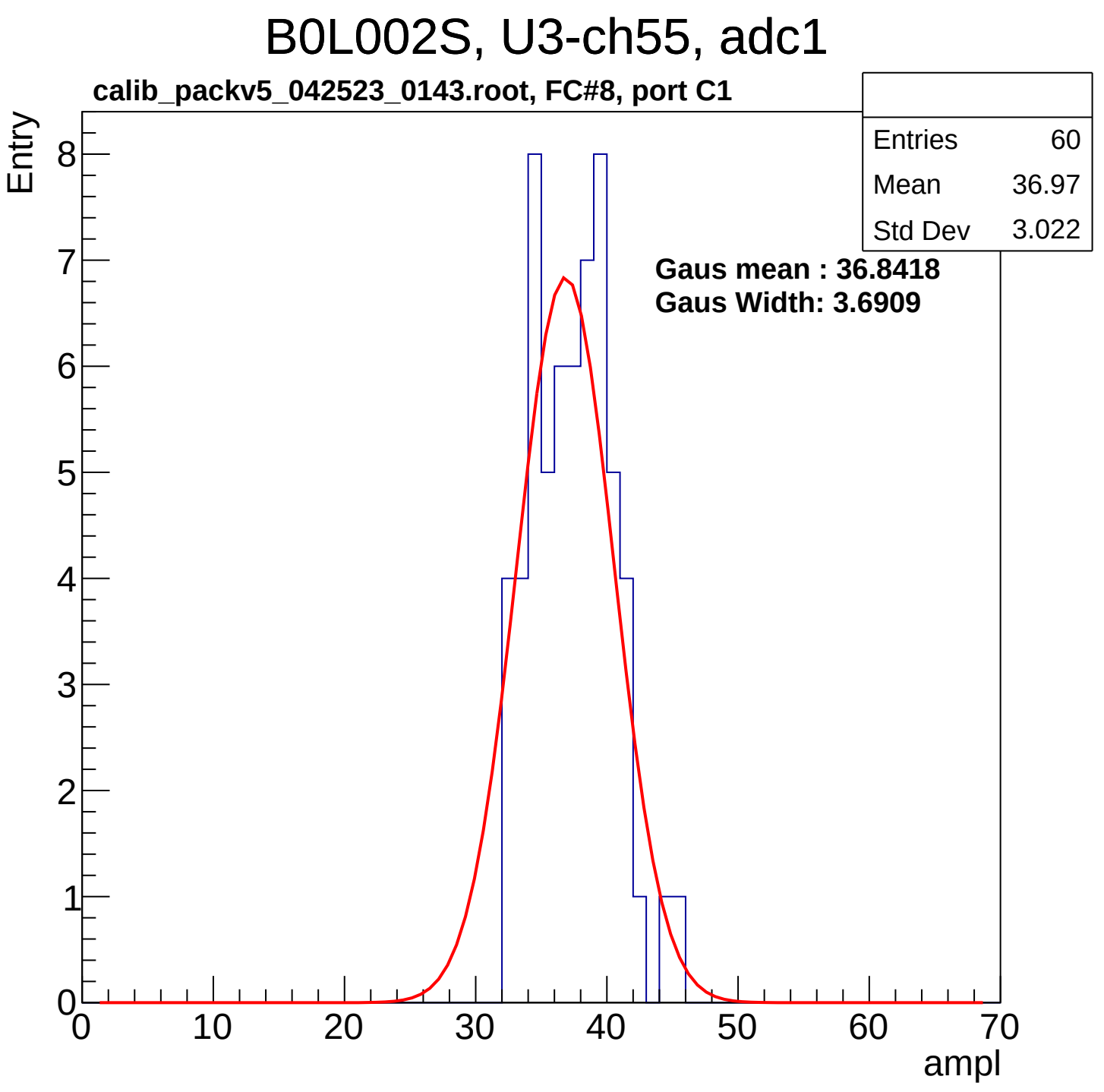
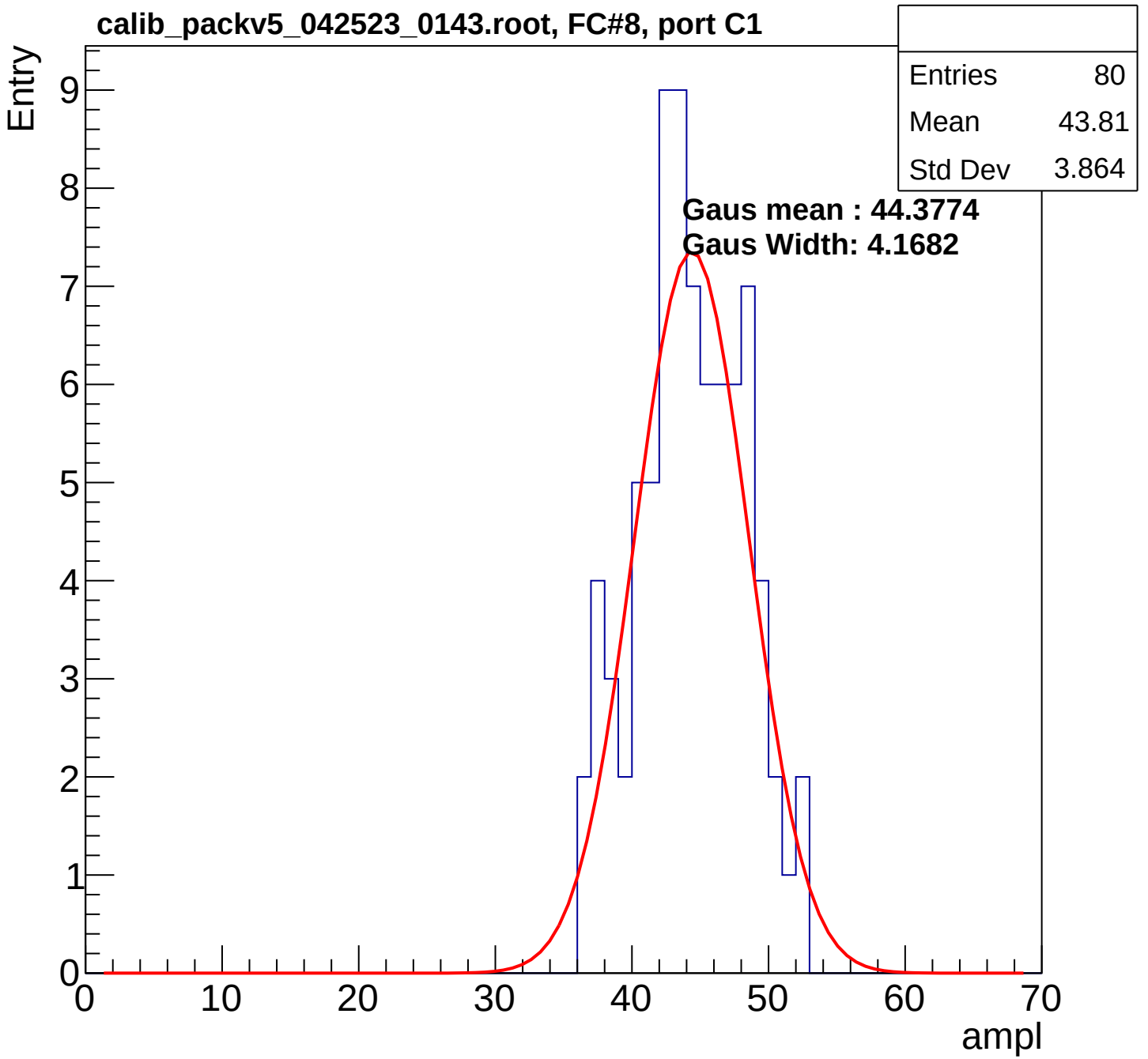
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch55, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 36.97
Std Dev 3.022
Gaus mean : 36.8418
Gaus Width: 3.6909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch55, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 43.81
Std Dev 3.864
Gaus mean : 44.3774
Gaus Width: 4.1682
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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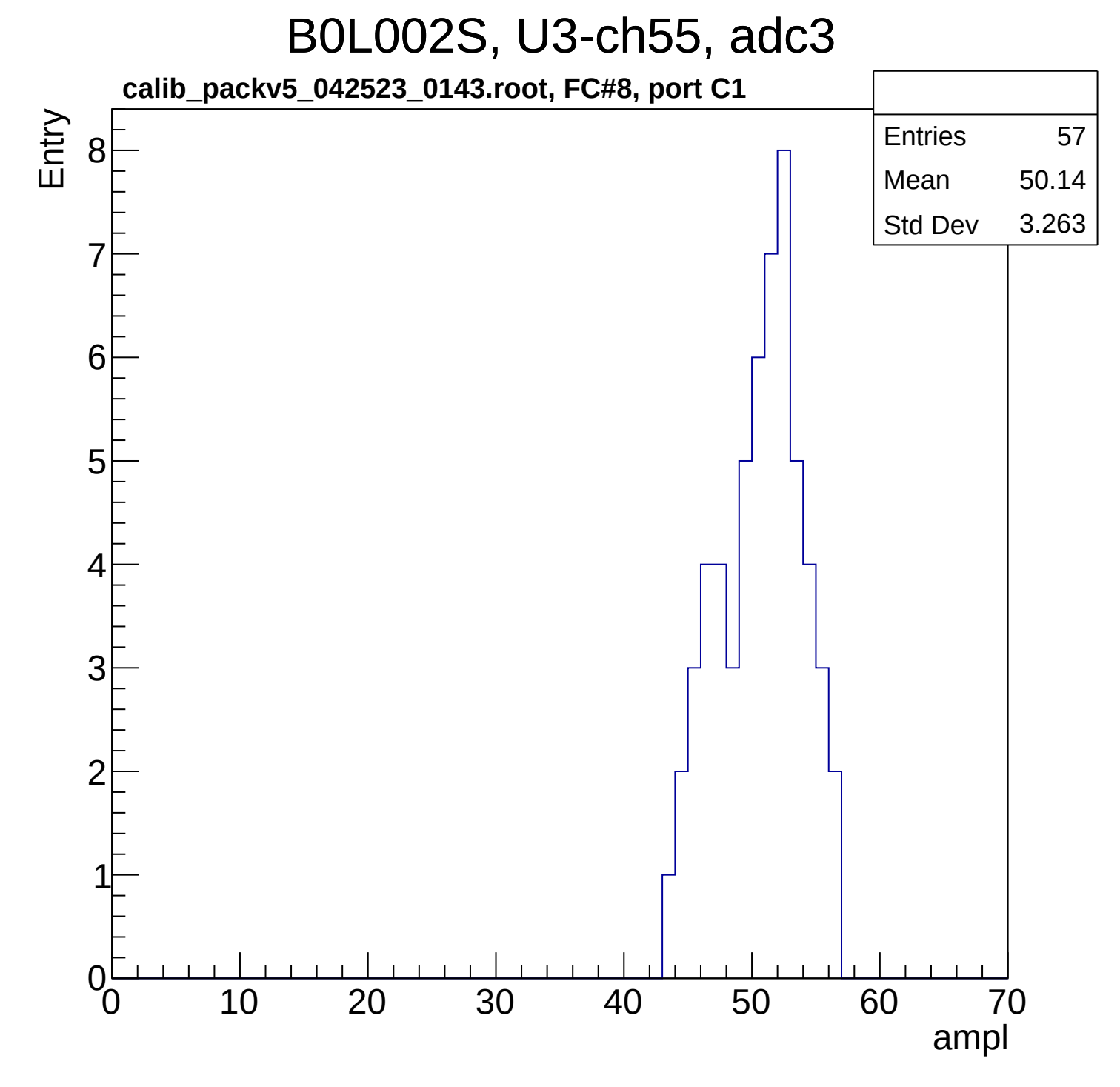
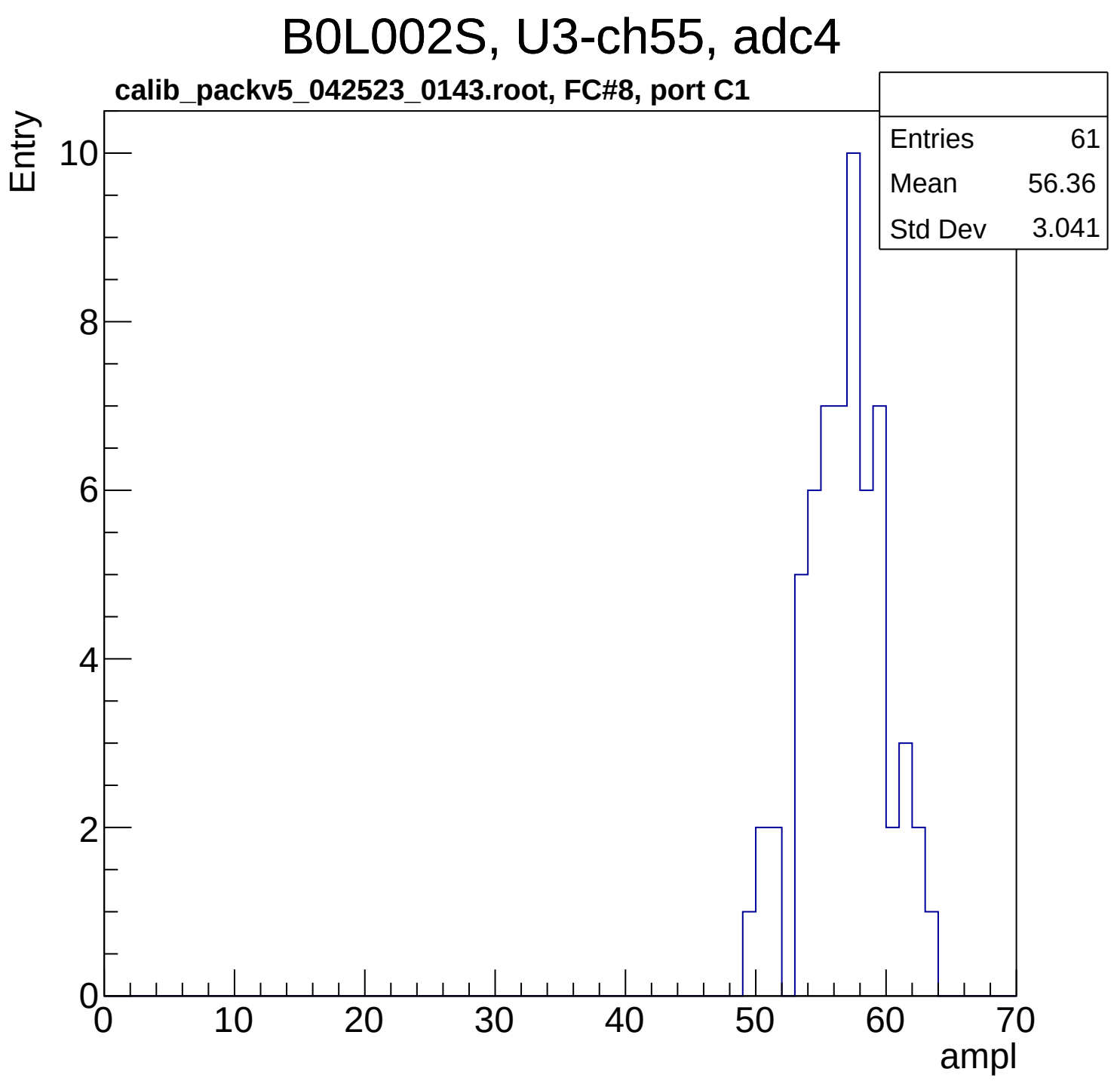
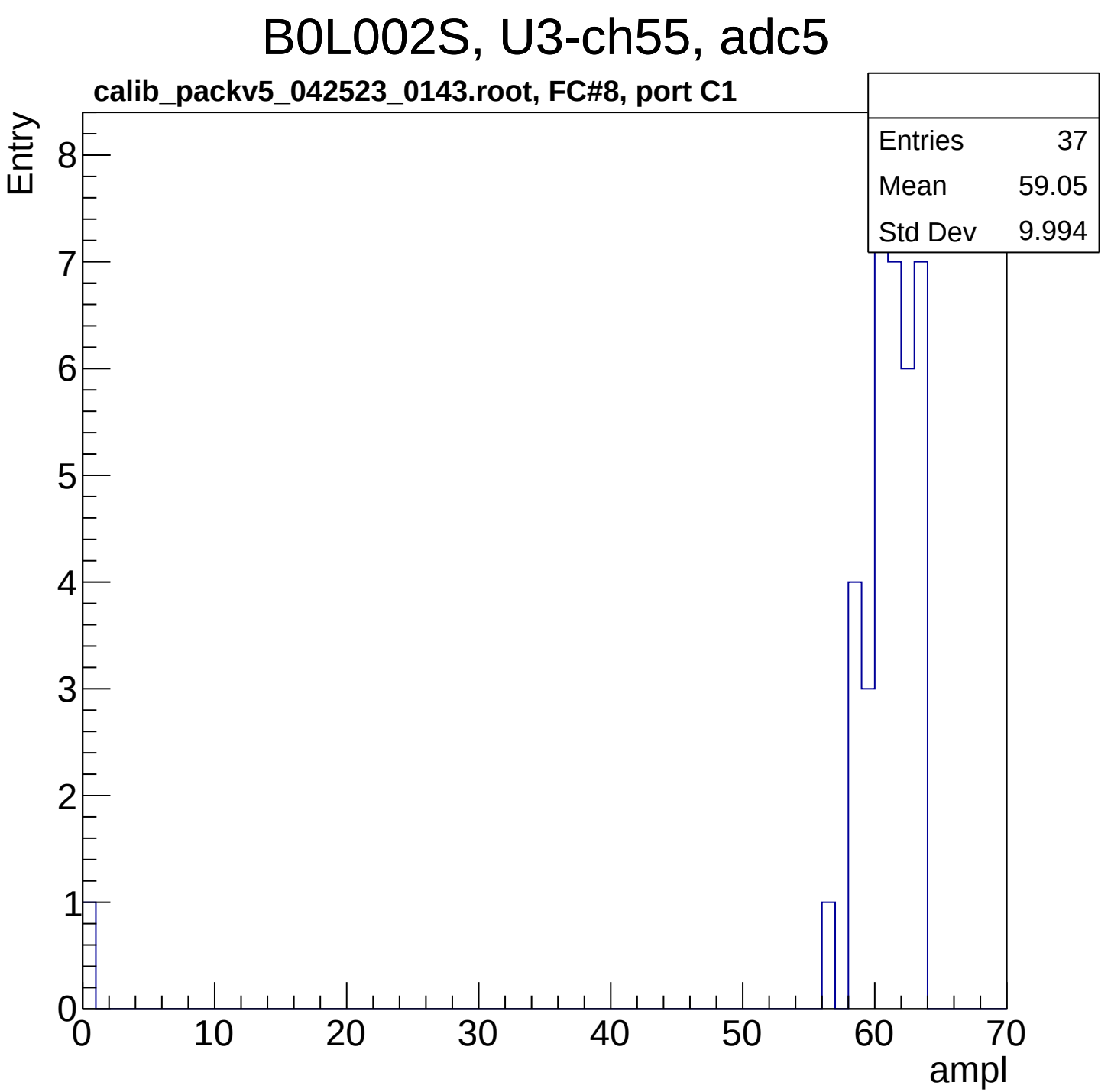

B0L002S, U3-ch55, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 50.14
Std Dev 3.263
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch55, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.36
Std Dev 3.041
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch55, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.05
Std Dev 9.994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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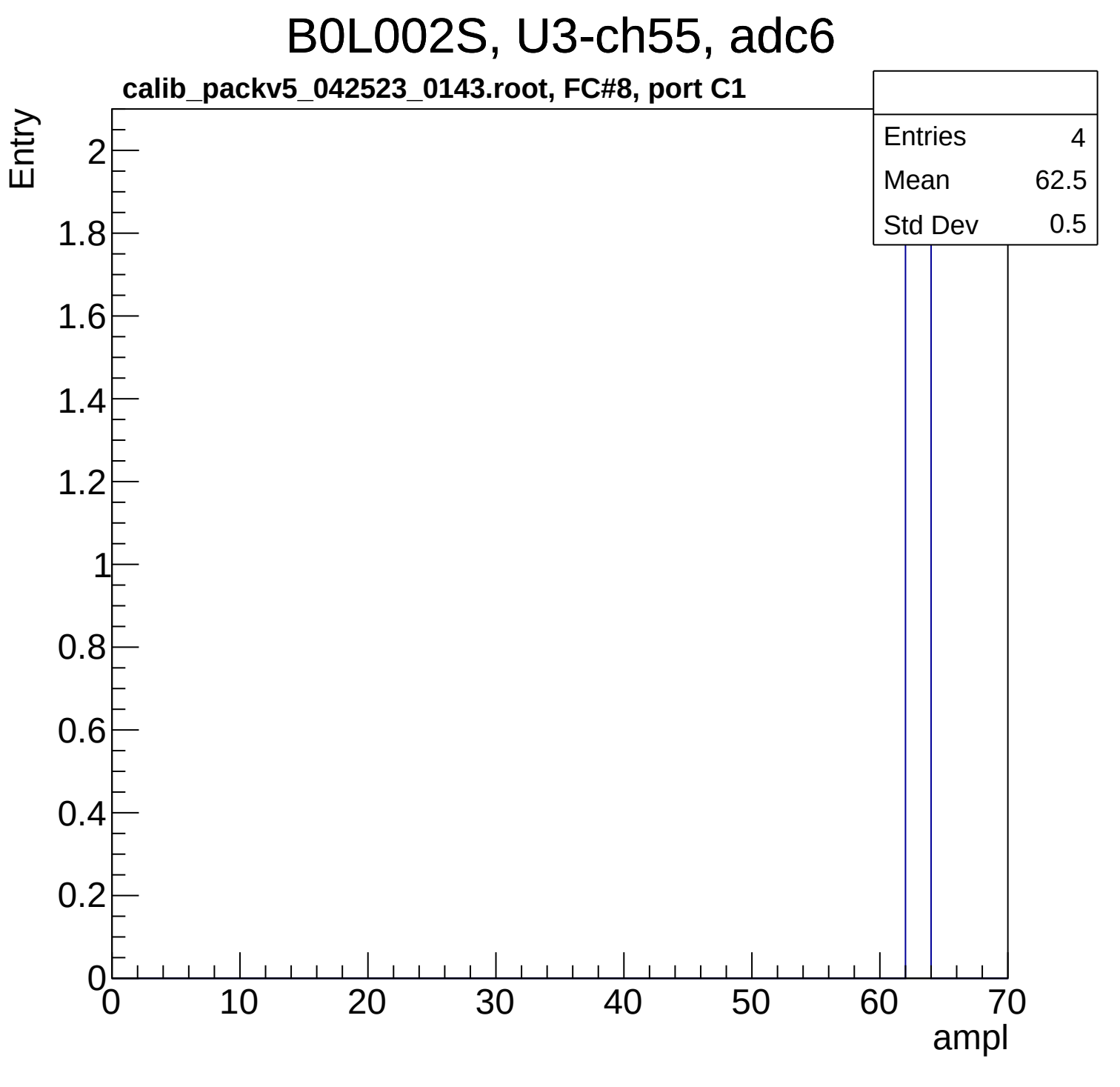
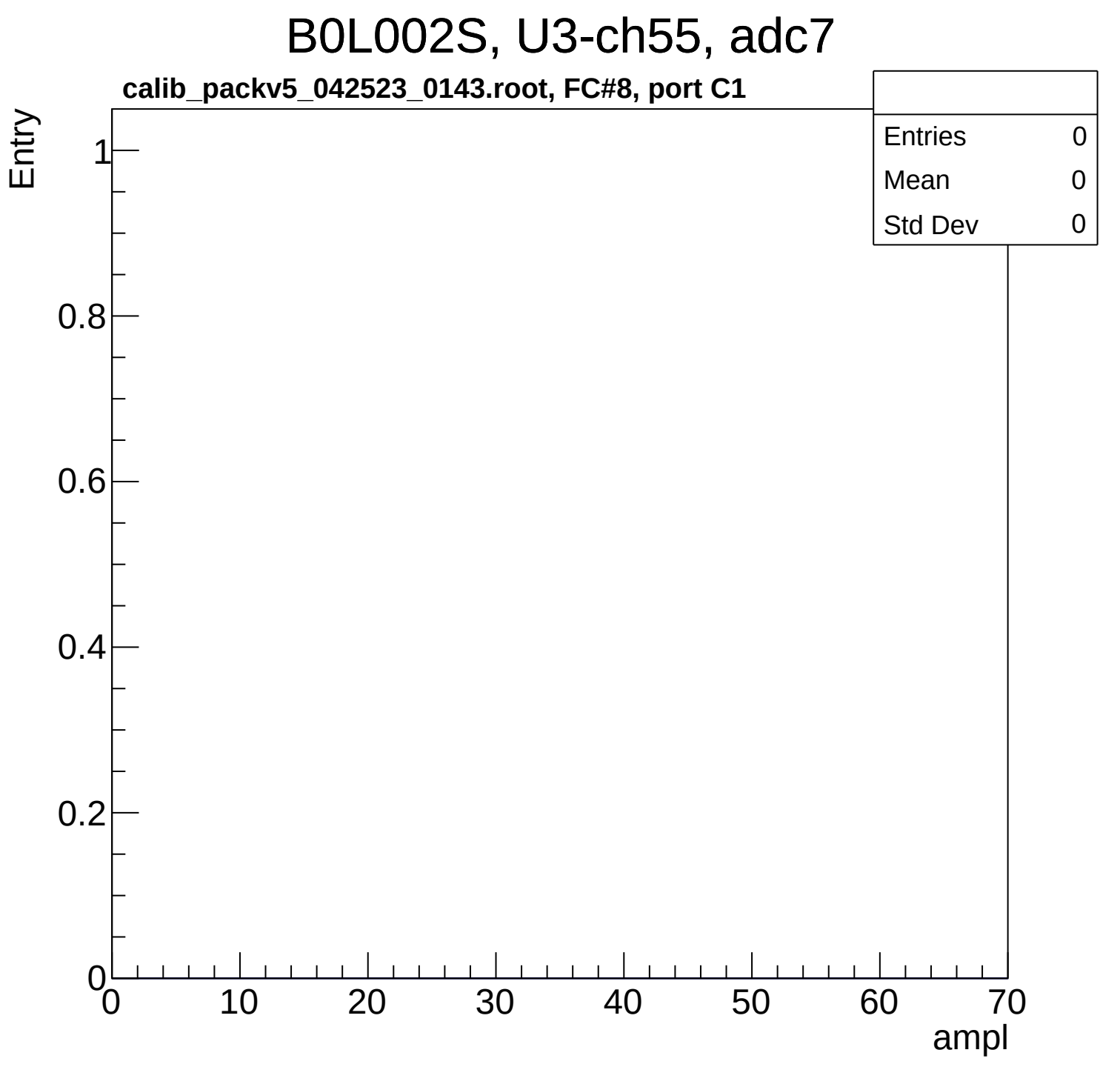
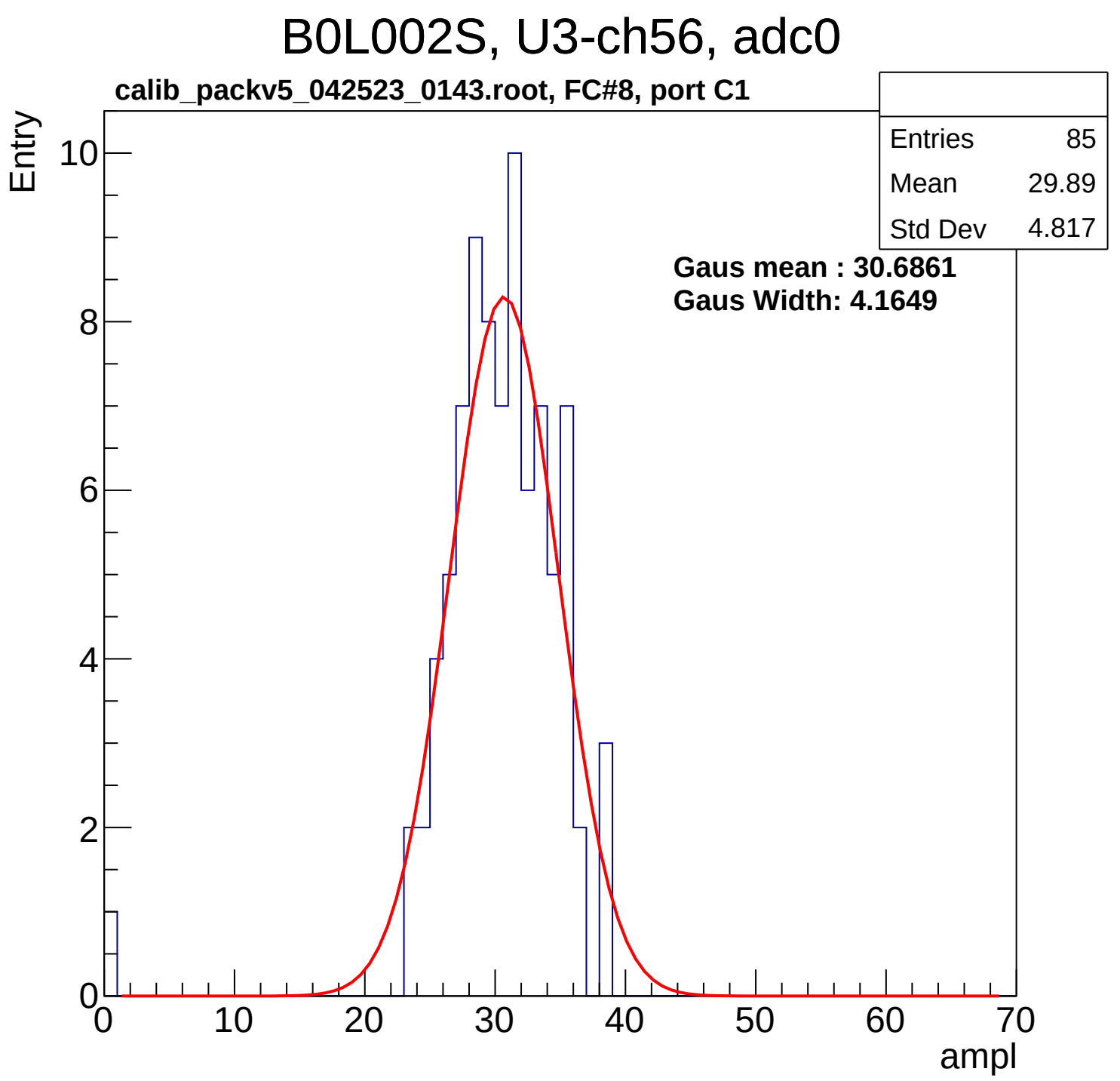
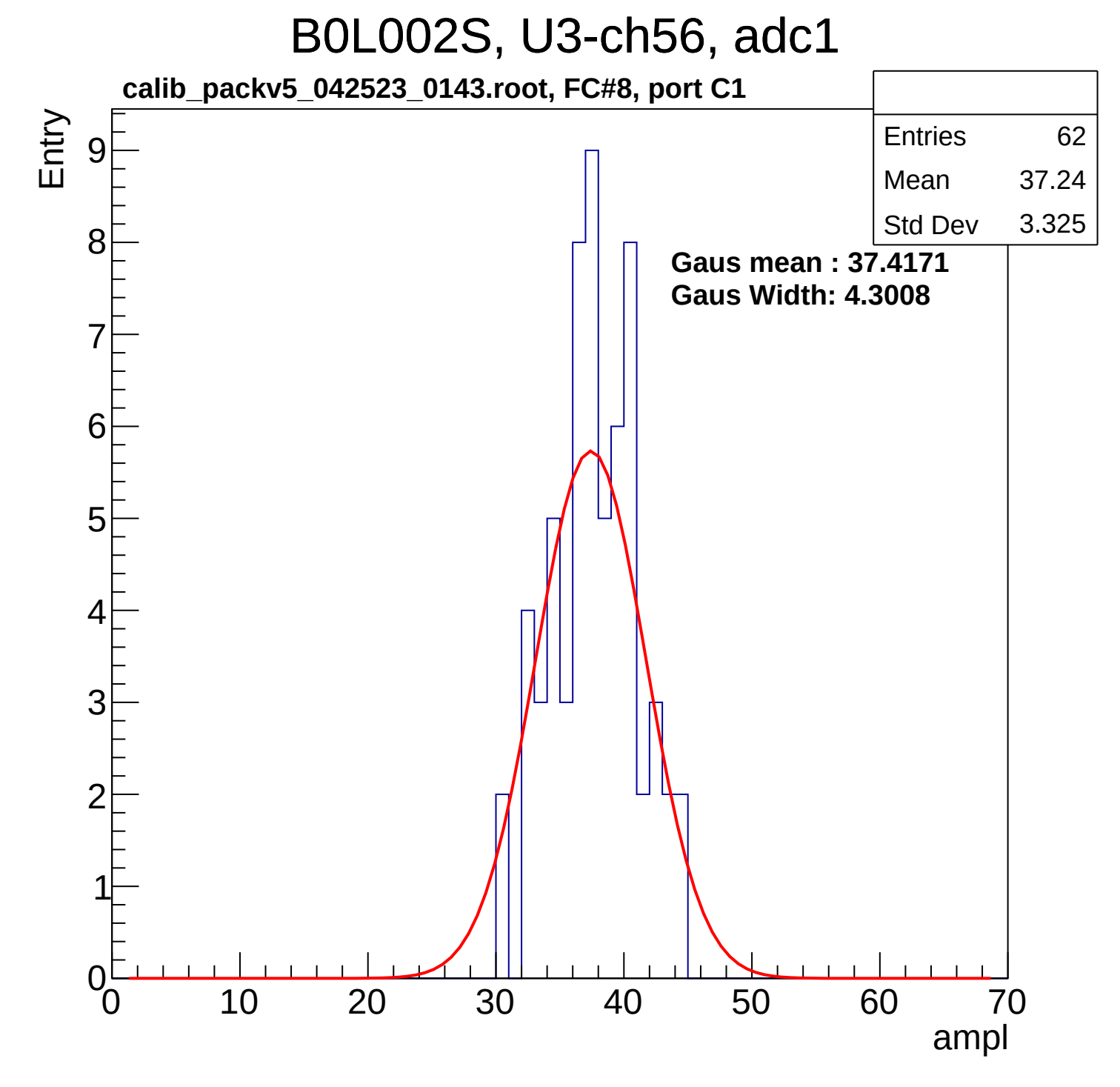
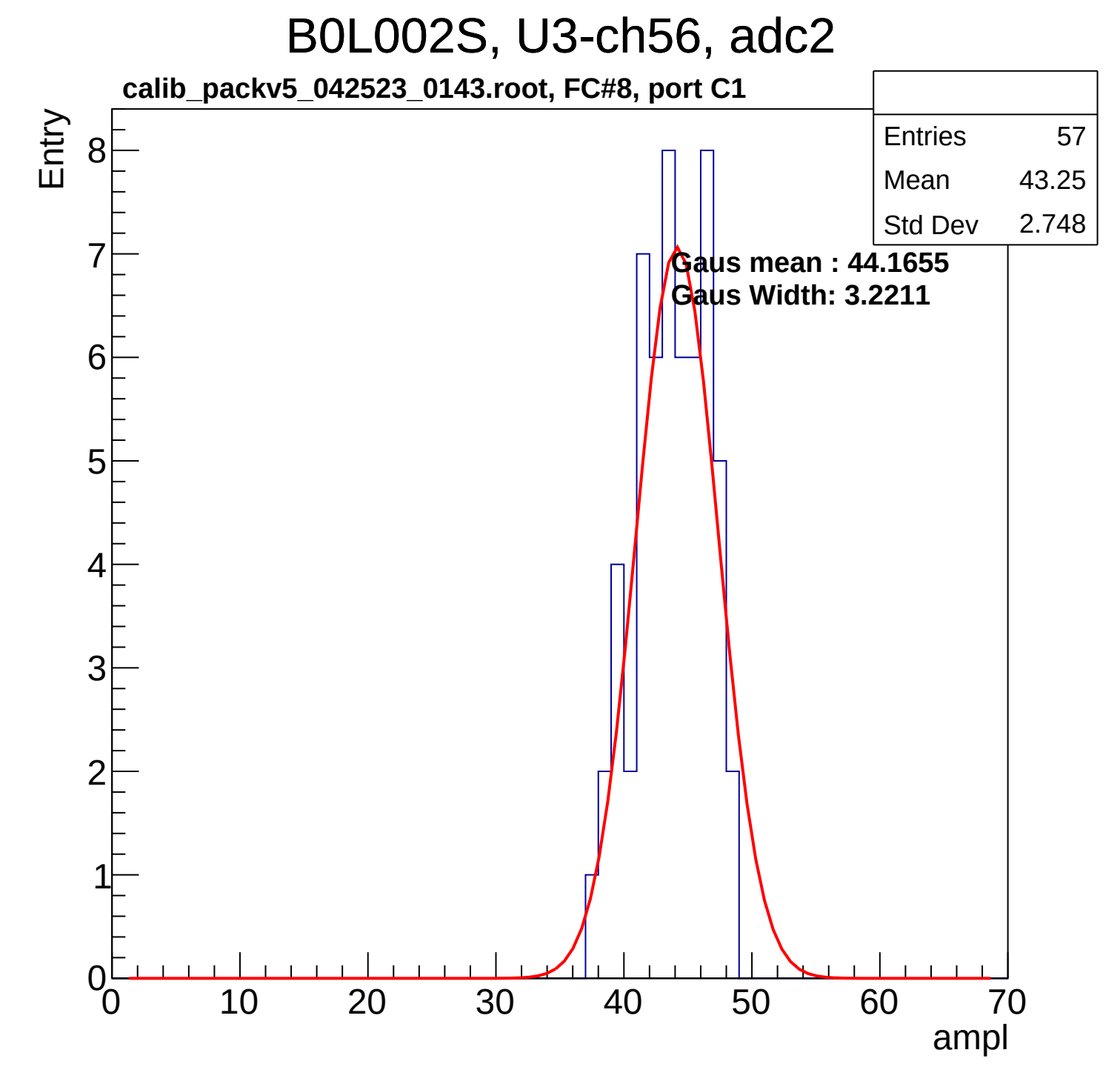
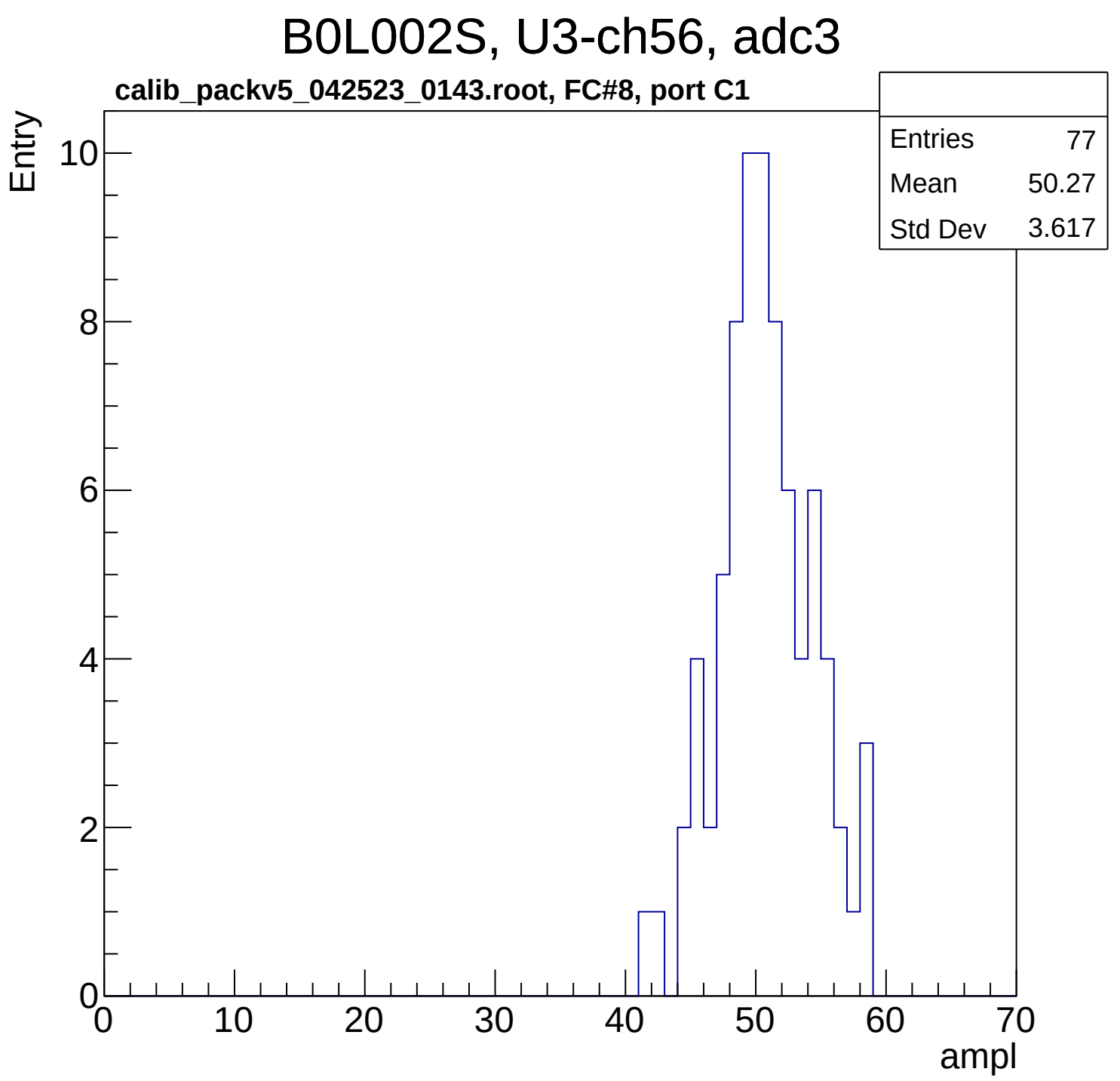

B0L002S, U3-ch55, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

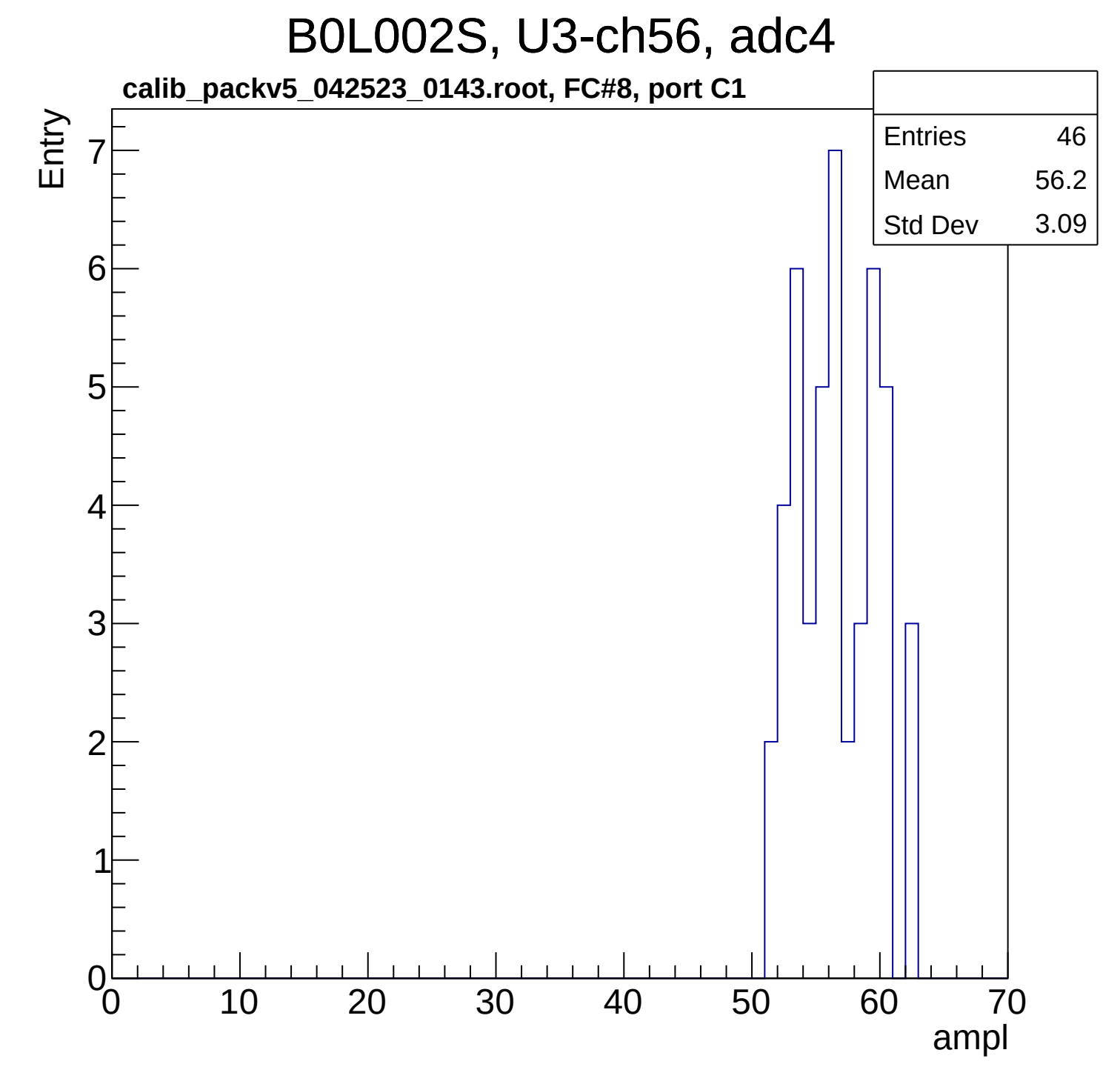

B0L002S, U3-ch55, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

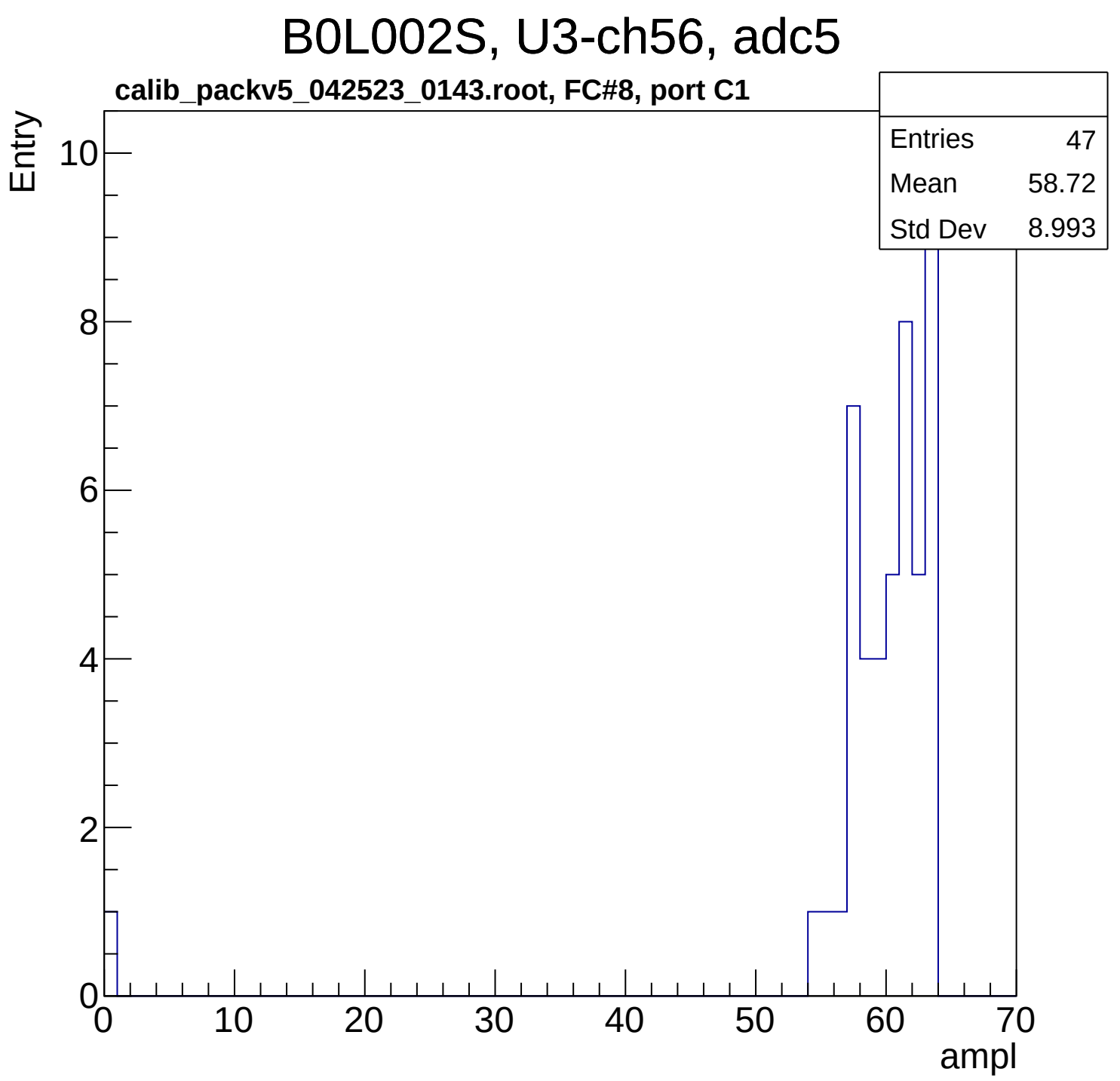

B0L002S, U3-ch56, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 29.89
Std Dev 4.817
Gaus mean : 30.6861
Gaus Width: 4.1649
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

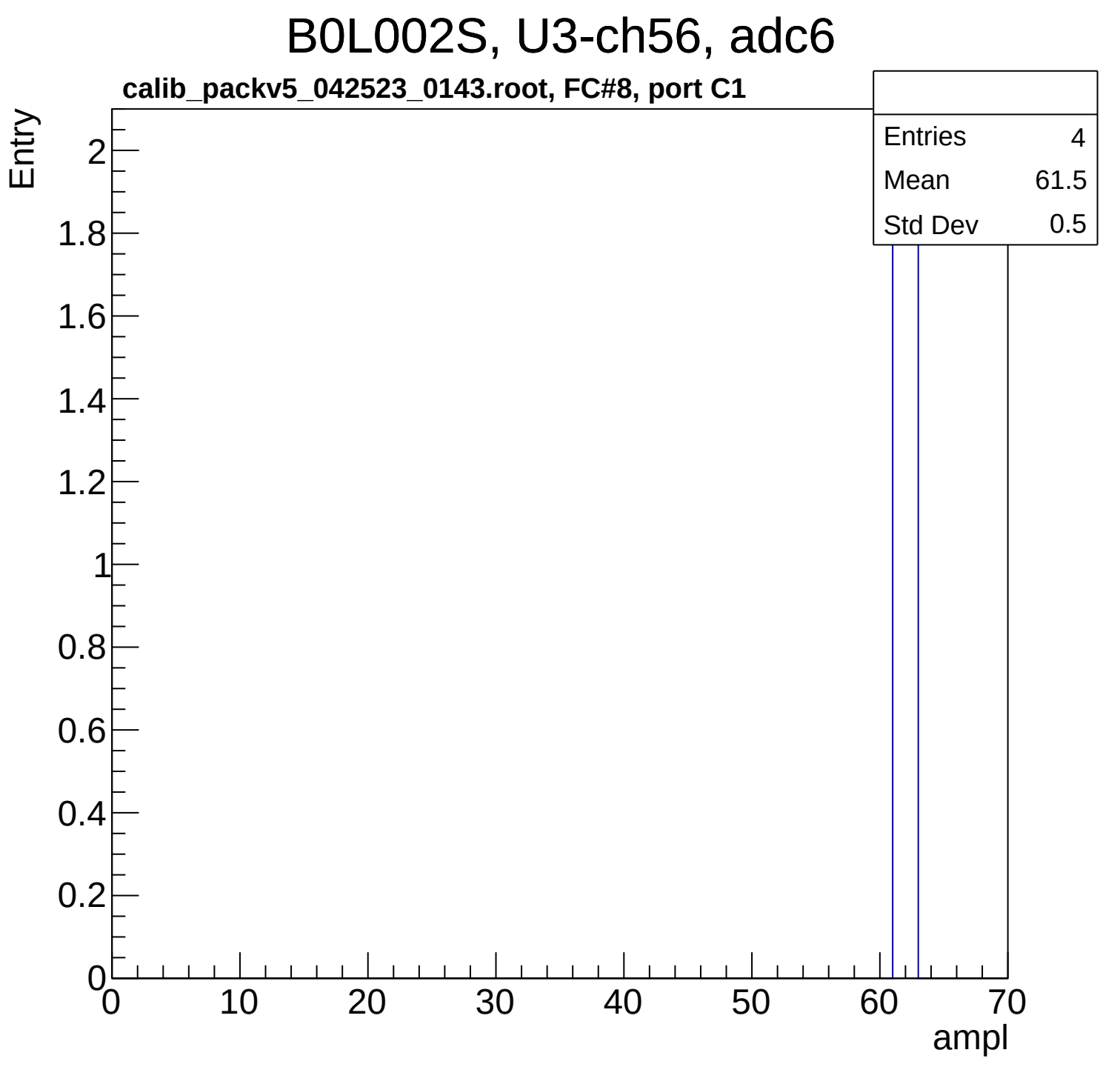

B0L002S, U3-ch56, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.24
Std Dev 3.325
Gaus mean : 37.4171
Gaus Width: 4.3008
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

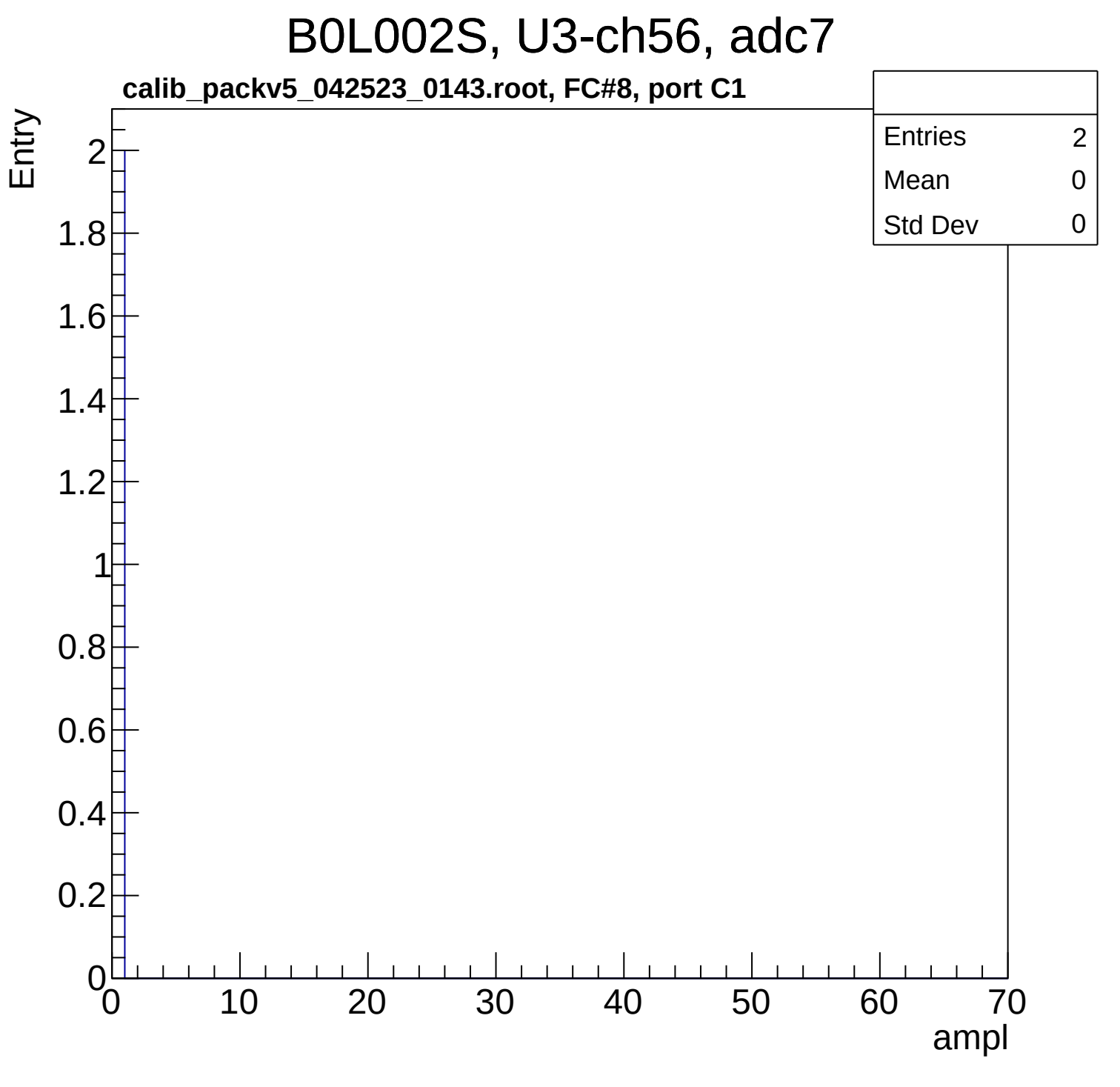

B0L002S, U3-ch56, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 43.25
Std Dev 2.748
Gaus mean : 44.1655
Gaus Width: 3.2211
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

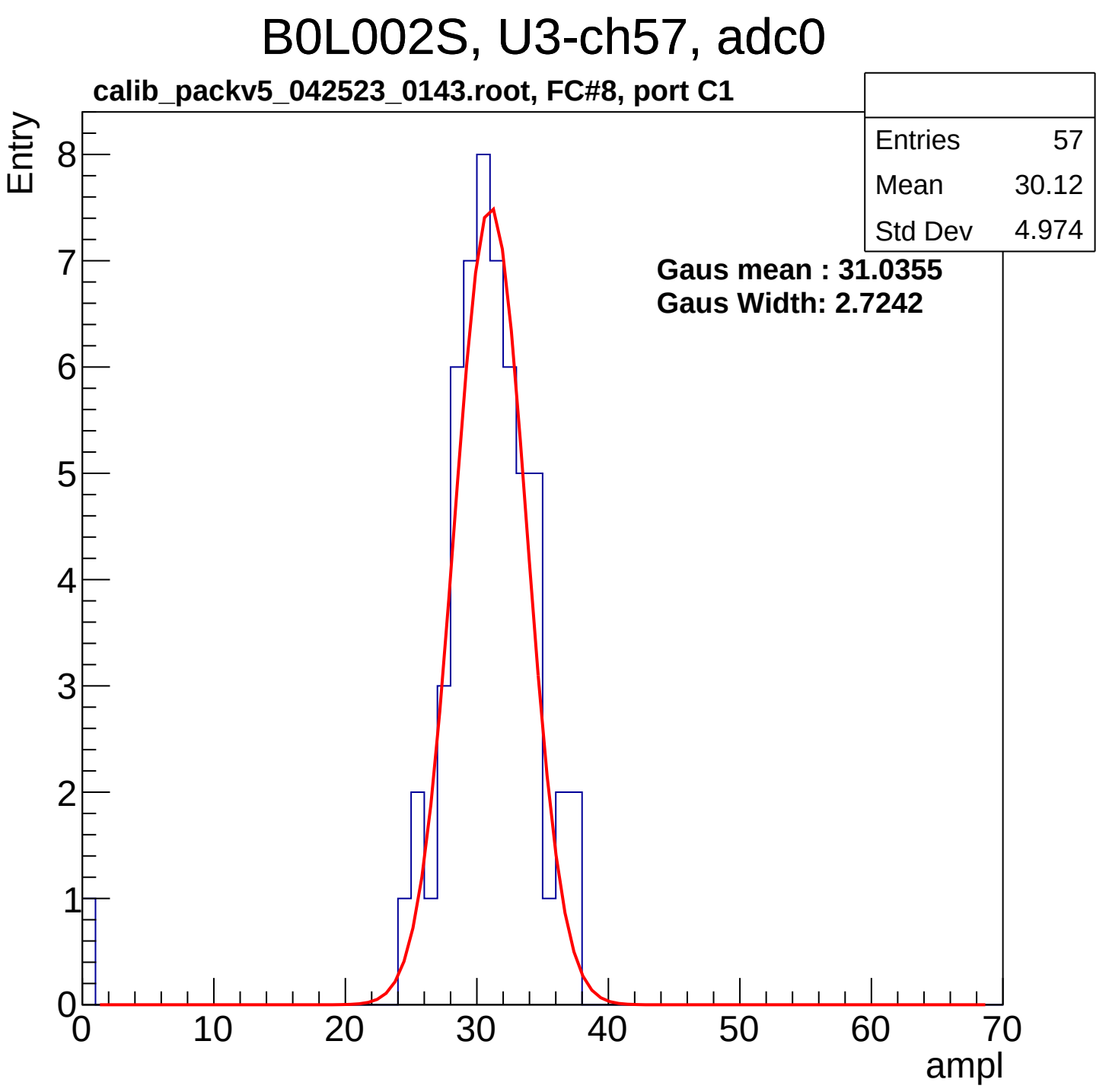

B0L002S, U3-ch56, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 50.27
Std Dev 3.617
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch56, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 56.2
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch56, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 58.72
Std Dev 8.993
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch56, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch56, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch57, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 30.12
Std Dev 4.974
Gaus mean : 31.0355
Gaus Width: 2.7242
Entry
ampl
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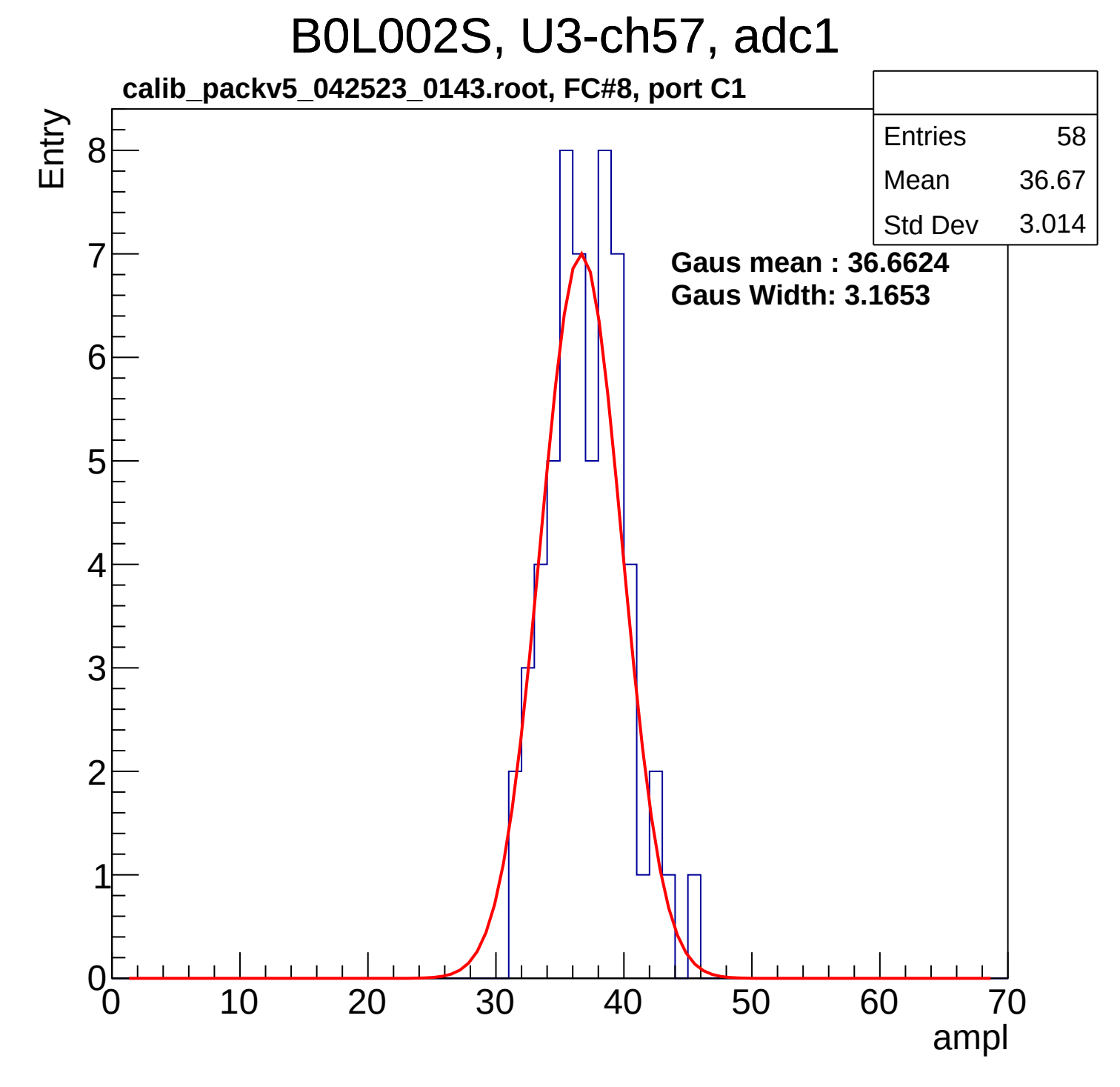
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch57, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 36.67
Std Dev 3.014
Gaus mean : 36.6624
Gaus Width: 3.1653
Entry
ampl
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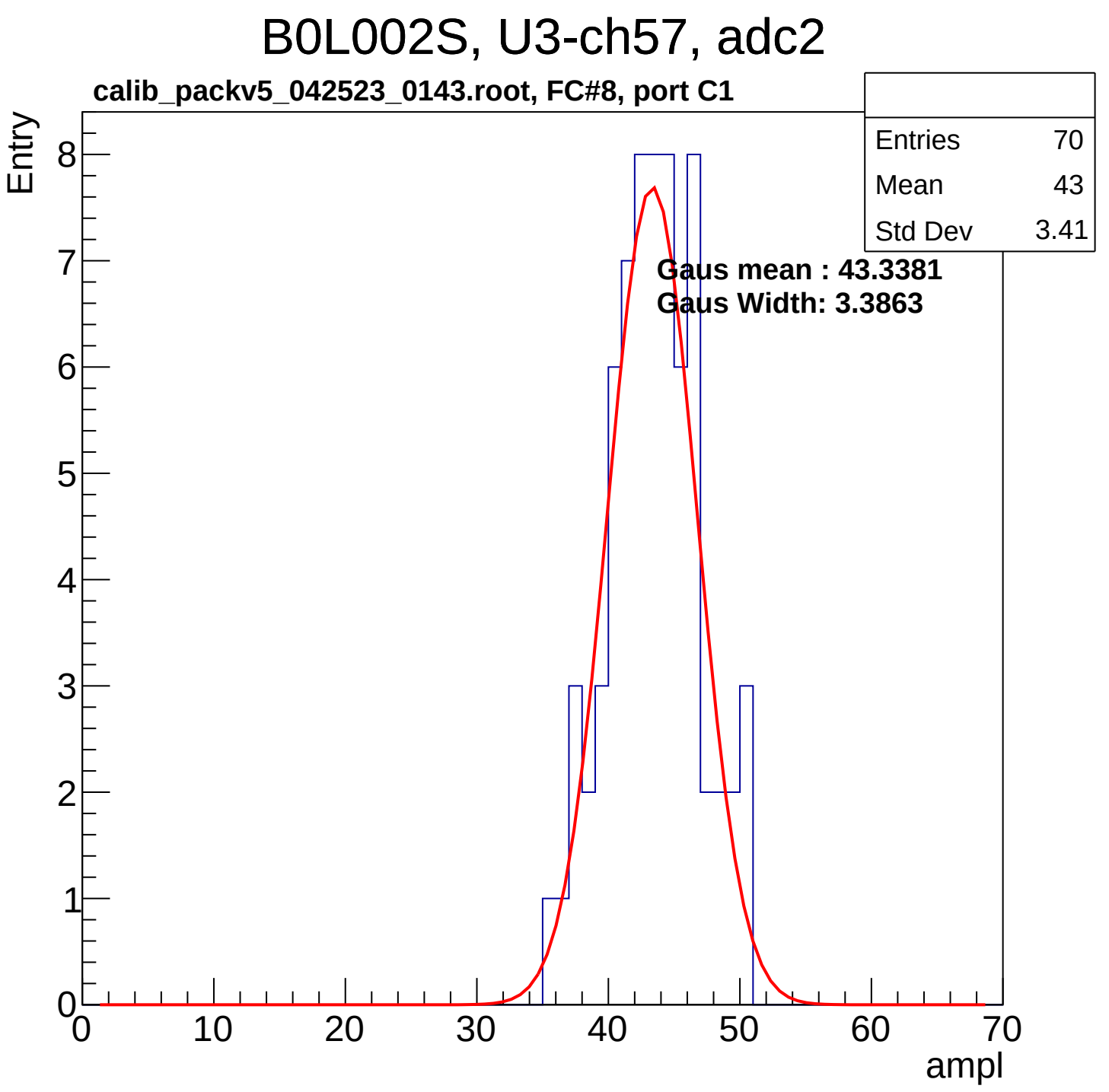
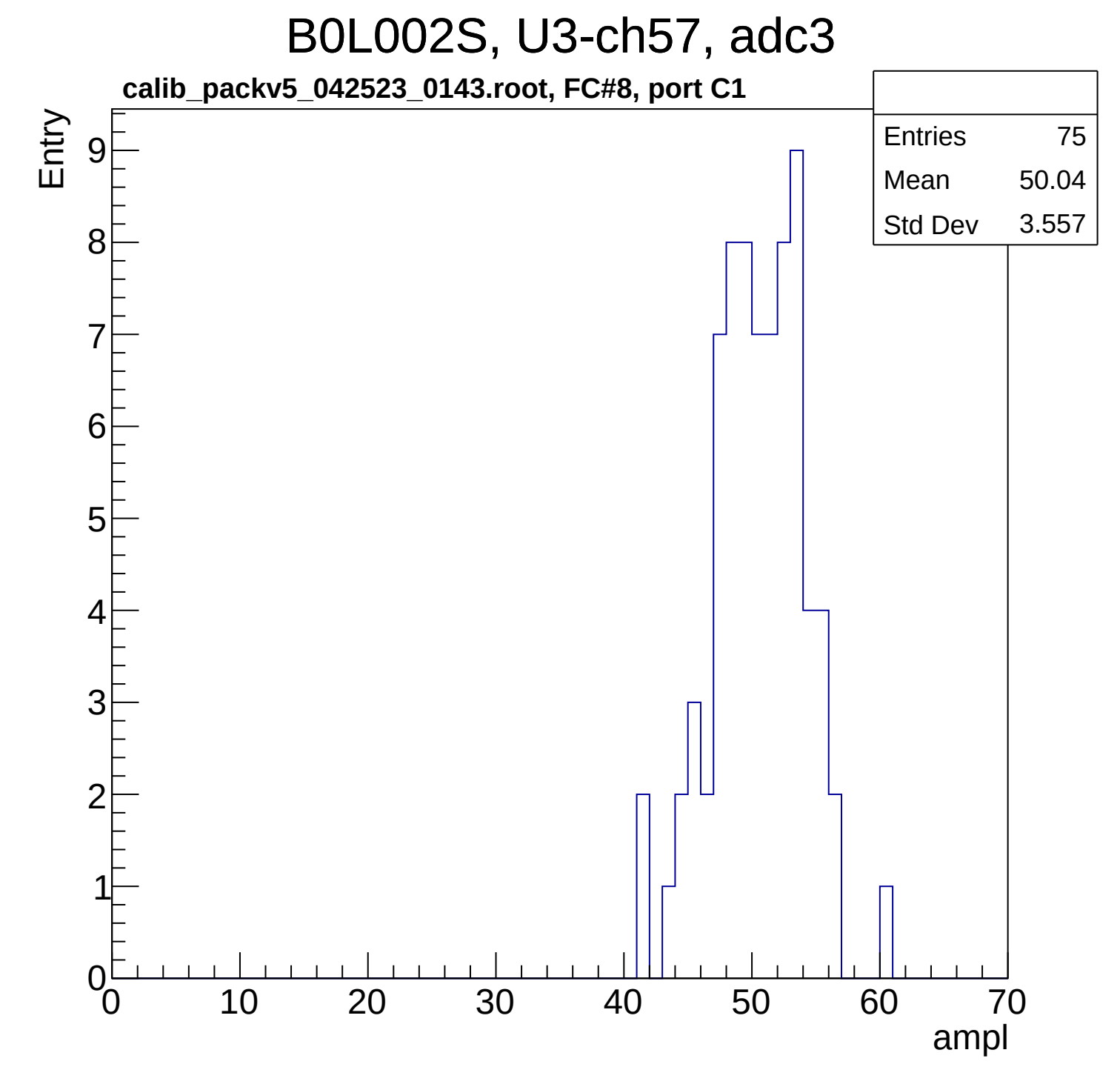
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch57, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43
Std Dev 3.41
Gaus mean : 43.3381
Gaus Width: 3.3863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch57, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.04
Std Dev 3.557
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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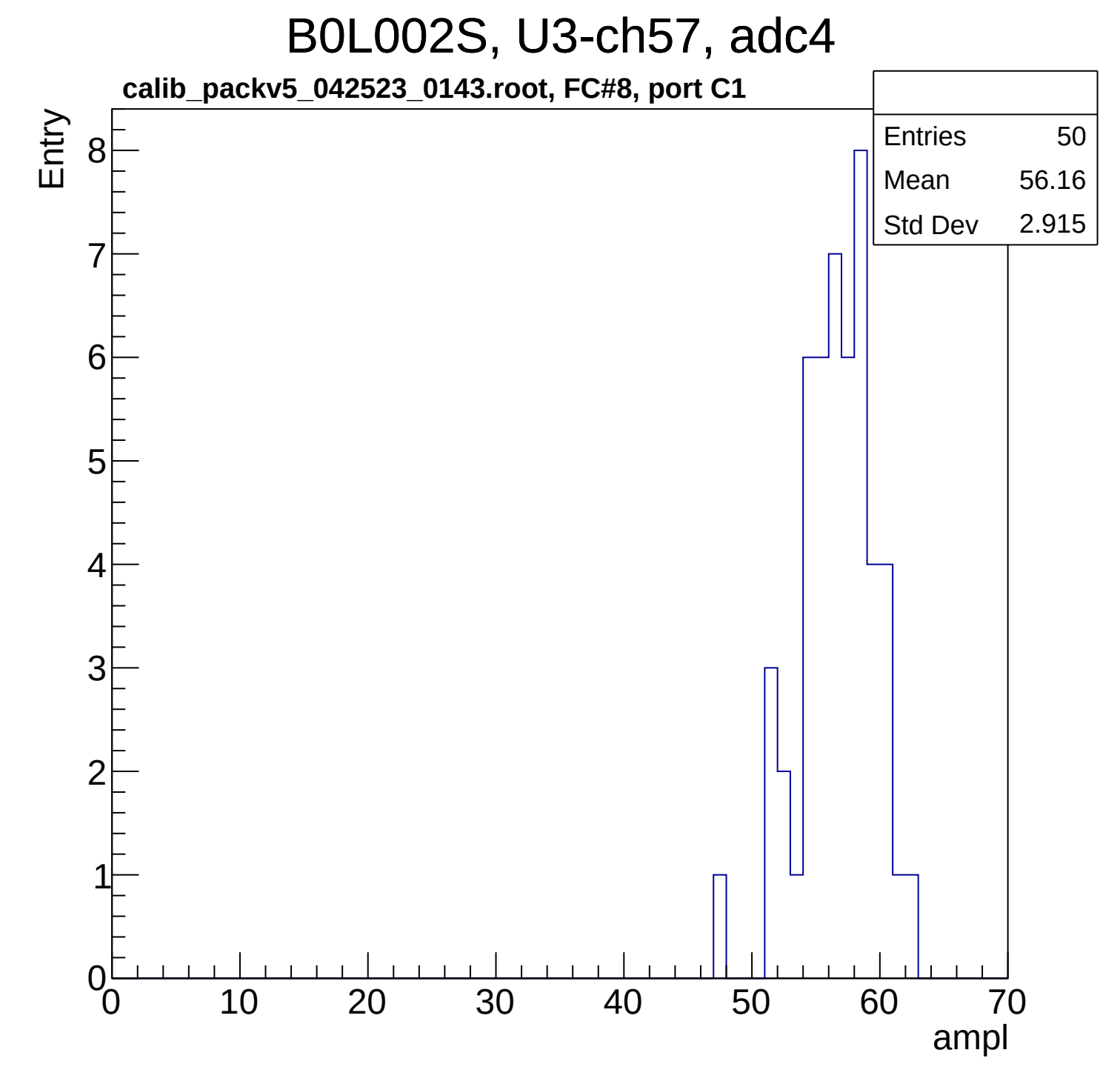
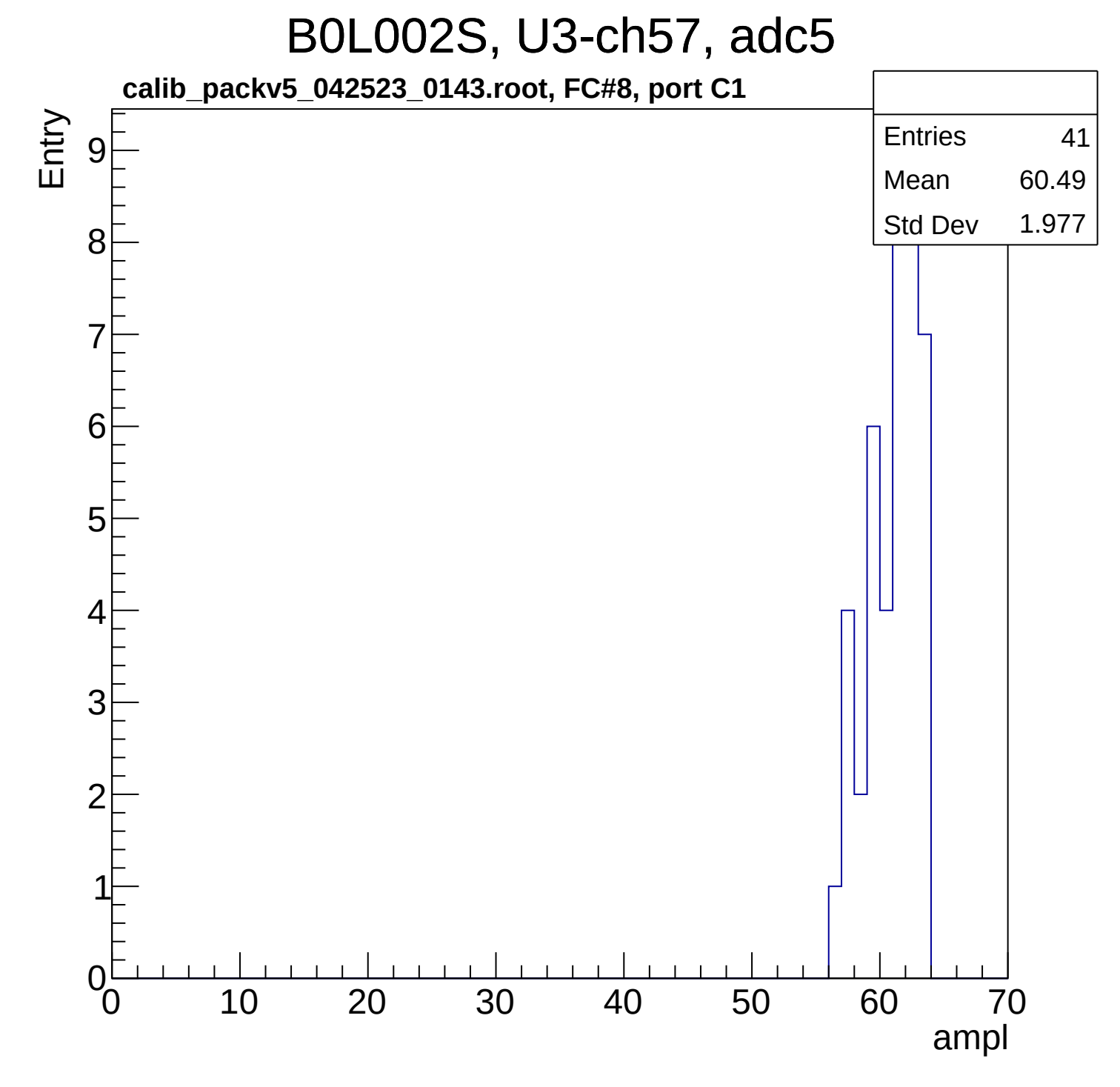
40
50
60
70


B0L002S, U3-ch57, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 56.16
Std Dev 2.915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch57, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 60.49
Std Dev 1.977
Entry
ampl
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2
3
4
5
6
7
8
9
0
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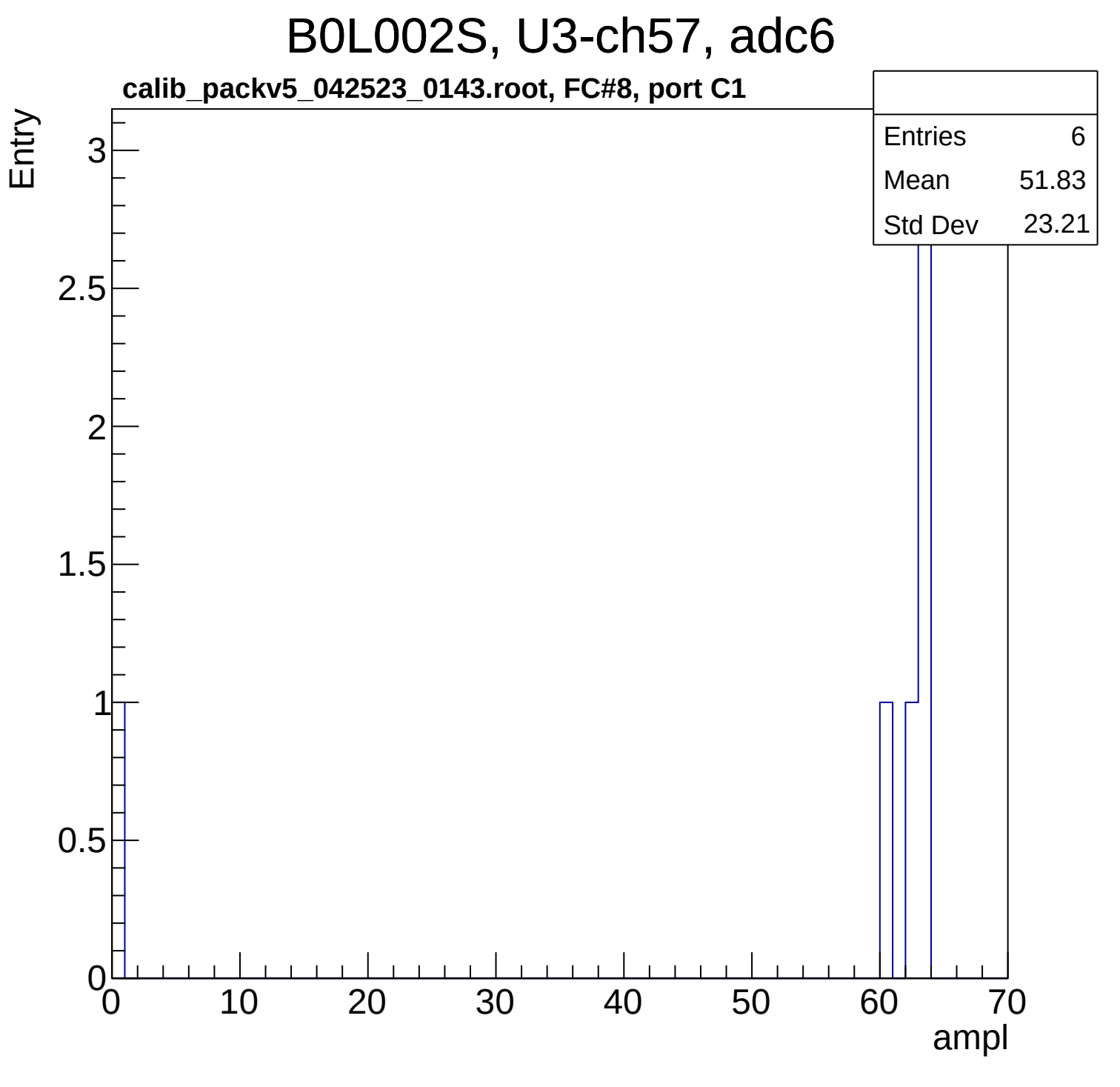
B0L002S, U3-ch57, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 51.83
Std Dev 23.21
Entry
ampl
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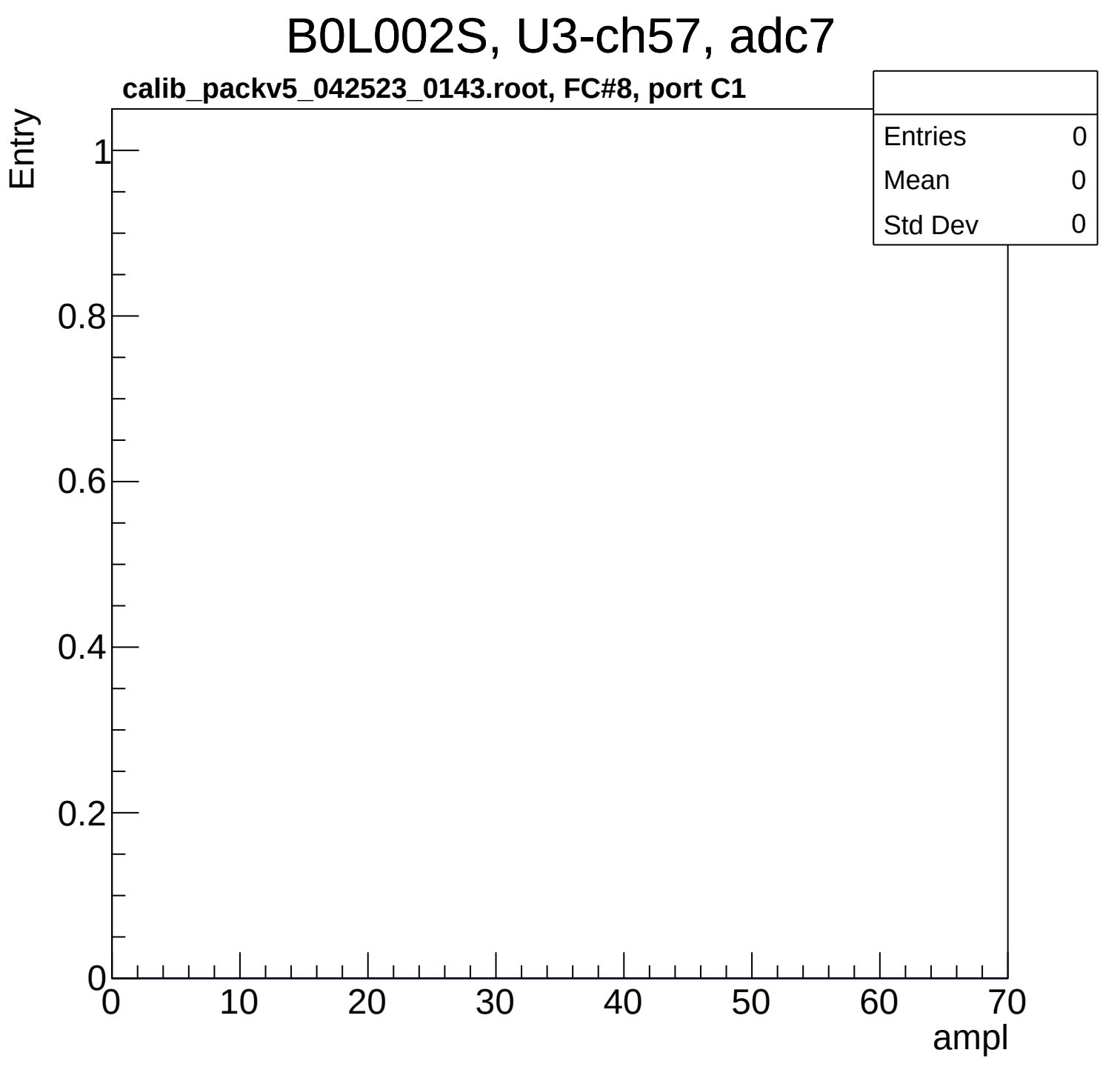
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch57, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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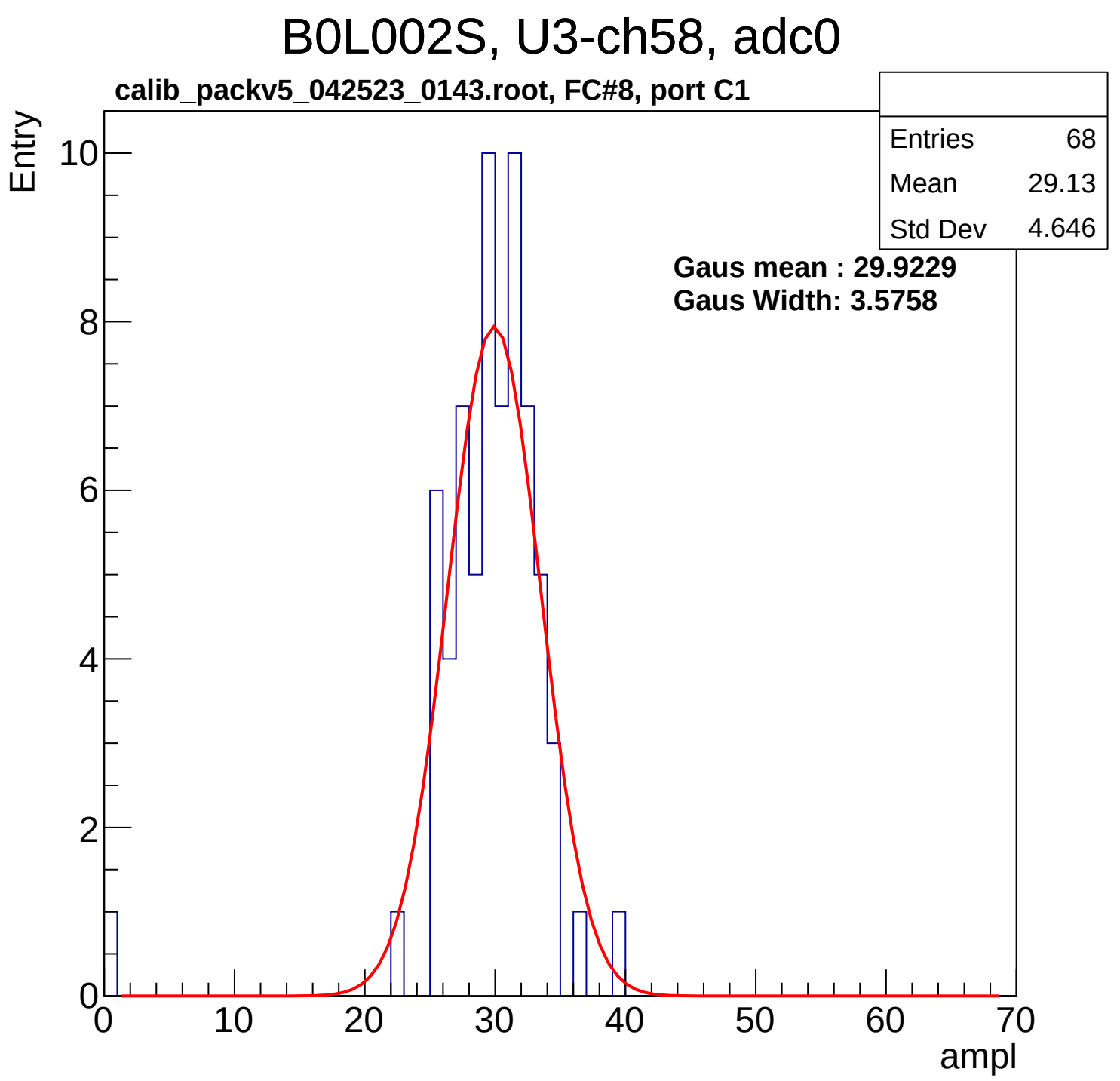
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch58, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 29.13
Std Dev 4.646
Gaus mean : 29.9229
Gaus Width: 3.5758
Entry
ampl
0
2
4
6
8
10
0
10
20
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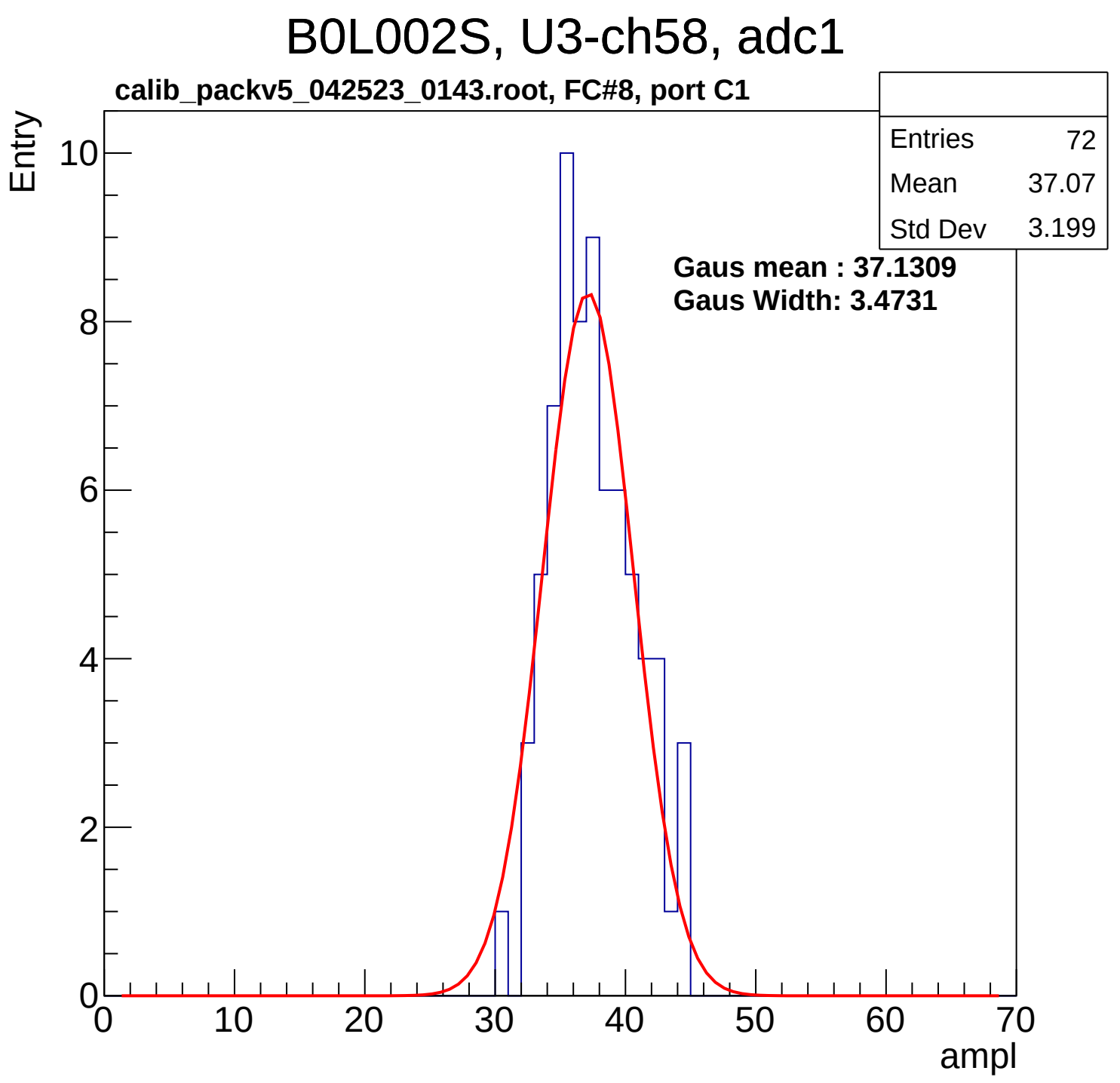
40
50
60
70


B0L002S, U3-ch58, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.07
Std Dev 3.199
Gaus mean : 37.1309
Gaus Width: 3.4731
Entry
ampl
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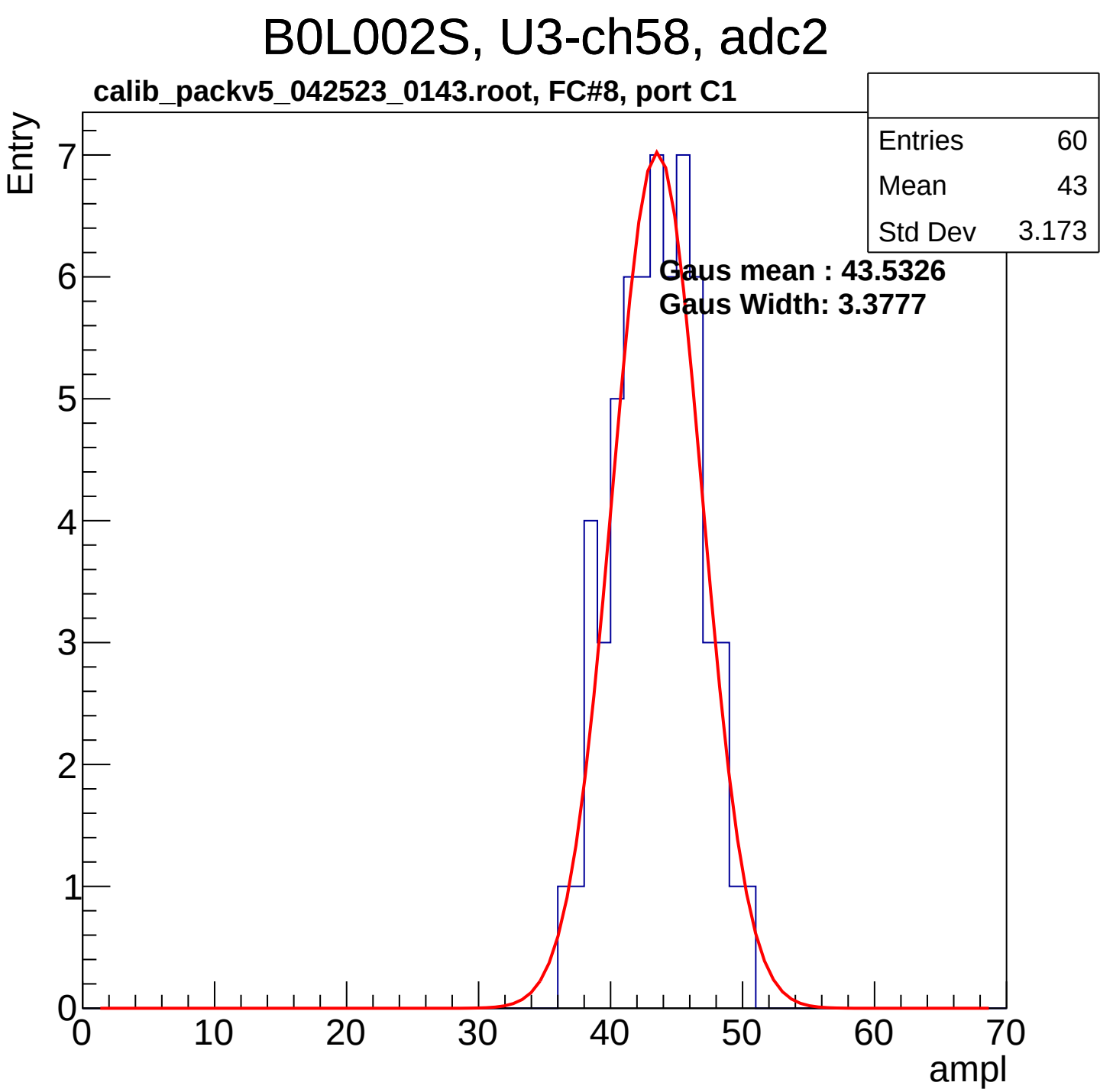
B0L002S, U3-ch58, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 43
Std Dev 3.173
Gaus mean : 43.5326
Gaus Width: 3.3777
Entry
ampl
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1
2
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0
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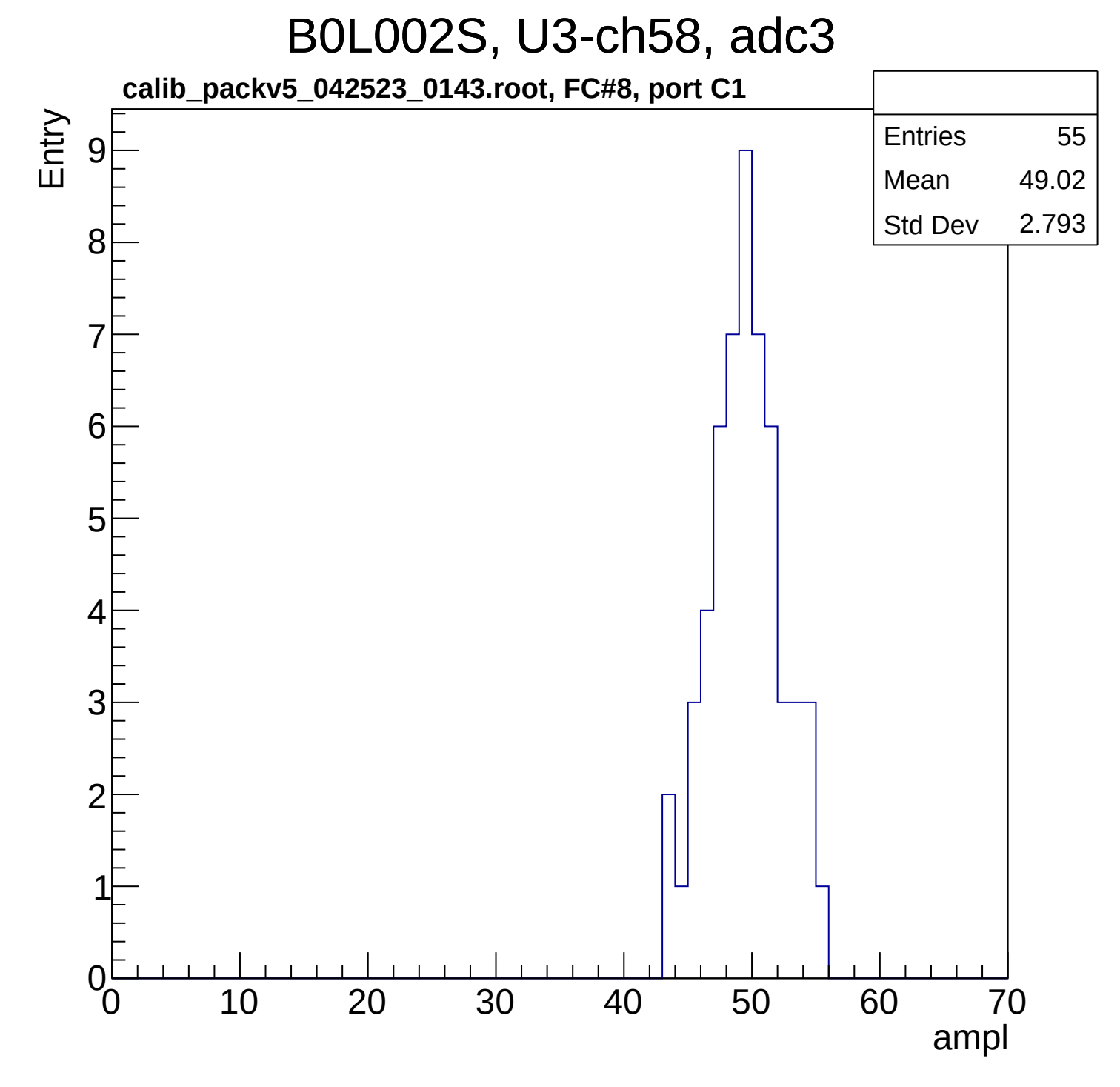
B0L002S, U3-ch58, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 49.02
Std Dev 2.793
Entry
ampl
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0
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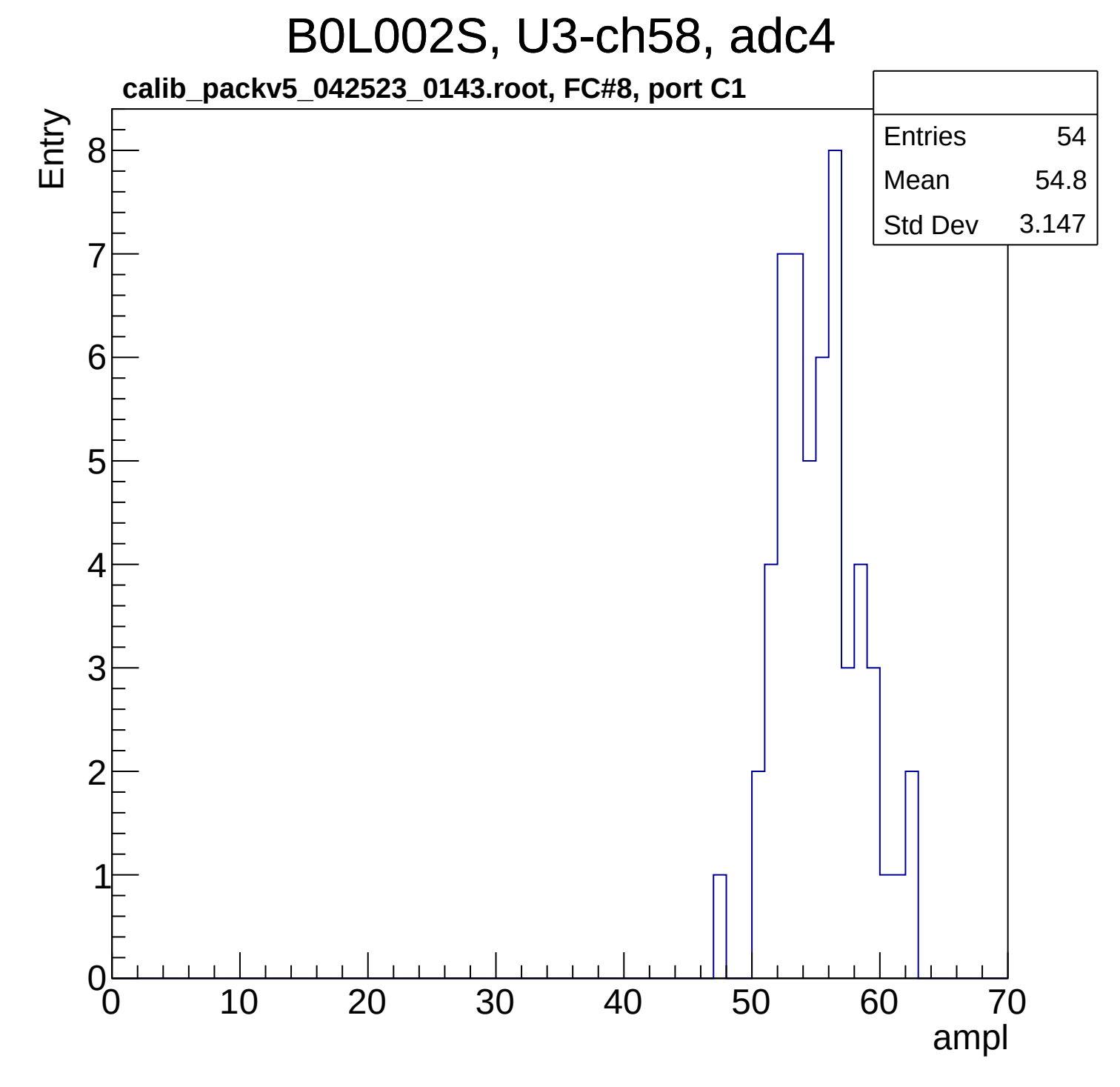
B0L002S, U3-ch58, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 54.8
Std Dev 3.147
Entry
ampl
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4
5
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0
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40
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60
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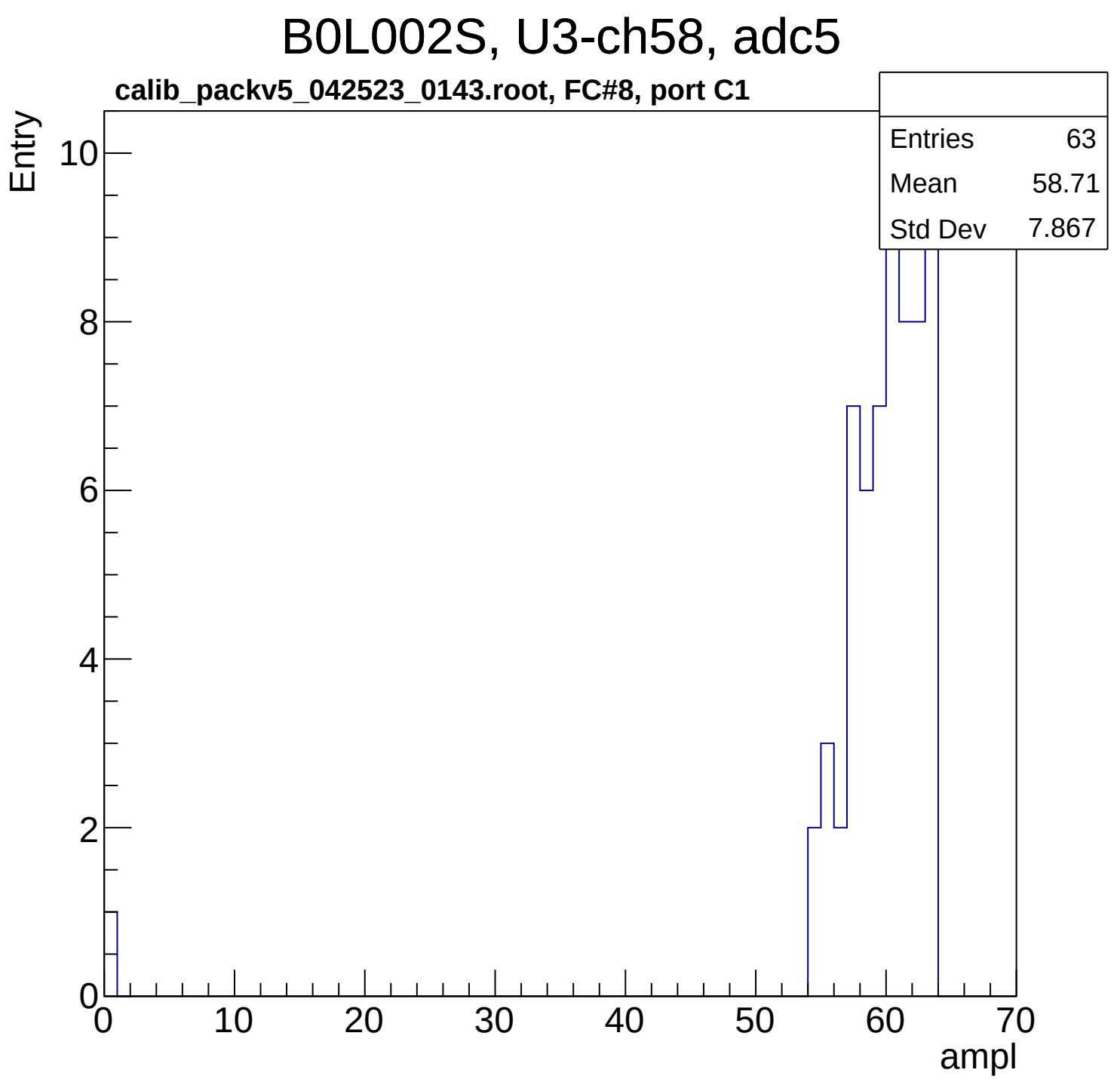
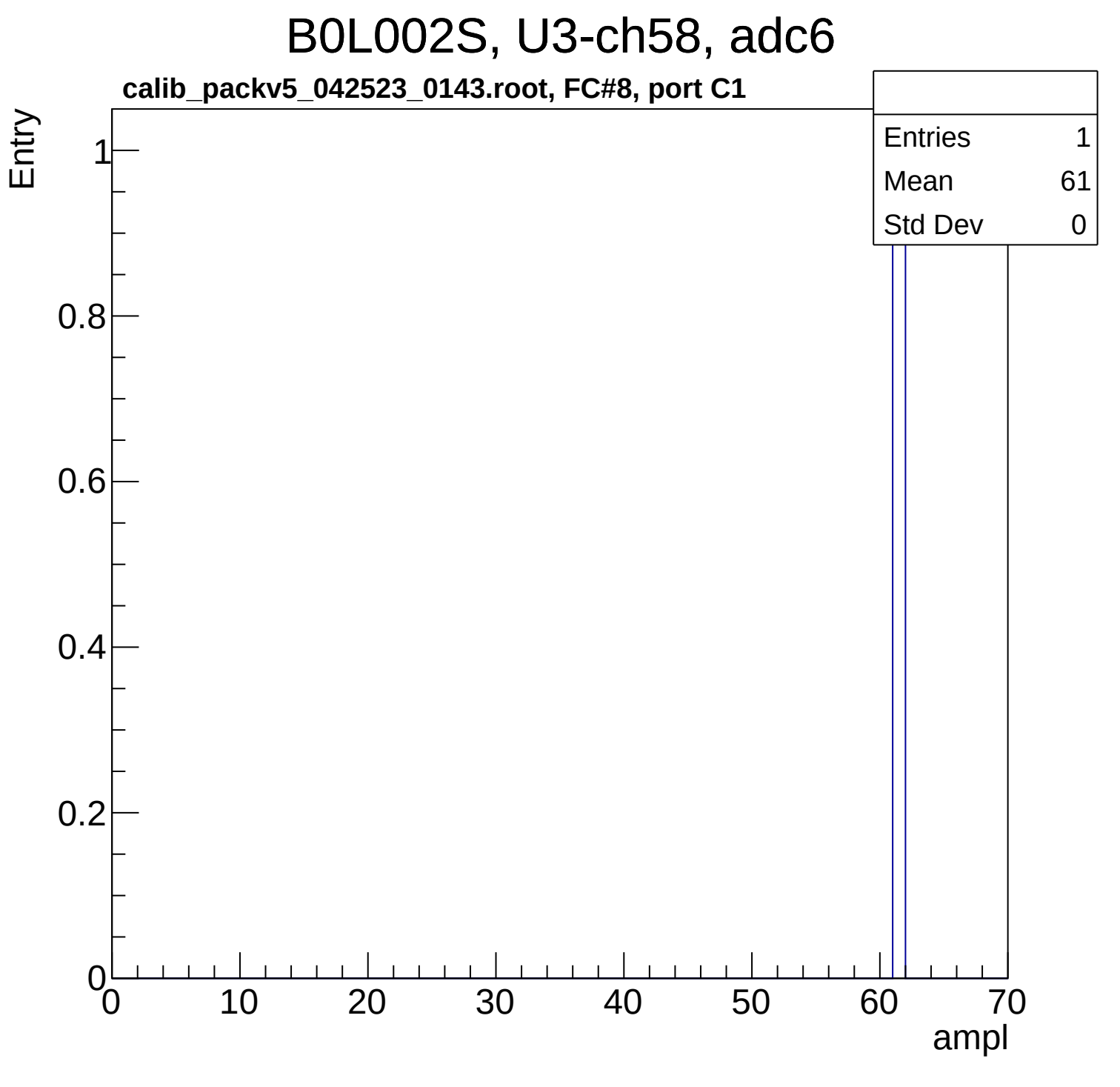
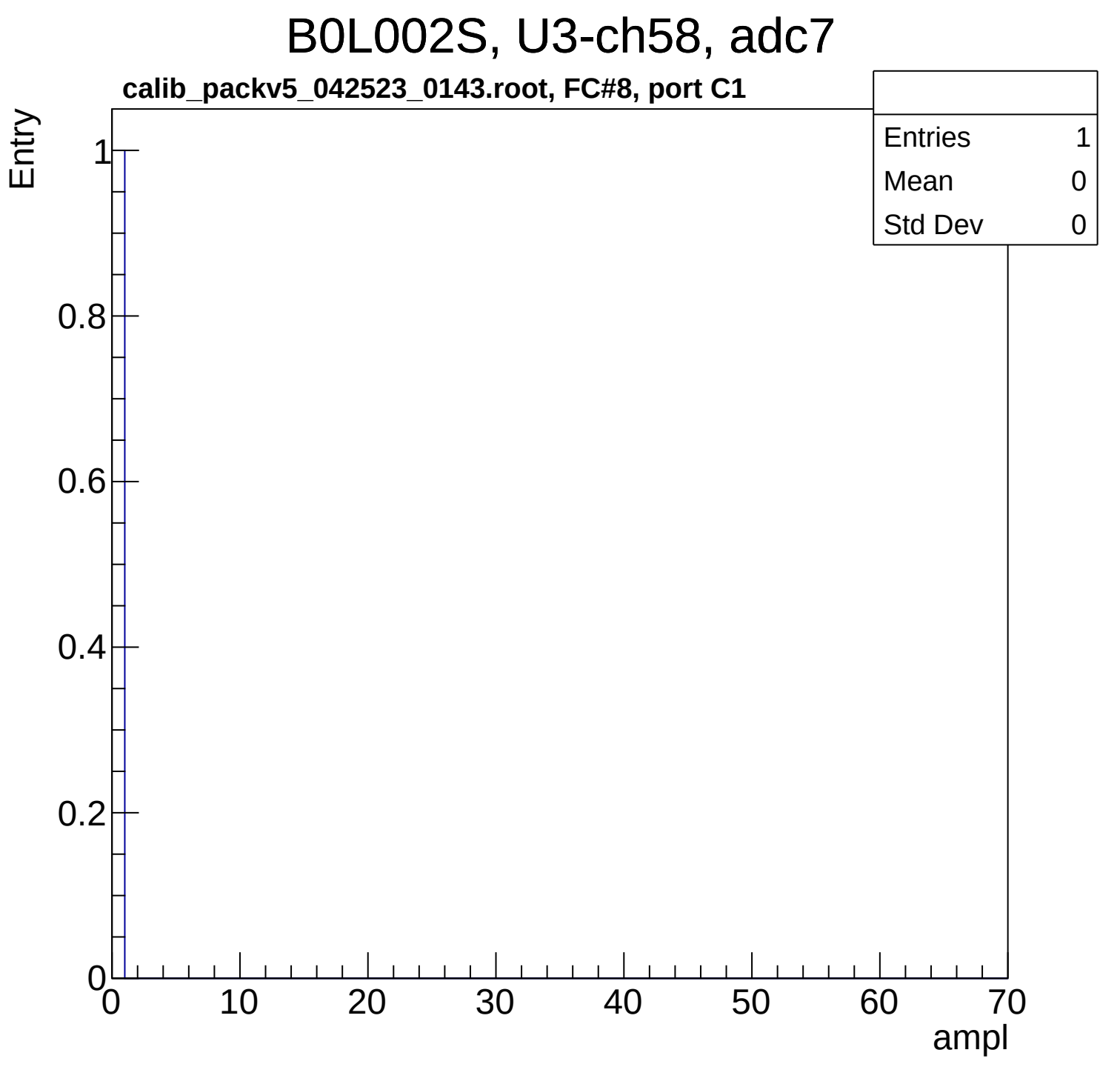
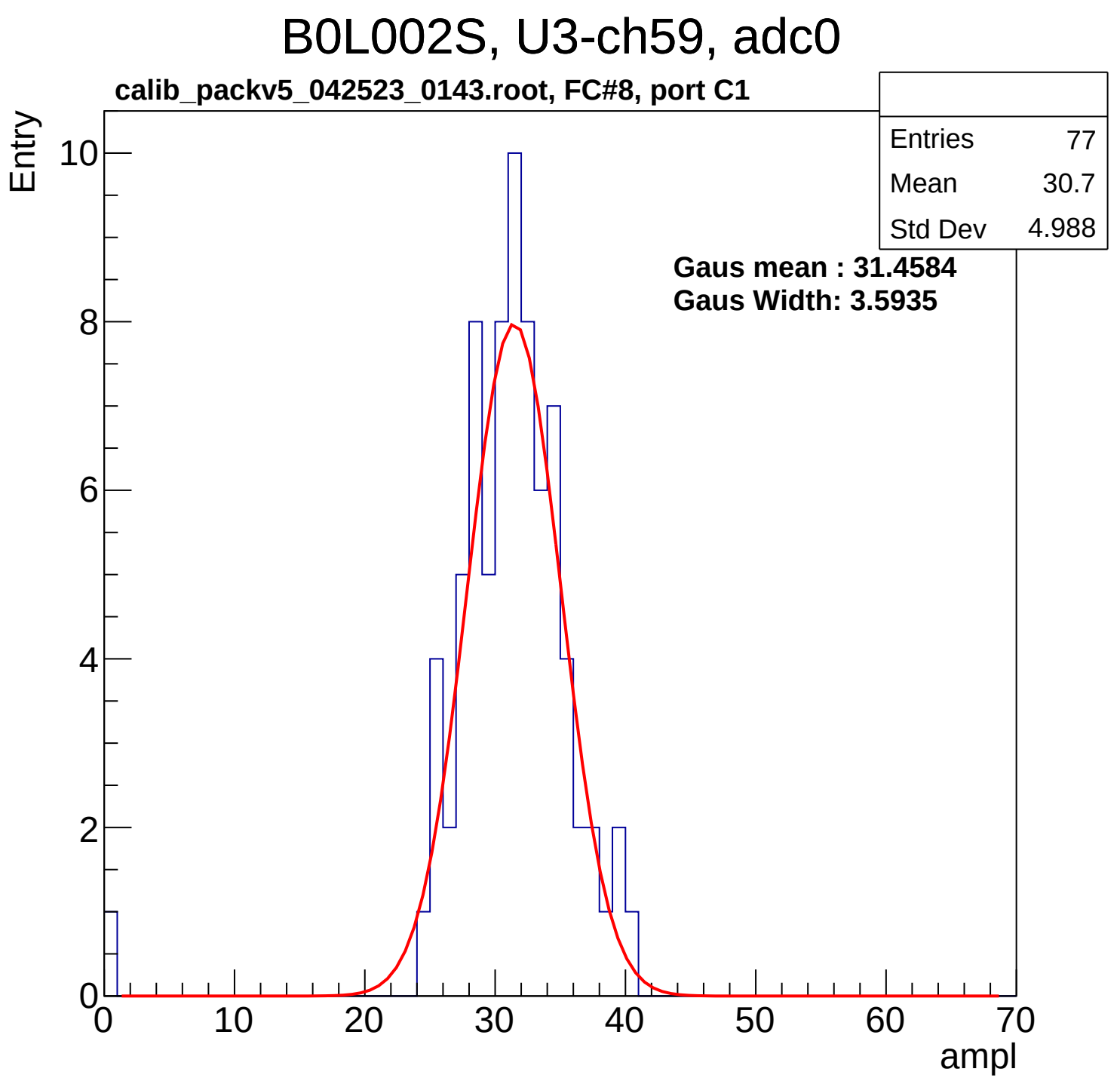
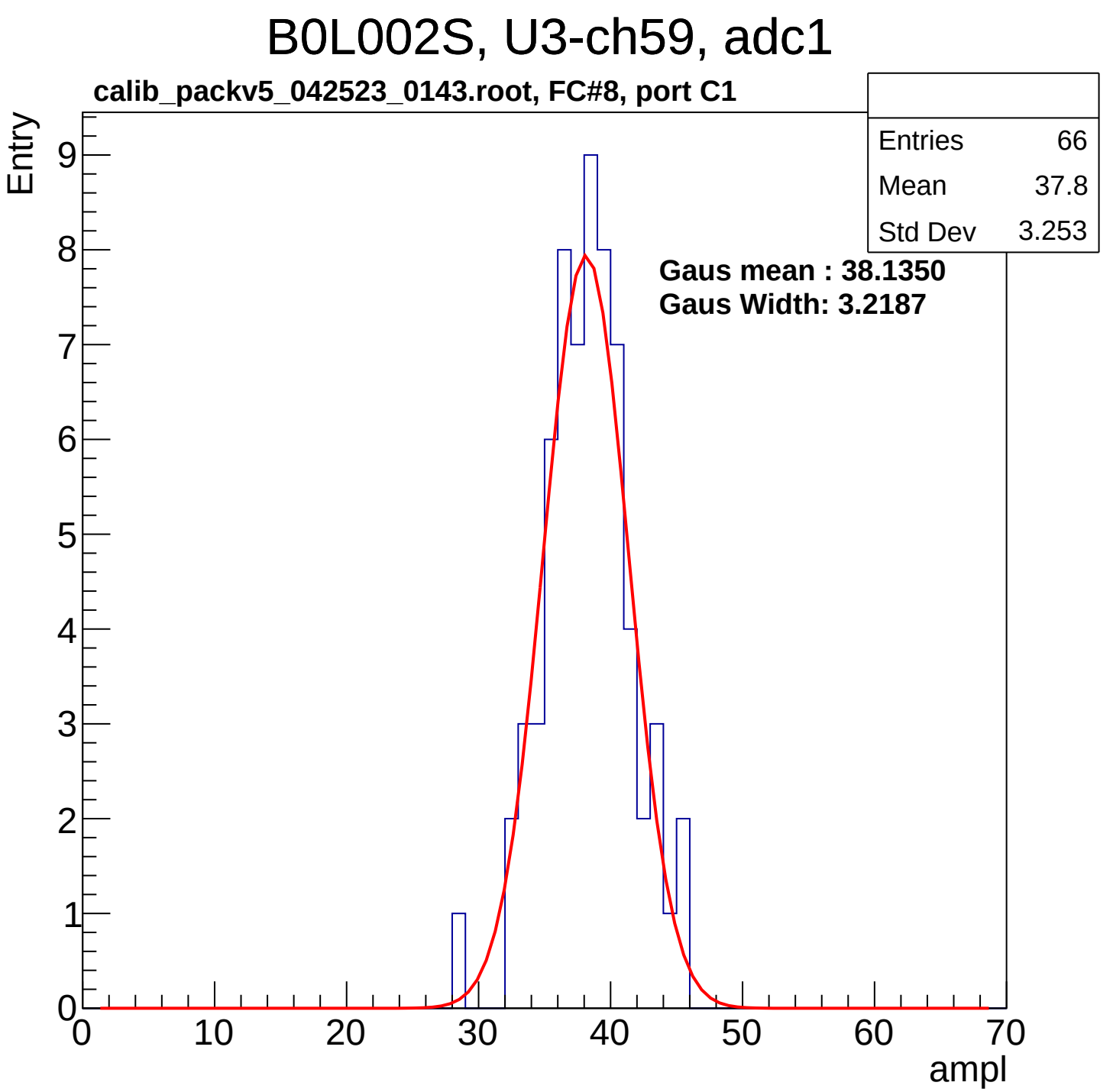

B0L002S, U3-ch58, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 58.71
Std Dev 7.867
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch58, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch58, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch59, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.7
Std Dev 4.988
Gaus mean : 31.4584
Gaus Width: 3.5935
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch59, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 37.8
Std Dev 3.253
Gaus mean : 38.1350
Gaus Width: 3.2187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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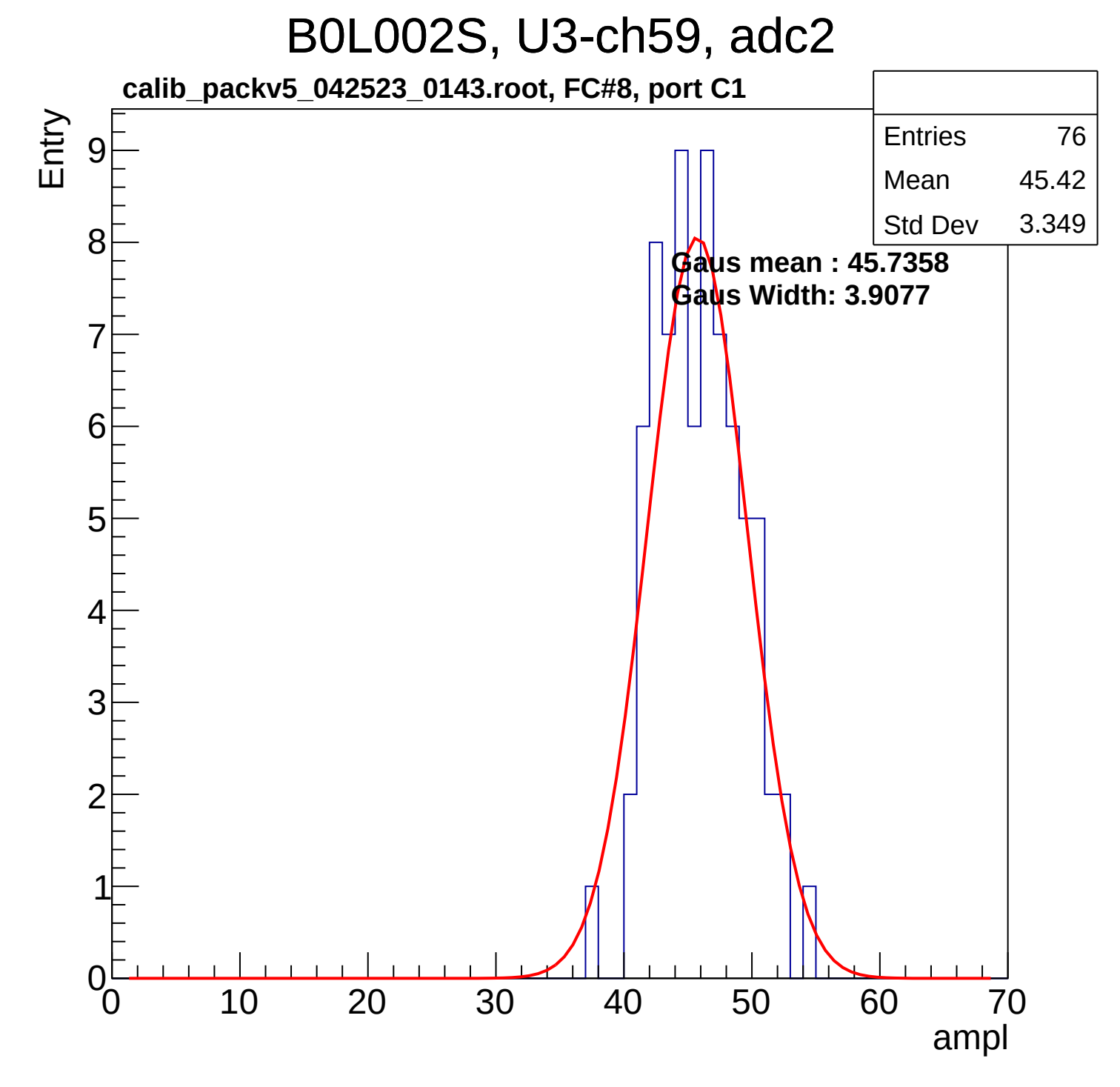
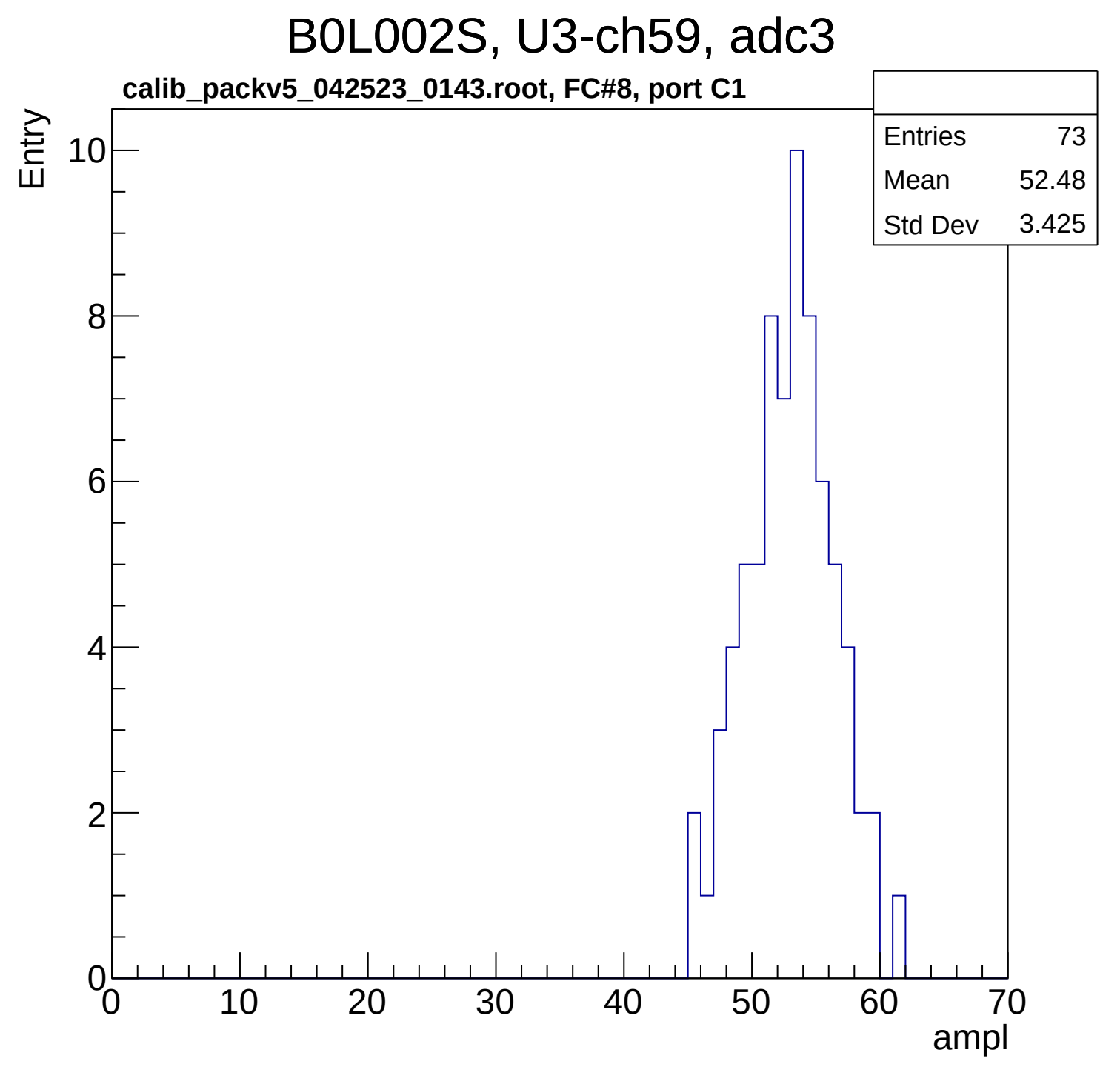
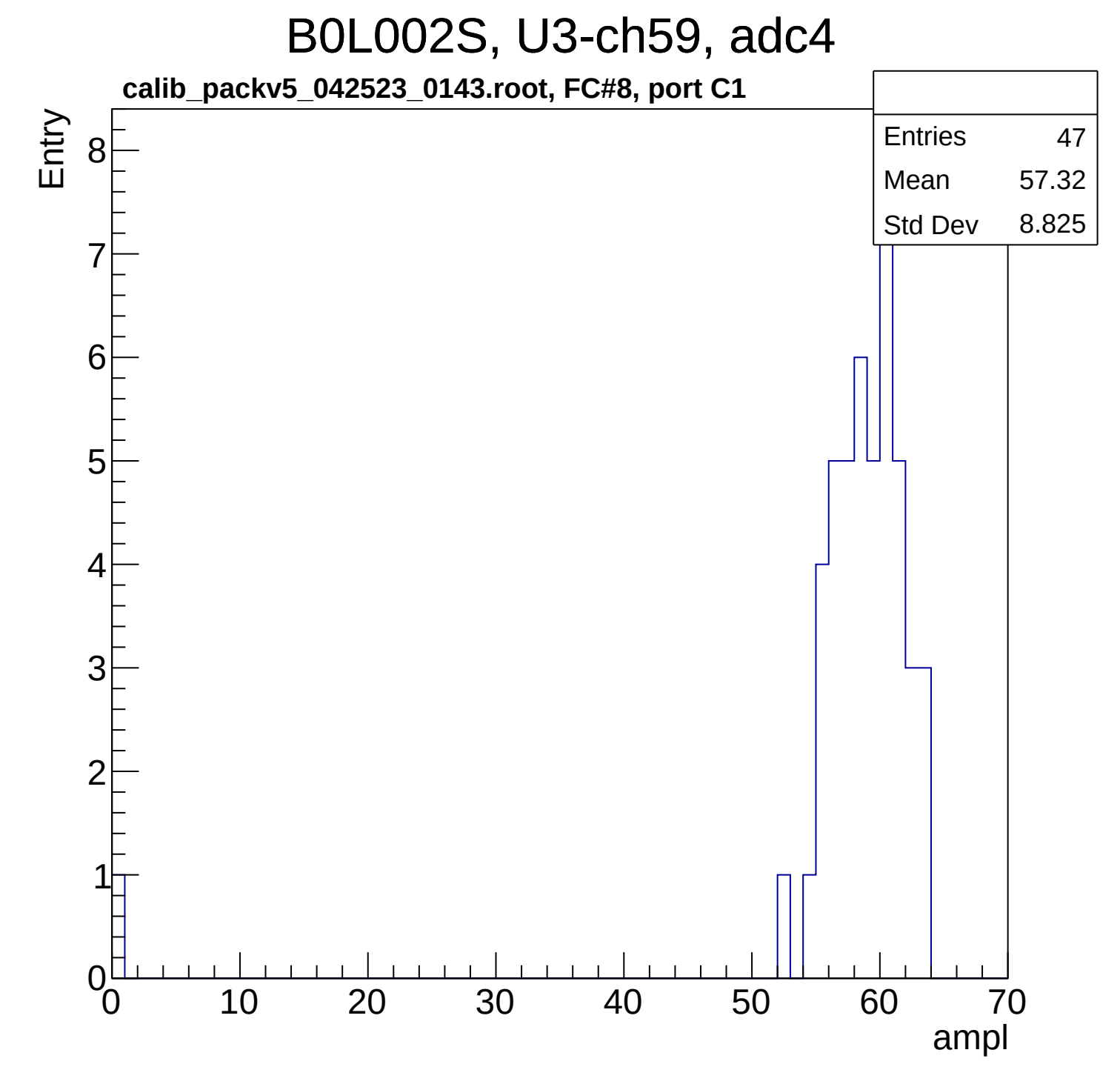

B0L002S, U3-ch59, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 45.42
Std Dev 3.349
Gaus mean : 45.7358
Gaus Width: 3.9077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch59, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 52.48
Std Dev 3.425
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch59, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 57.32
Std Dev 8.825
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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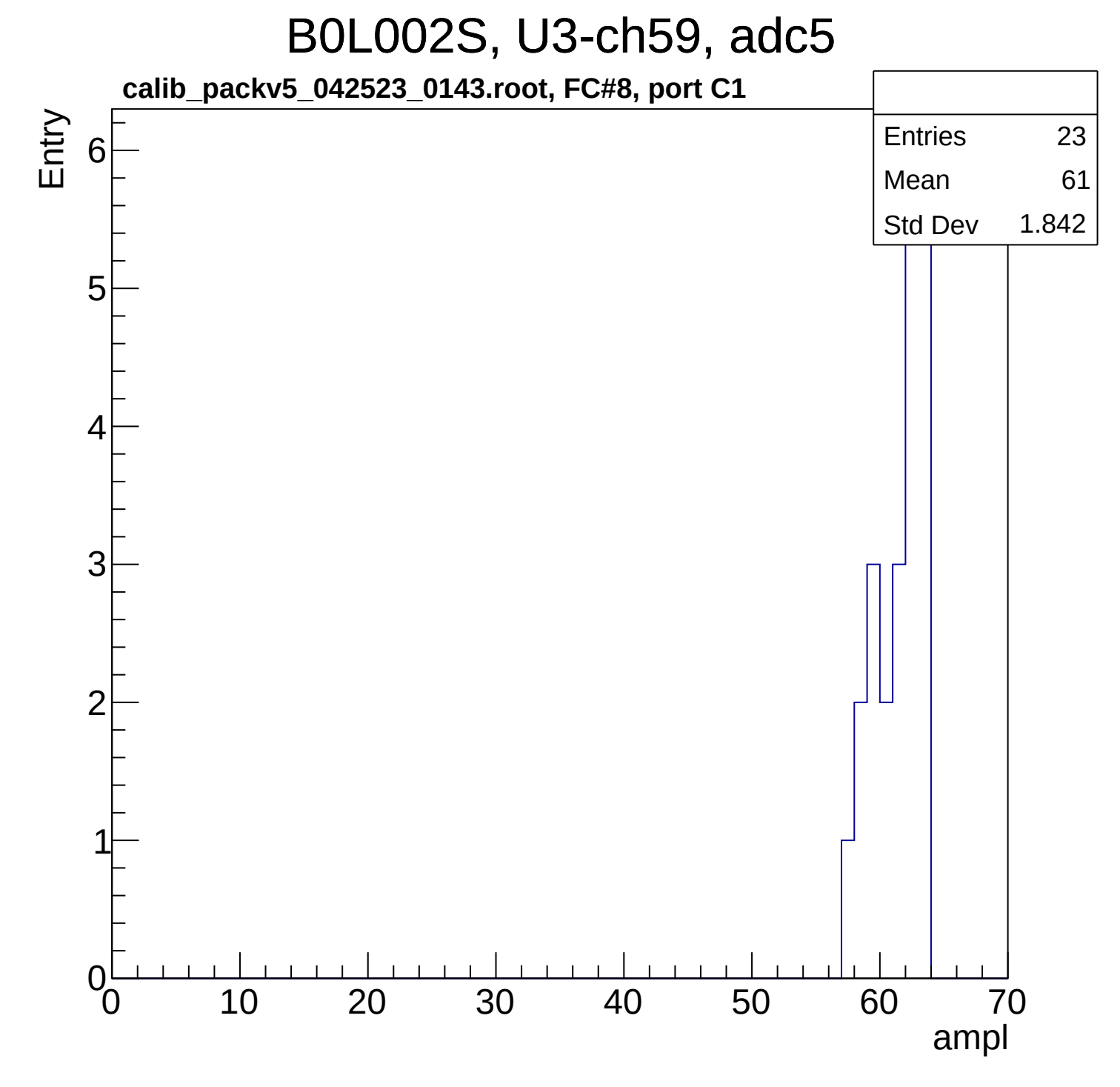
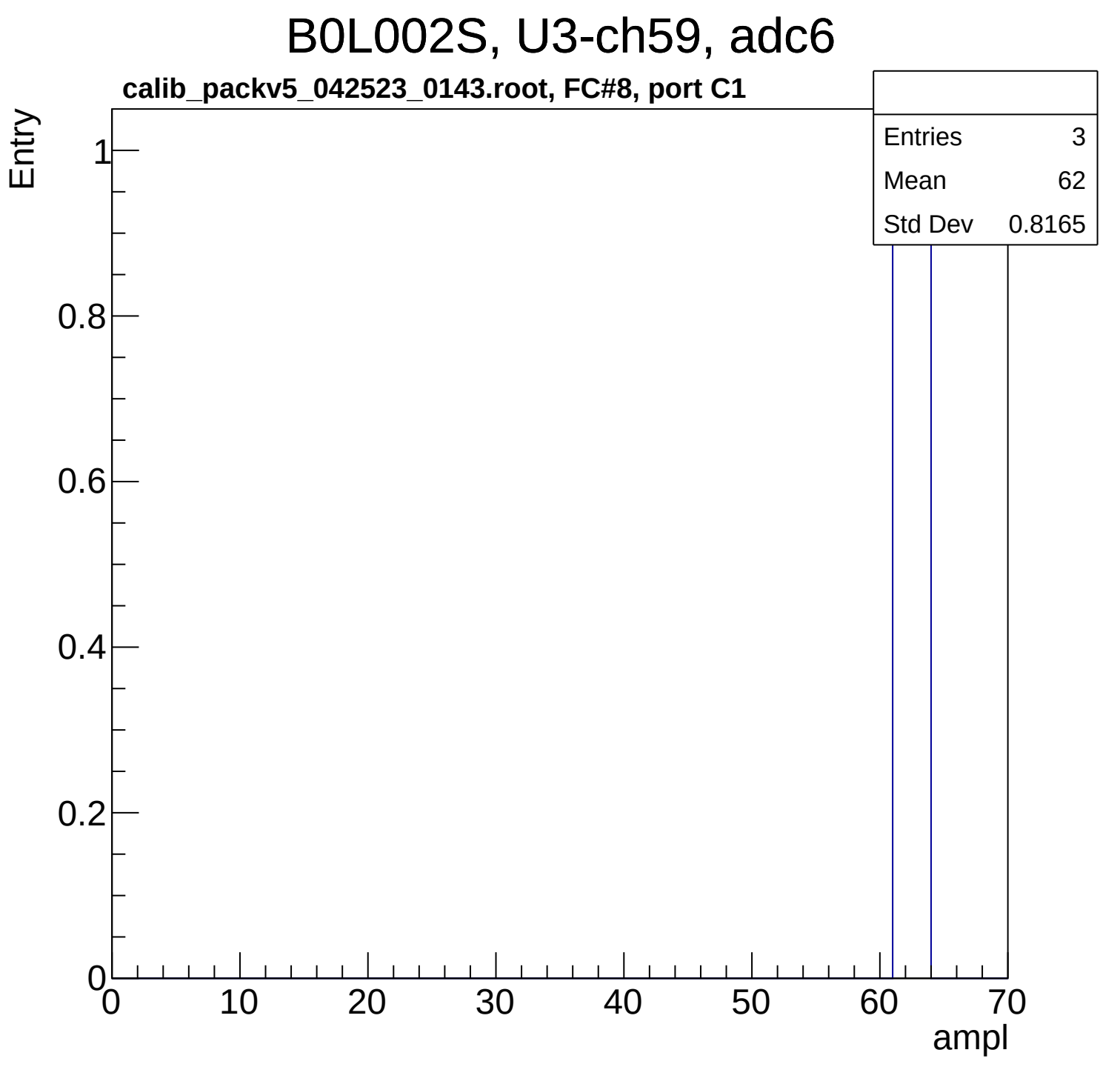

B0L002S, U3-ch59, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 61
Std Dev 1.842
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch59, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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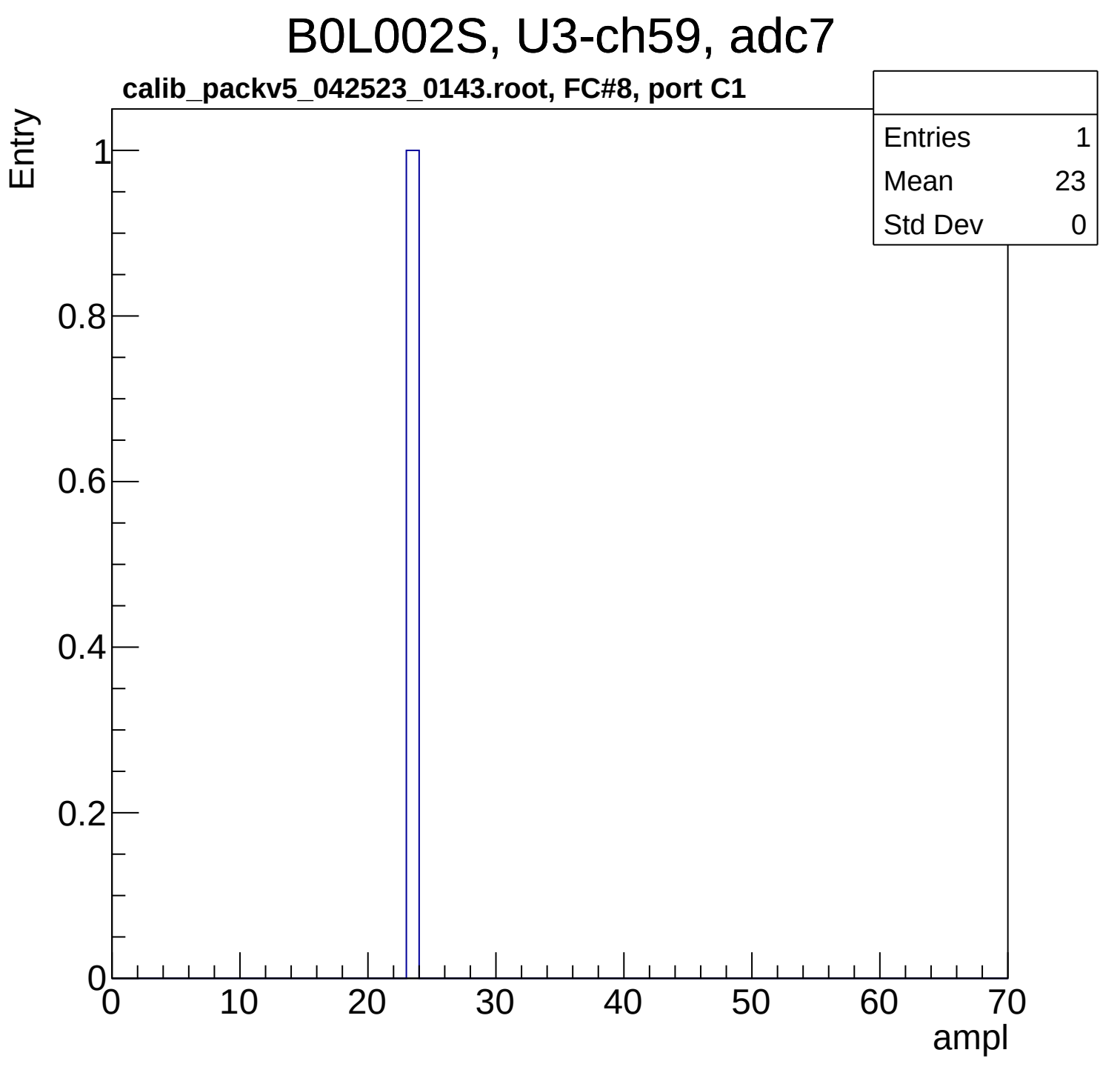
50
60
70
ampl


B0L002S, U3-ch59, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
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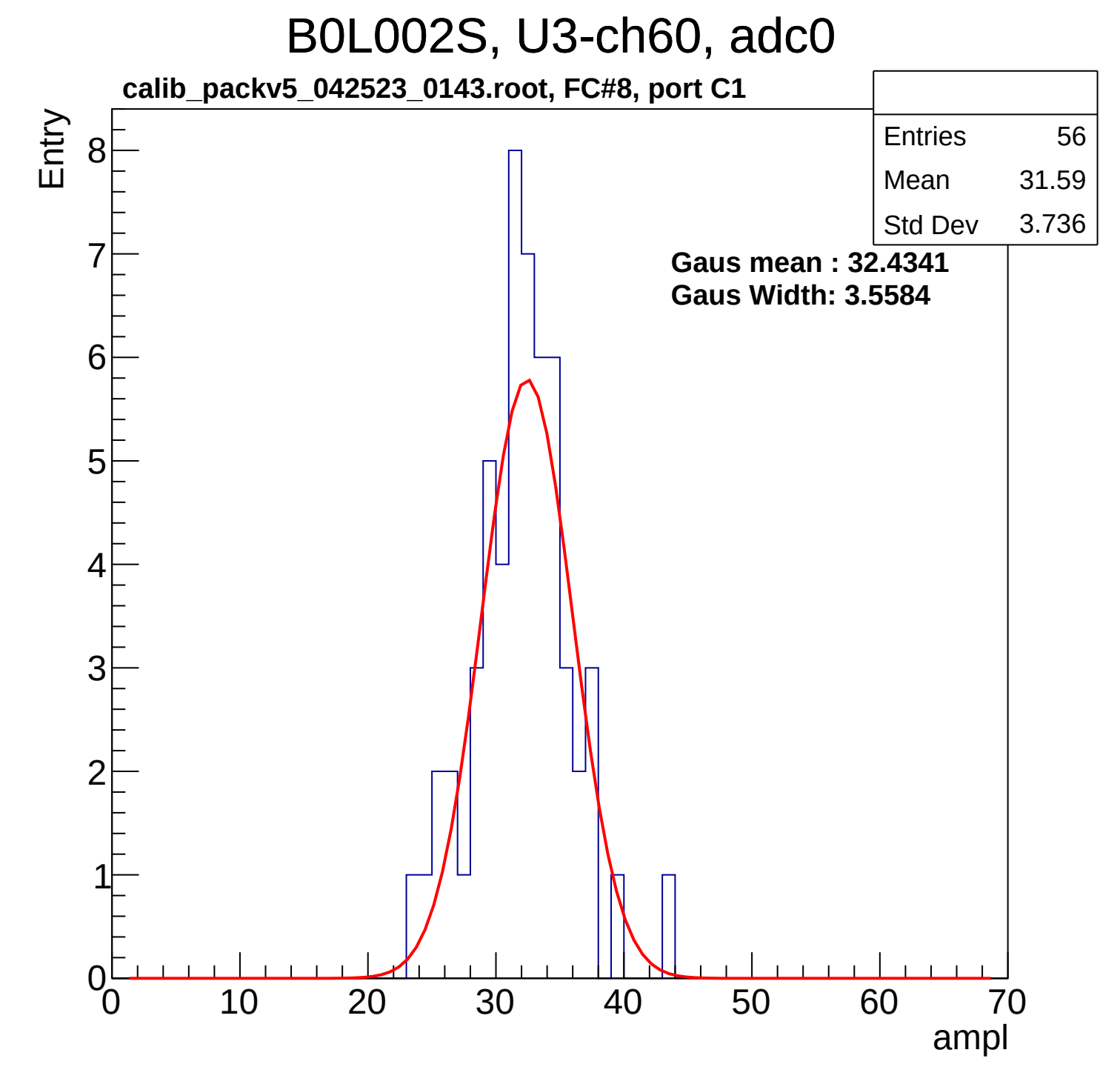
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch60, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 31.59
Std Dev 3.736
Gaus mean : 32.4341
Gaus Width: 3.5584
Entry
ampl
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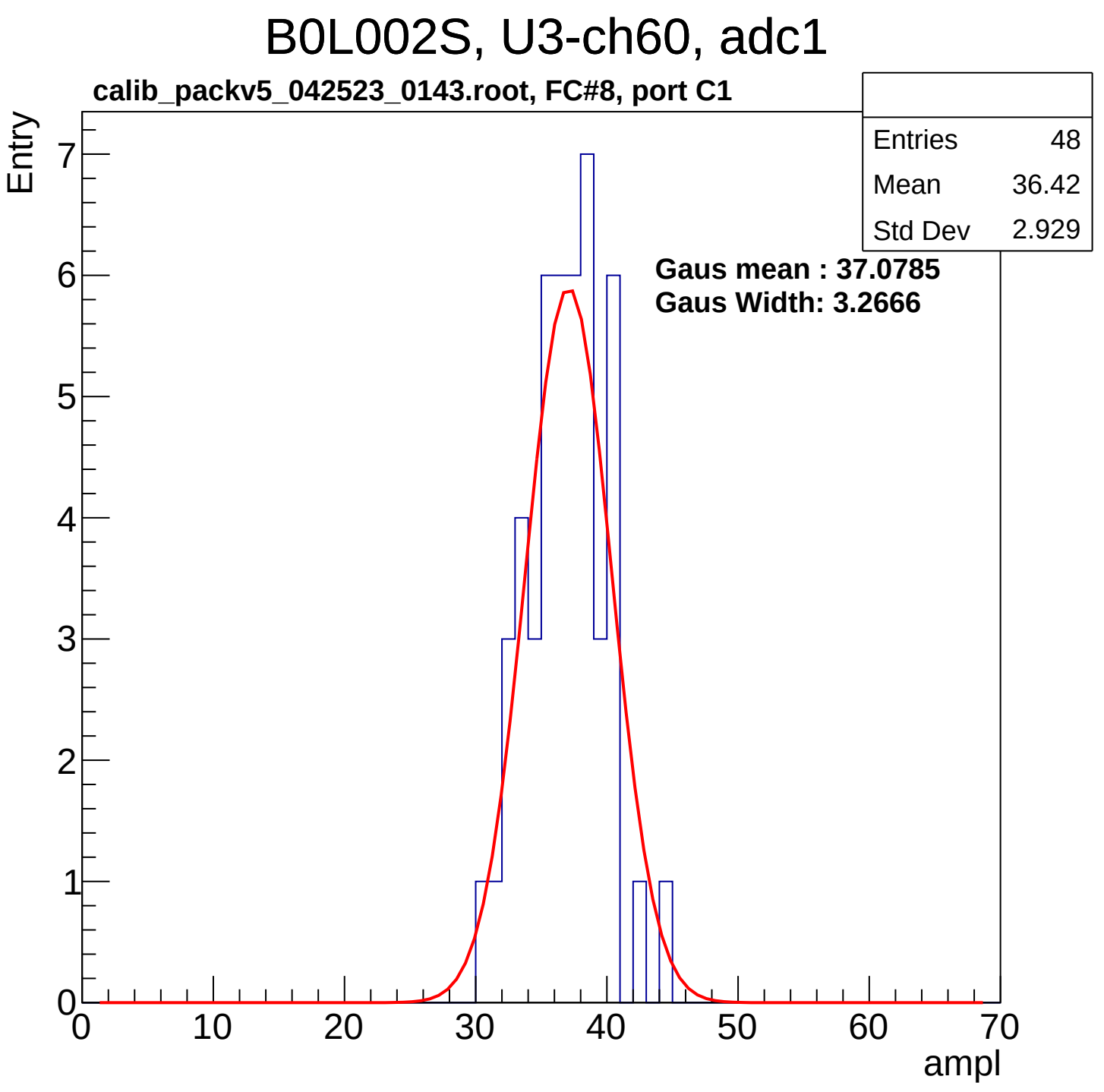
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch60, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 36.42
Std Dev 2.929
Gaus mean : 37.0785
Gaus Width: 3.2666
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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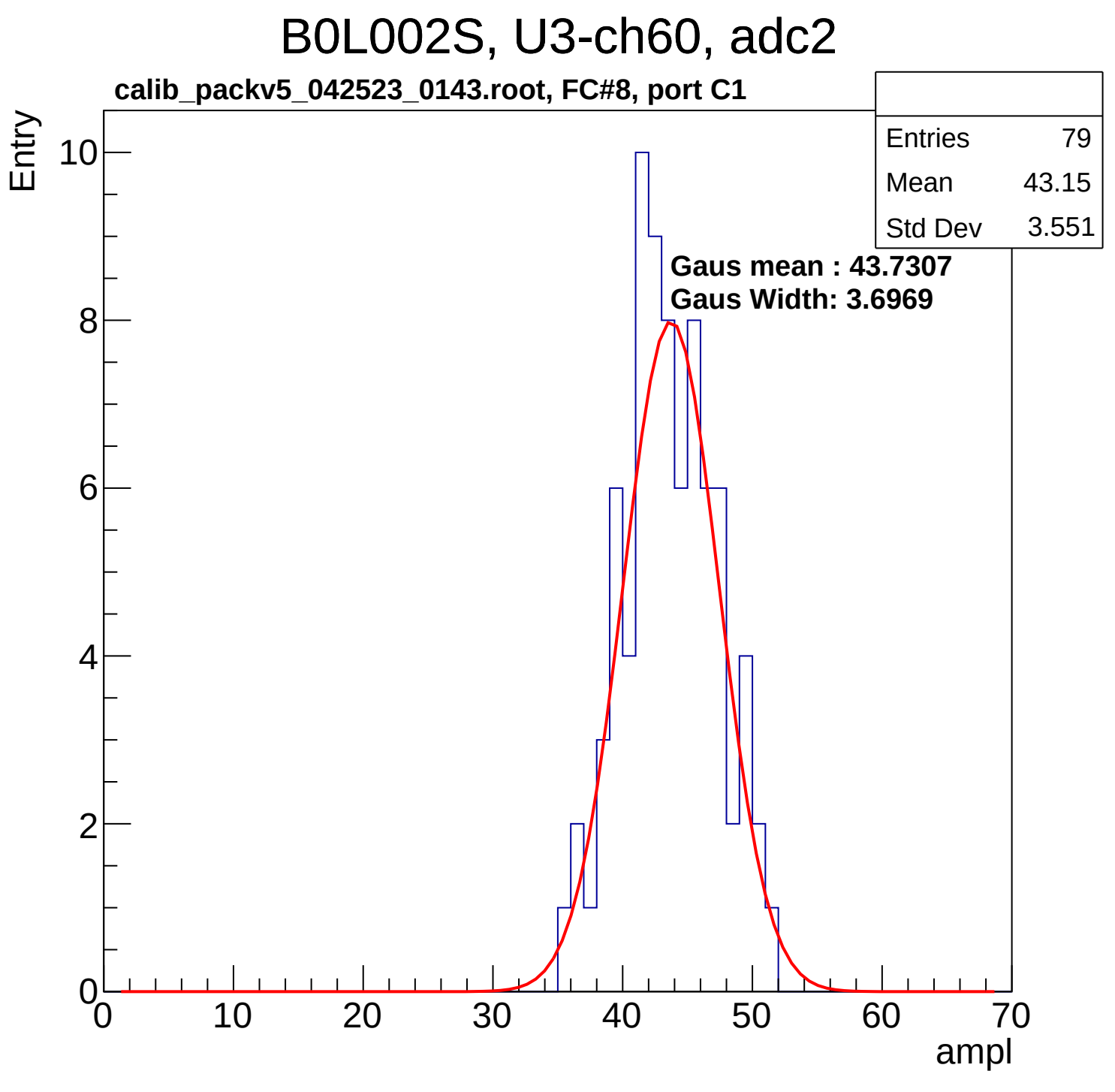
B0L002S, U3-ch60, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 43.15
Std Dev 3.551
Gaus mean : 43.7307
Gaus Width: 3.6969
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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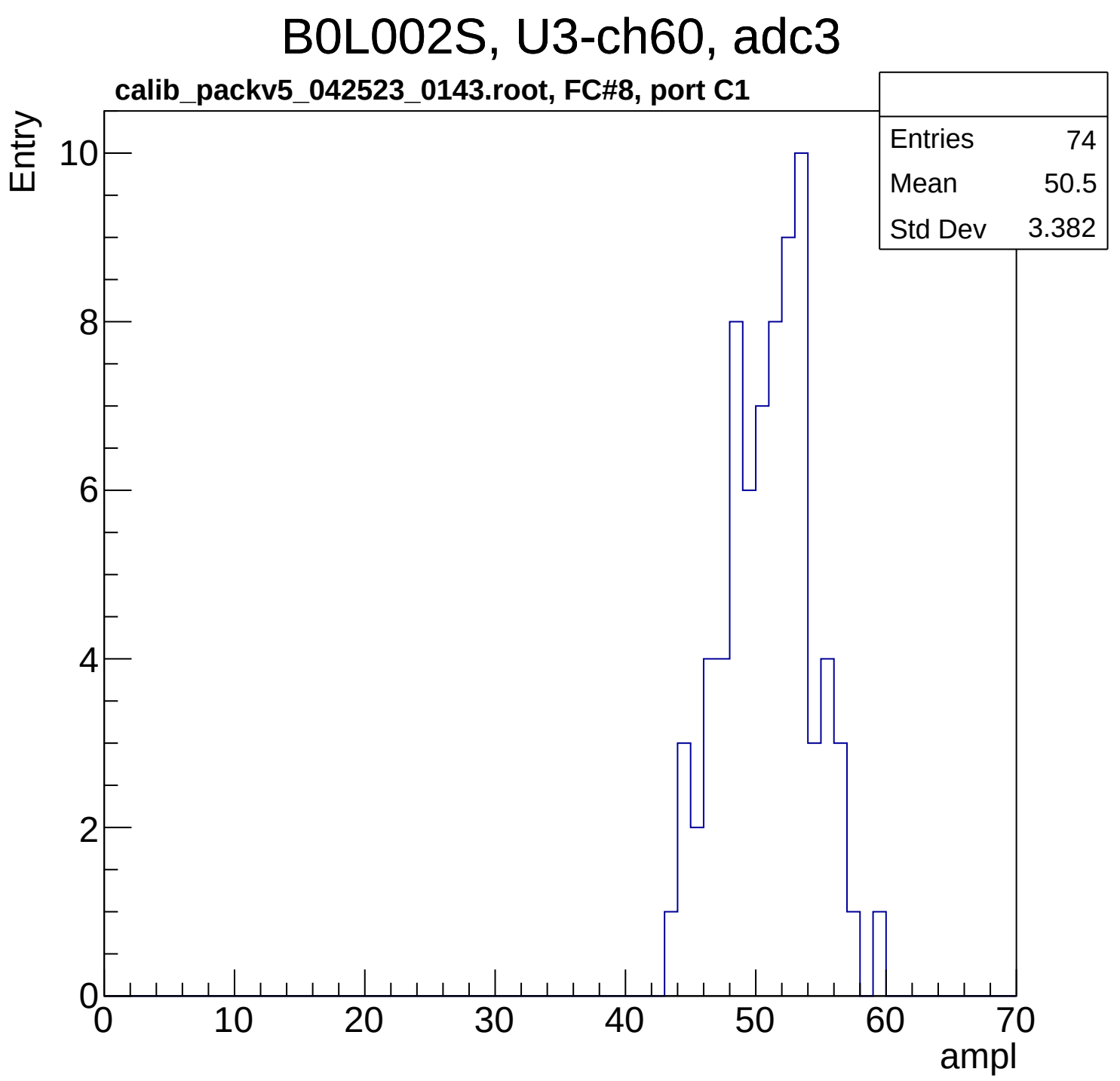
B0L002S, U3-ch60, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 50.5
Std Dev 3.382
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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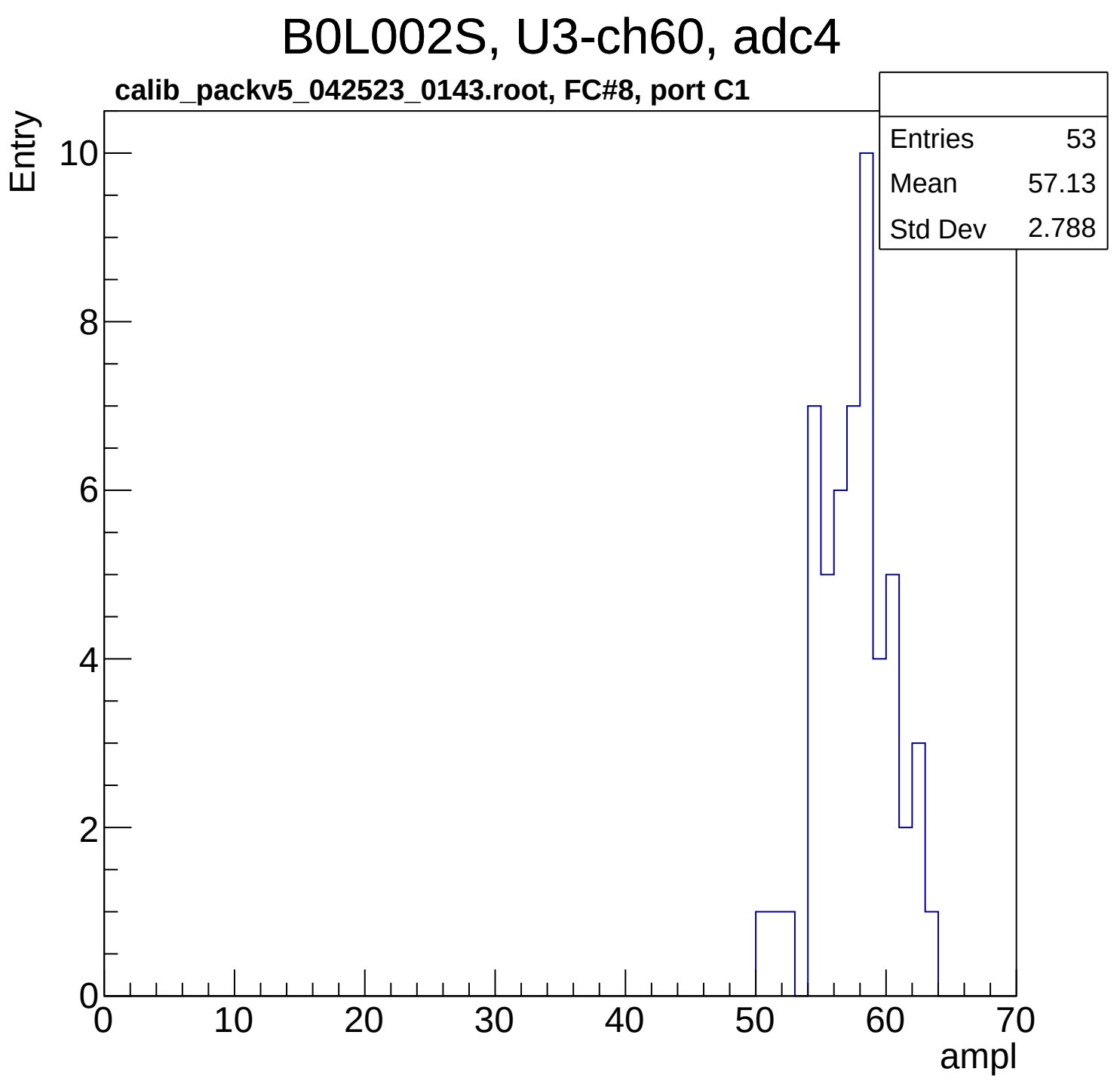
B0L002S, U3-ch60, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.13
Std Dev 2.788
Entry
ampl
0
2
4
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10
0
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60
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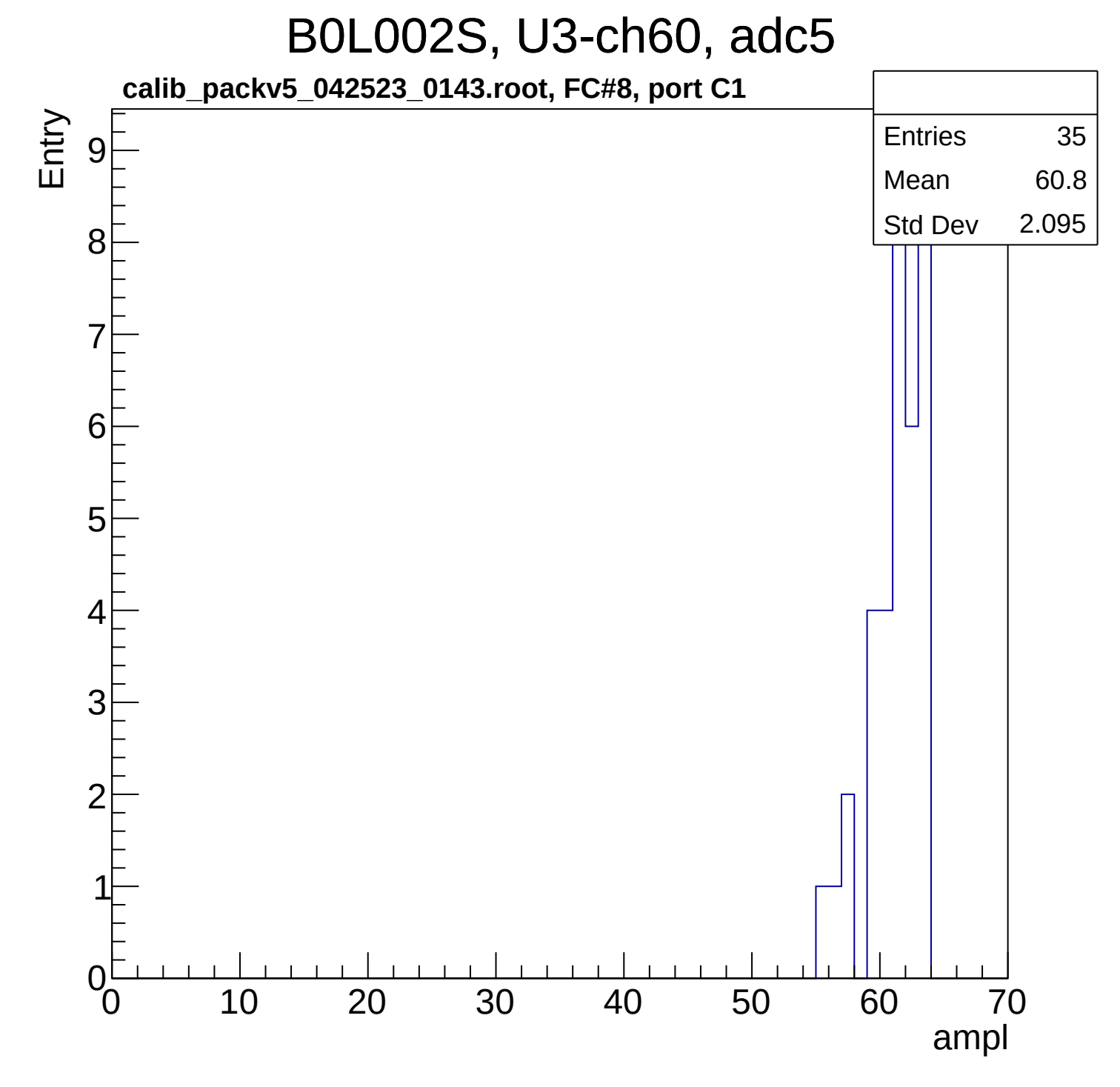
B0L002S, U3-ch60, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 60.8
Std Dev 2.095
Entry
ampl
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6
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0
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60
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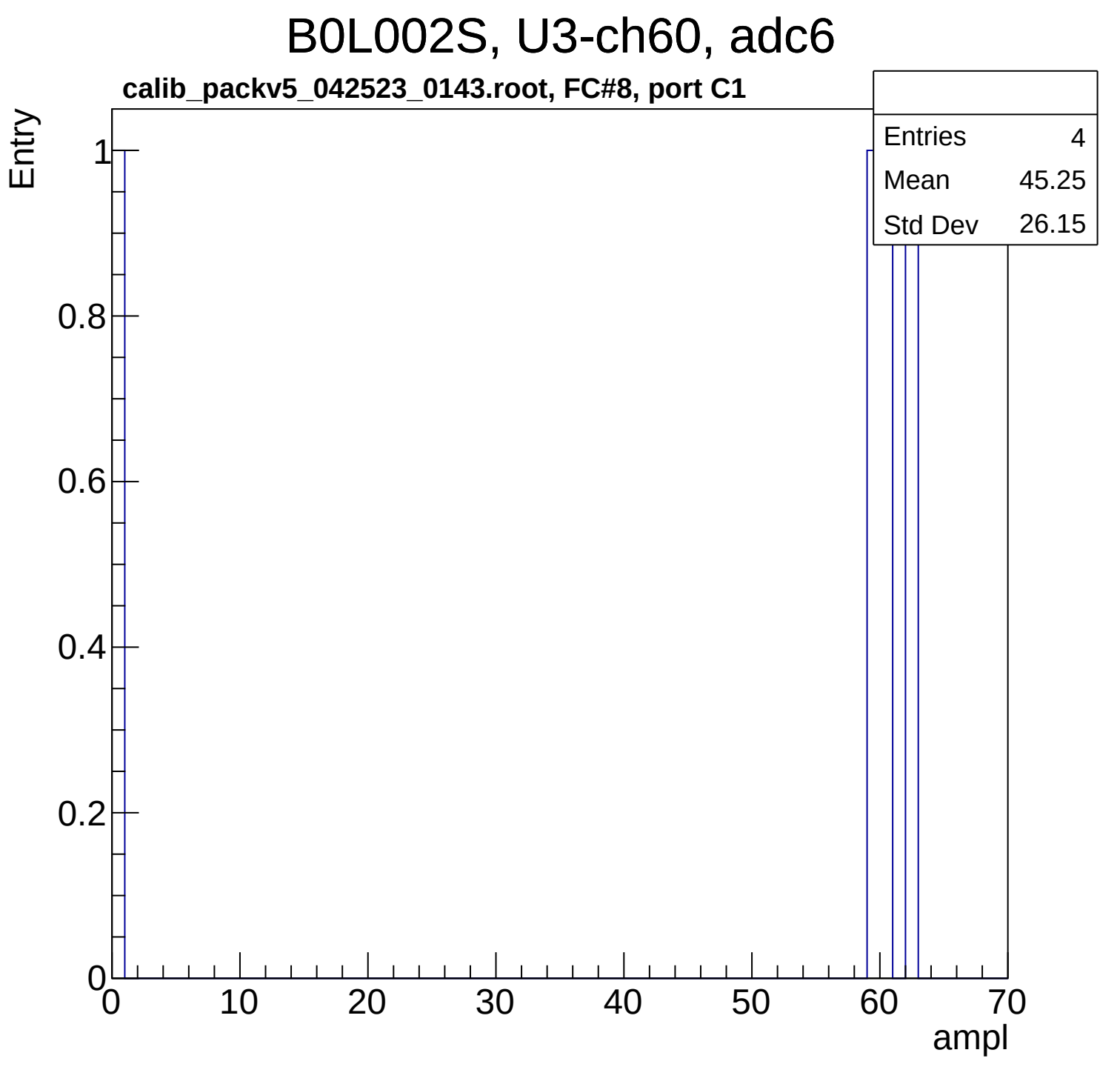
B0L002S, U3-ch60, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 45.25
Std Dev 26.15
Entry
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0.6
0.4
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0
0
10
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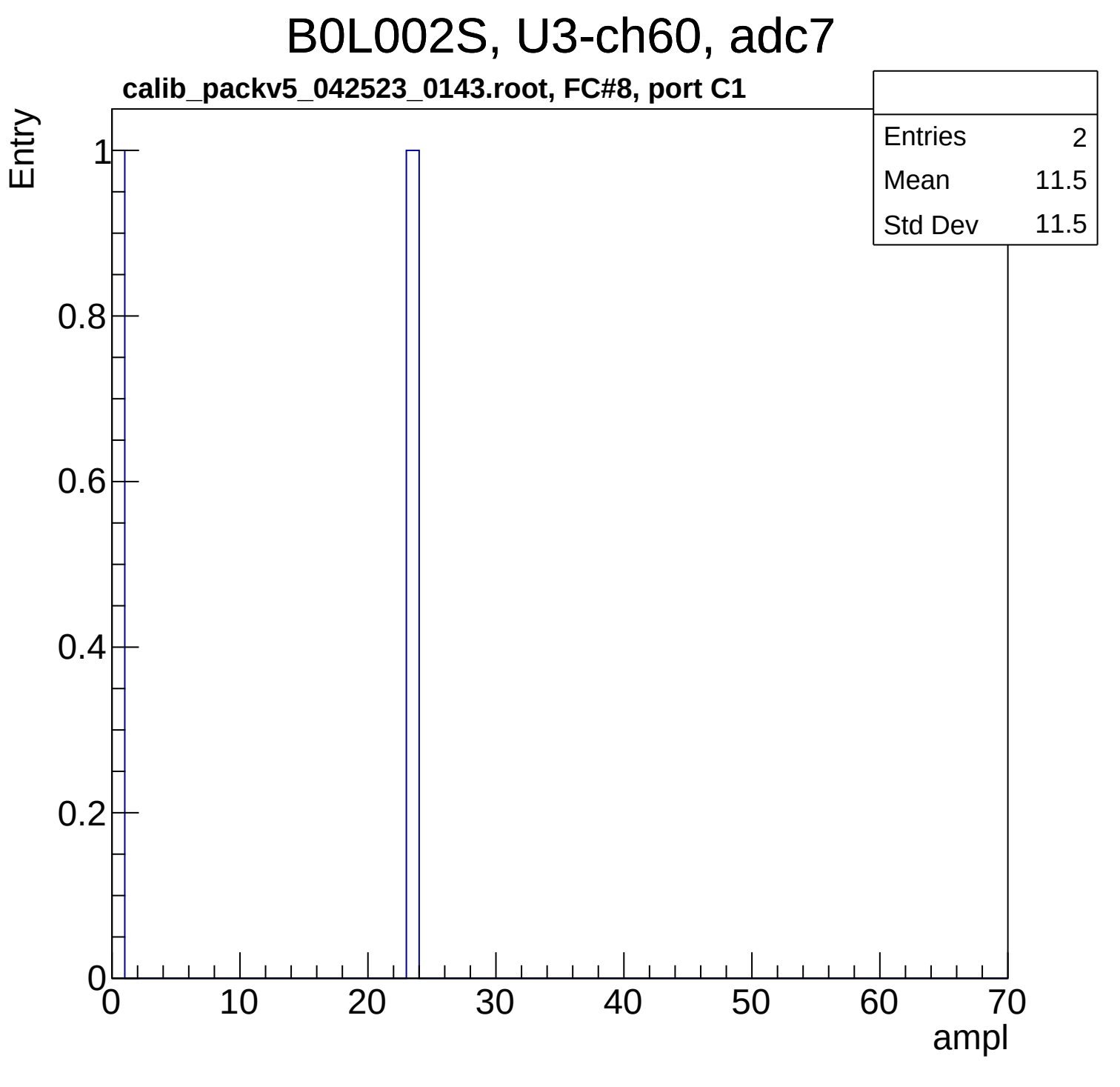
50
60
70
ampl


B0L002S, U3-ch60, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.4
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0
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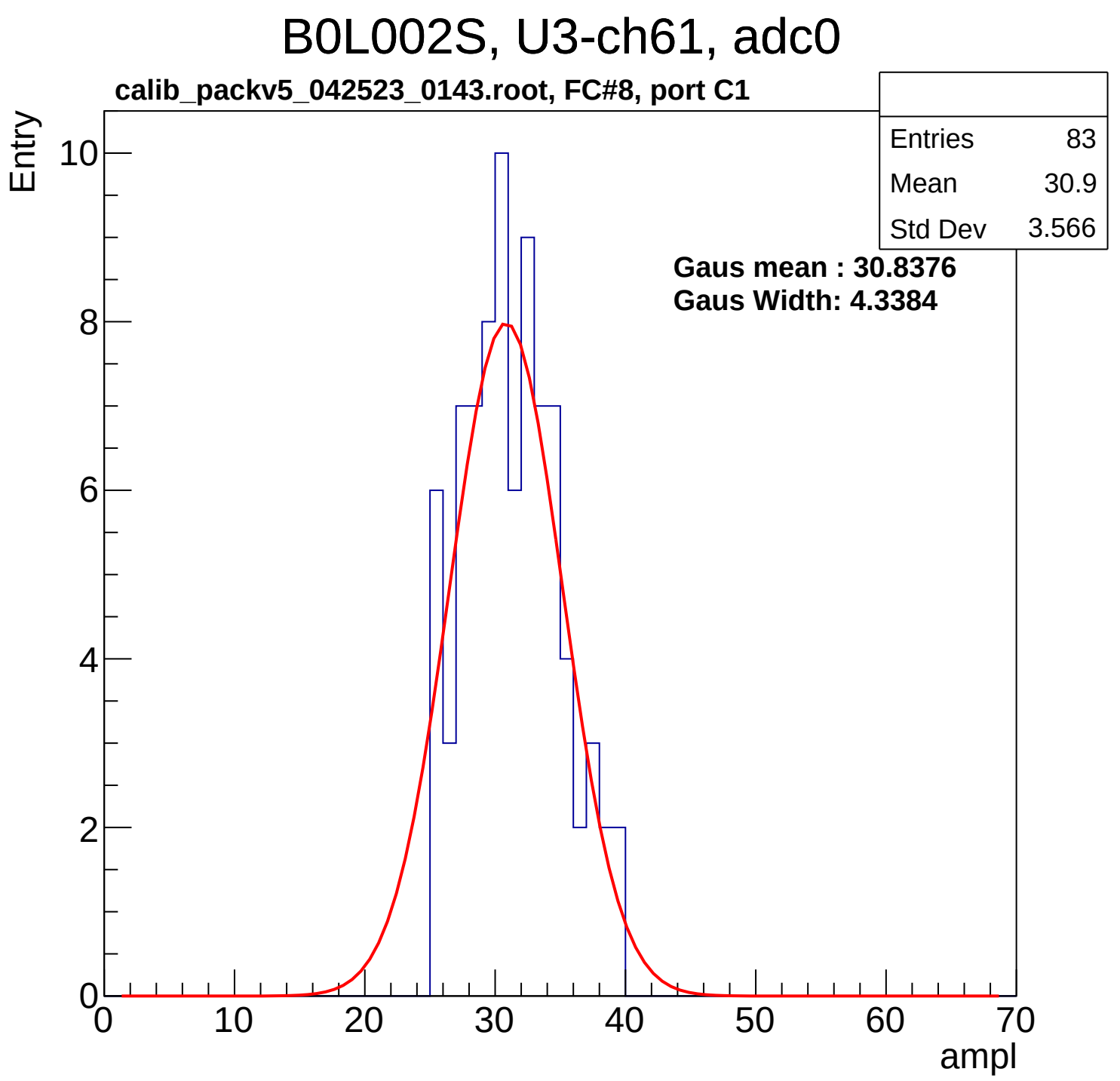
50
60
70
ampl


B0L002S, U3-ch61, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 30.9
Std Dev 3.566
Gaus mean : 30.8376
Gaus Width: 4.3384
Entry
ampl
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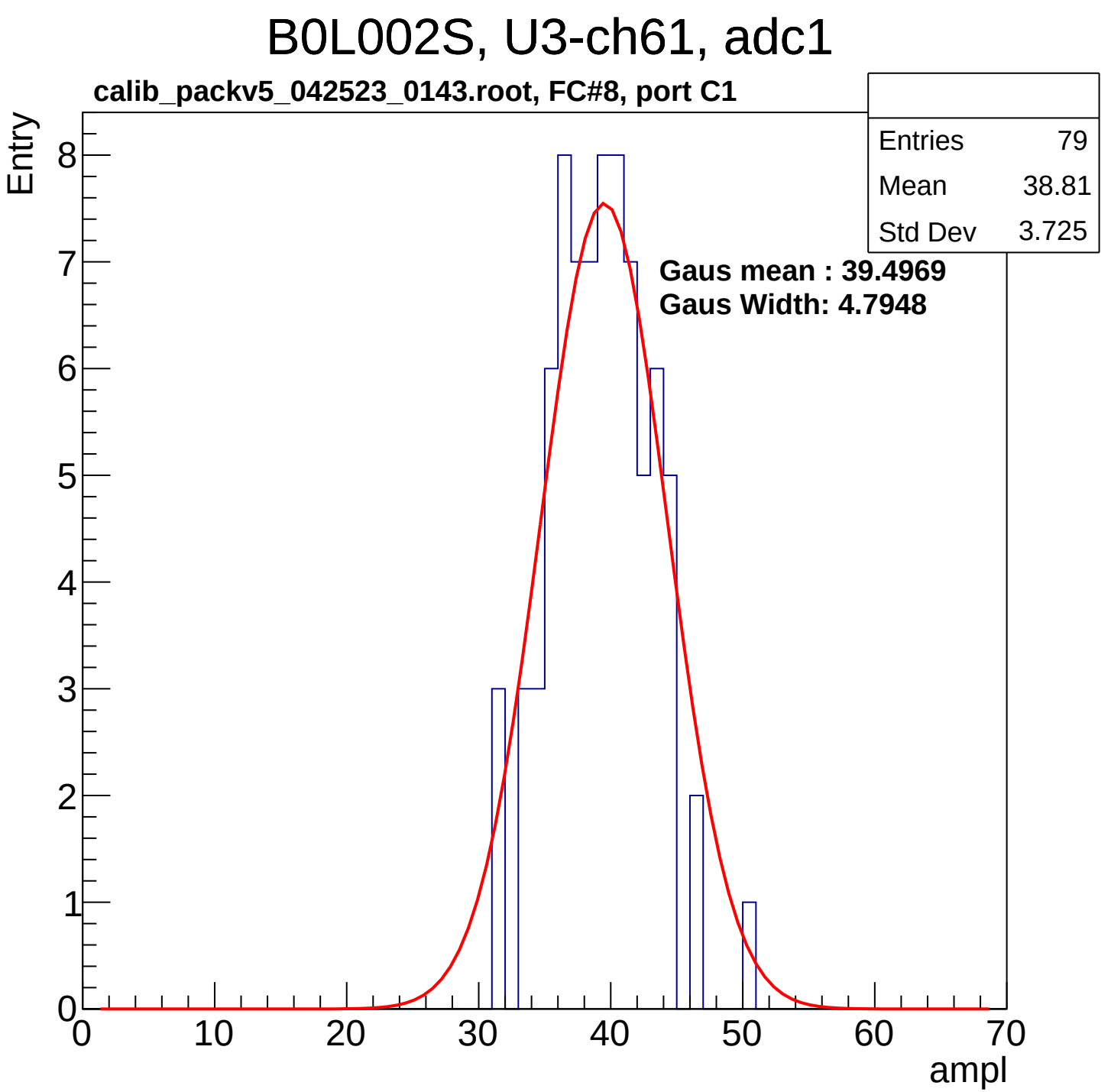
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch61, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 38.81
Std Dev 3.725
Gaus mean : 39.4969
Gaus Width: 4.7948
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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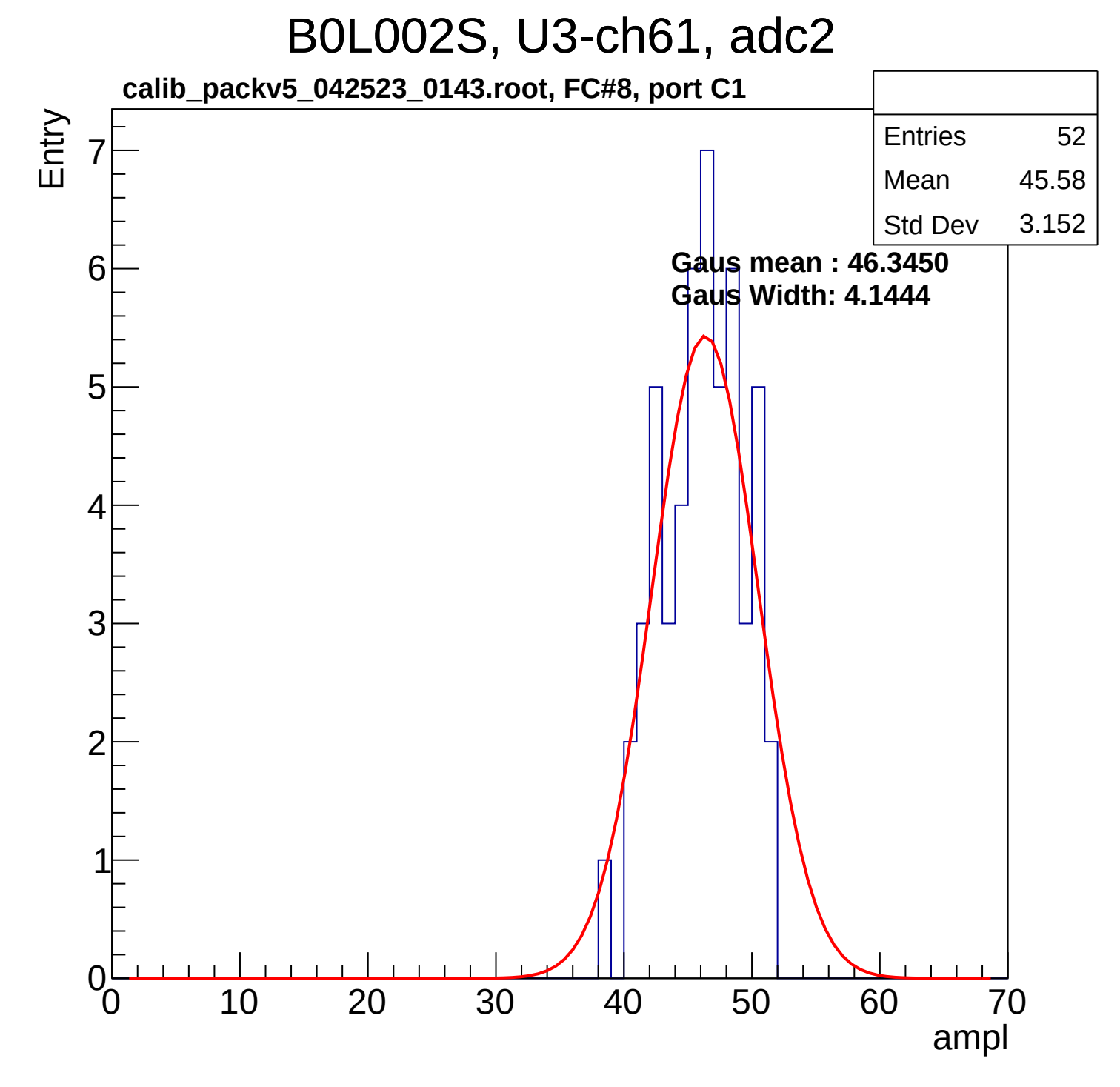
40
50
60
70


B0L002S, U3-ch61, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 45.58
Std Dev 3.152
Gaus mean : 46.3450
Gaus Width: 4.1444
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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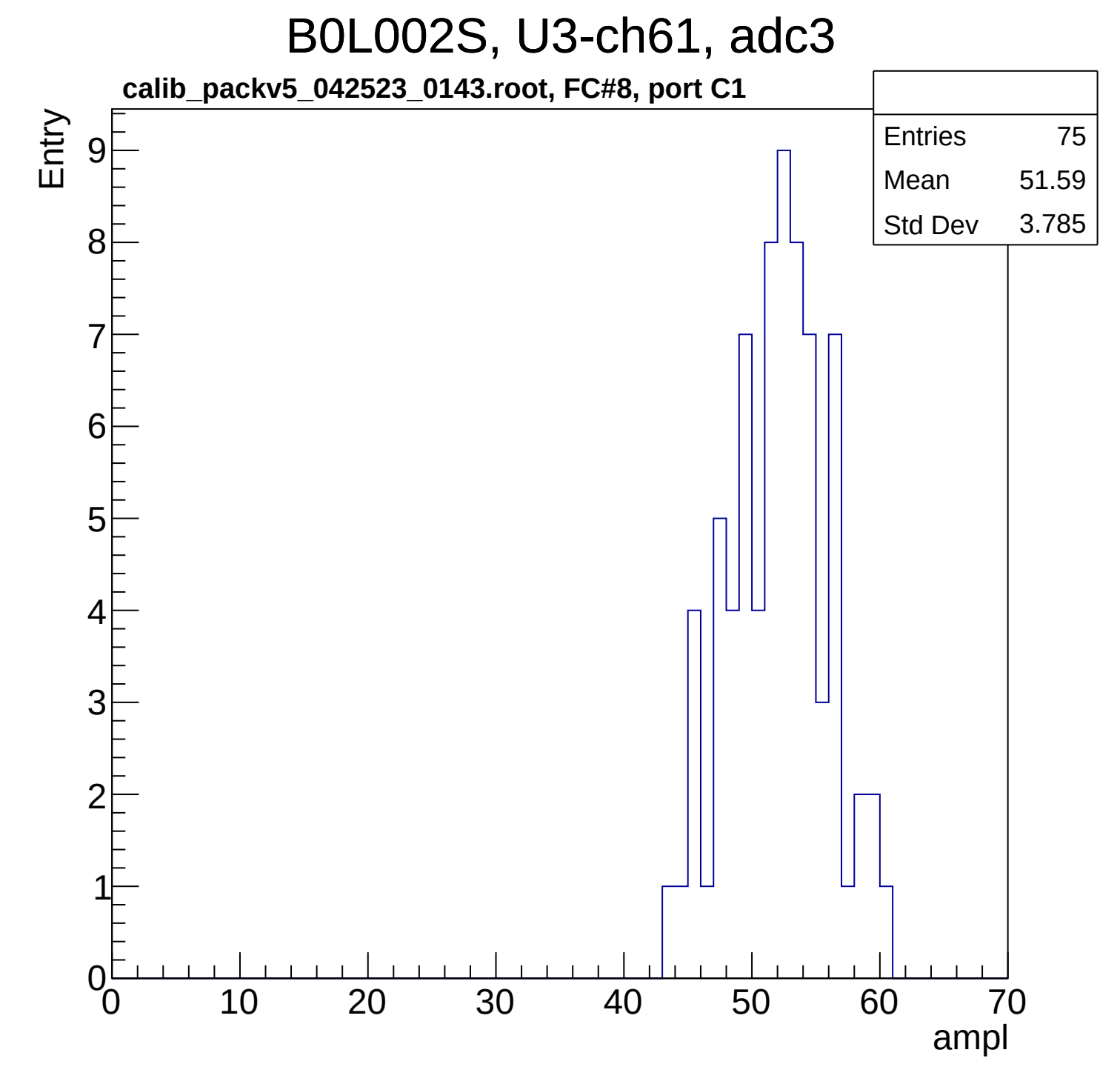
B0L002S, U3-ch61, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.59
Std Dev 3.785
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
50
60
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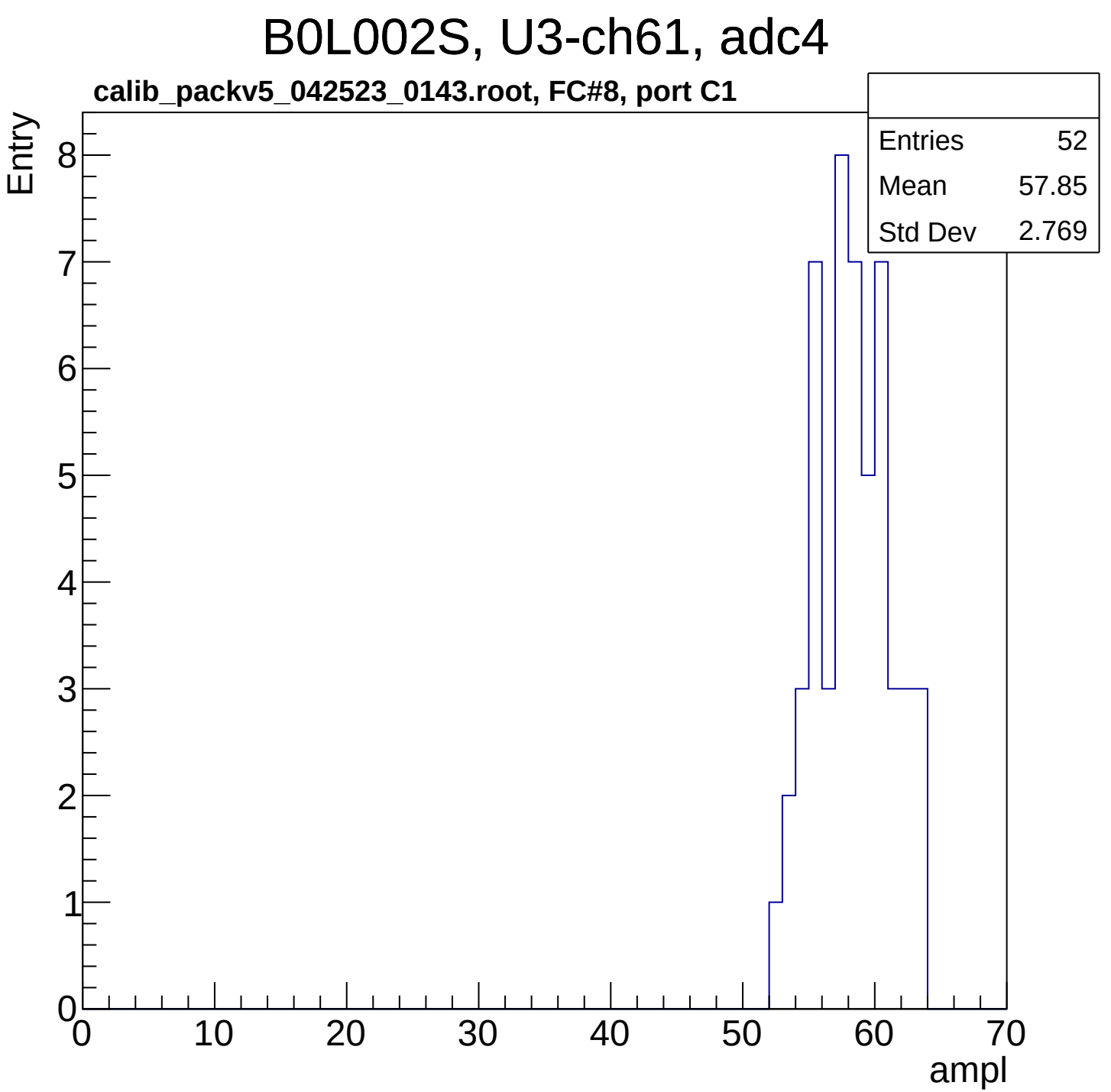
B0L002S, U3-ch61, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.85
Std Dev 2.769
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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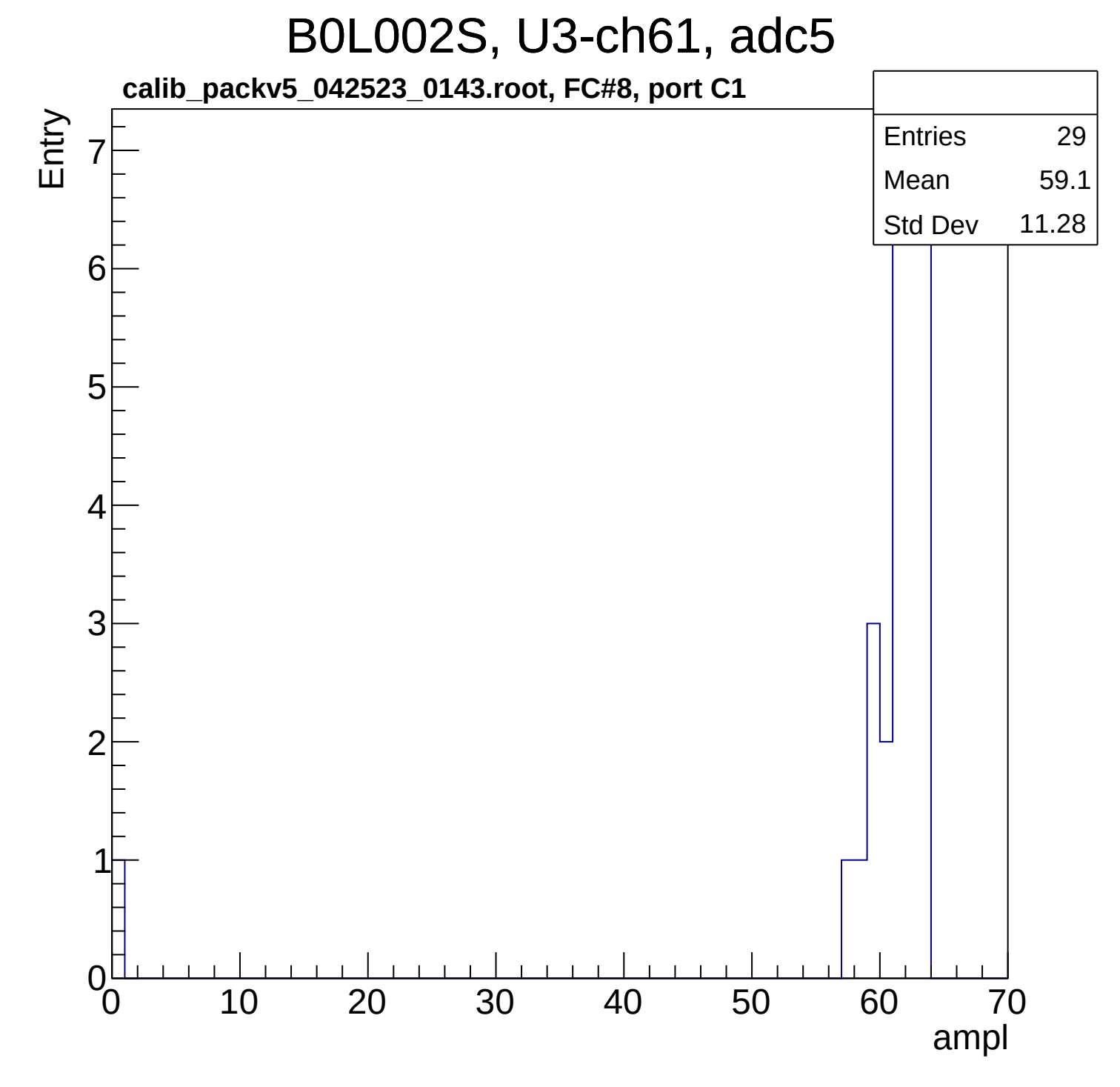
B0L002S, U3-ch61, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.1
Std Dev 11.28
Entry
ampl
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60
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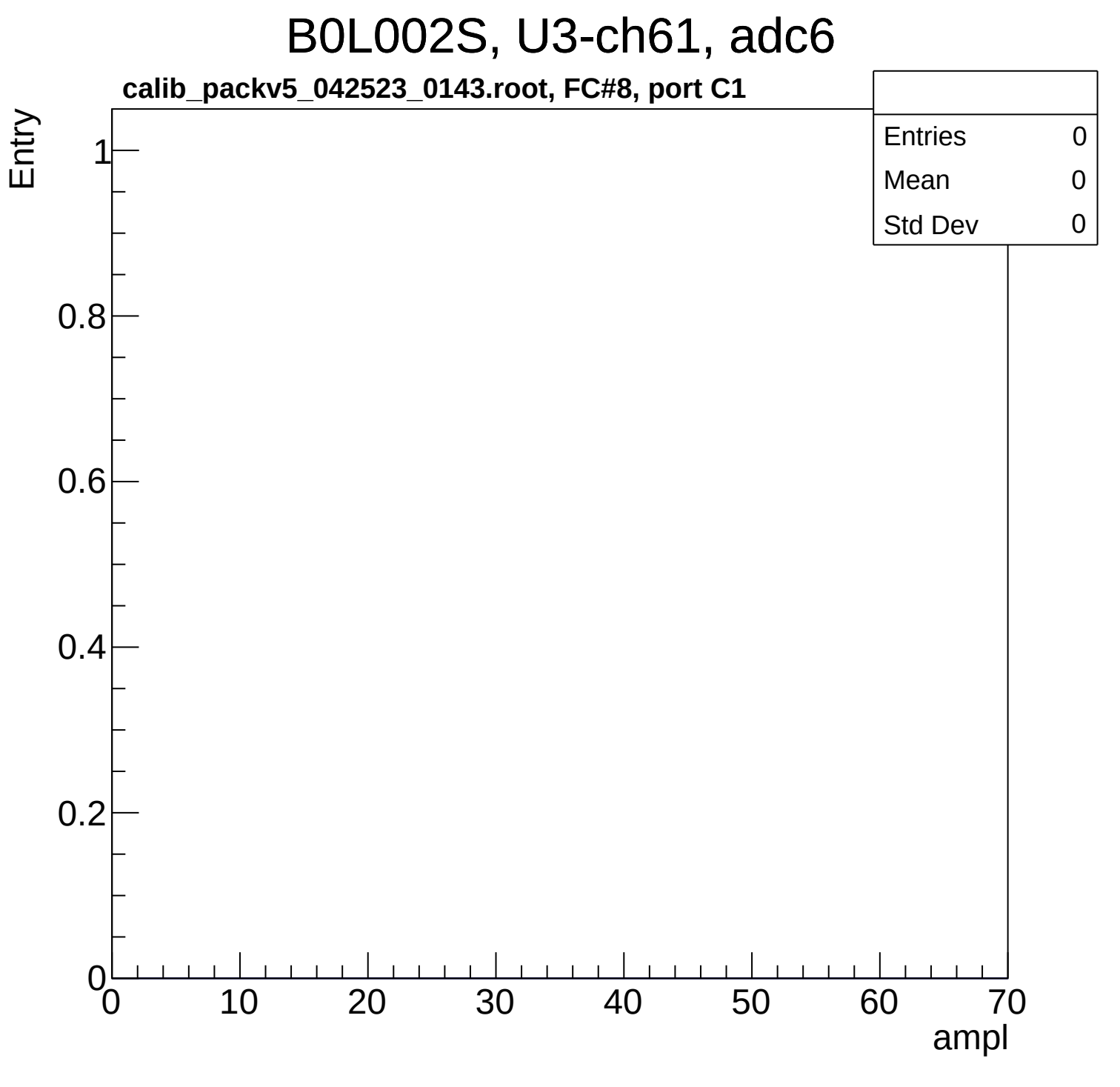
B0L002S, U3-ch61, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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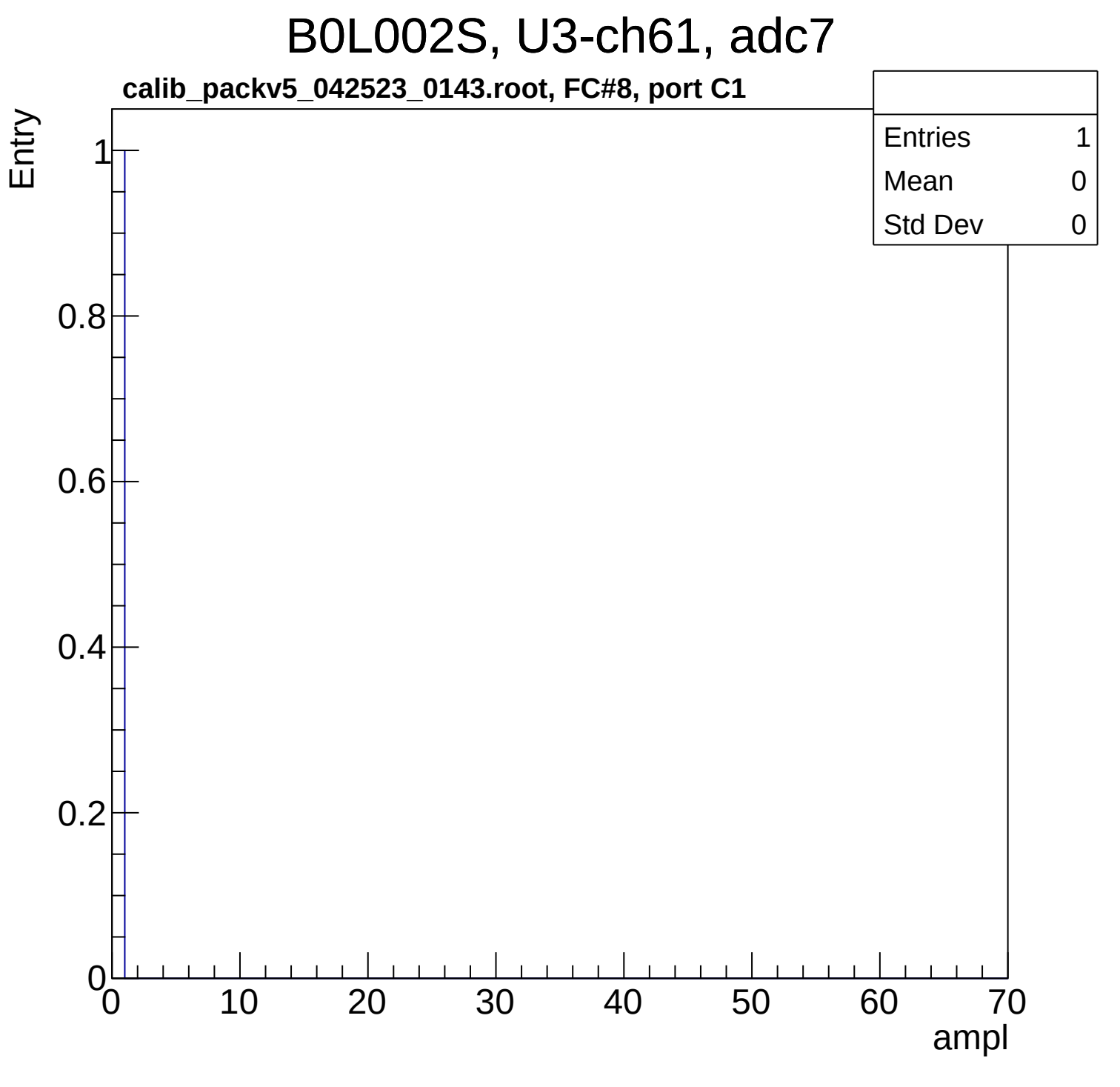
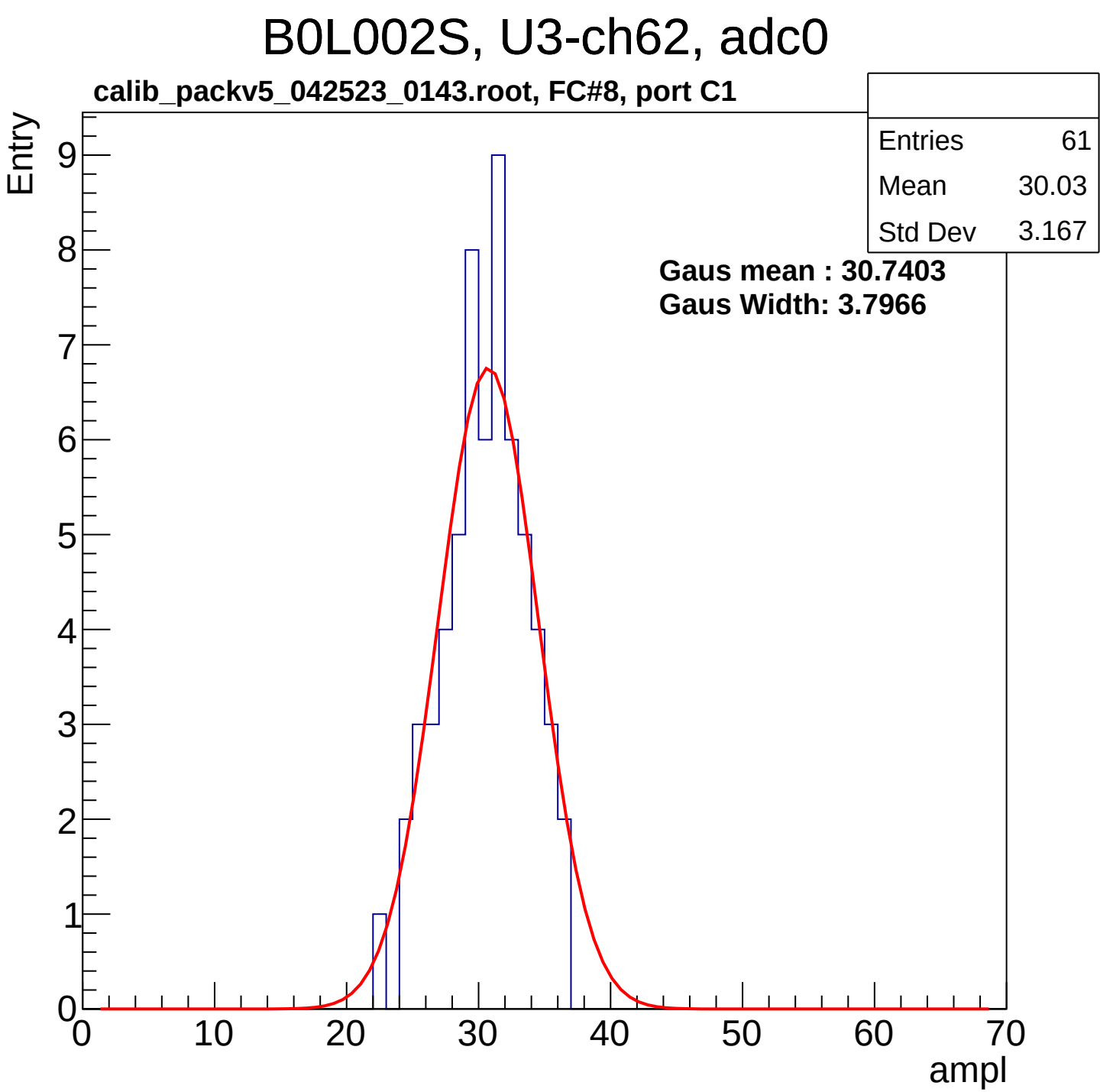
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch61, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch62, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.03
Std Dev 3.167
Gaus mean : 30.7403
Gaus Width: 3.7966
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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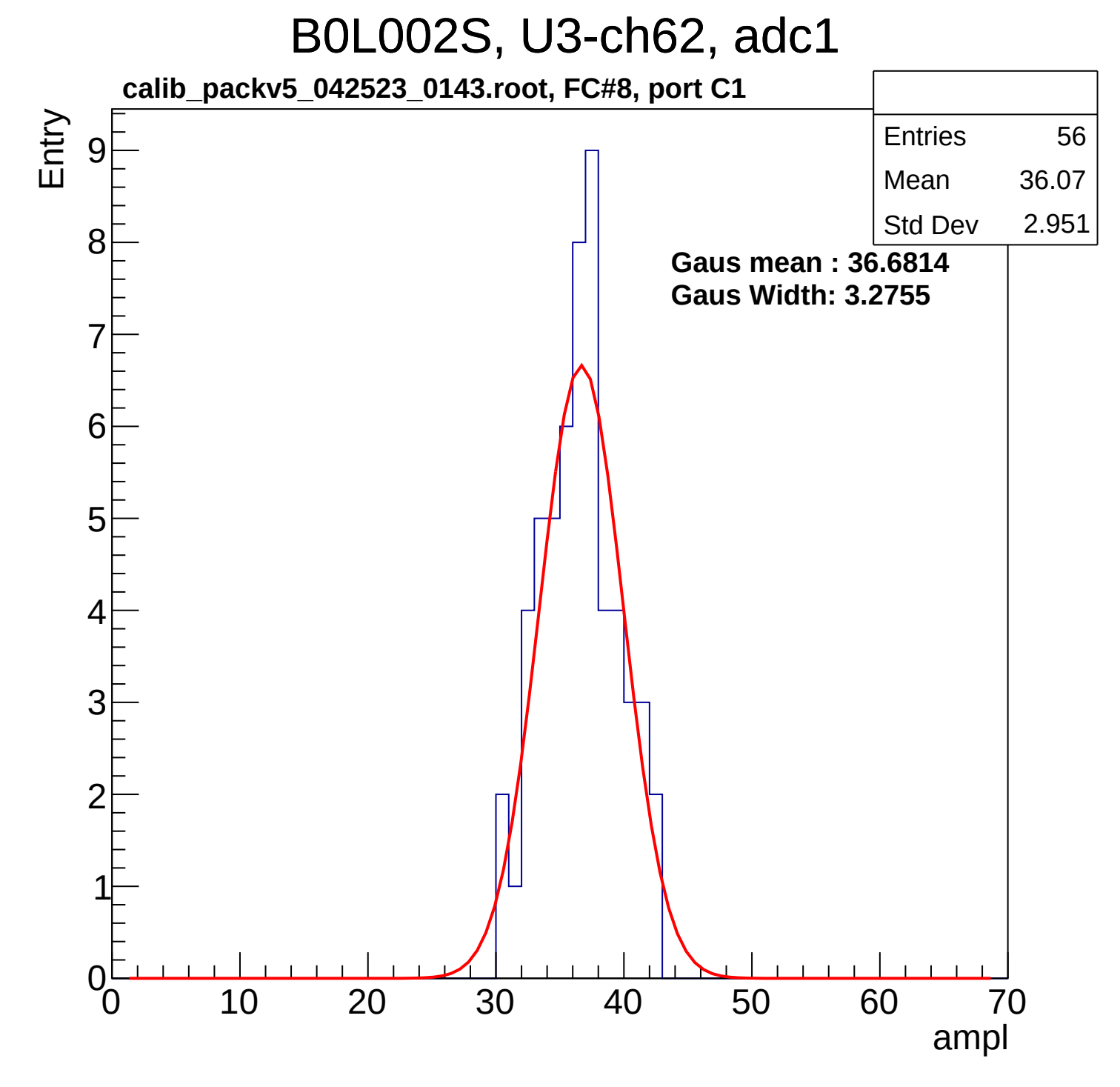
B0L002S, U3-ch62, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 36.07
Std Dev 2.951
Gaus mean : 36.6814
Gaus Width: 3.2755
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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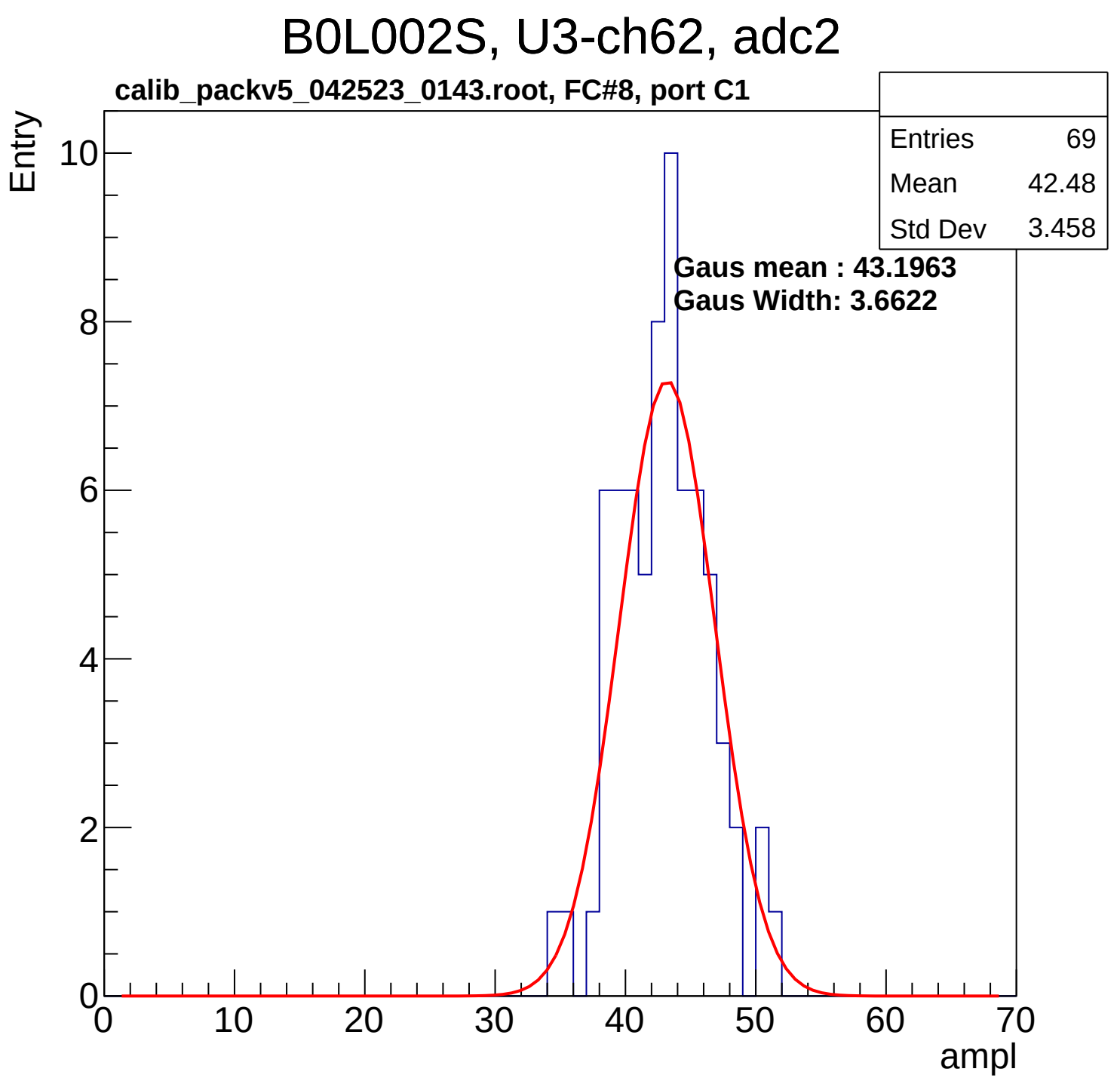
B0L002S, U3-ch62, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 42.48
Std Dev 3.458
Gaus mean : 43.1963
Gaus Width: 3.6622
Entry
ampl
0
2
4
6
8
10
0
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60
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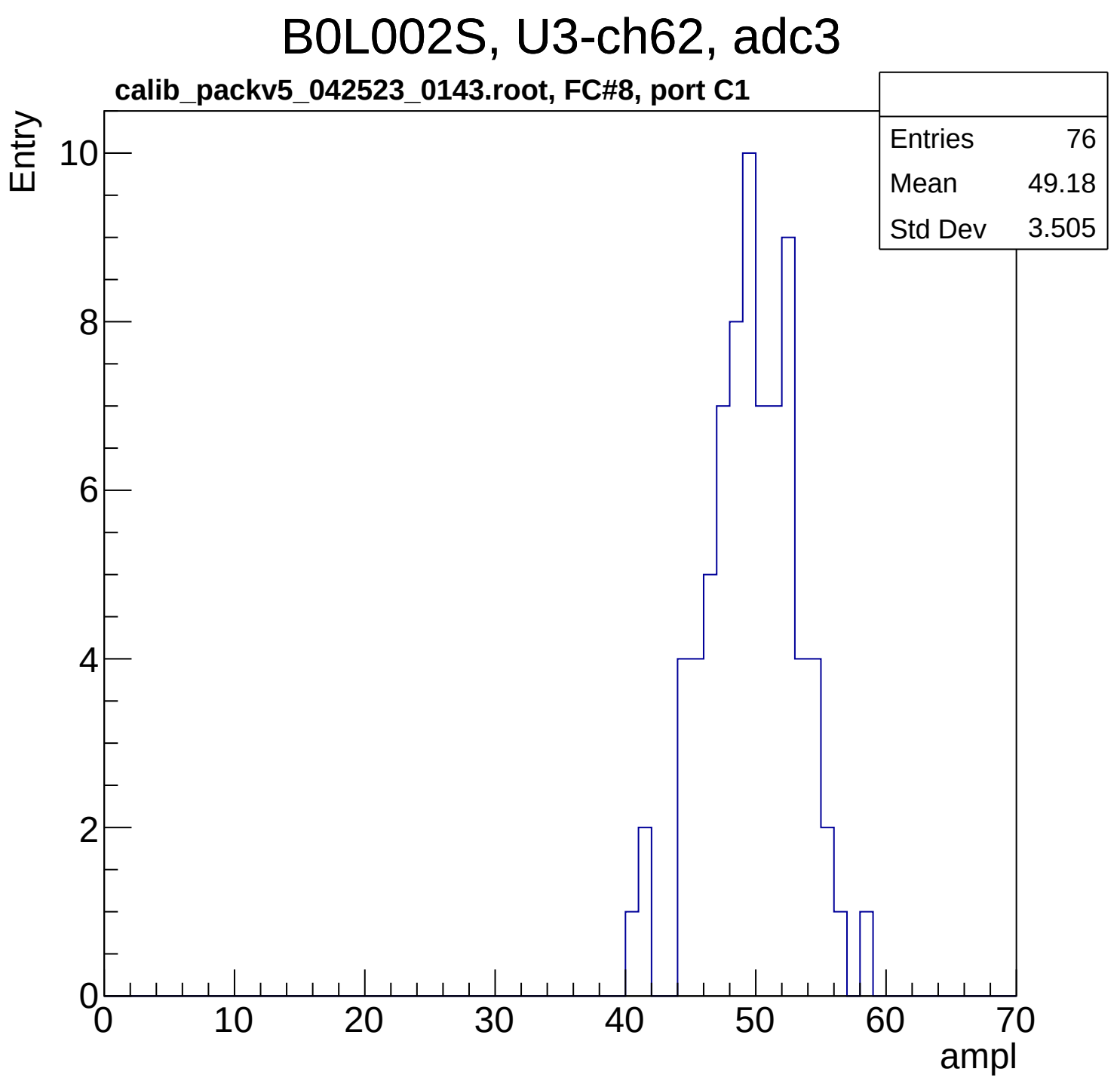
B0L002S, U3-ch62, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 49.18
Std Dev 3.505
Entry
ampl
0
2
4
6
8
10
0
10
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40
50
60
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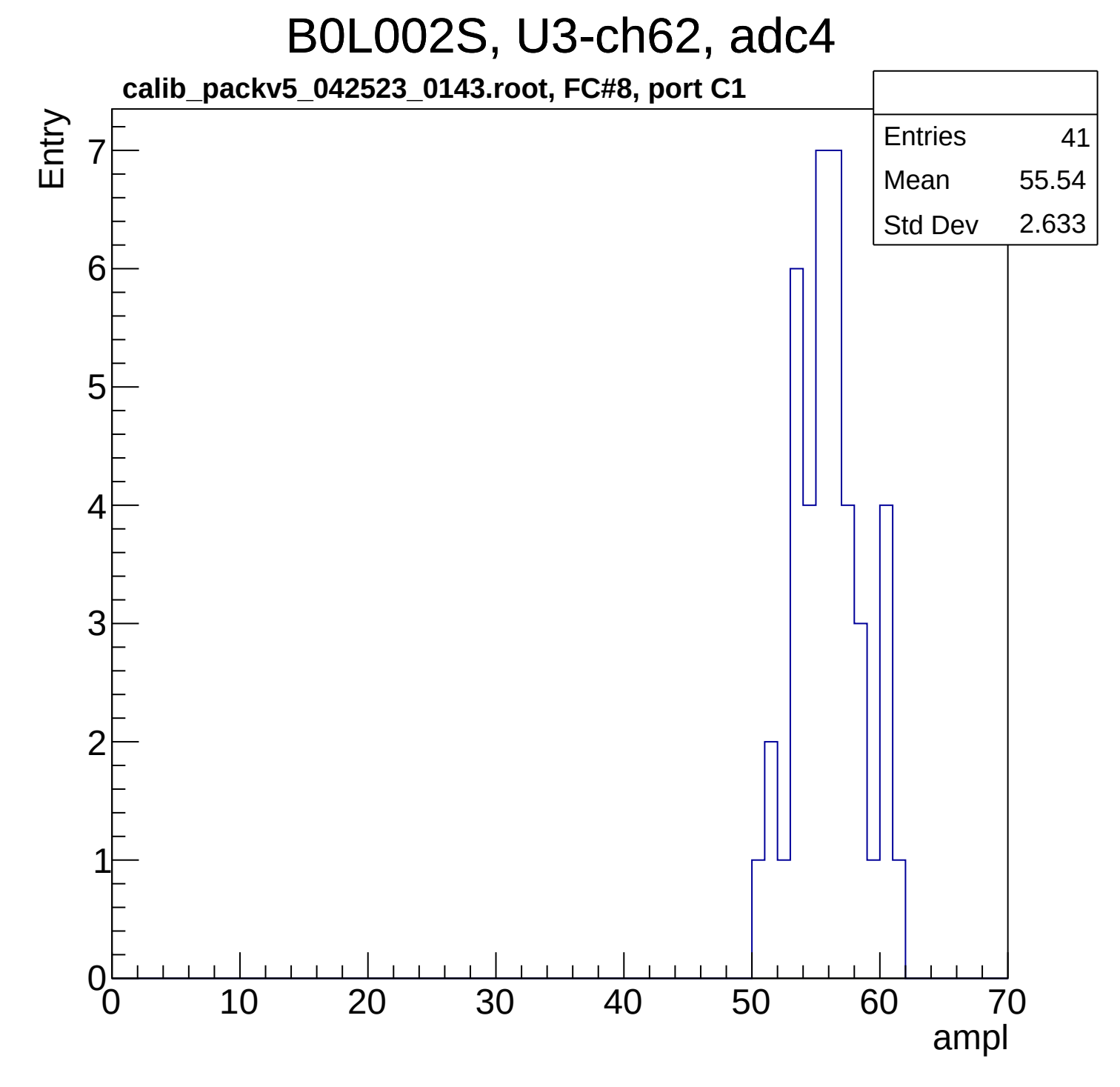
B0L002S, U3-ch62, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 55.54
Std Dev 2.633
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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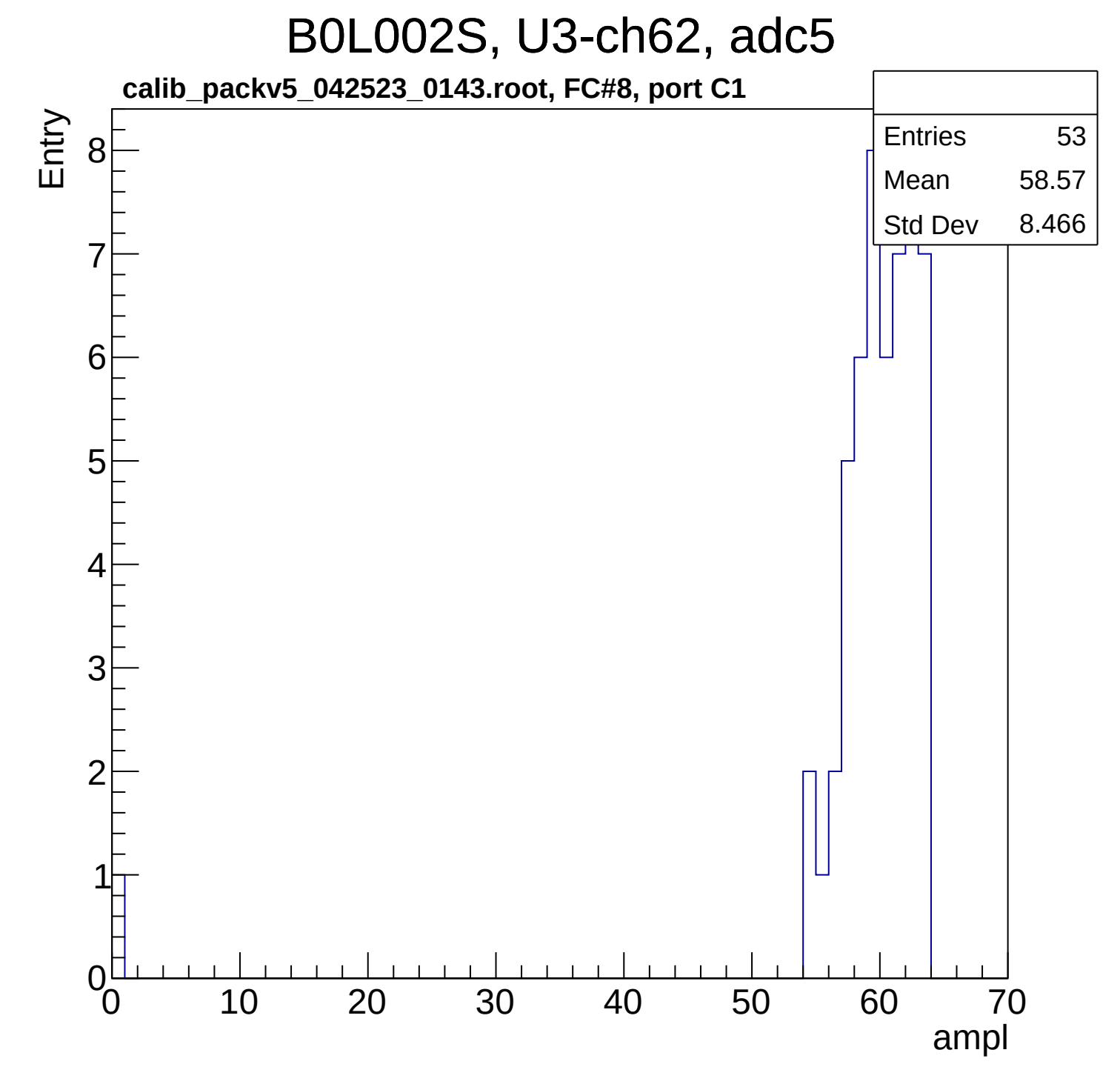
B0L002S, U3-ch62, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 58.57
Std Dev 8.466
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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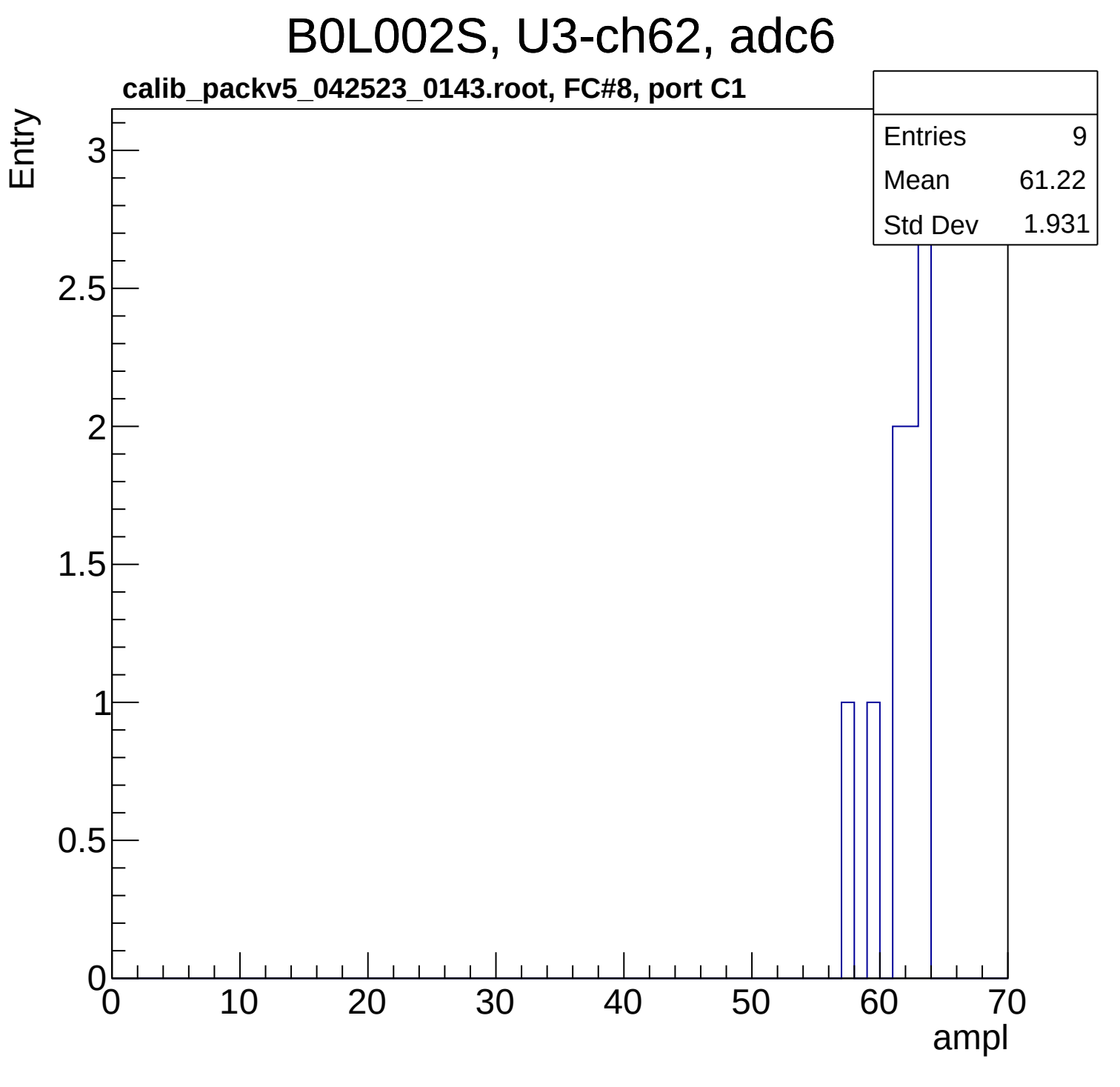
B0L002S, U3-ch62, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 61.22
Std Dev 1.931
Entry
ampl
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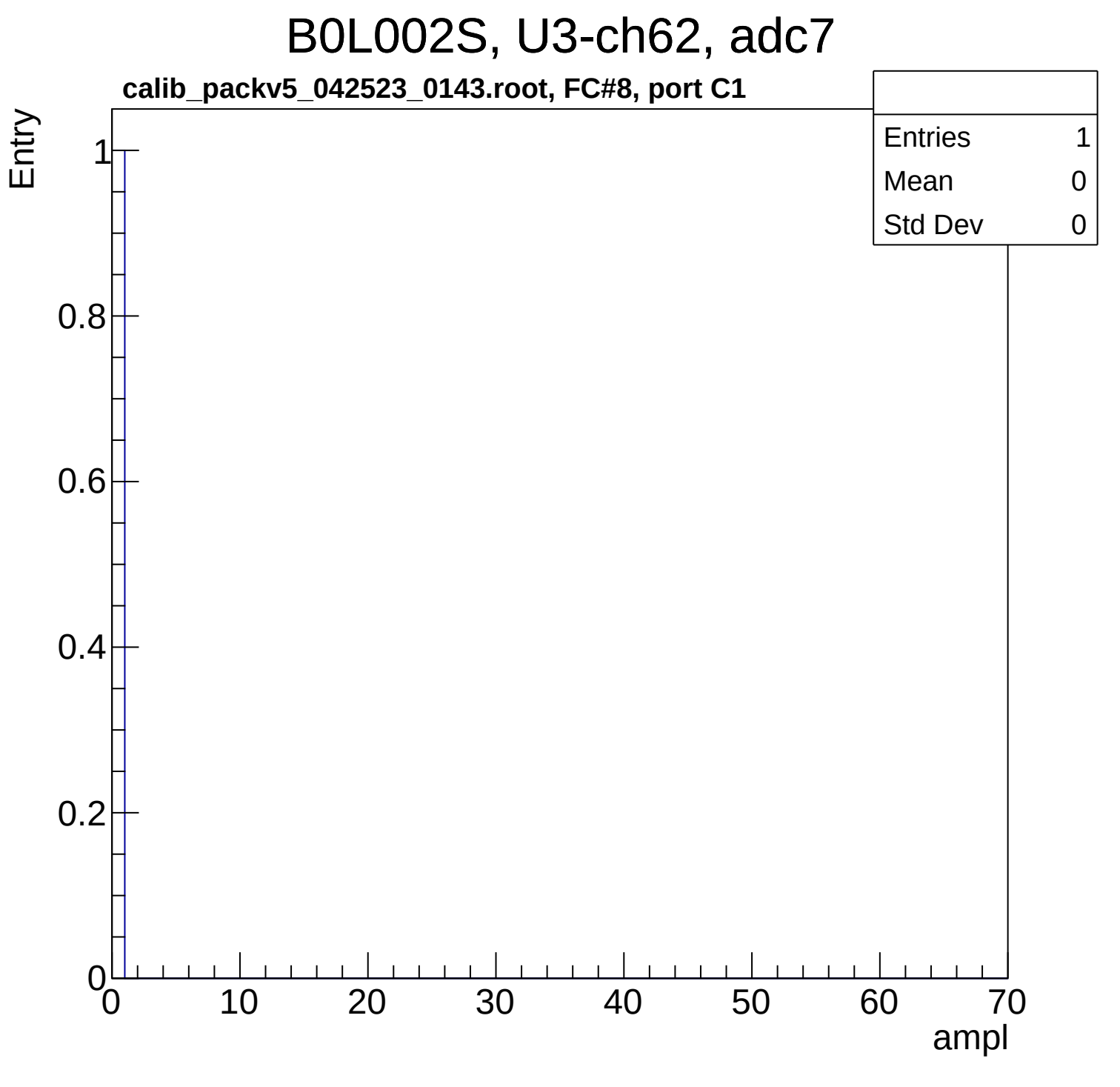
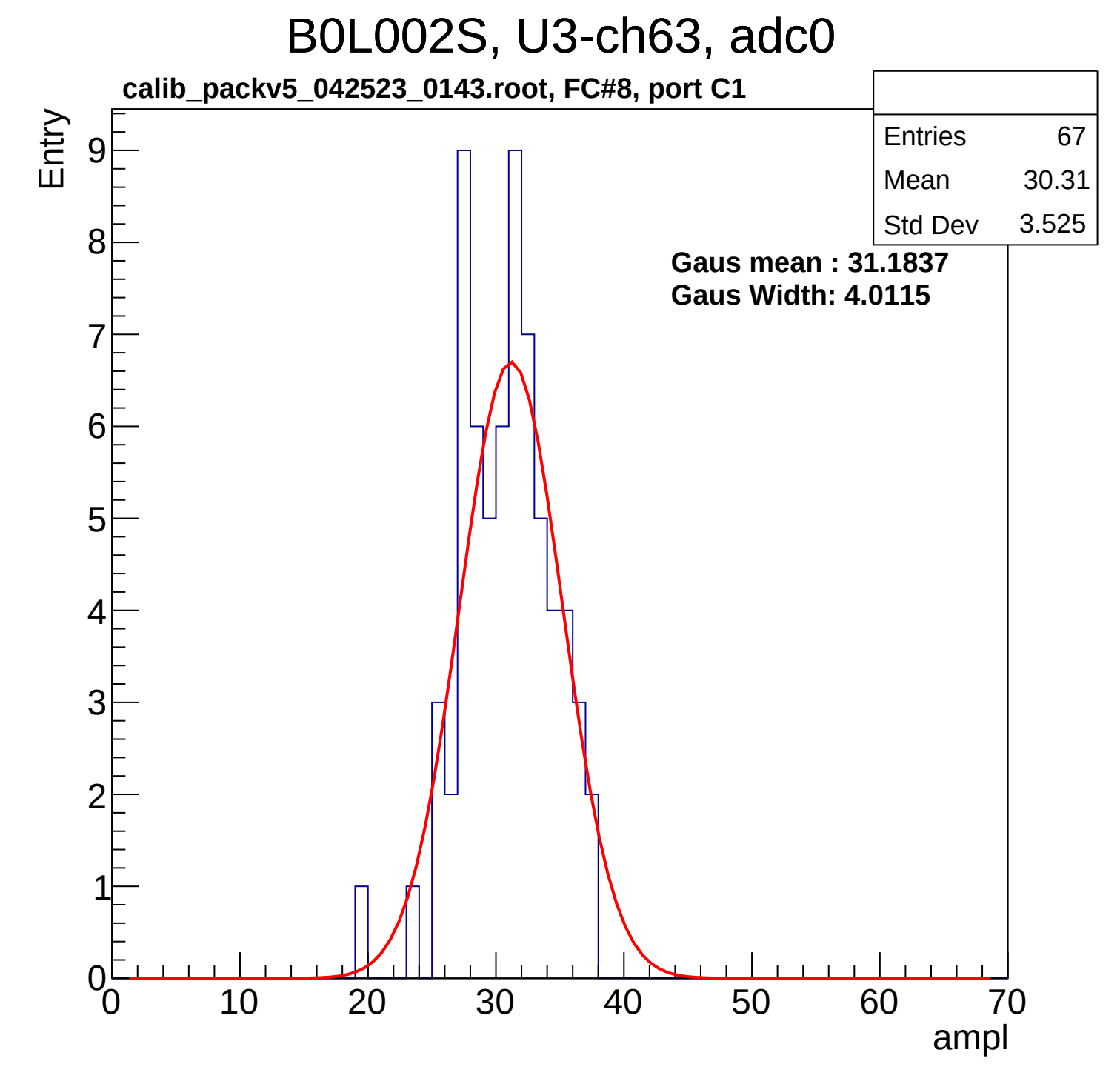
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch62, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch63, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.31
Std Dev 3.525
Gaus mean : 31.1837
Gaus Width: 4.0115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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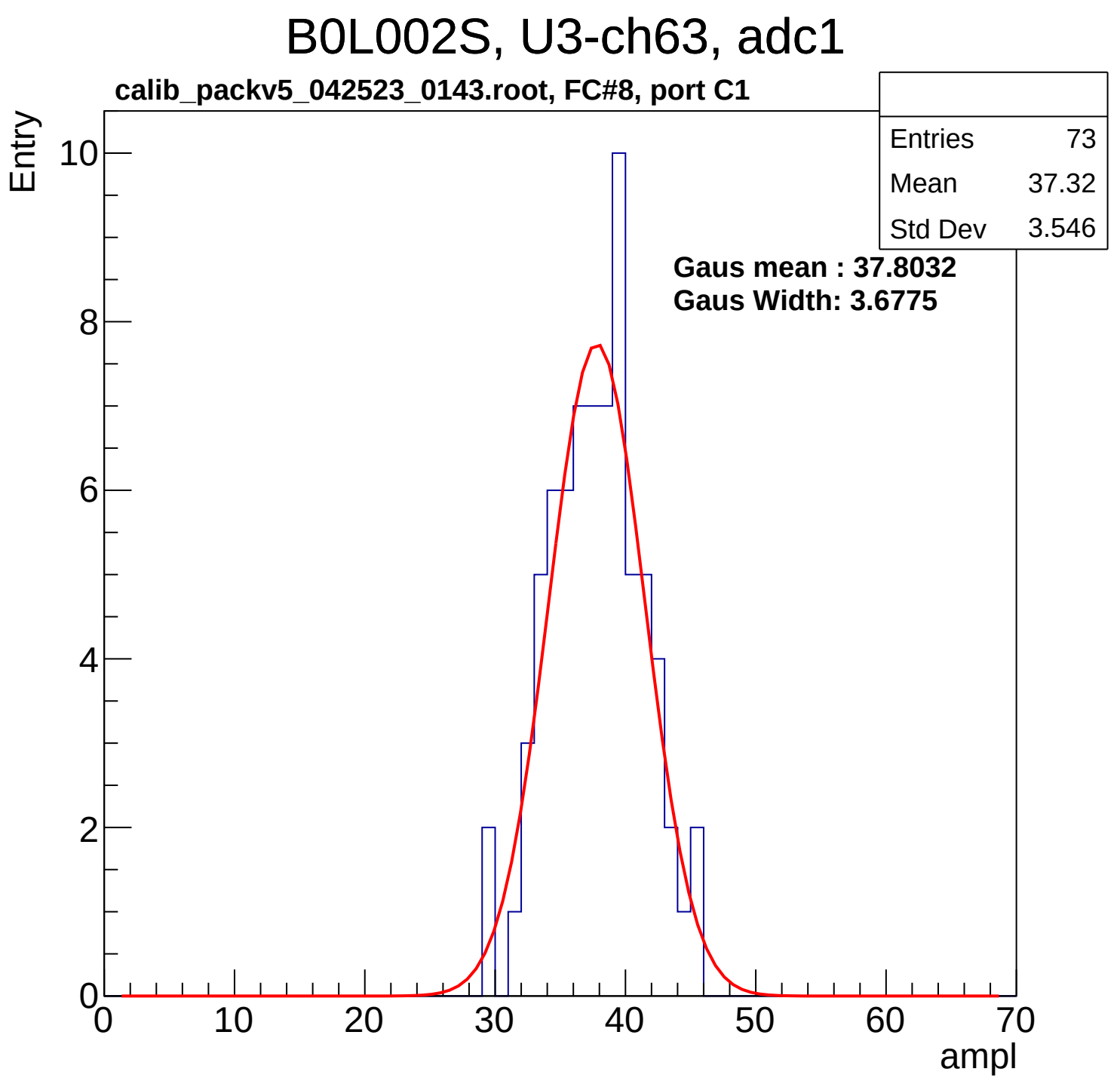
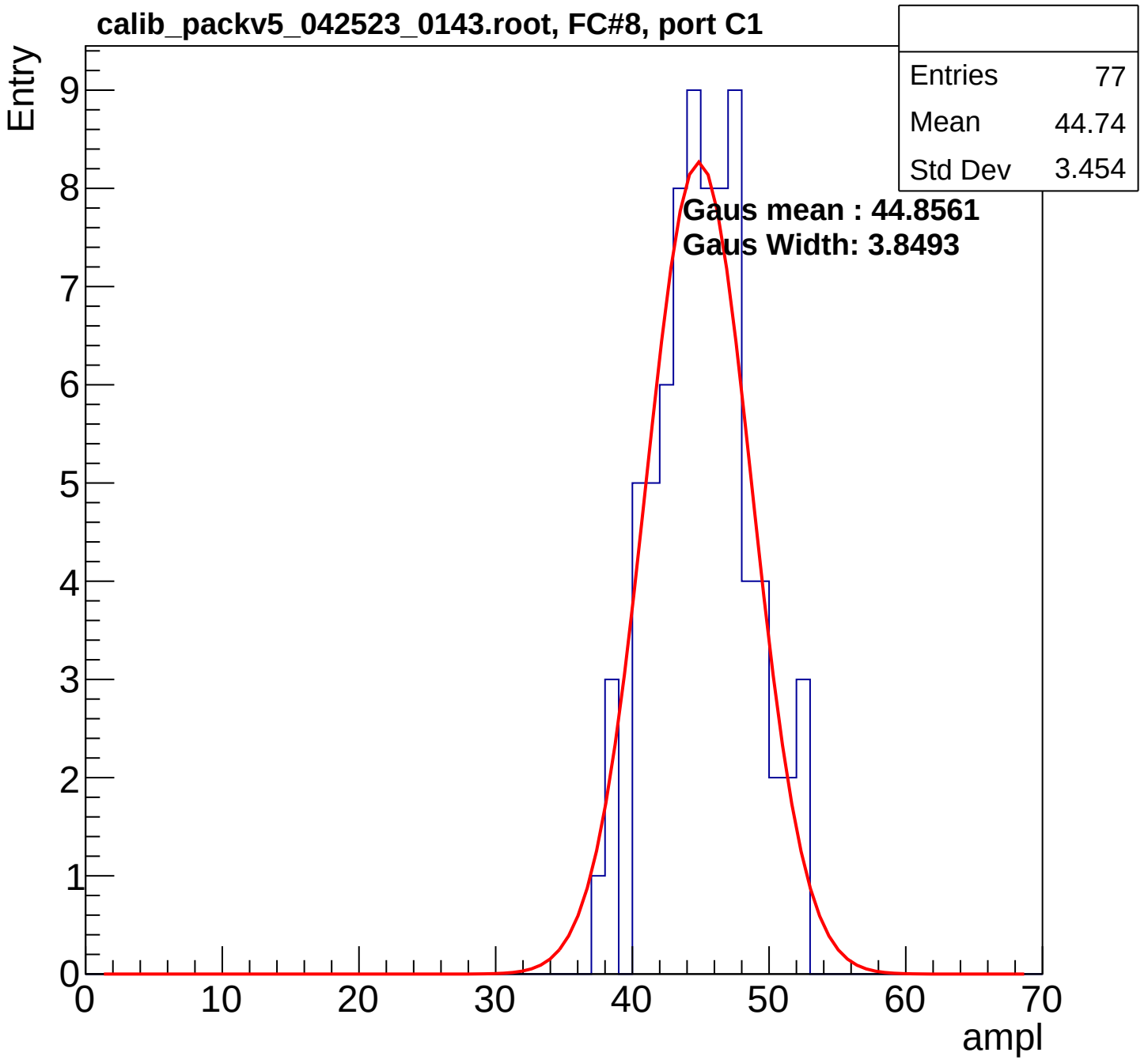

B0L002S, U3-ch63, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.32
Std Dev 3.546
Gaus mean : 37.8032
Gaus Width: 3.6775
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch63, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 44.74
Std Dev 3.454
Gaus mean : 44.8561
Gaus Width: 3.8493
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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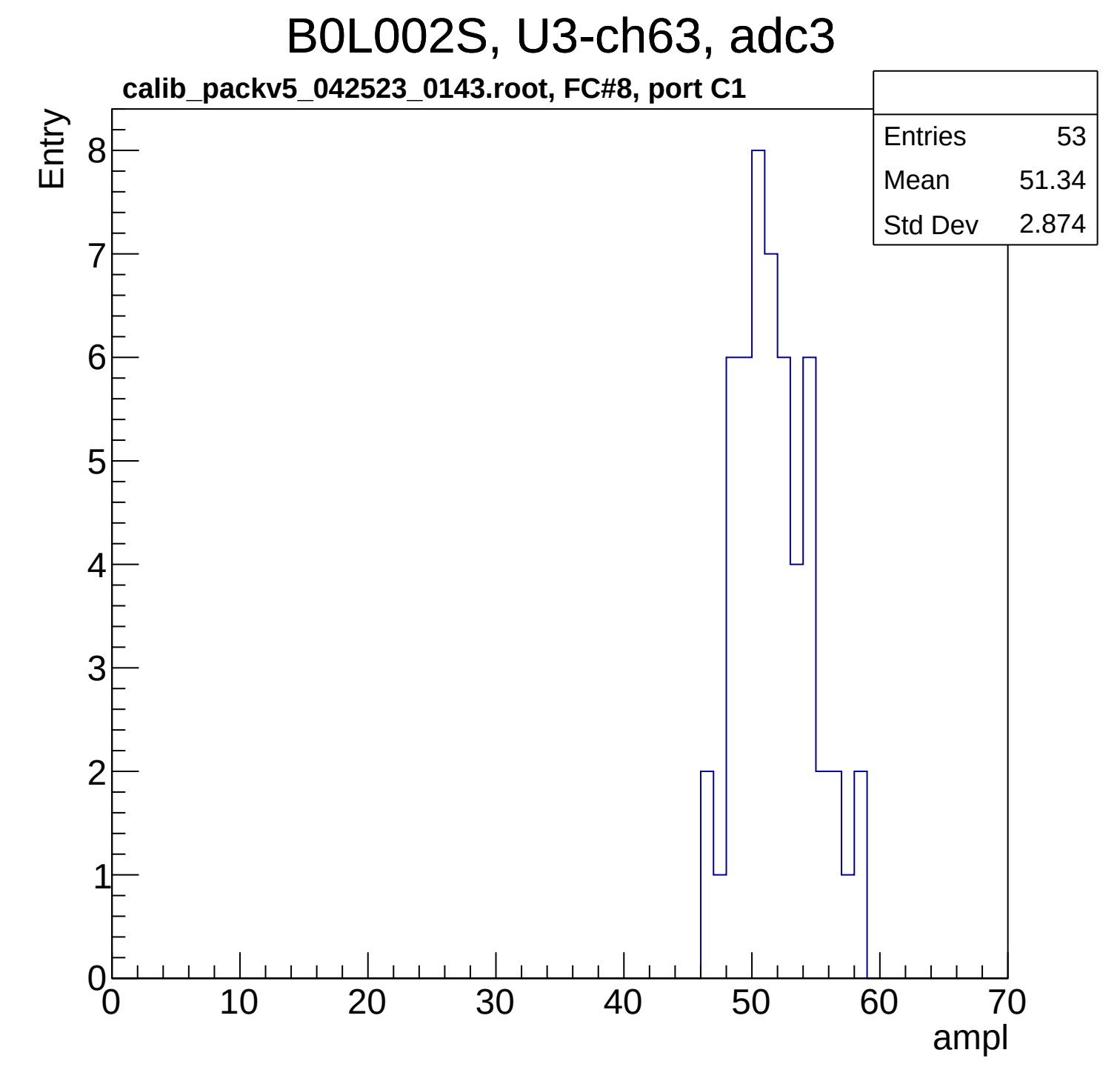
B0L002S, U3-ch63, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 51.34
Std Dev 2.874
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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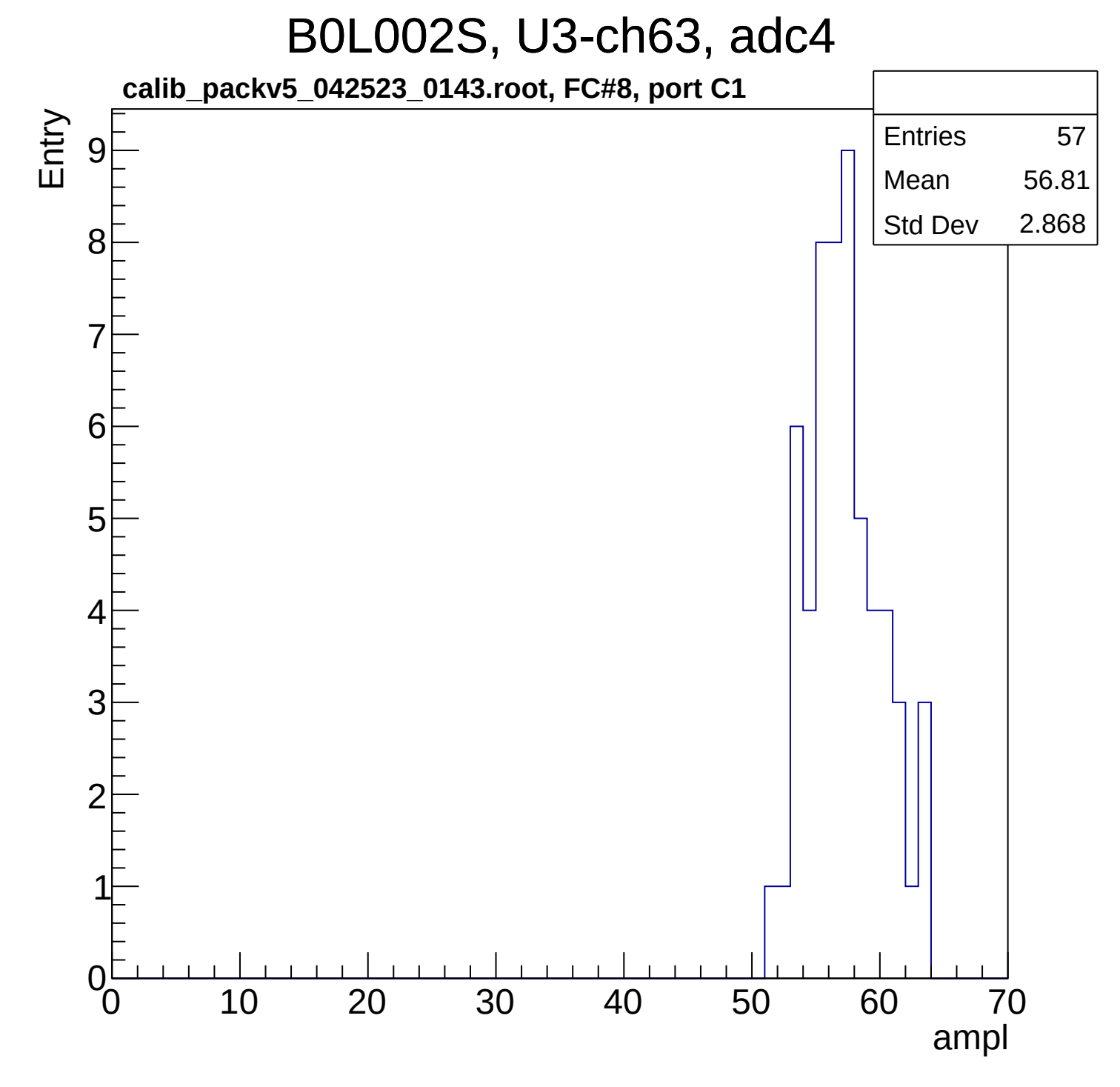
40
50
60
70


B0L002S, U3-ch63, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.81
Std Dev 2.868
Entry
ampl
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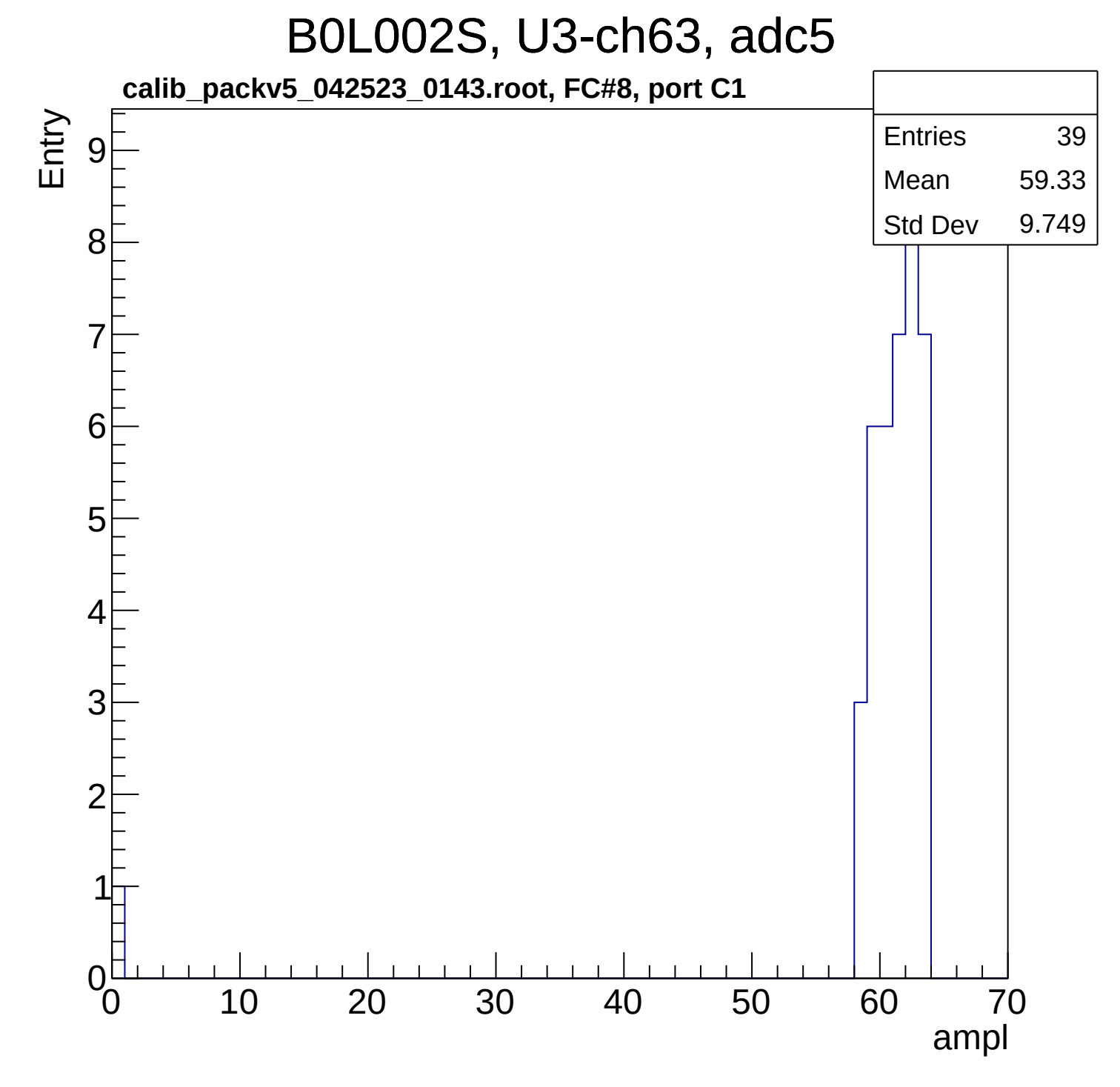
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch63, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.33
Std Dev 9.749
Entry
ampl
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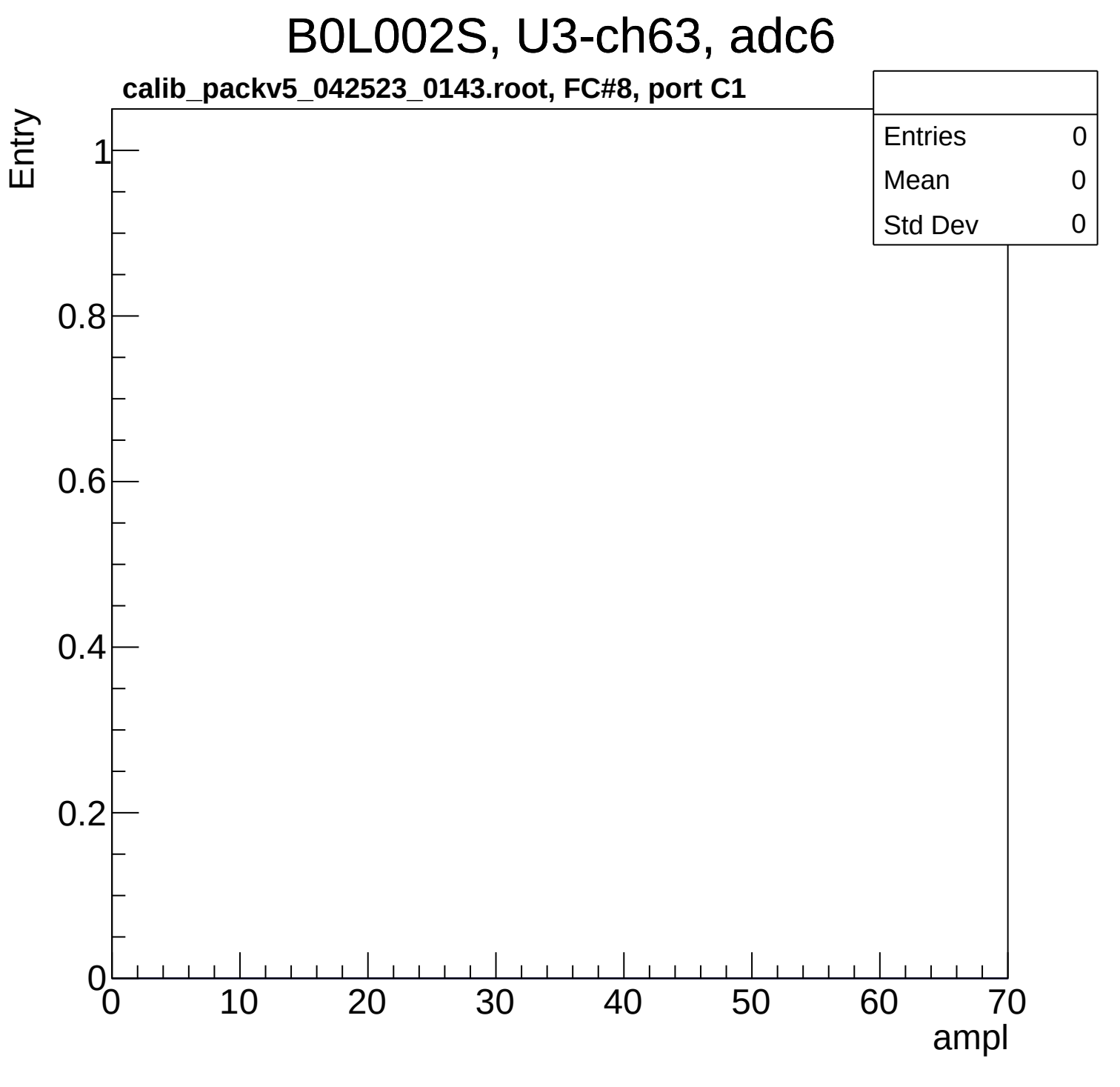
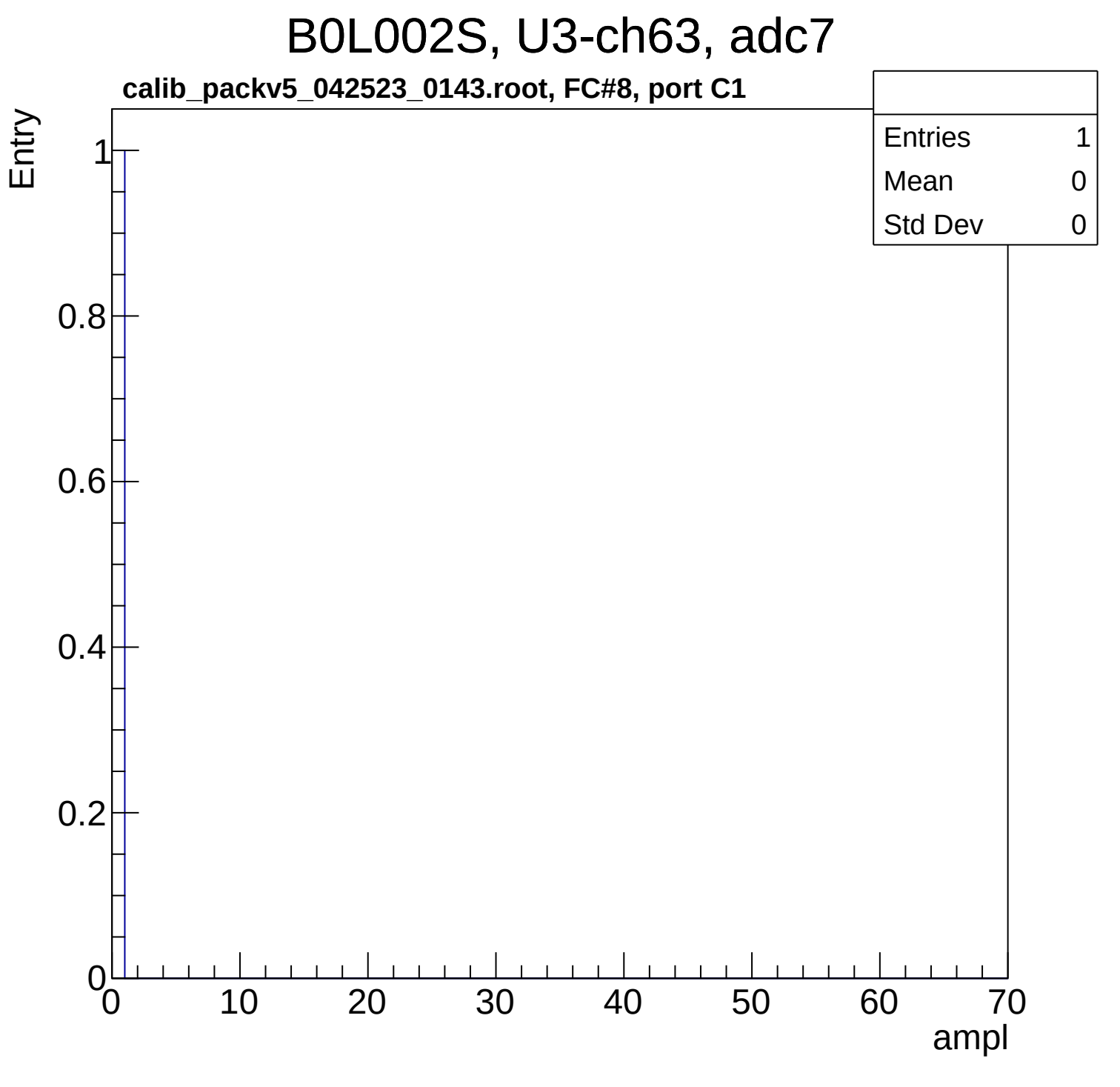
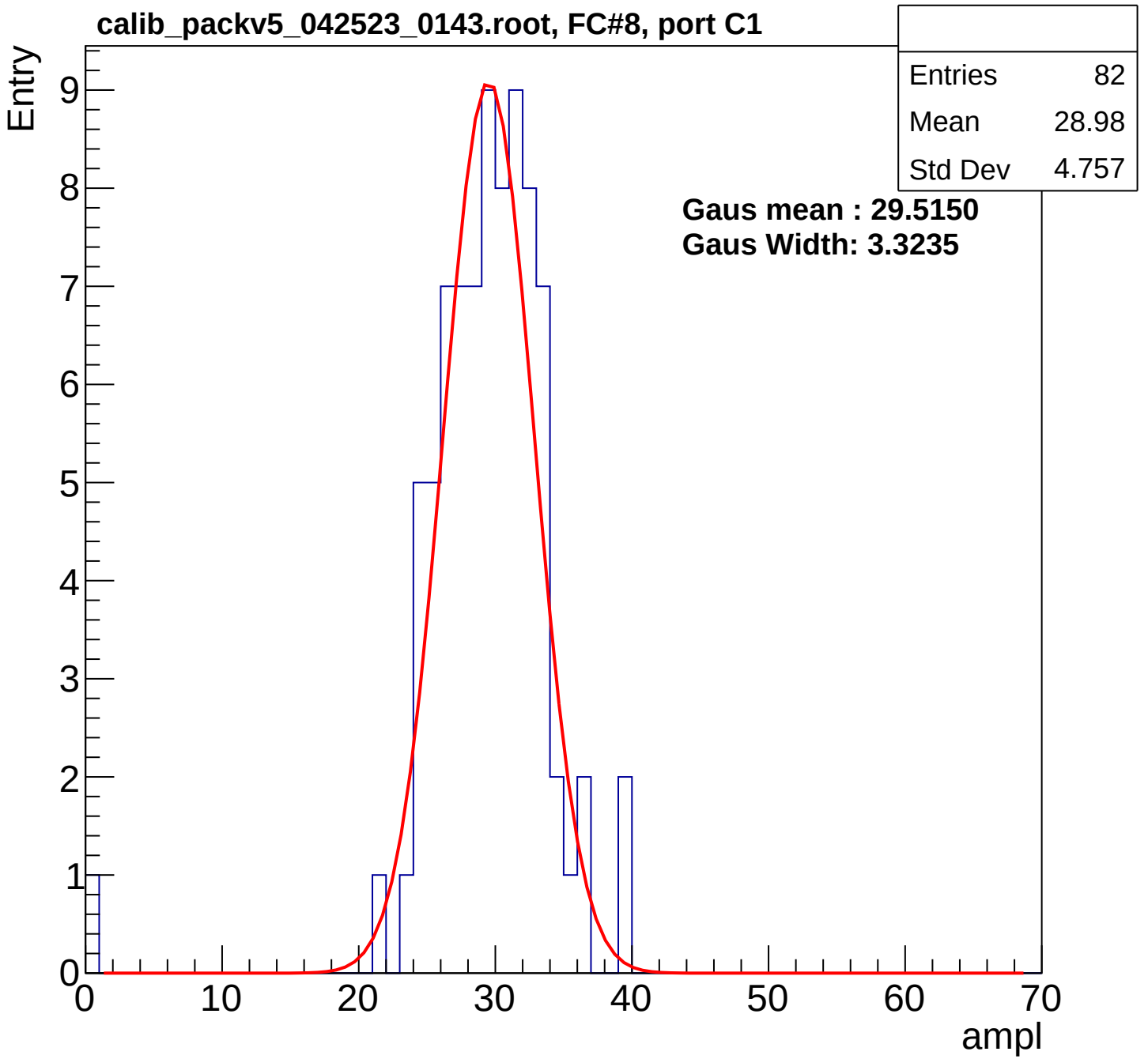
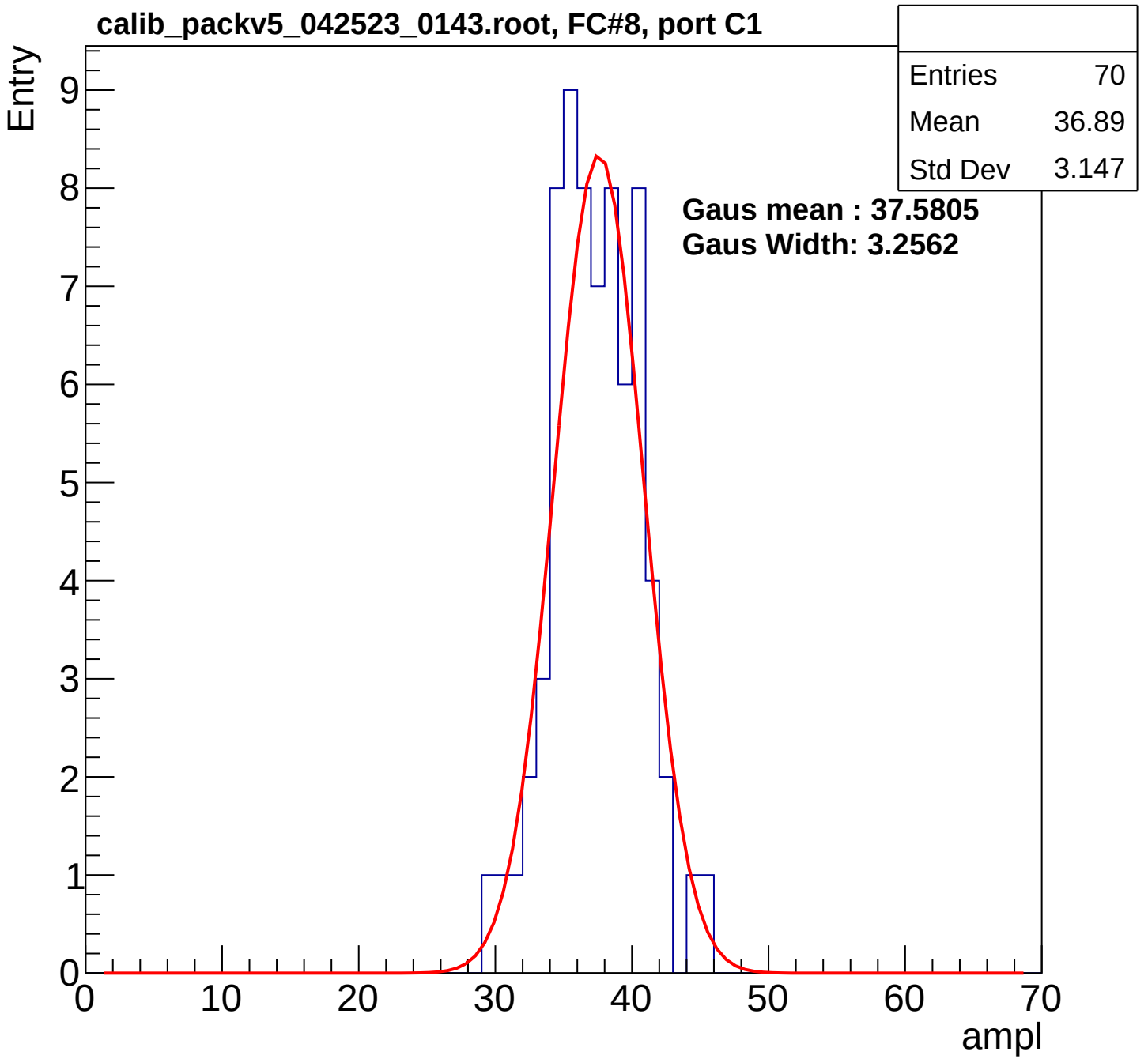
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch63, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch63, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch64, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 28.98
Std Dev 4.757
Gaus mean : 29.5150
Gaus Width: 3.3235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch64, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.89
Std Dev 3.147
Gaus mean : 37.5805
Gaus Width: 3.2562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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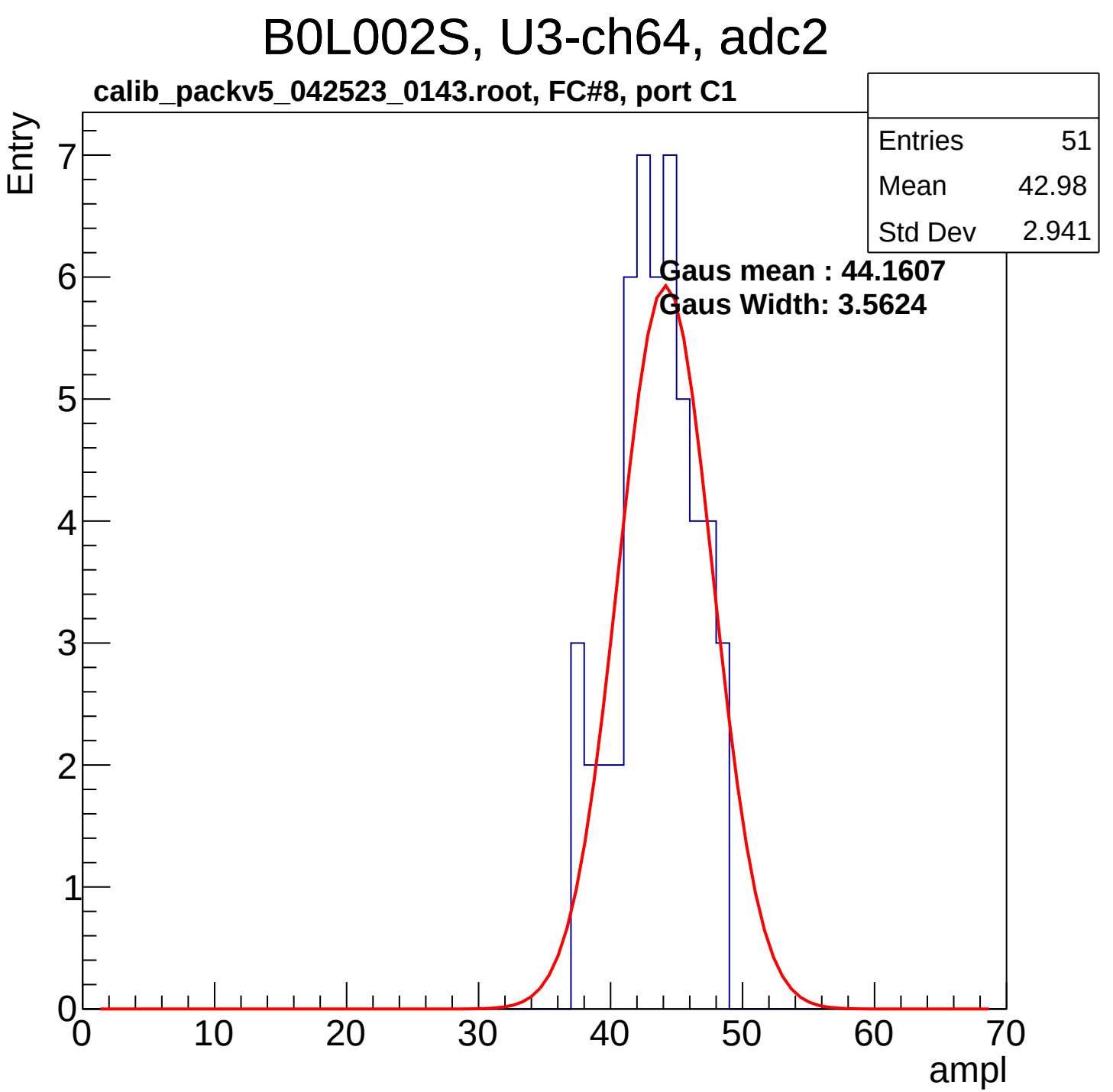
B0L002S, U3-ch64, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 42.98
Std Dev 2.941
Gaus mean : 44.1607
Gaus Width: 3.5624
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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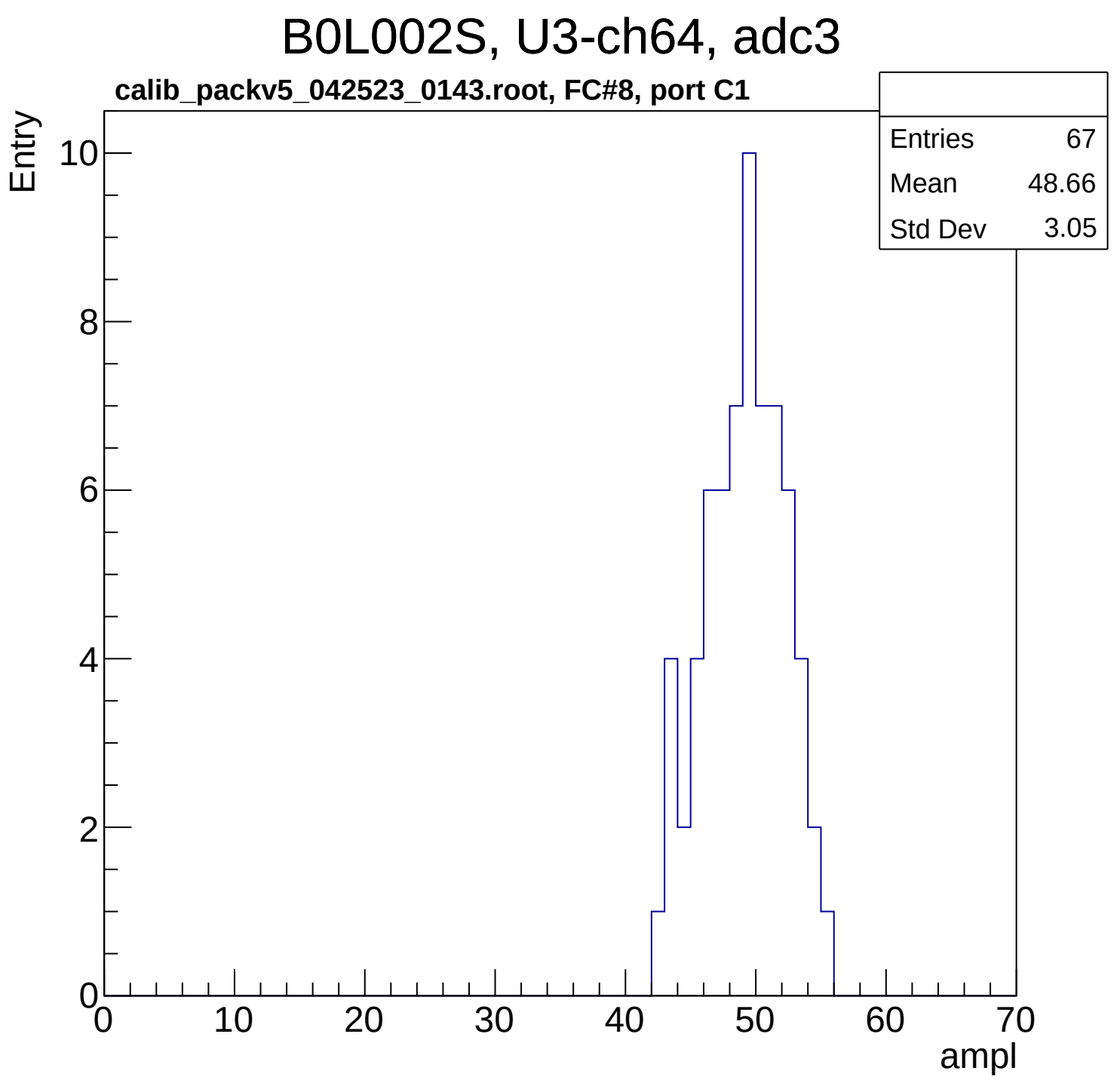
B0L002S, U3-ch64, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 48.66
Std Dev 3.05
Entry
0
2
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10
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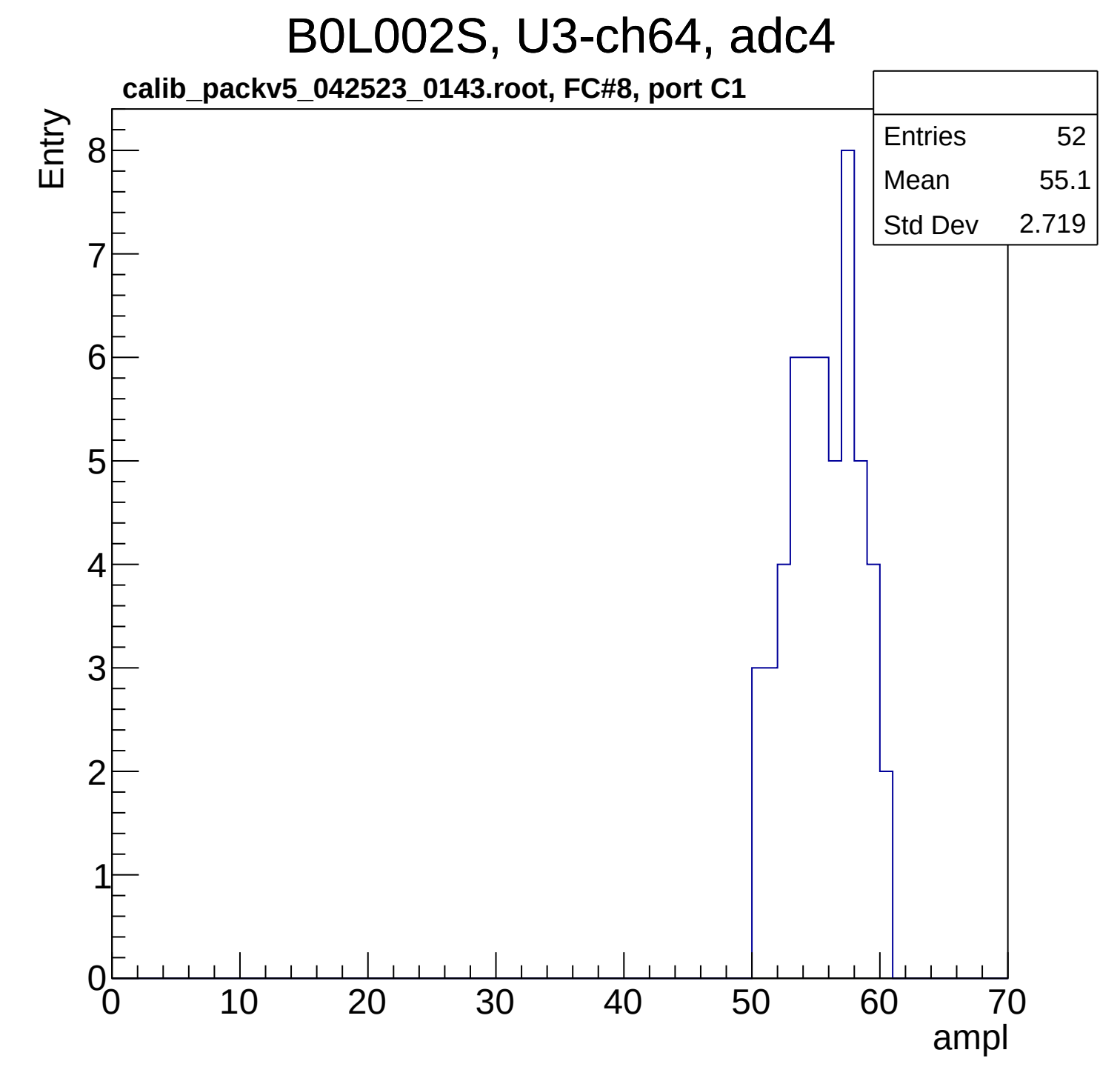
70
ampl


B0L002S, U3-ch64, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 55.1
Std Dev 2.719
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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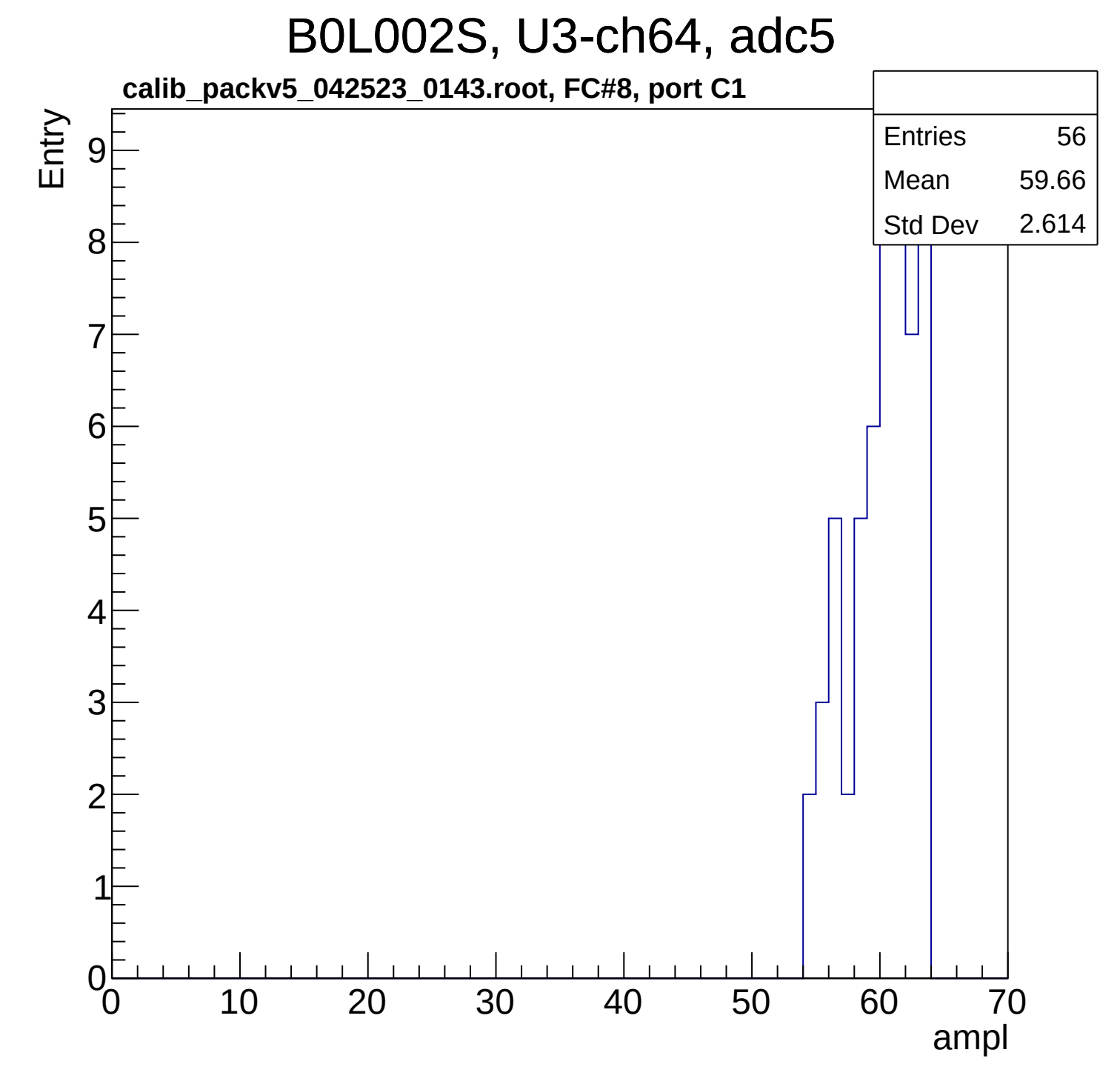
40
50
60
70


B0L002S, U3-ch64, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 59.66
Std Dev 2.614
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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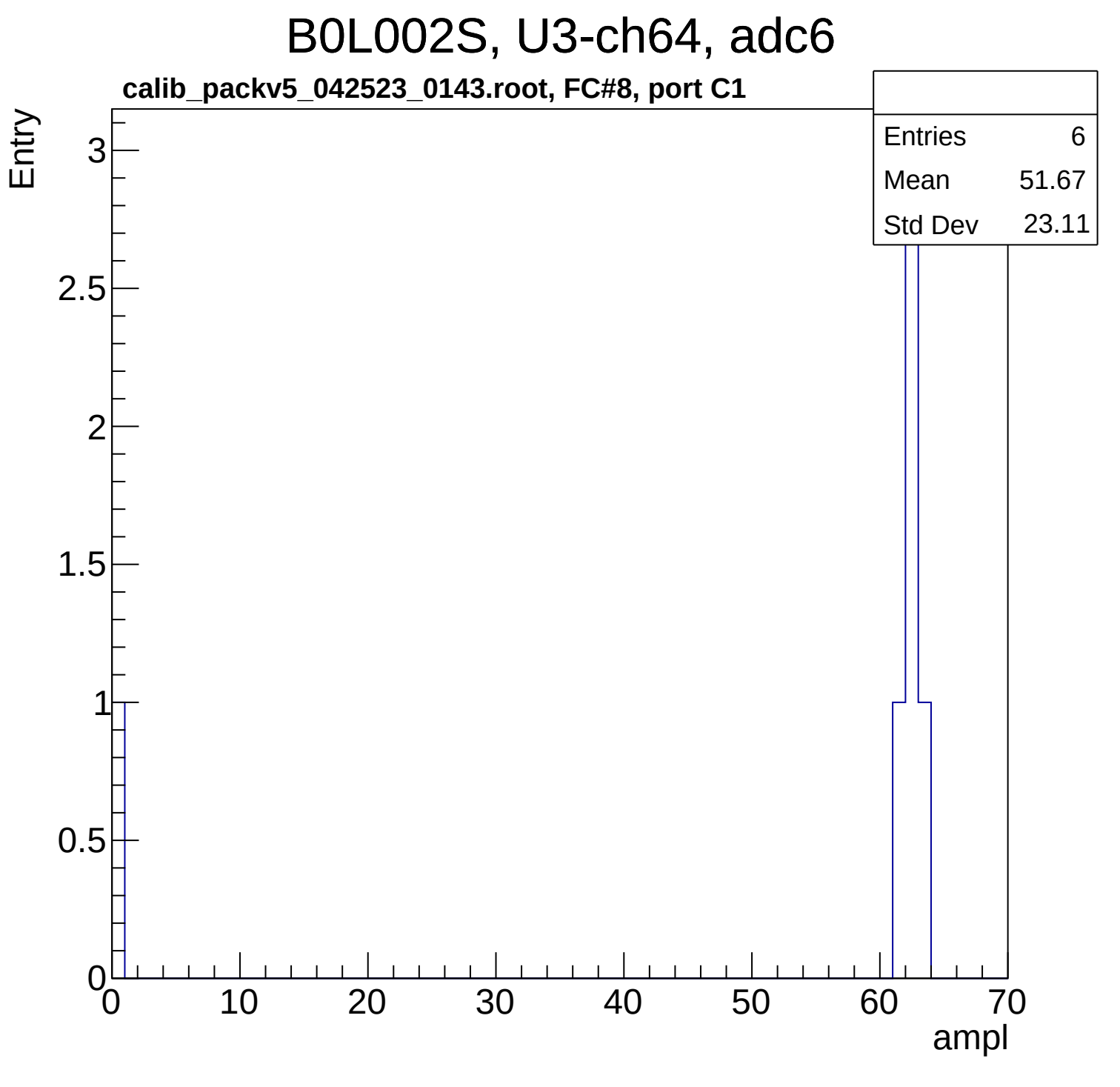
B0L002S, U3-ch64, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 51.67
Std Dev 23.11
Entry
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1
1.5
2
2.5
3
0
10
20
30
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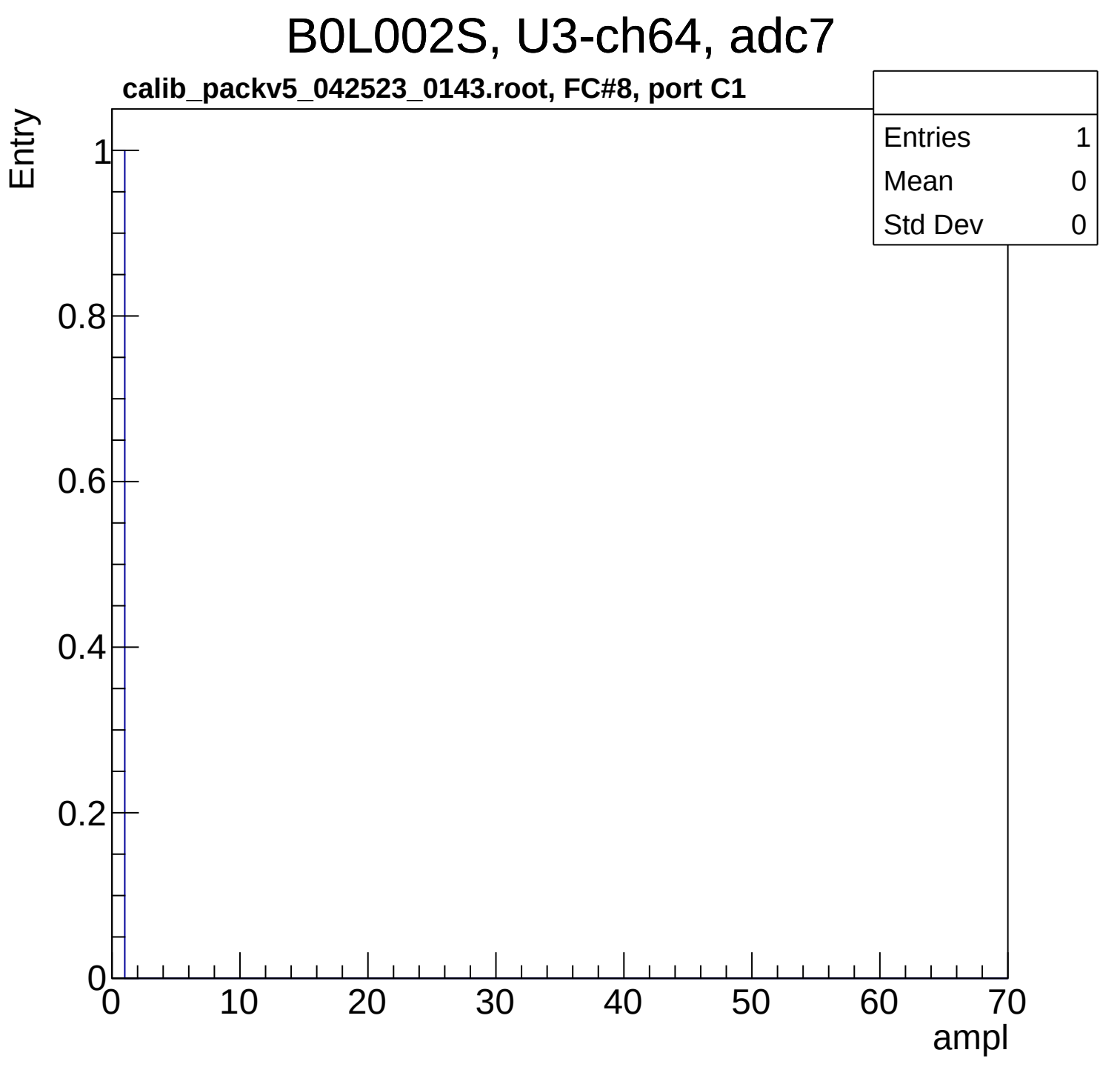
50
60
70
ampl


B0L002S, U3-ch64, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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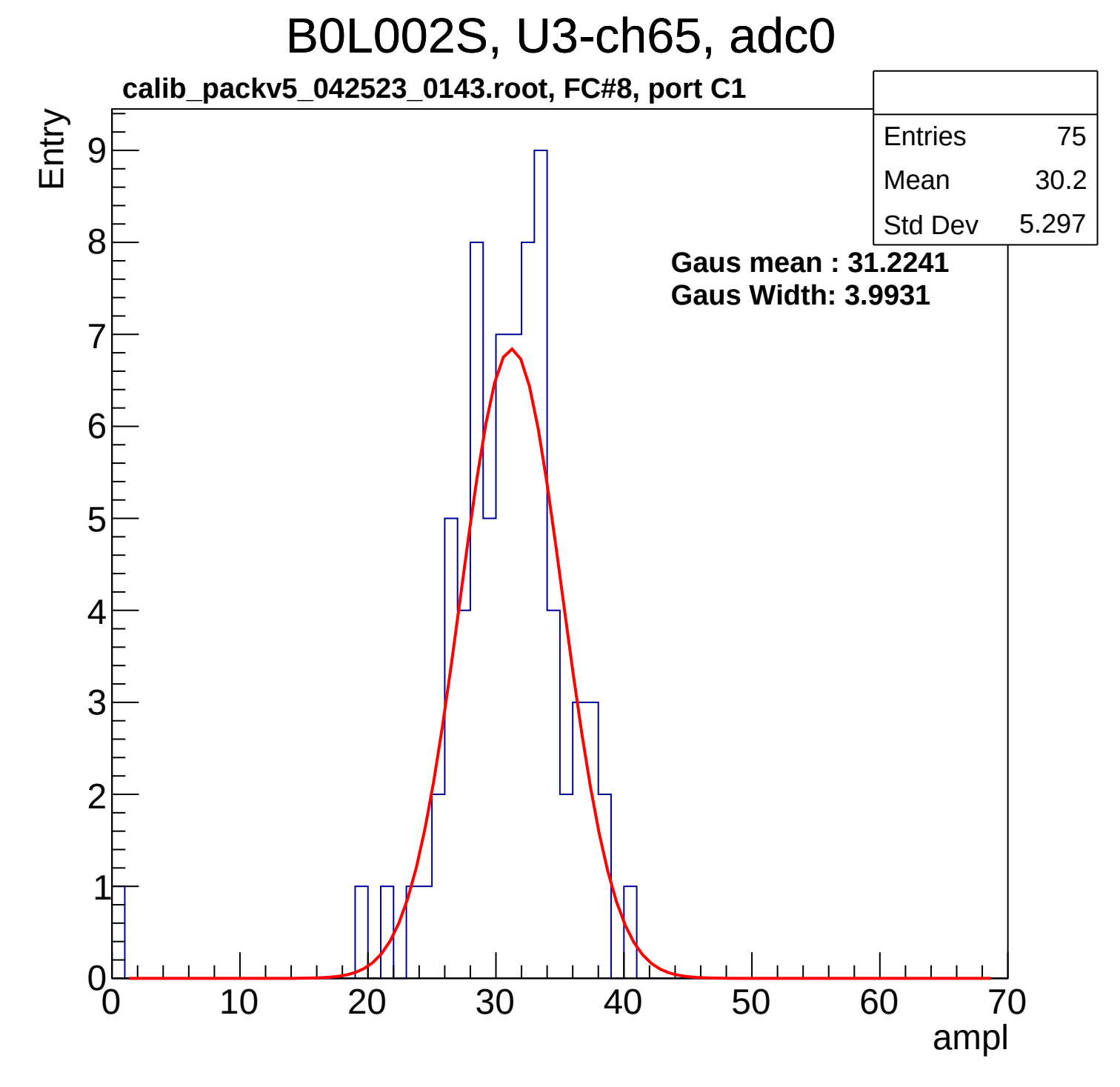
50
60
70
ampl


B0L002S, U3-ch65, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 30.2
Std Dev 5.297
Gaus mean : 31.2241
Gaus Width: 3.9931
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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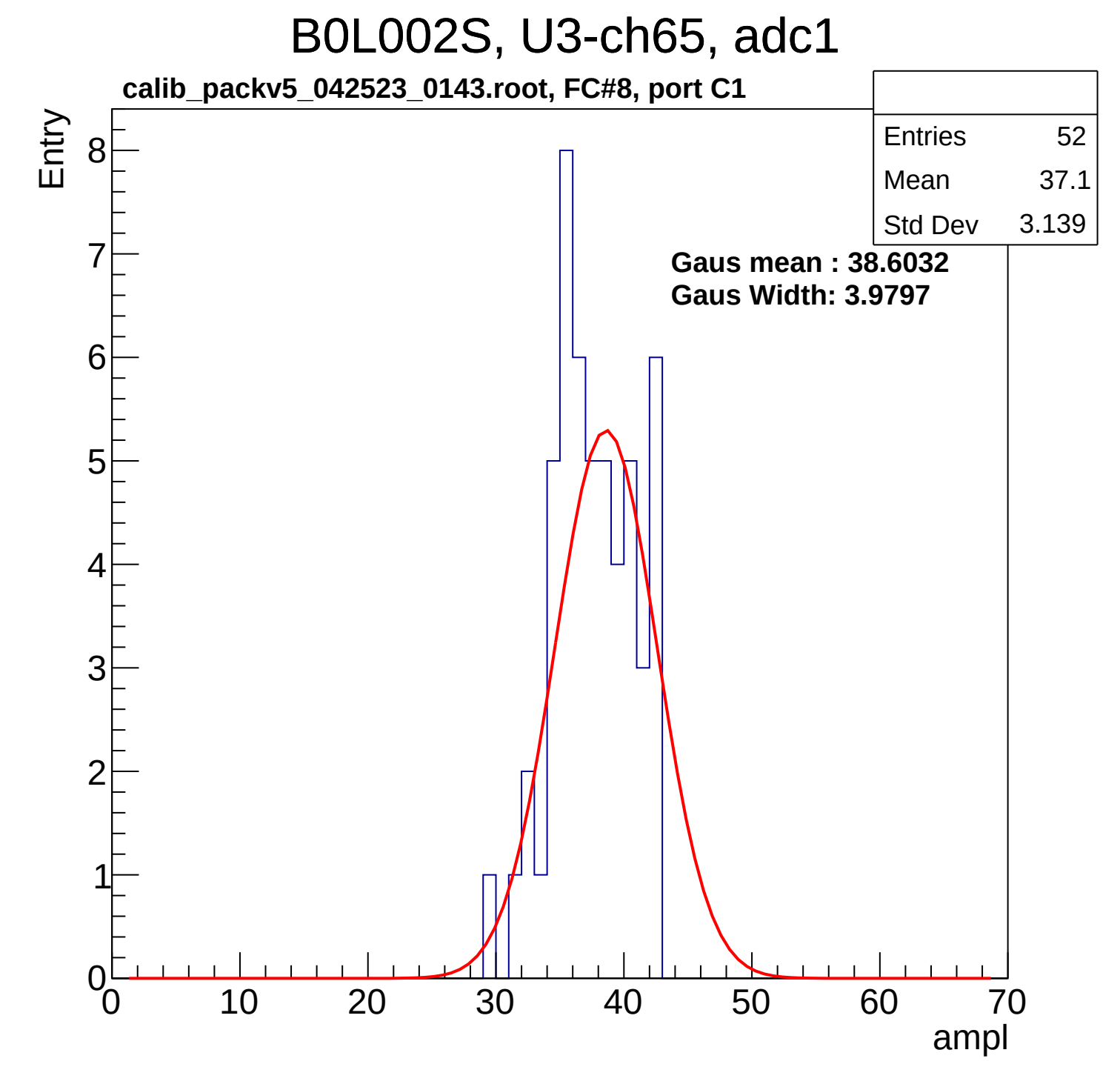
B0L002S, U3-ch65, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 37.1
Std Dev 3.139
Gaus mean : 38.6032
Gaus Width: 3.9797
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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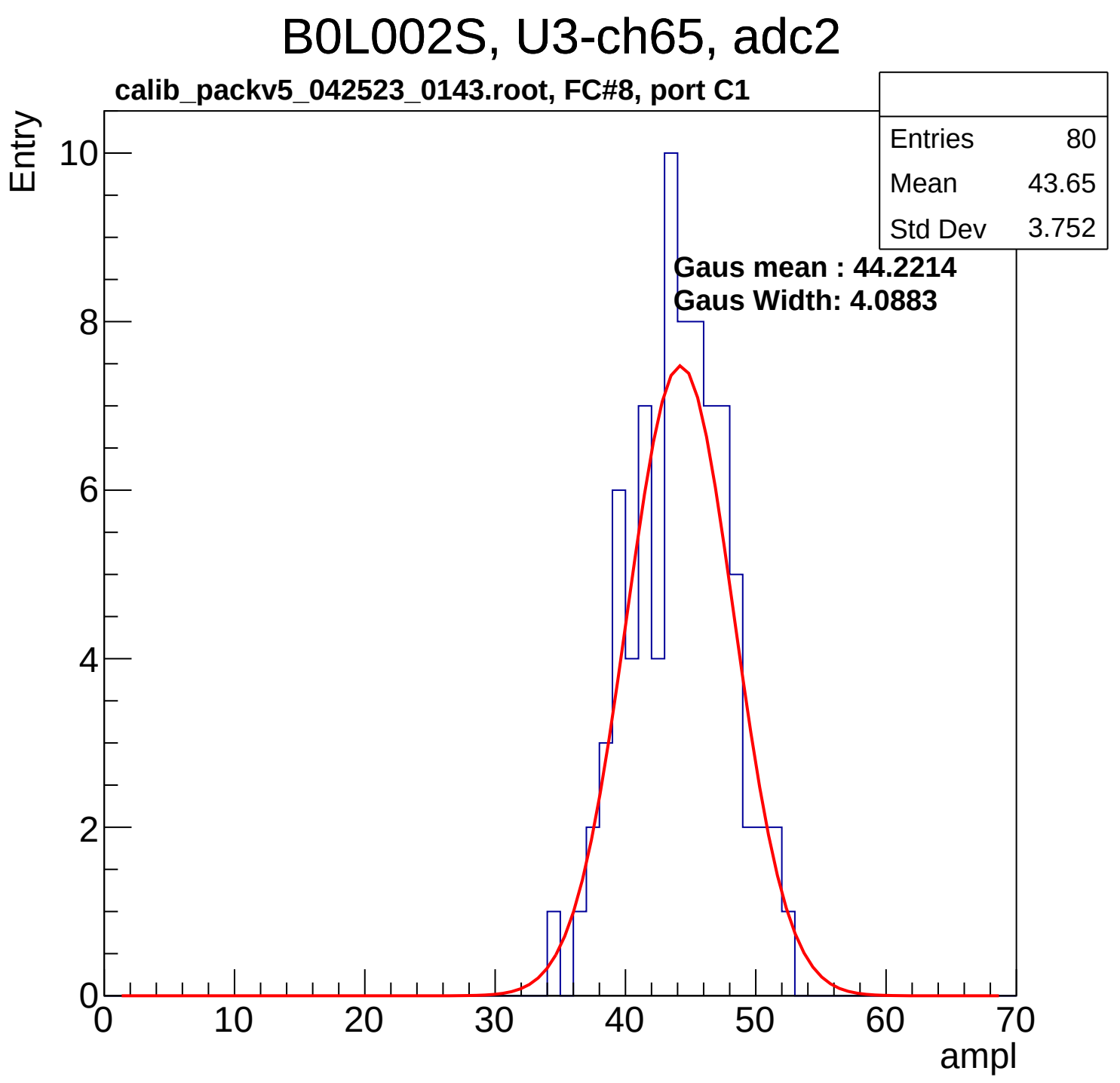
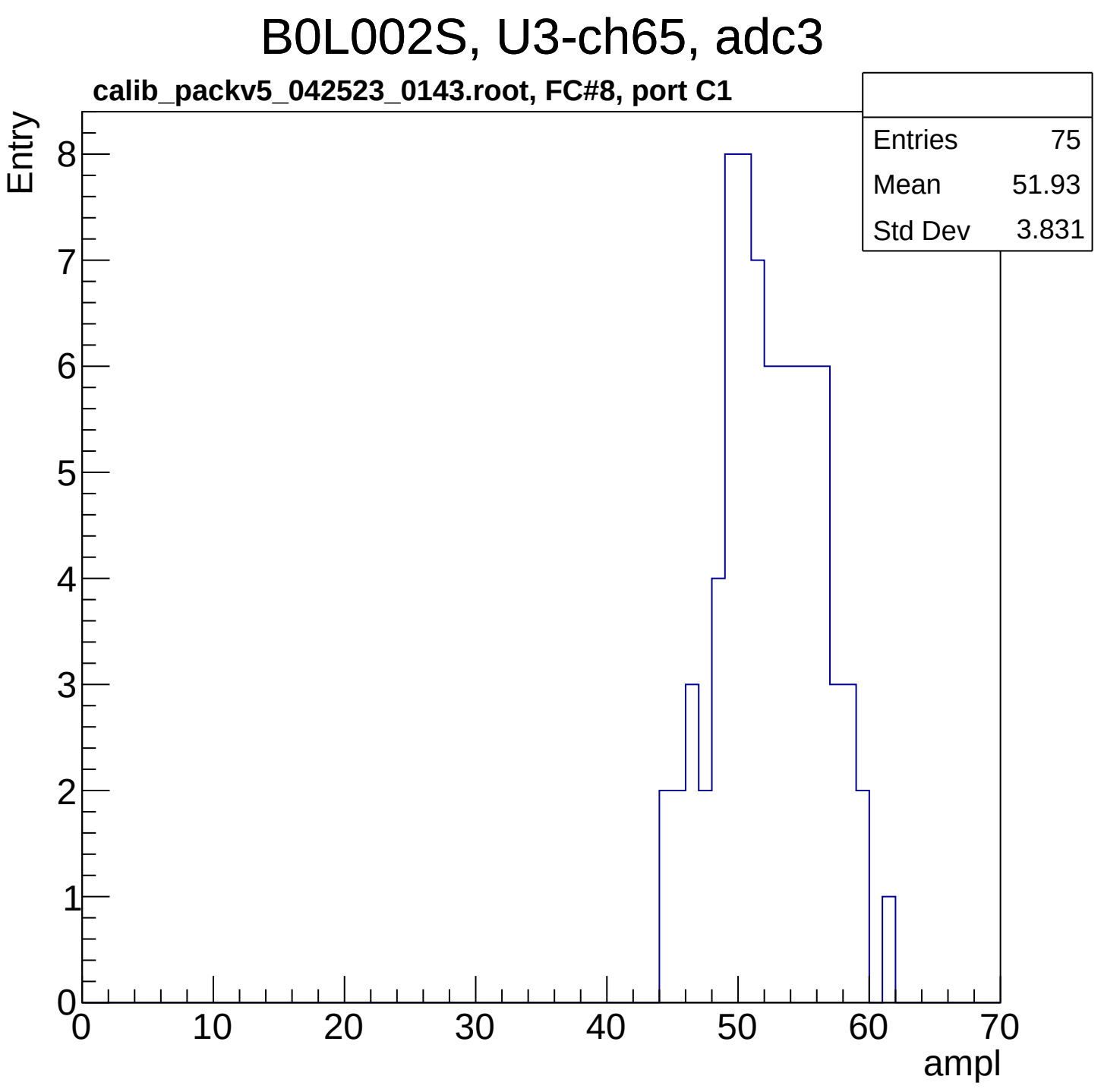

B0L002S, U3-ch65, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 43.65
Std Dev 3.752
Gaus mean : 44.2214
Gaus Width: 4.0883
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch65, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.93
Std Dev 3.831
Entry
ampl
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1
2
3
4
5
6
7
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0
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30
40
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60
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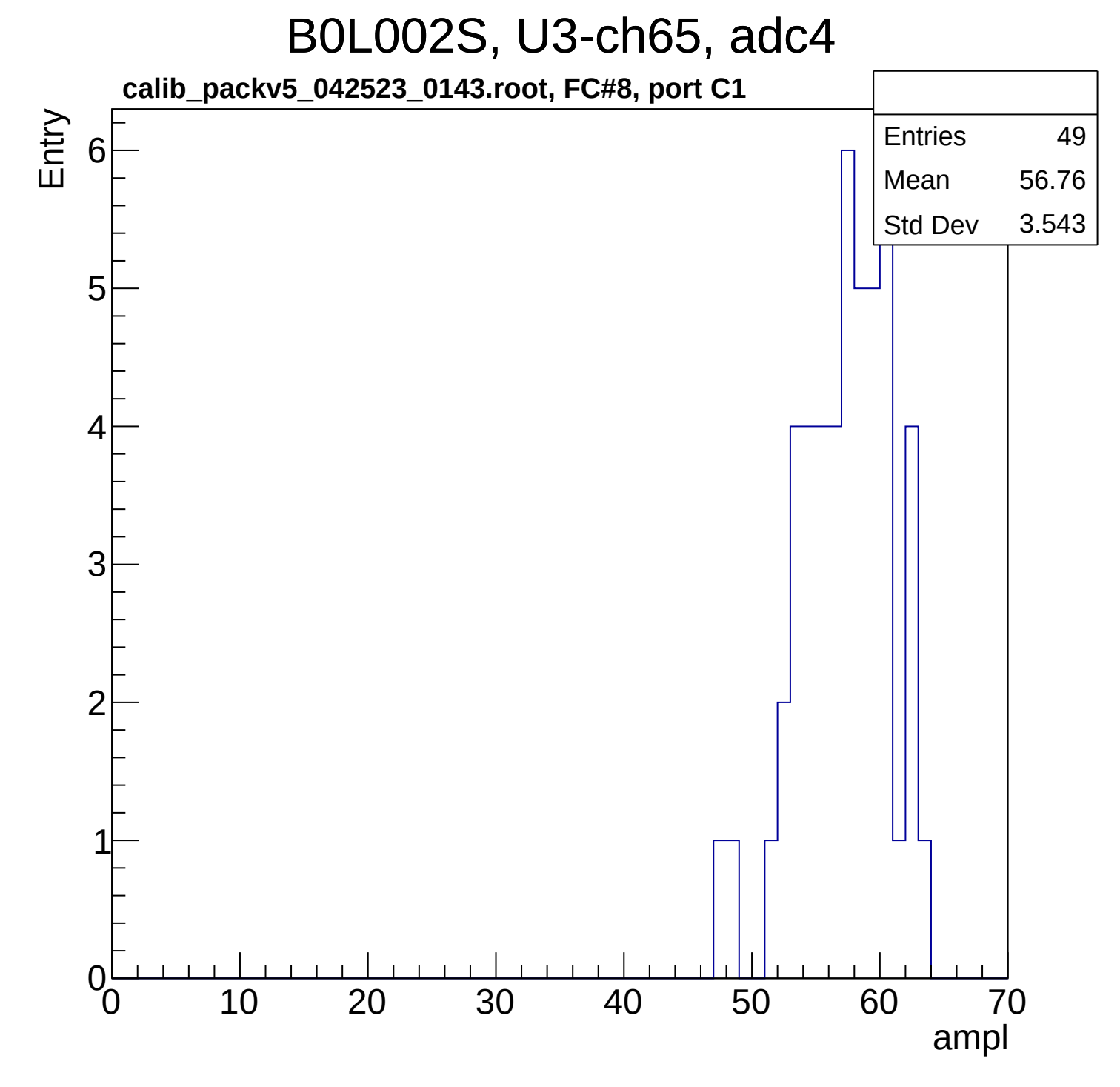
B0L002S, U3-ch65, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 56.76
Std Dev 3.543
Entry
ampl
0
1
2
3
4
5
6
0
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20
30
40
50
60
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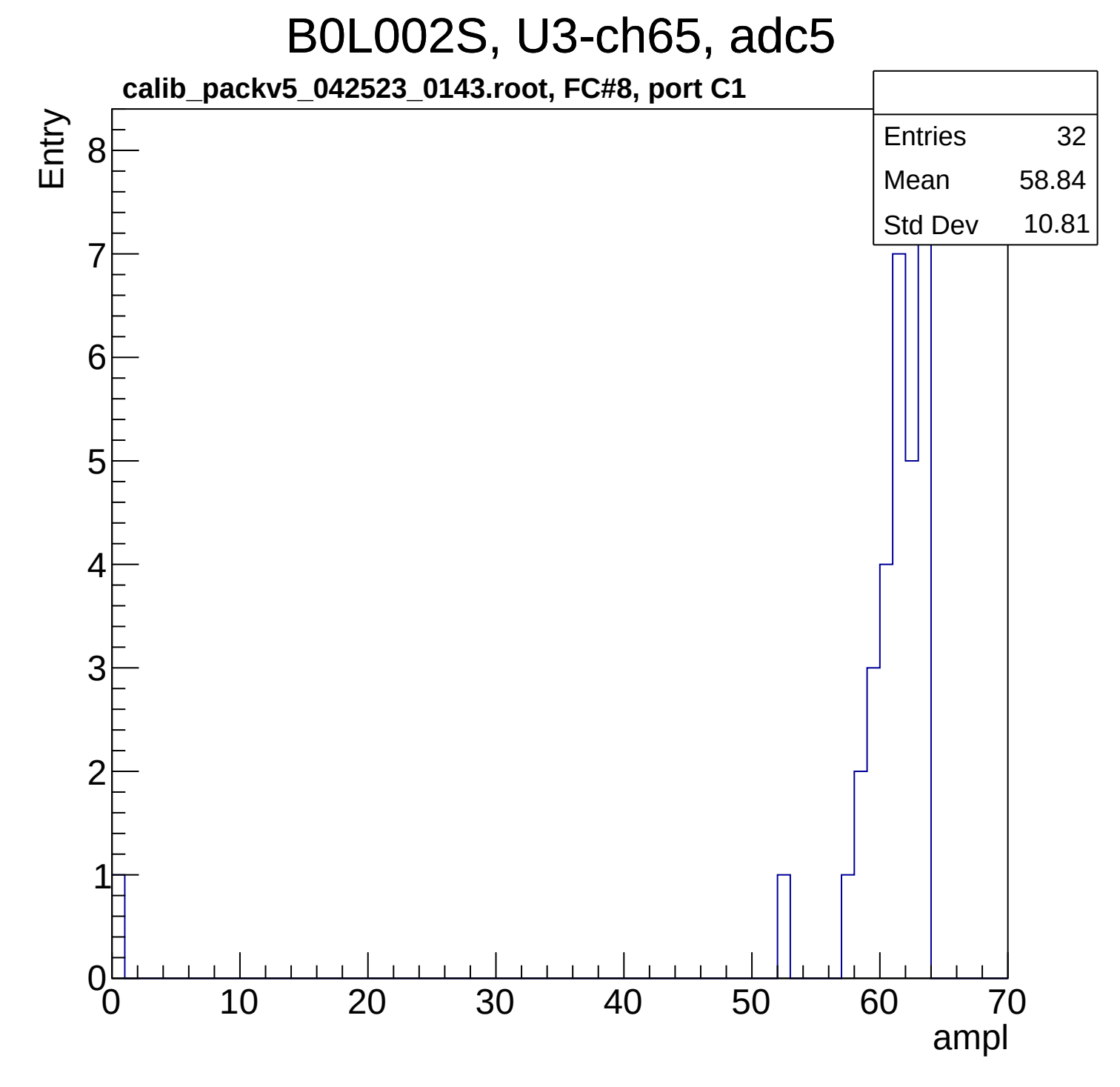
B0L002S, U3-ch65, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.84
Std Dev 10.81
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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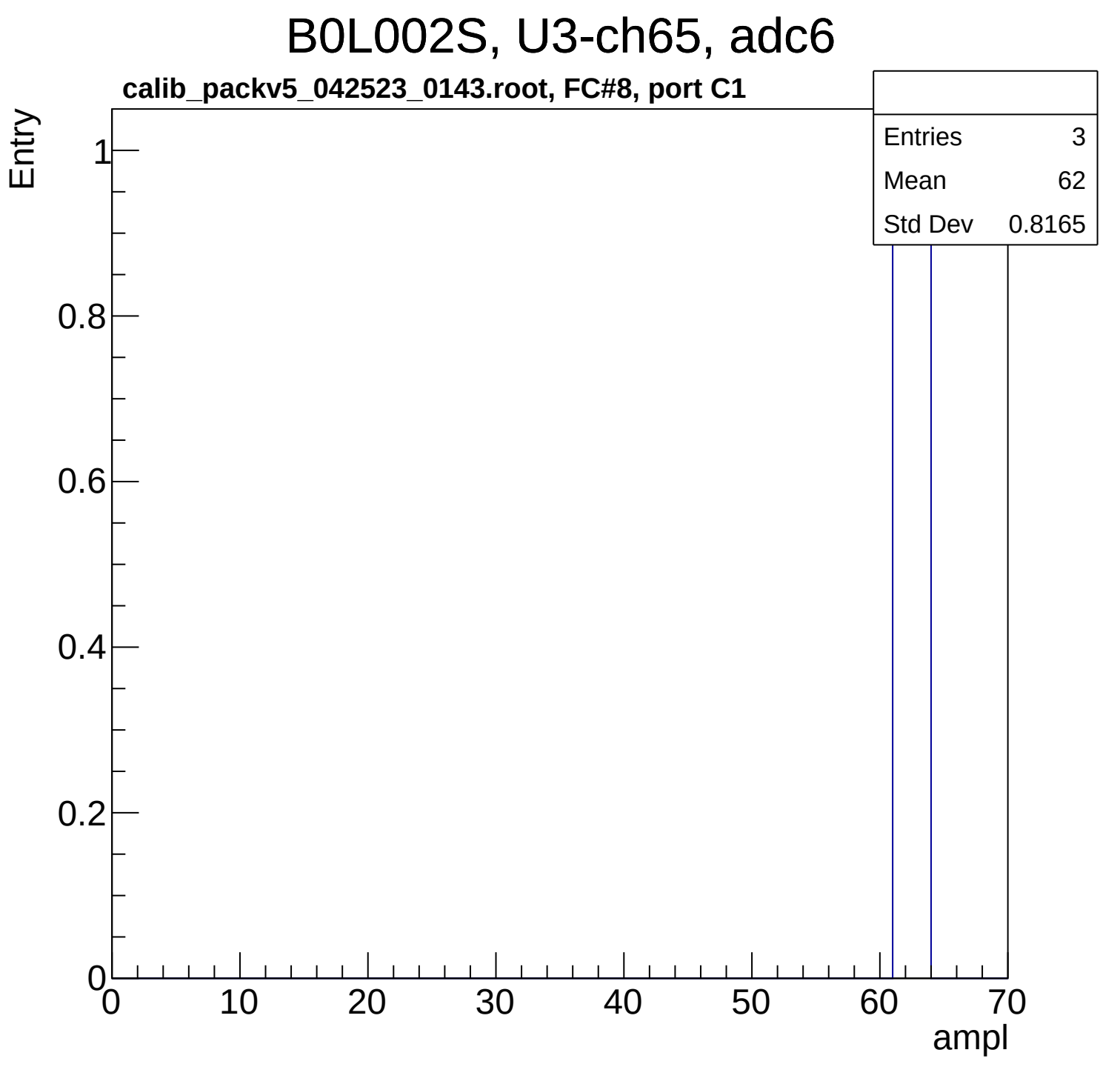
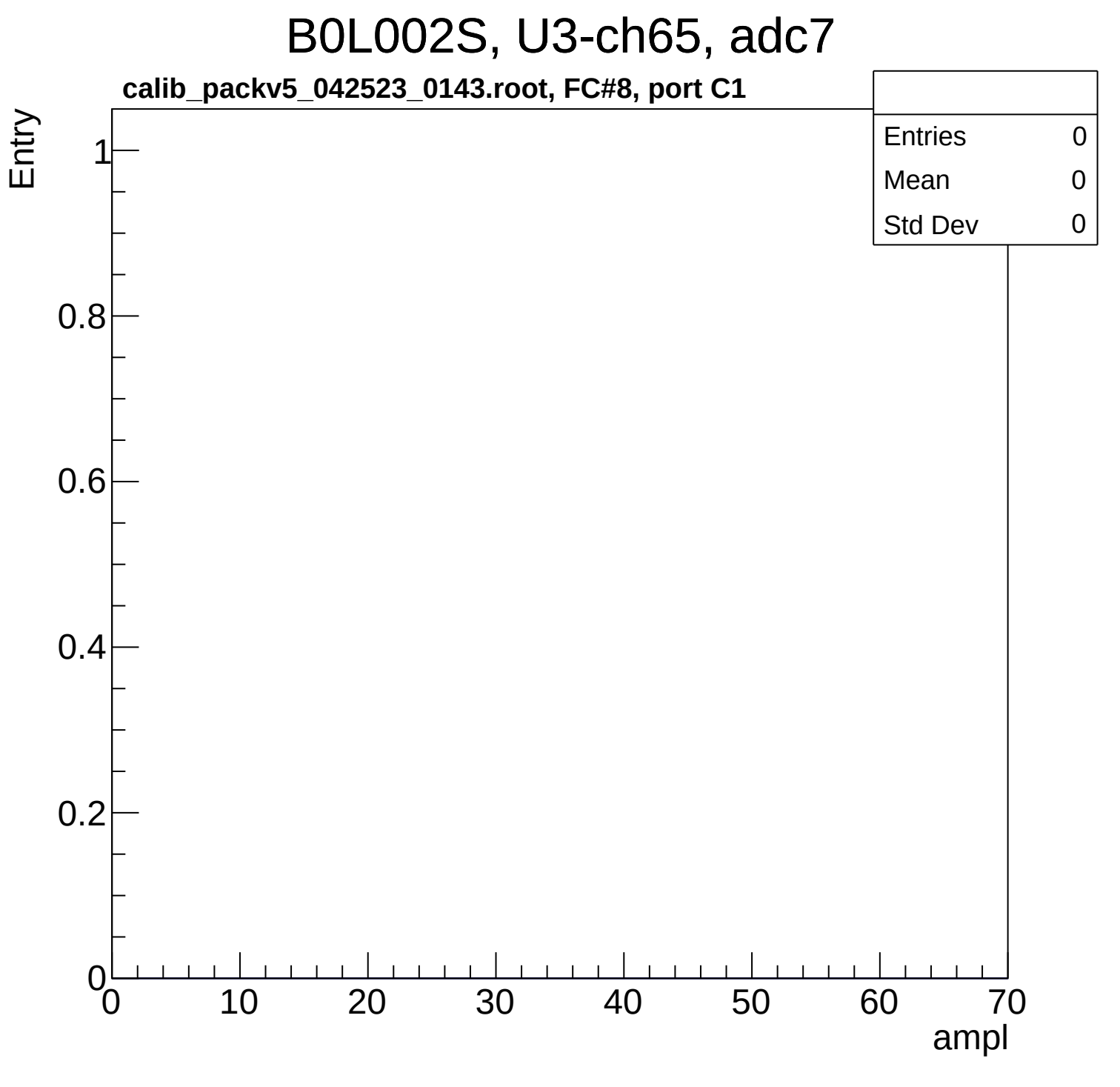

B0L002S, U3-ch65, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch65, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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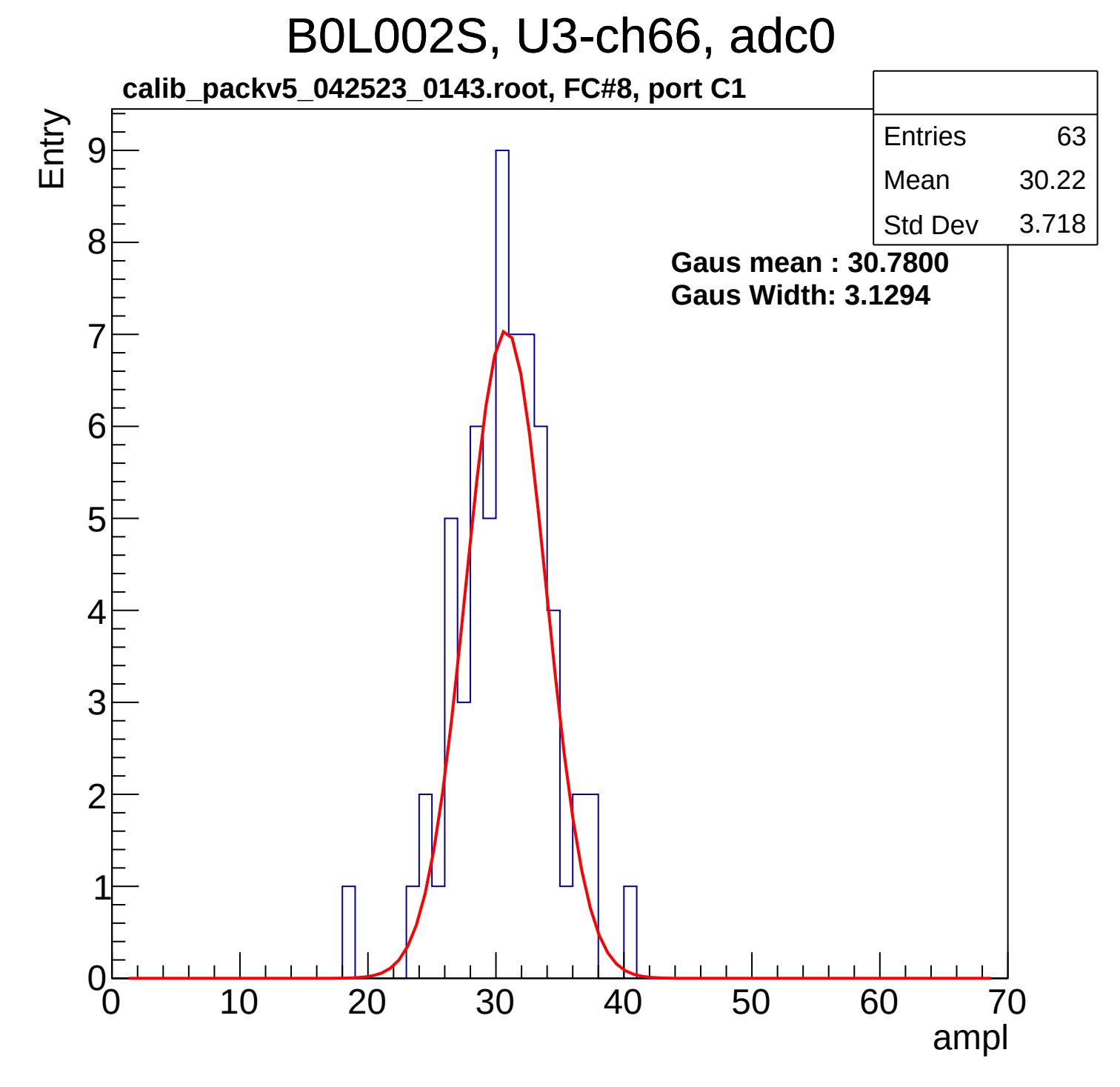
40
50
60
70
ampl


B0L002S, U3-ch66, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.22
Std Dev 3.718
Gaus mean : 30.7800
Gaus Width: 3.1294
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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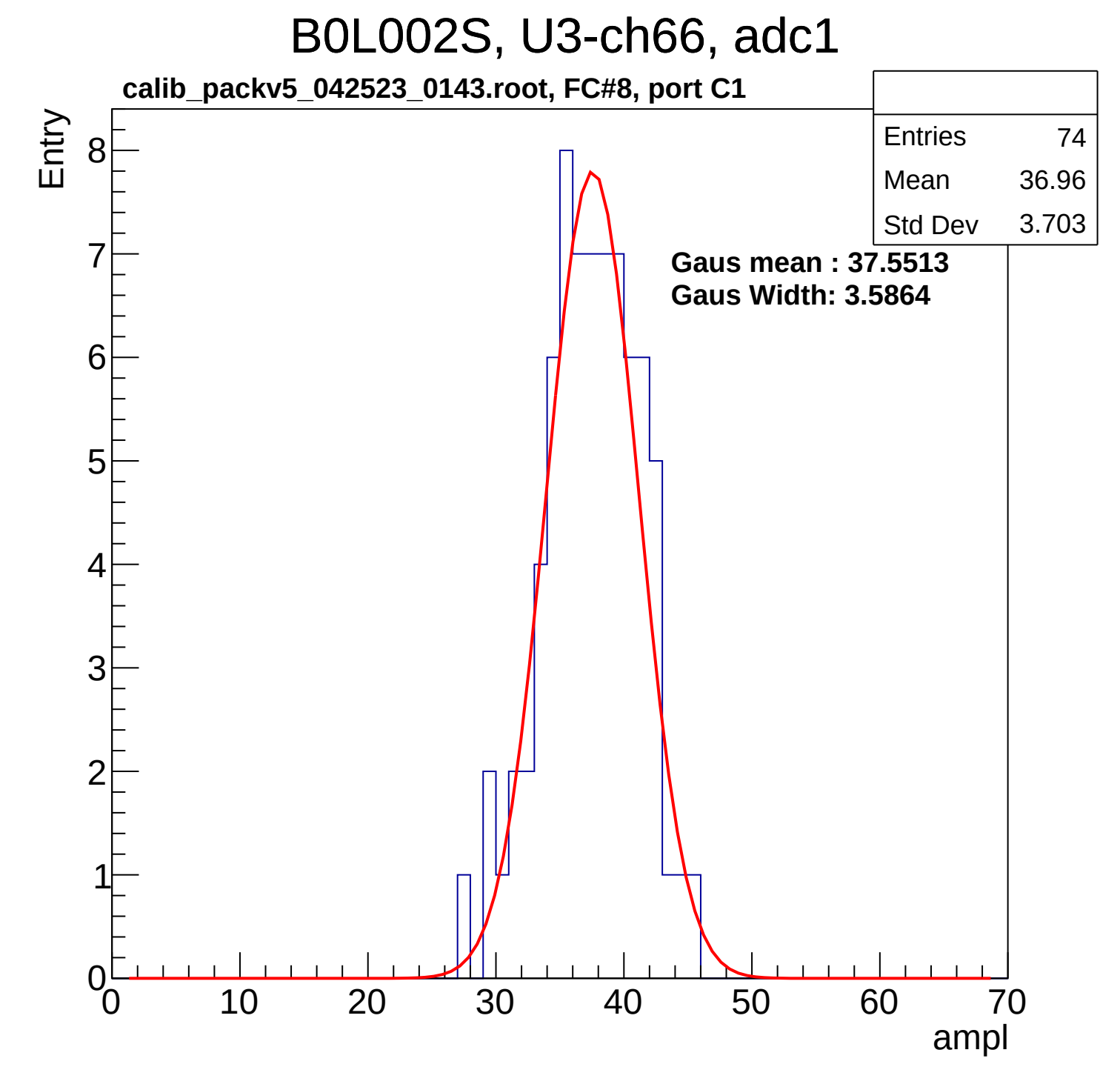
B0L002S, U3-ch66, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 36.96
Std Dev 3.703
Gaus mean : 37.5513
Gaus Width: 3.5864
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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60
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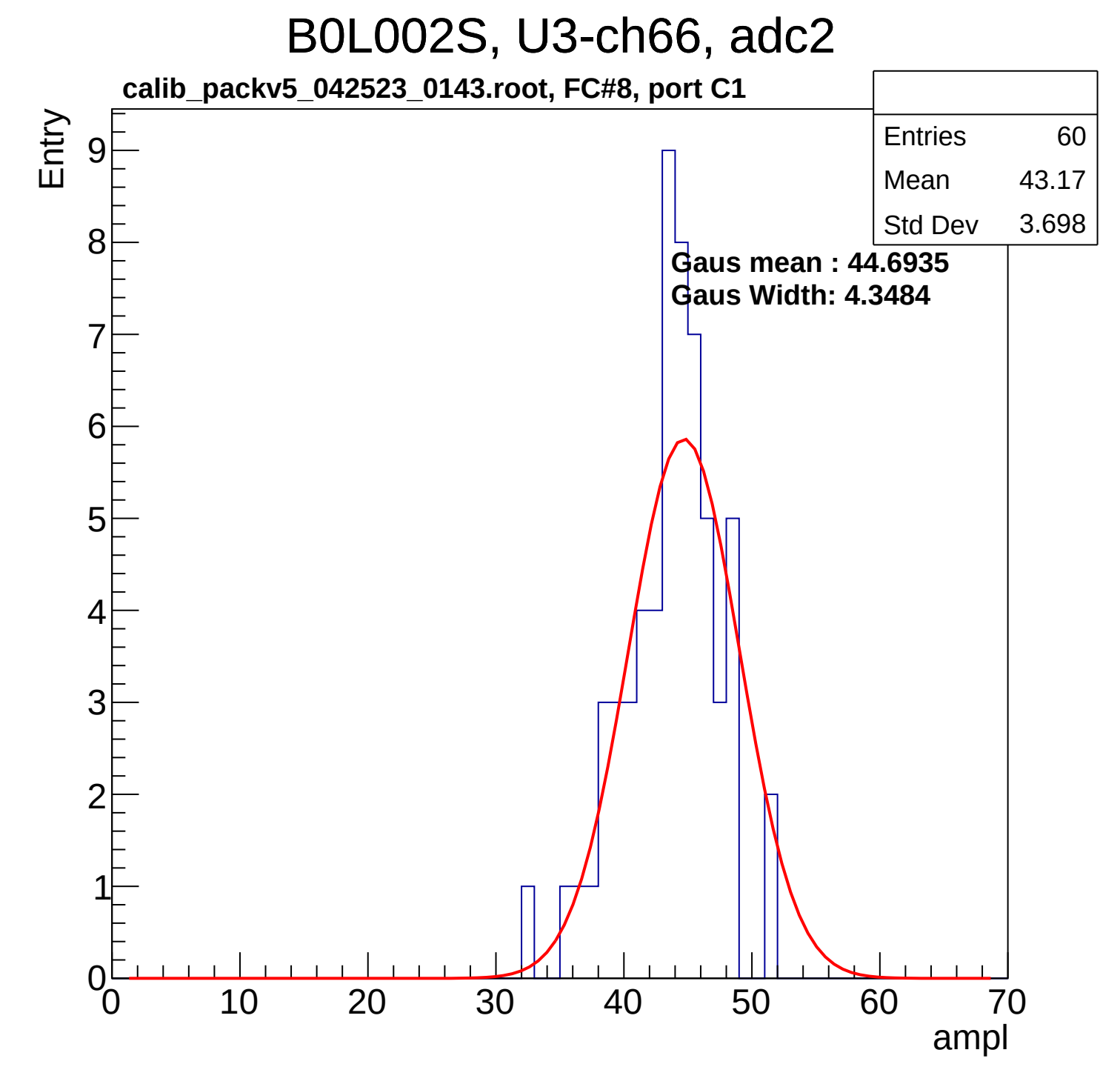
B0L002S, U3-ch66, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 43.17
Std Dev 3.698
Gaus mean : 44.6935
Gaus Width: 4.3484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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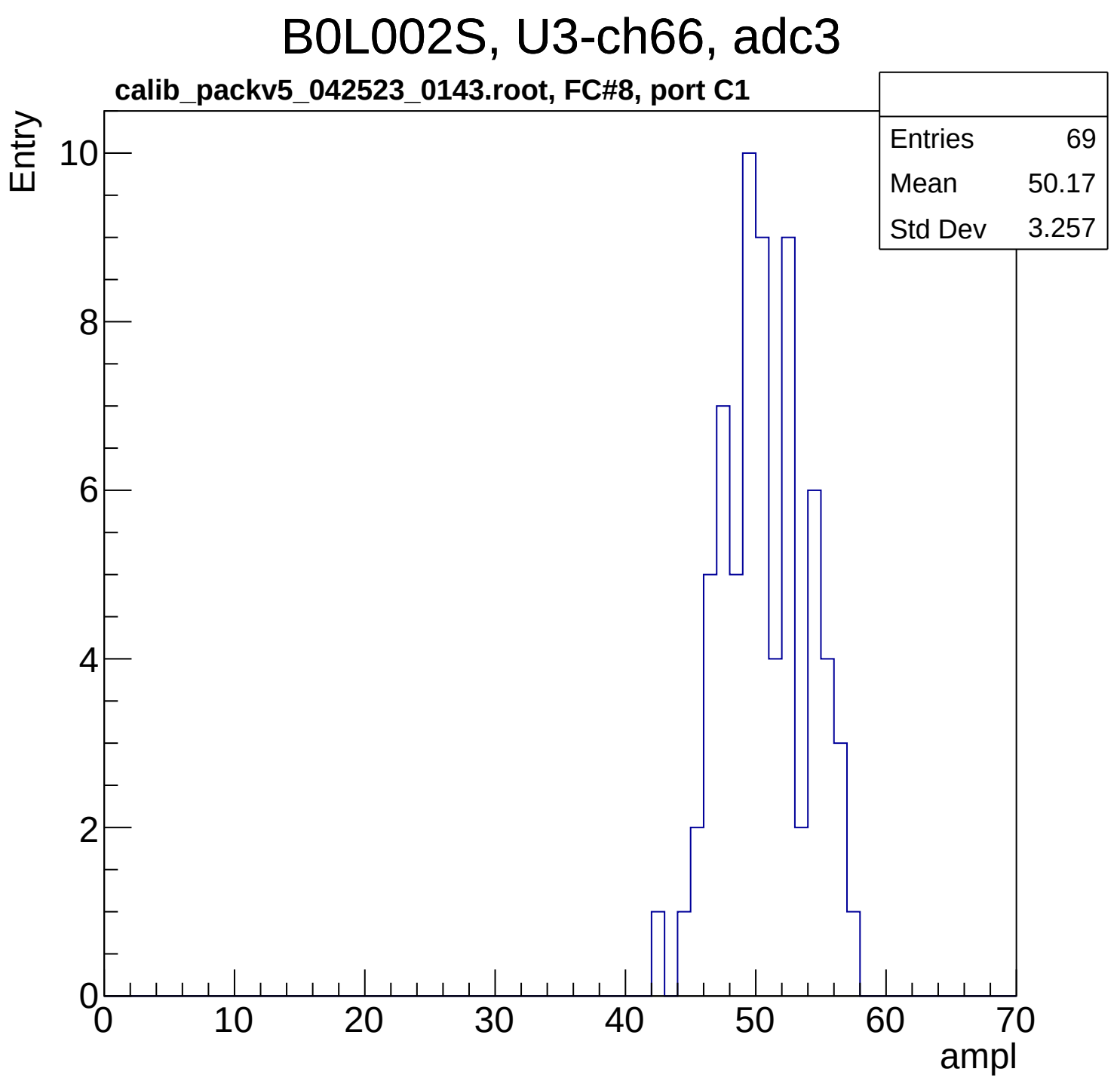
B0L002S, U3-ch66, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 50.17
Std Dev 3.257
Entry
0
2
4
6
8
10
0
10
20
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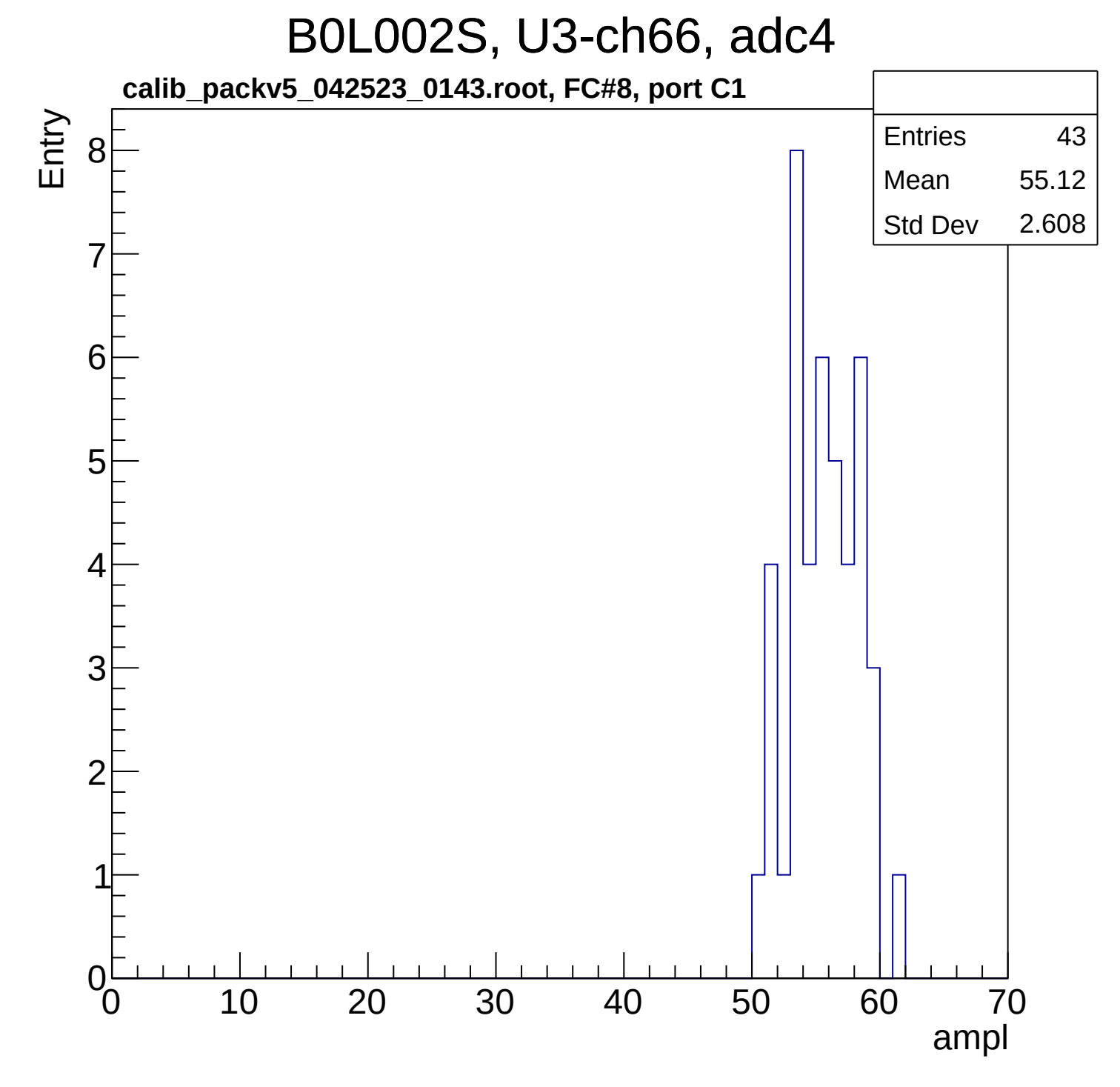
40
50
60
70
ampl


B0L002S, U3-ch66, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 55.12
Std Dev 2.608
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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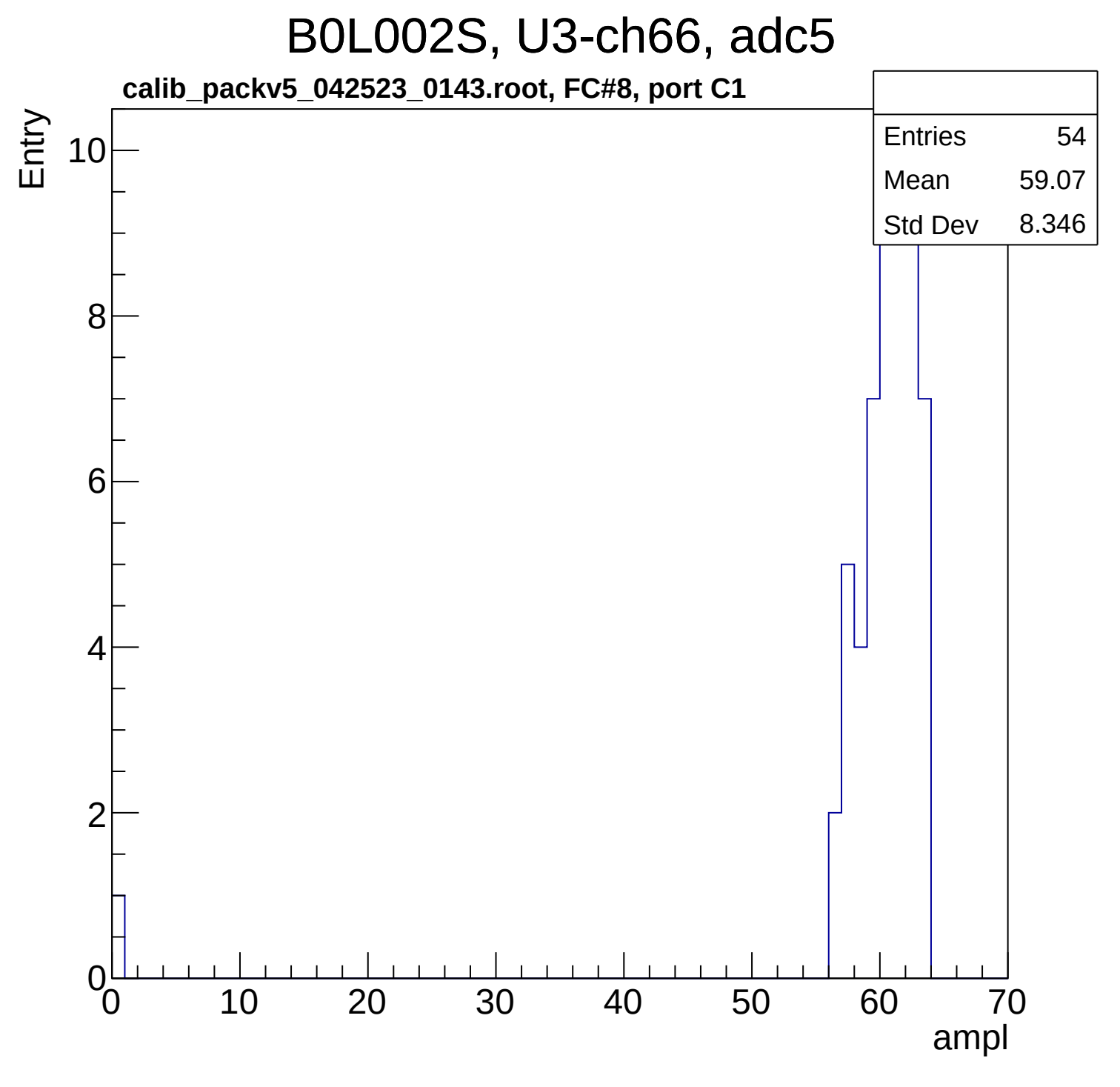
B0L002S, U3-ch66, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 59.07
Std Dev 8.346
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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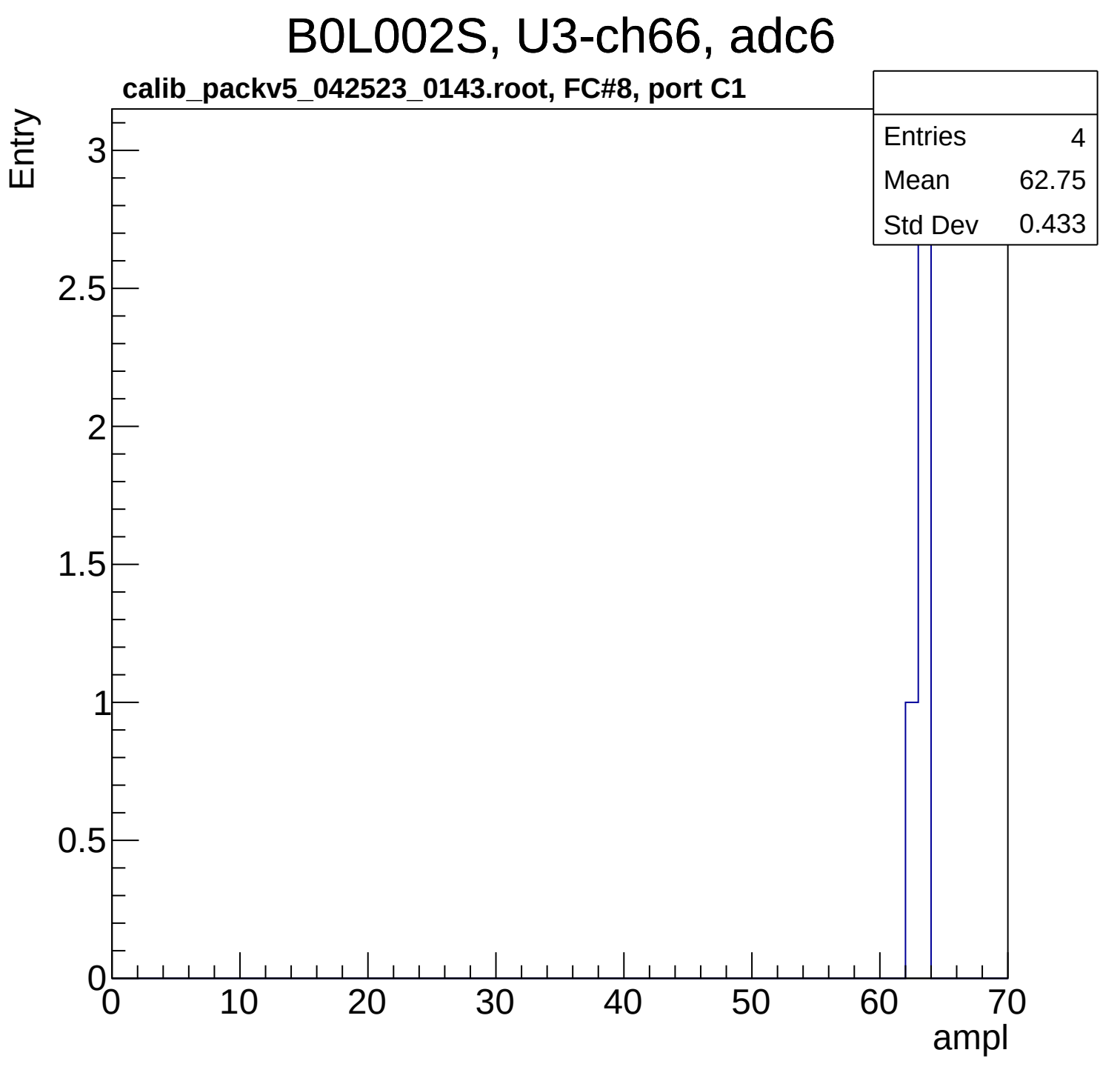
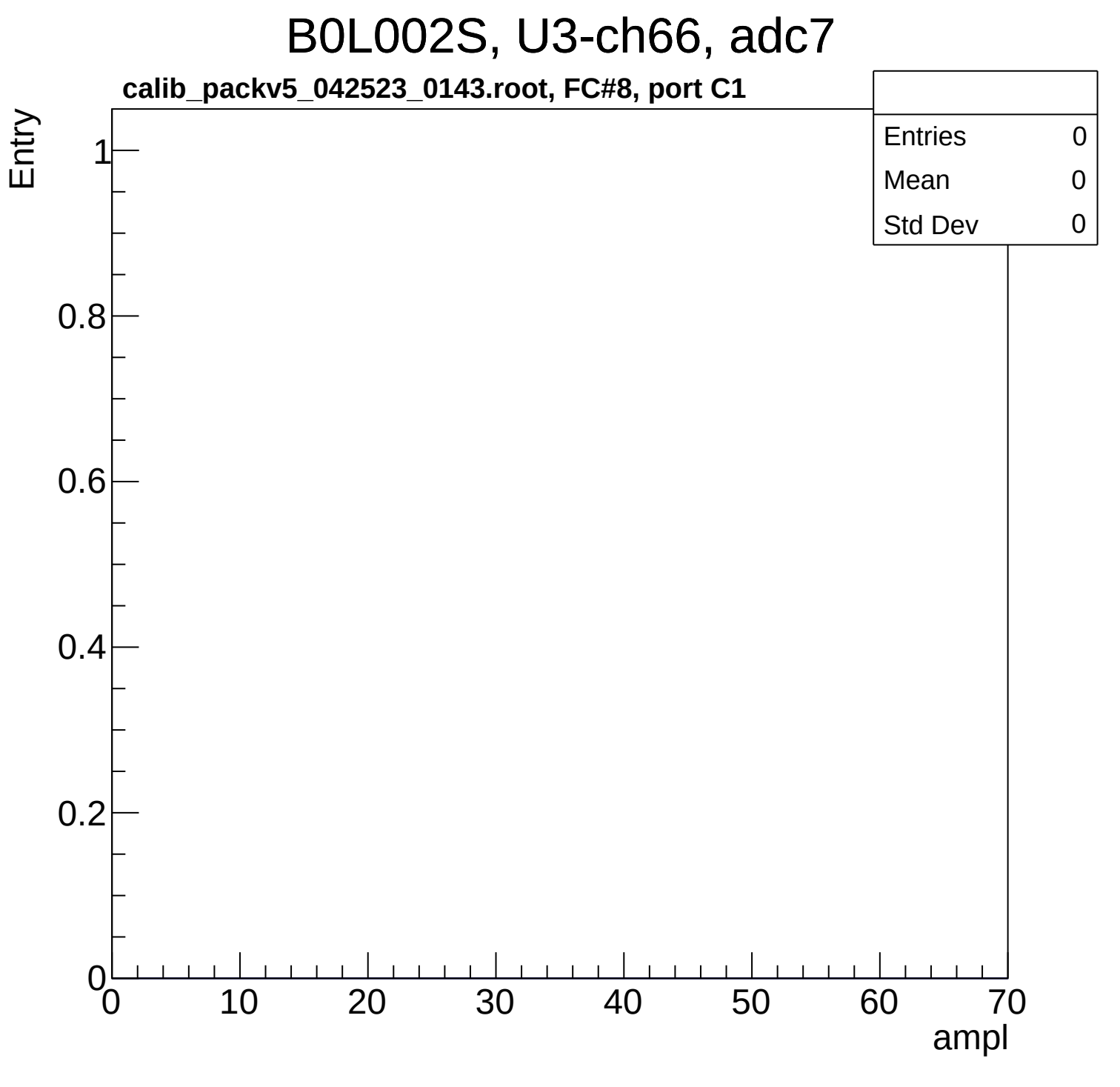
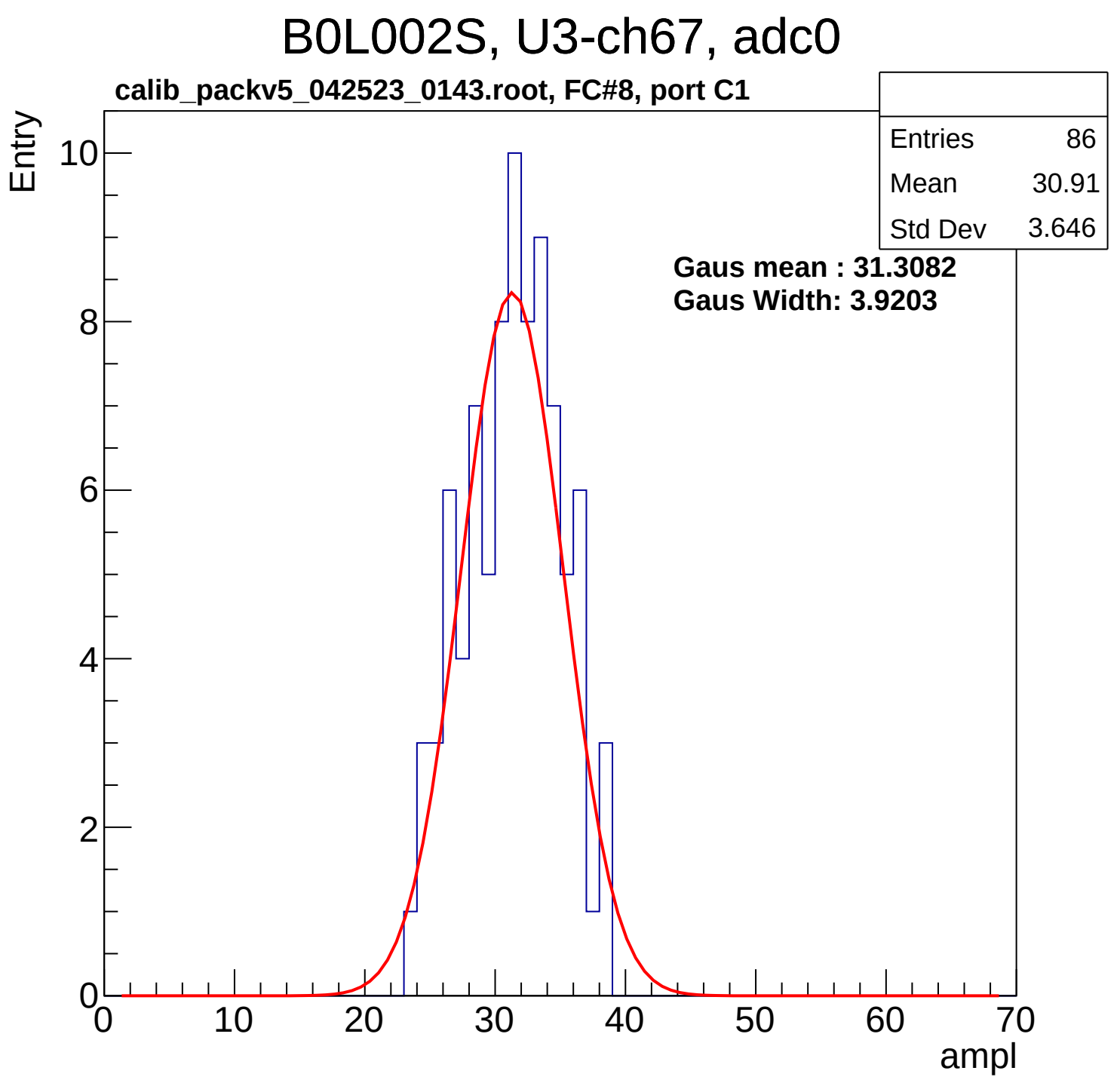
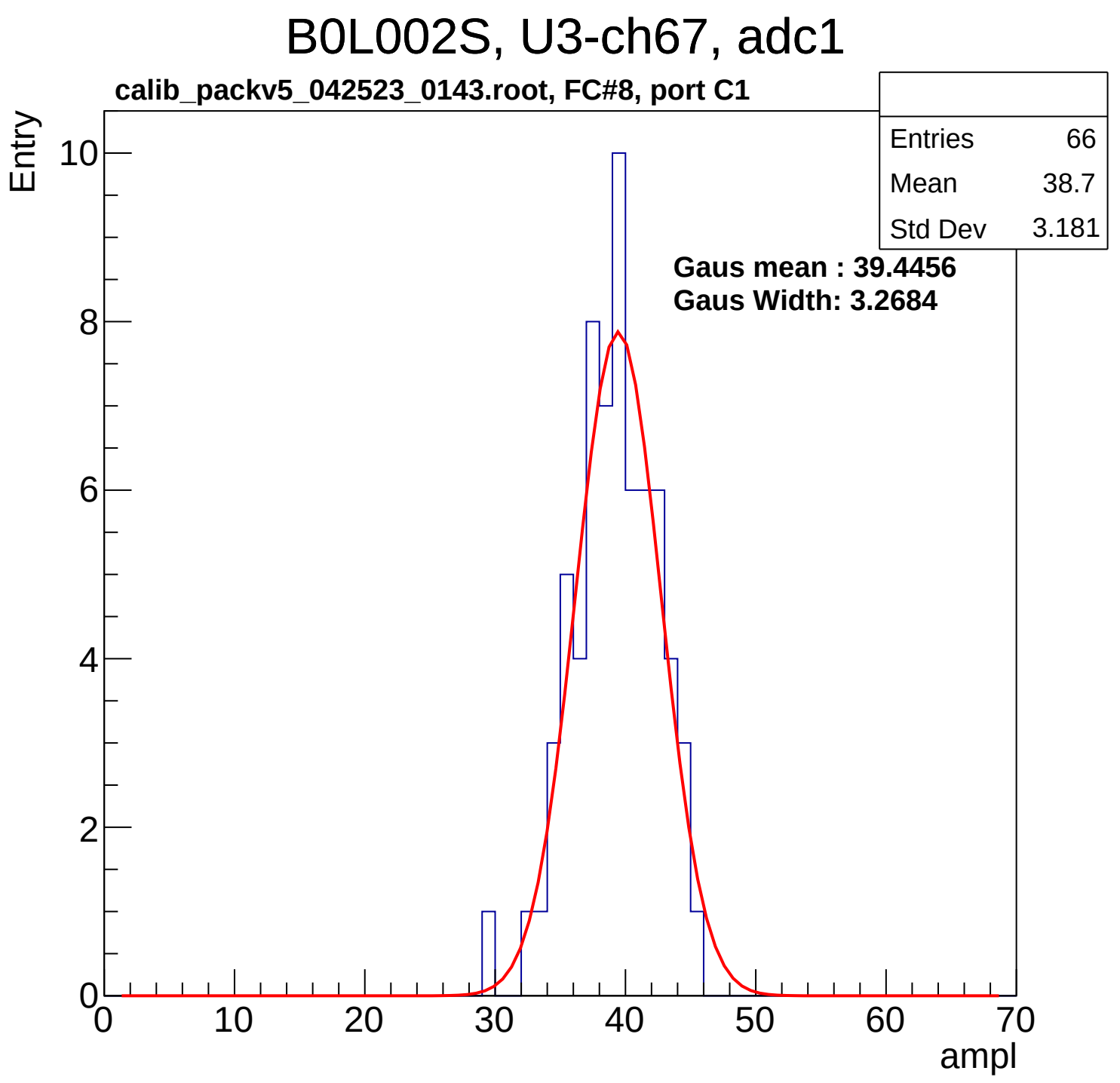
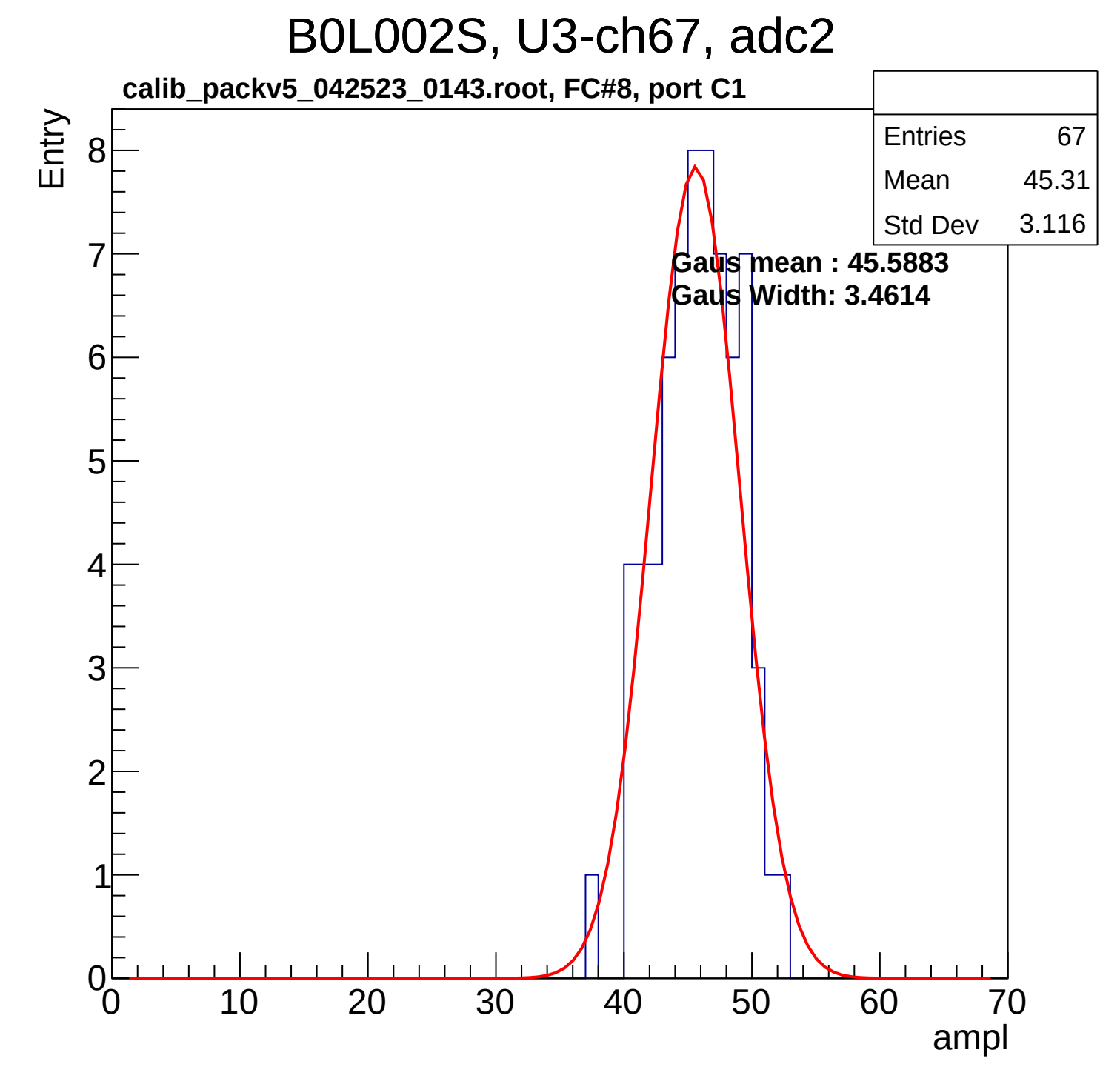

B0L002S, U3-ch66, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch66, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch67, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 30.91
Std Dev 3.646
Gaus mean : 31.3082
Gaus Width: 3.9203
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch67, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 38.7
Std Dev 3.181
Gaus mean : 39.4456
Gaus Width: 3.2684
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch67, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 45.31
Std Dev 3.116
Gaus mean : 45.5883
Gaus Width: 3.4614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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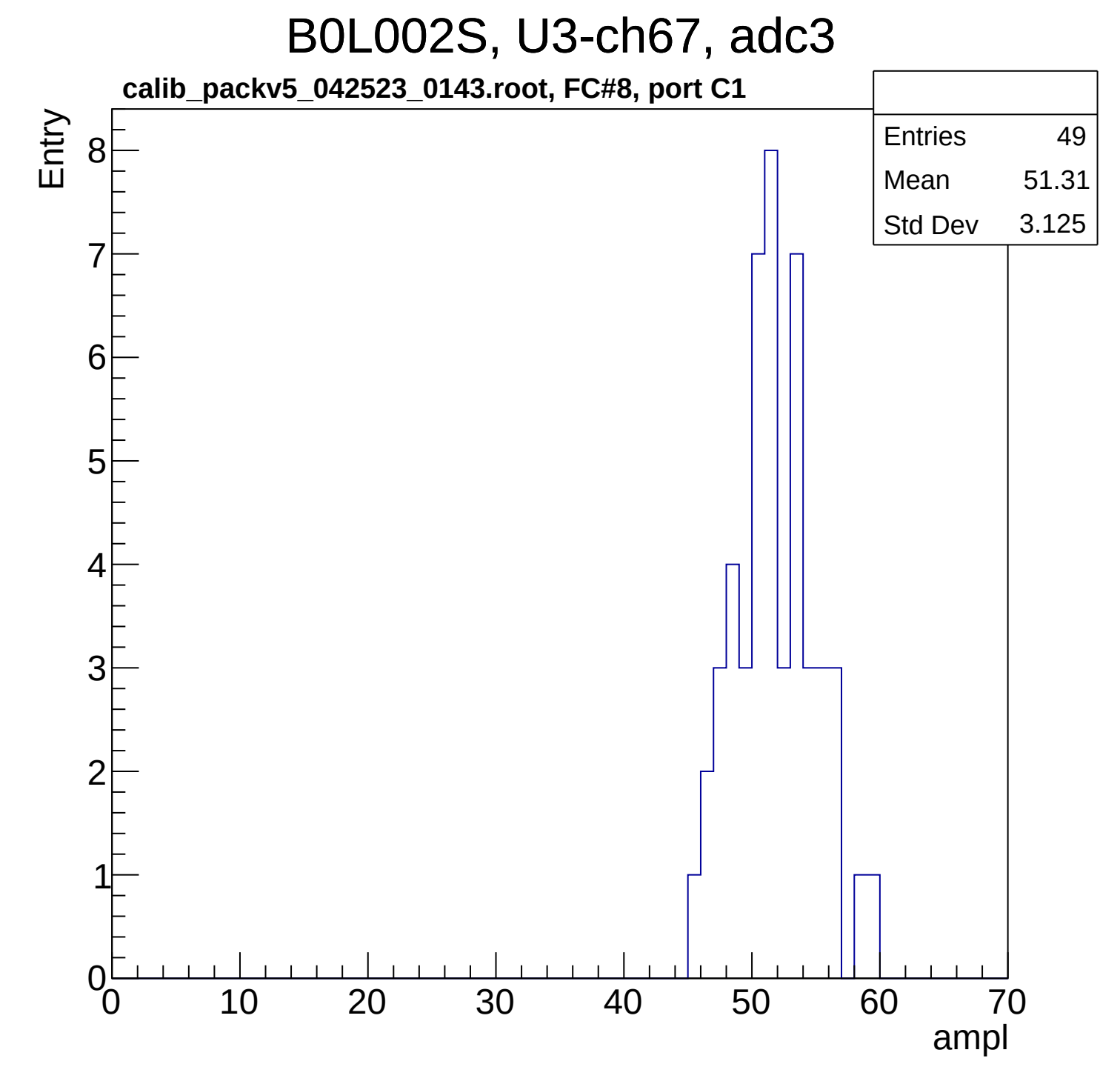
B0L002S, U3-ch67, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 51.31
Std Dev 3.125
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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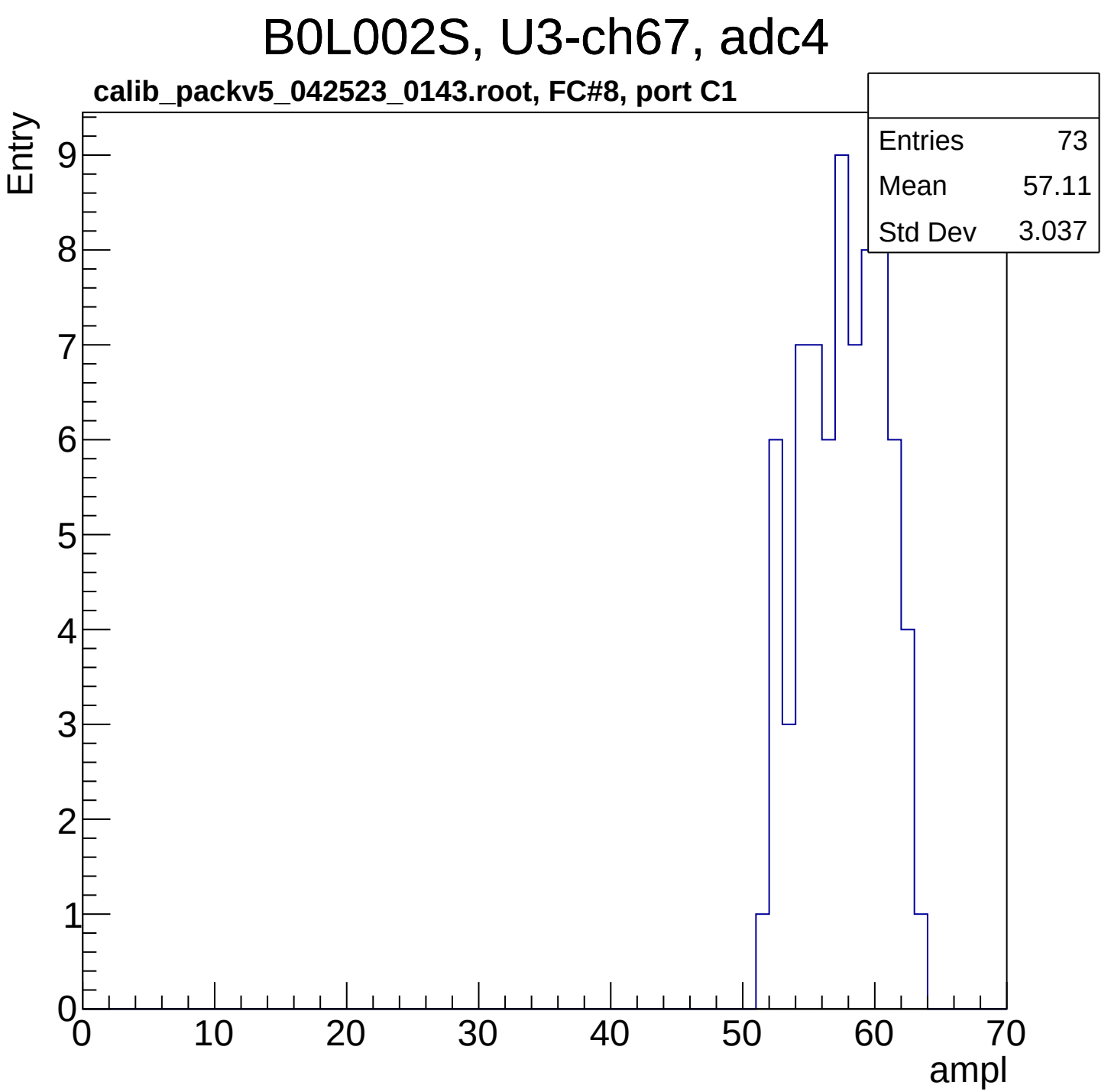
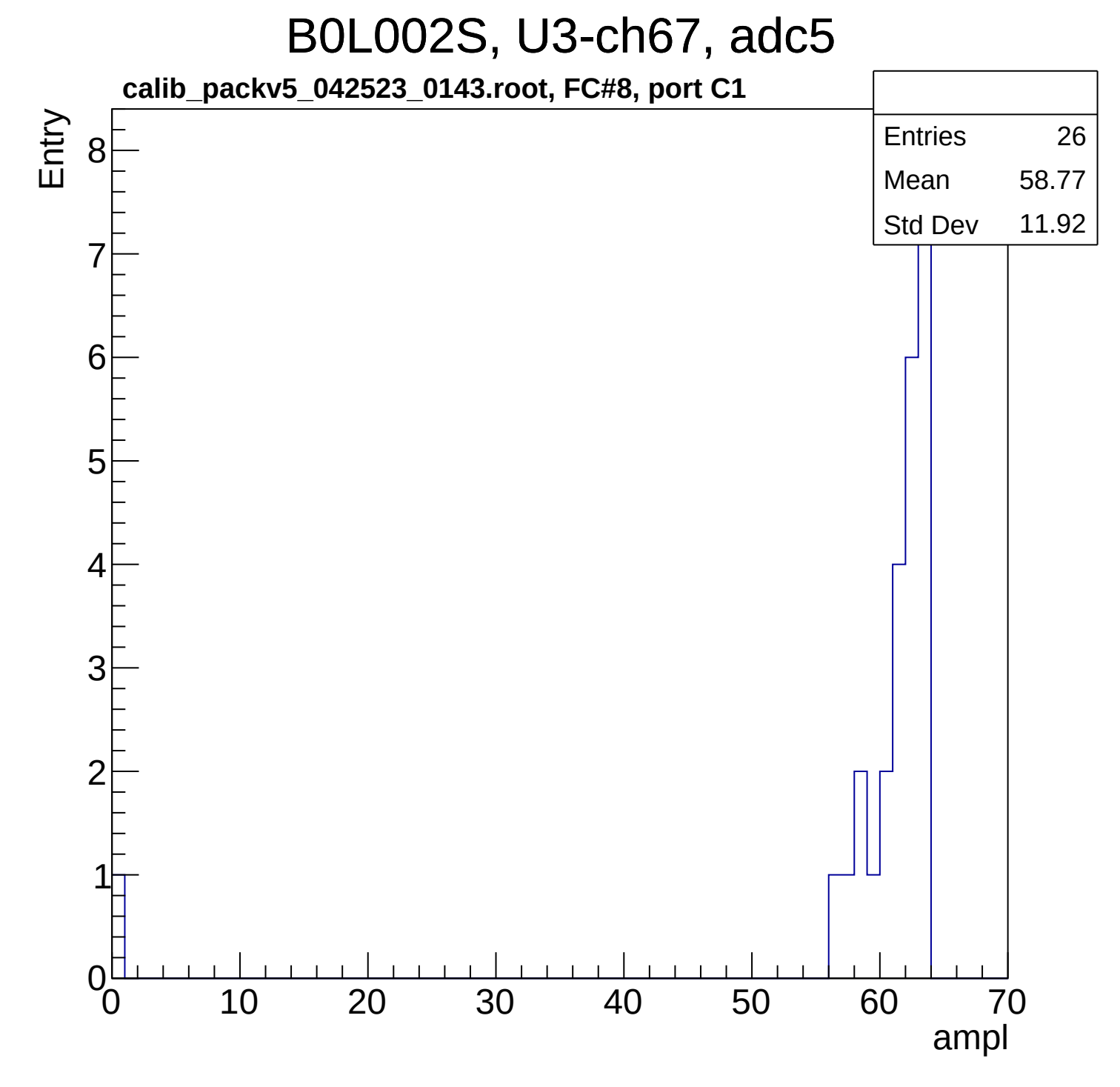

B0L002S, U3-ch67, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 57.11
Std Dev 3.037
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch67, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.77
Std Dev 11.92
Entry
ampl
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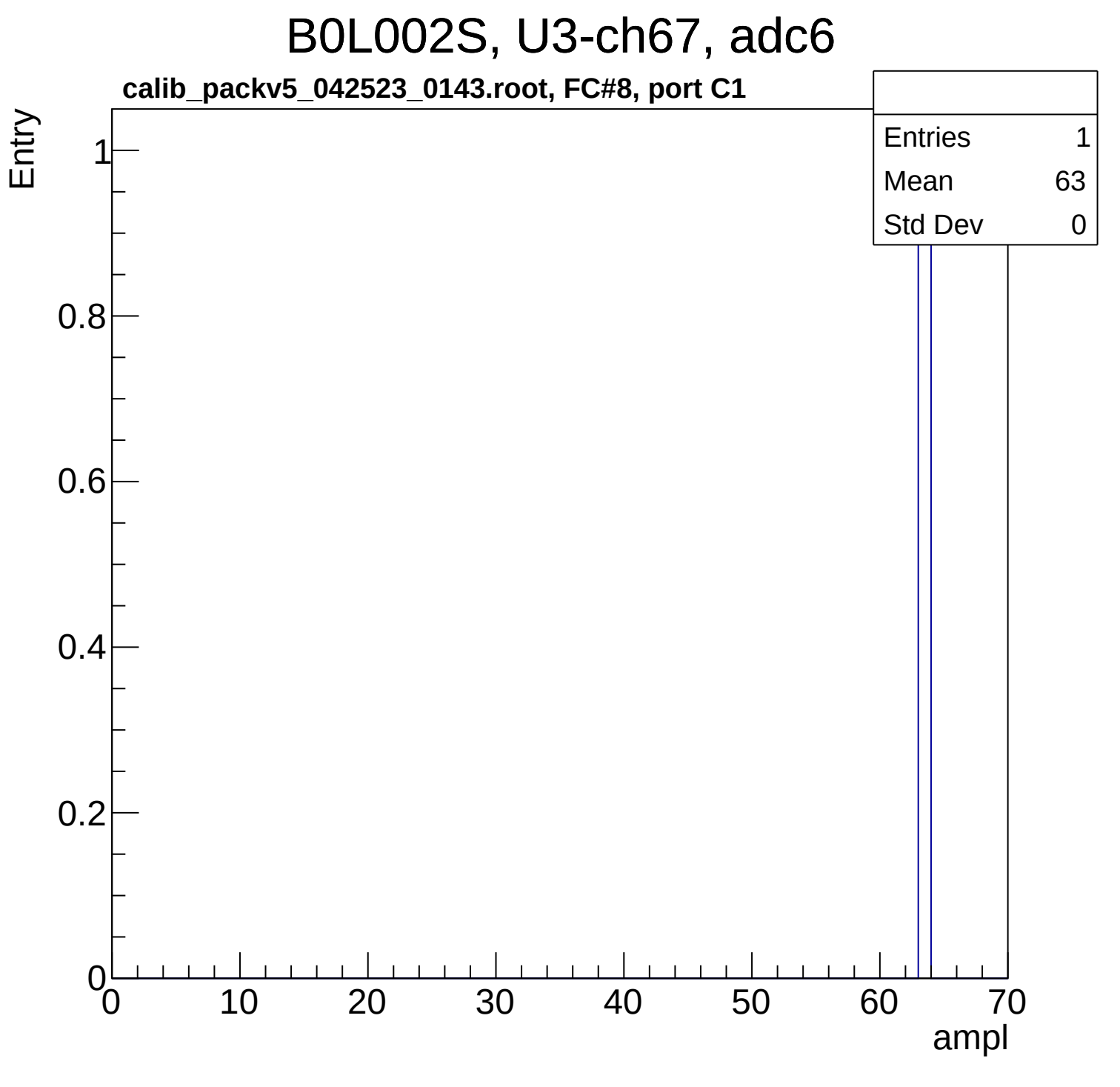
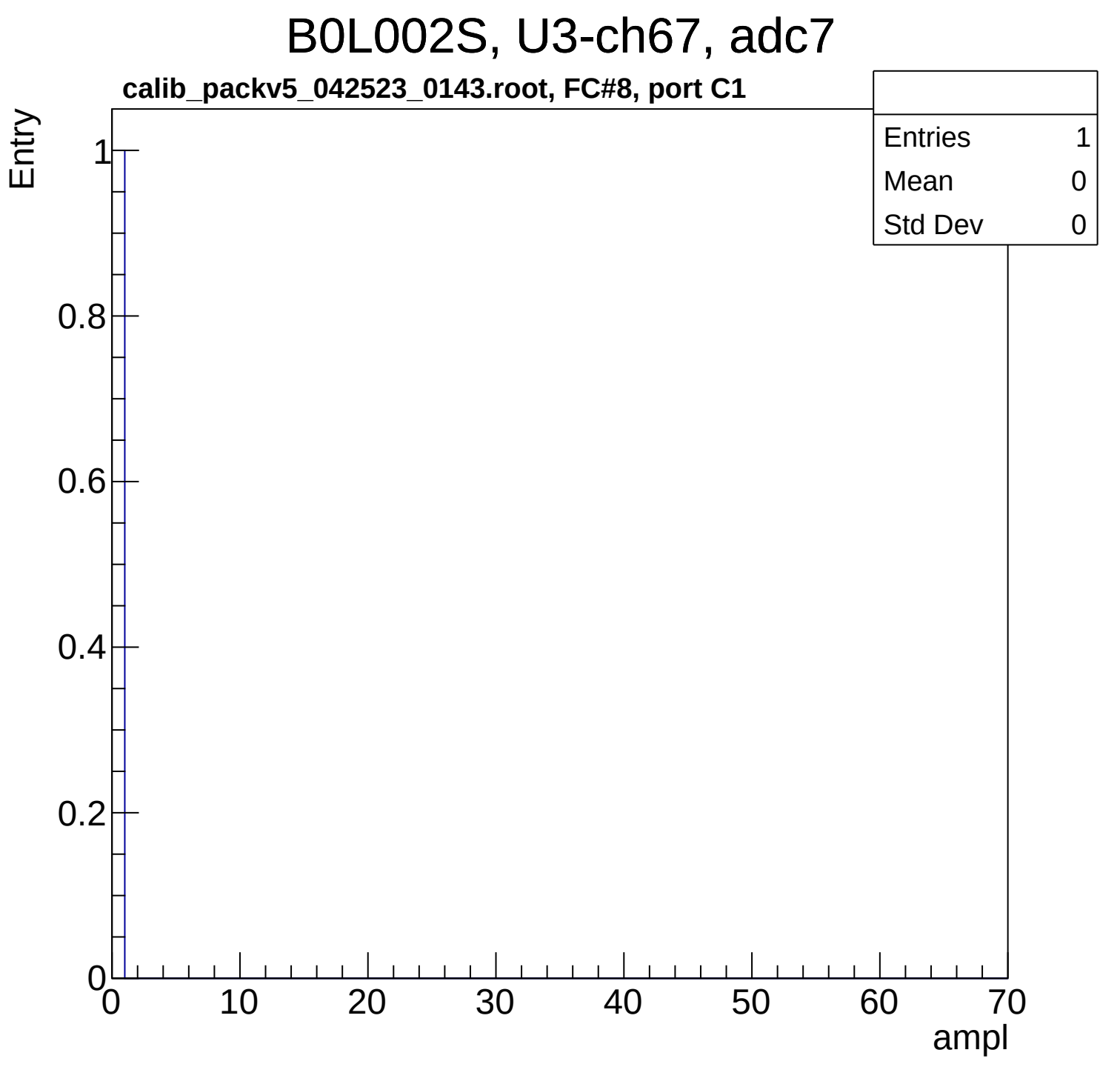
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch67, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch67, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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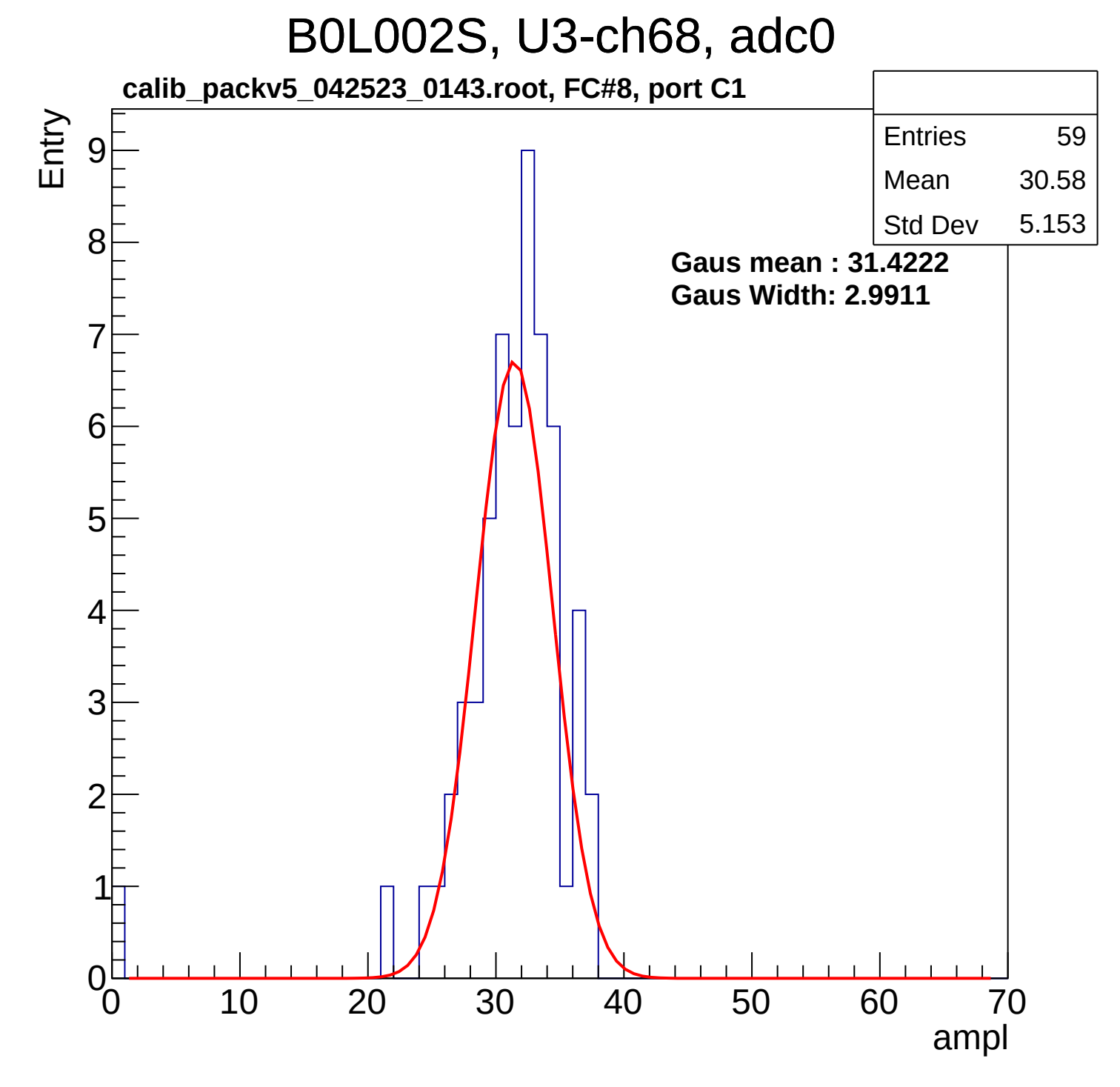
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch68, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 30.58
Std Dev 5.153
Gaus mean : 31.4222
Gaus Width: 2.9911
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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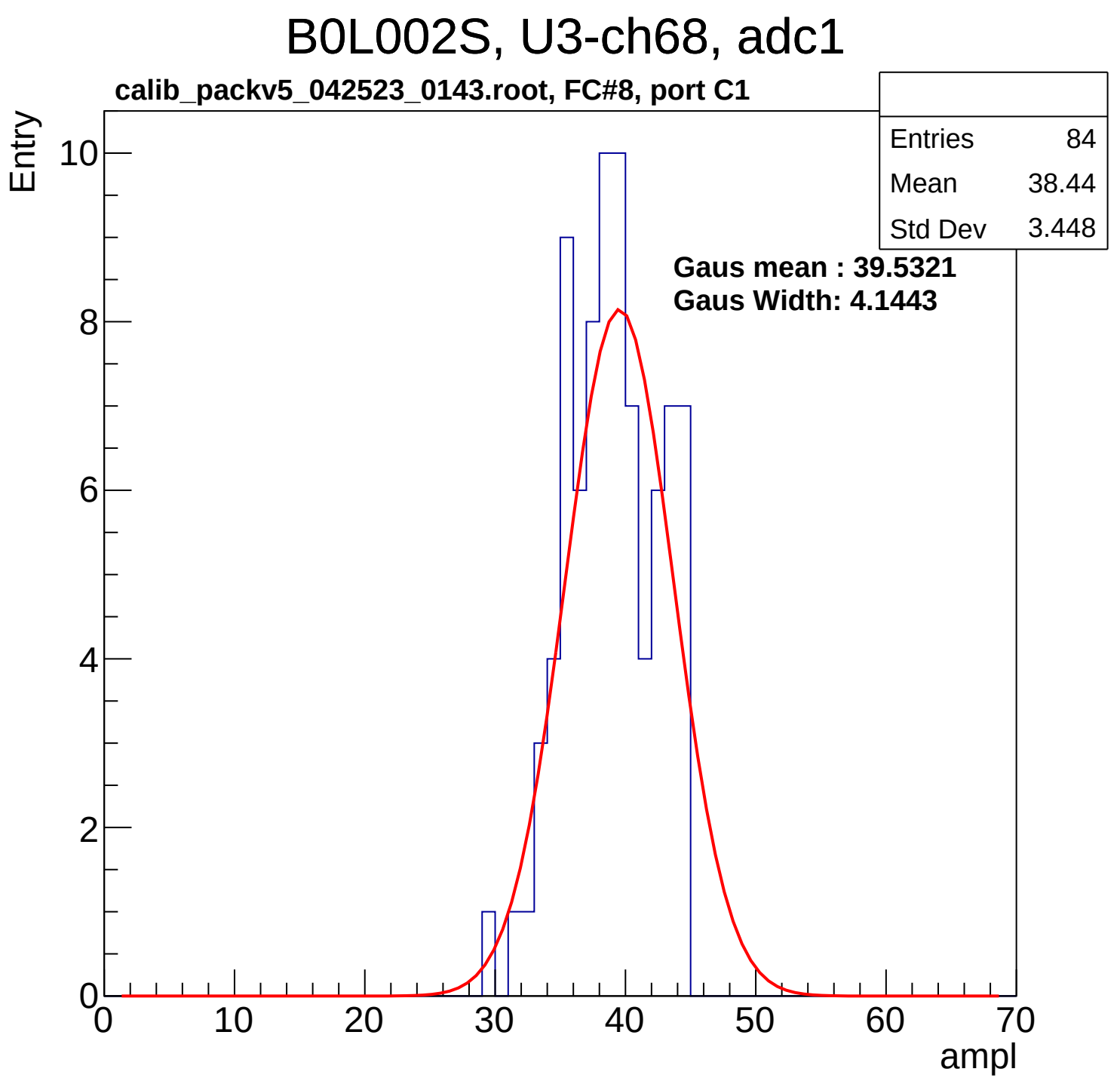
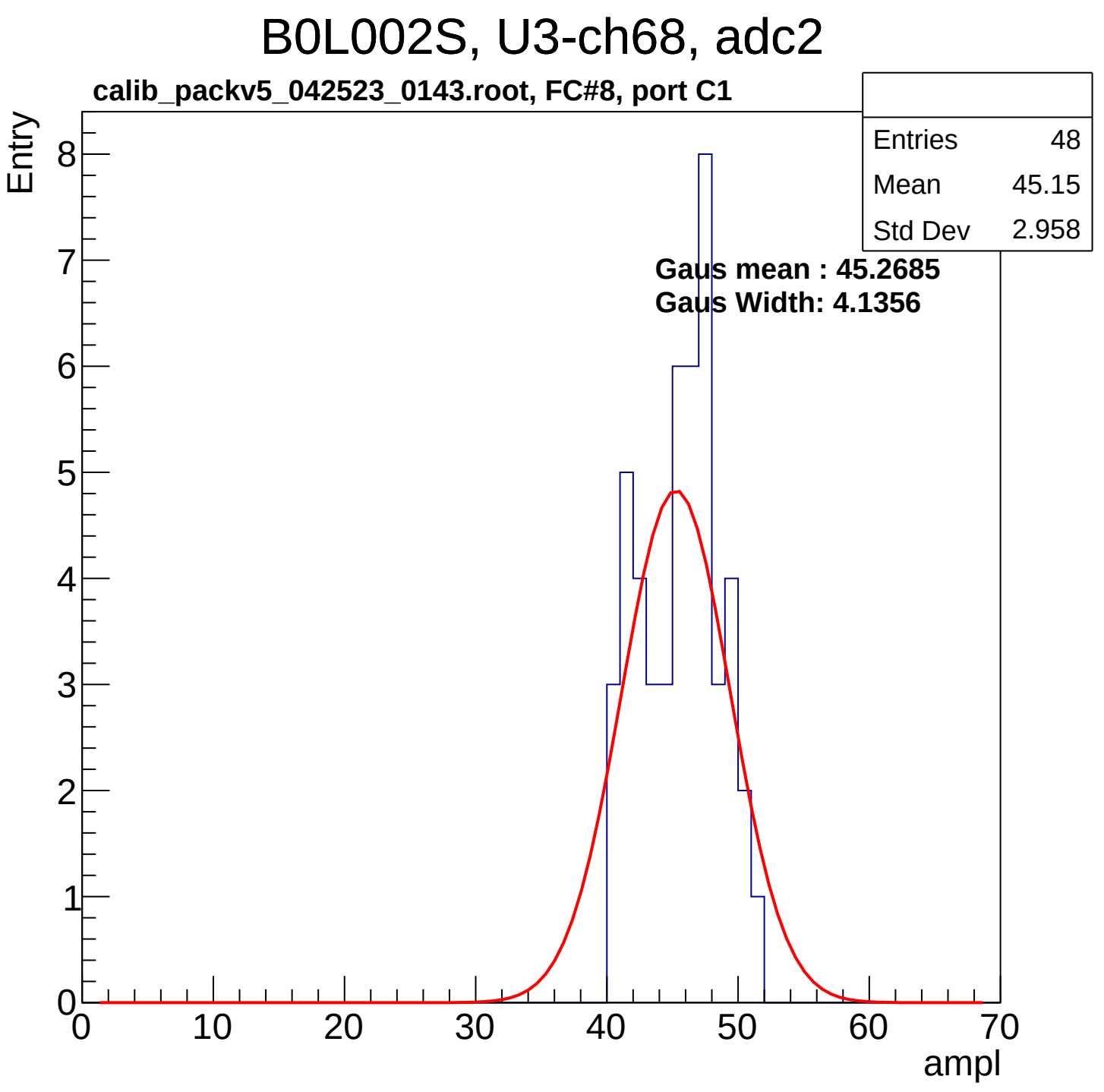

B0L002S, U3-ch68, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 38.44
Std Dev 3.448
Gaus mean : 39.5321
Gaus Width: 4.1443
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch68, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 45.15
Std Dev 2.958
Gaus mean : 45.2685
Gaus Width: 4.1356
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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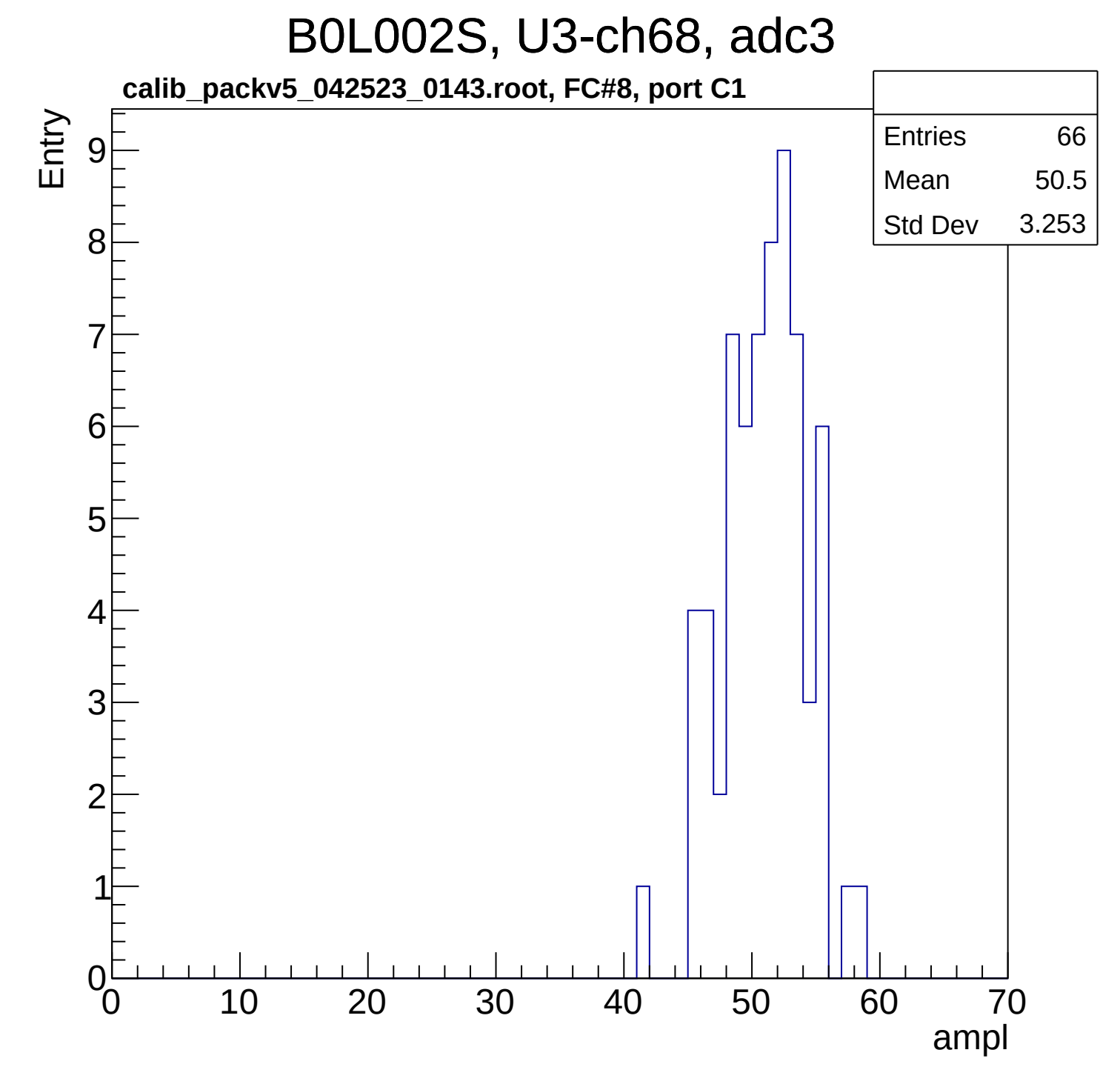
B0L002S, U3-ch68, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.5
Std Dev 3.253
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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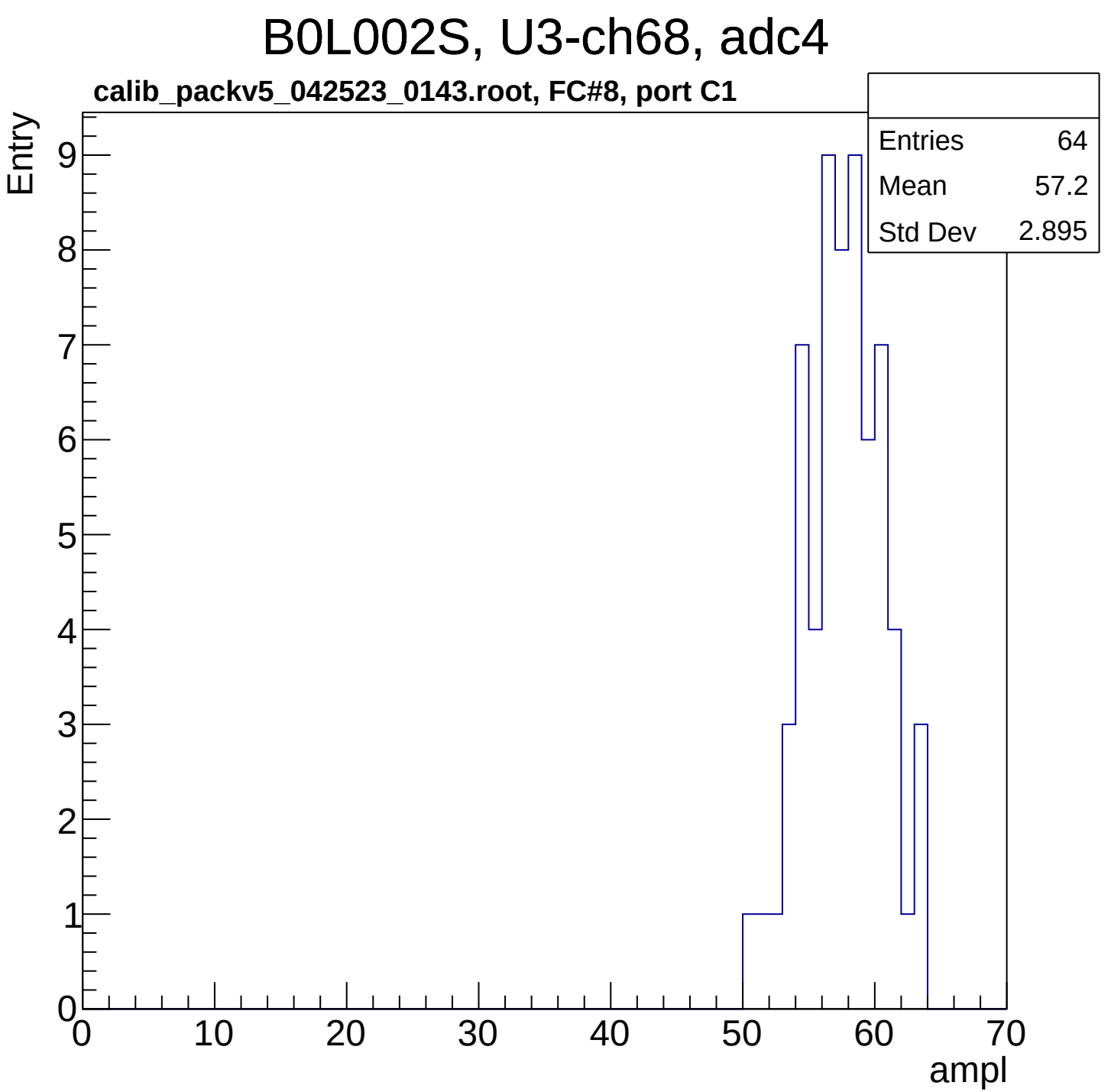
B0L002S, U3-ch68, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 57.2
Std Dev 2.895
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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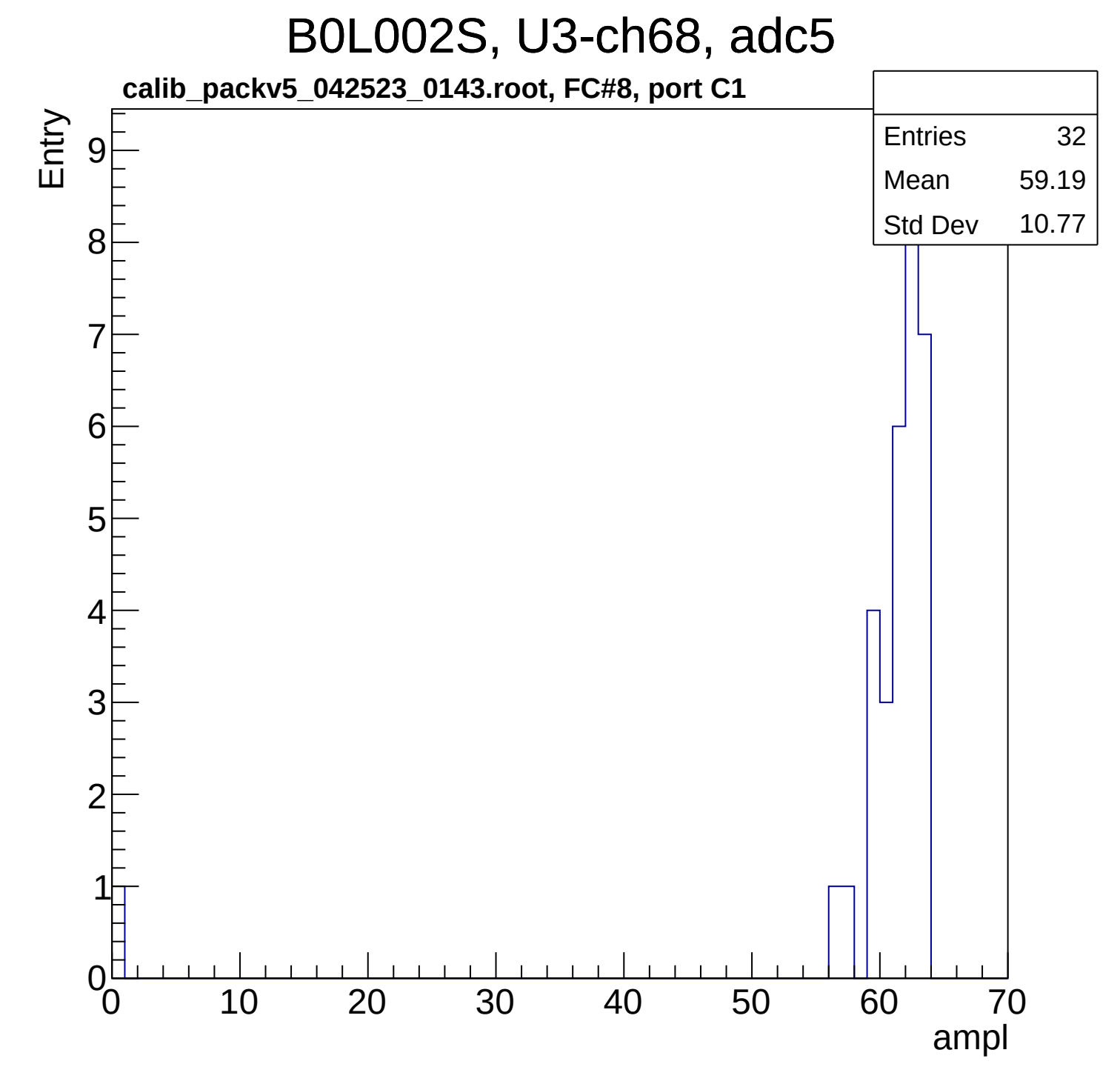
B0L002S, U3-ch68, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.19
Std Dev 10.77
Entry
ampl
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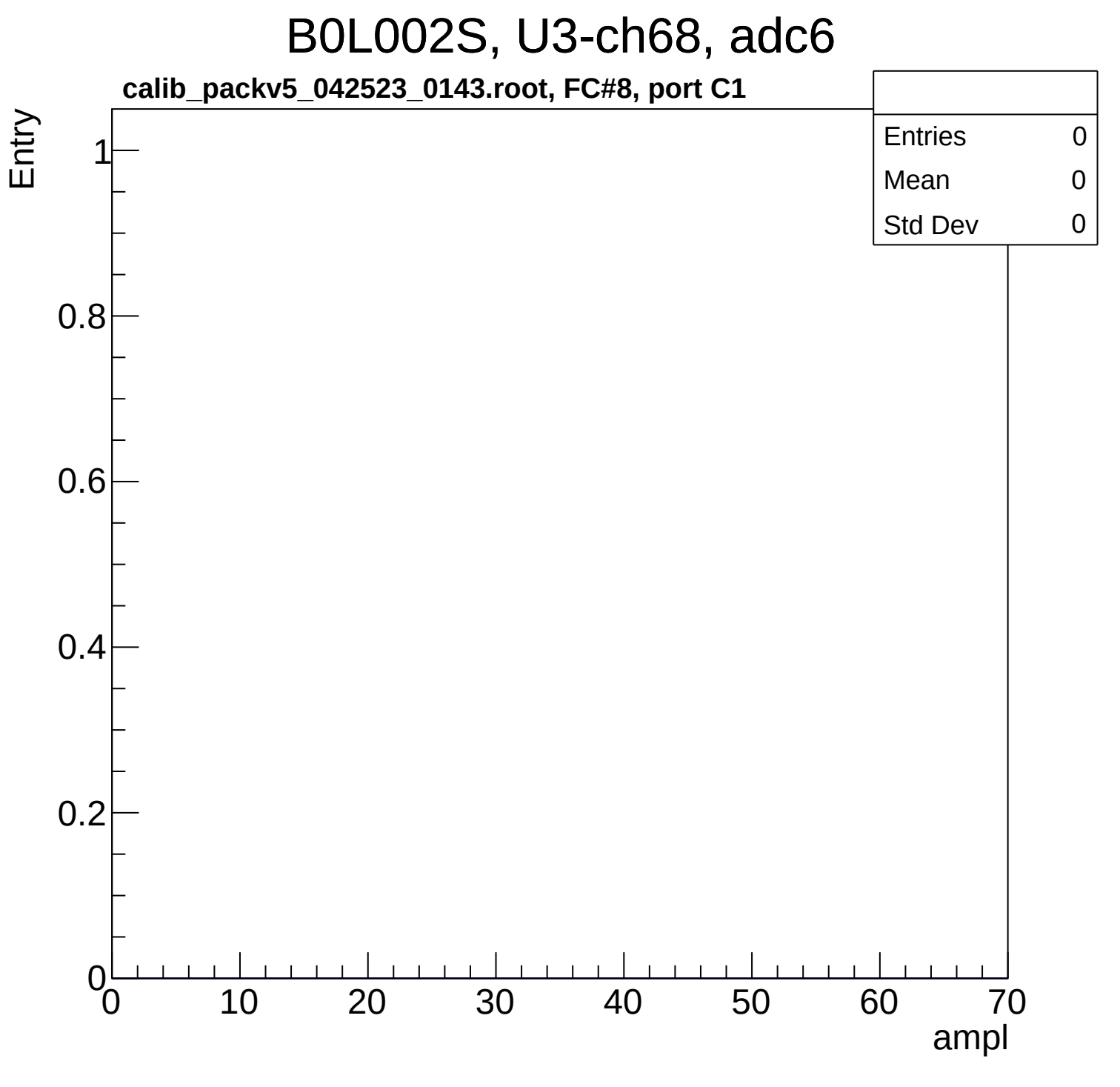
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch68, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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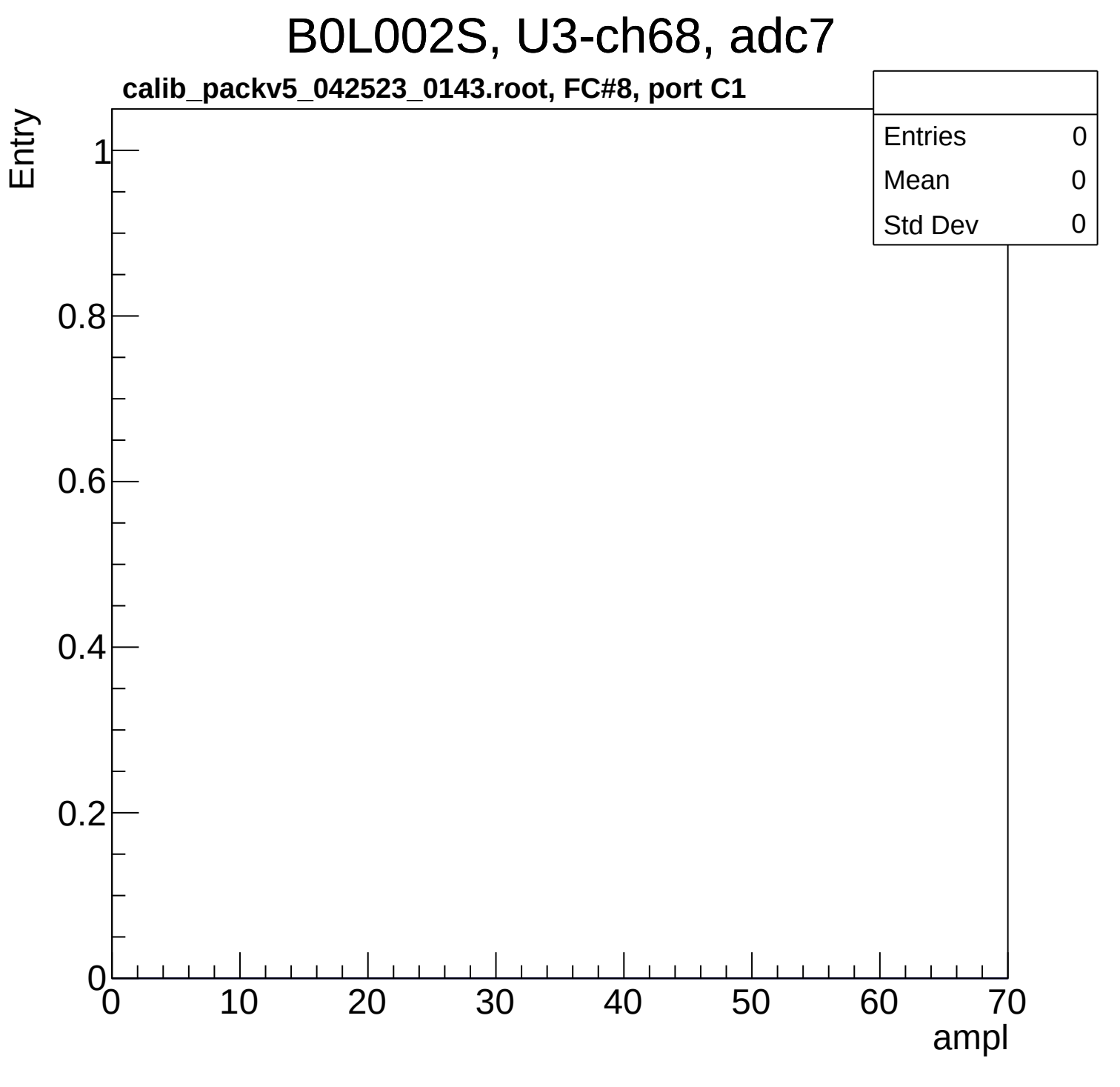
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch68, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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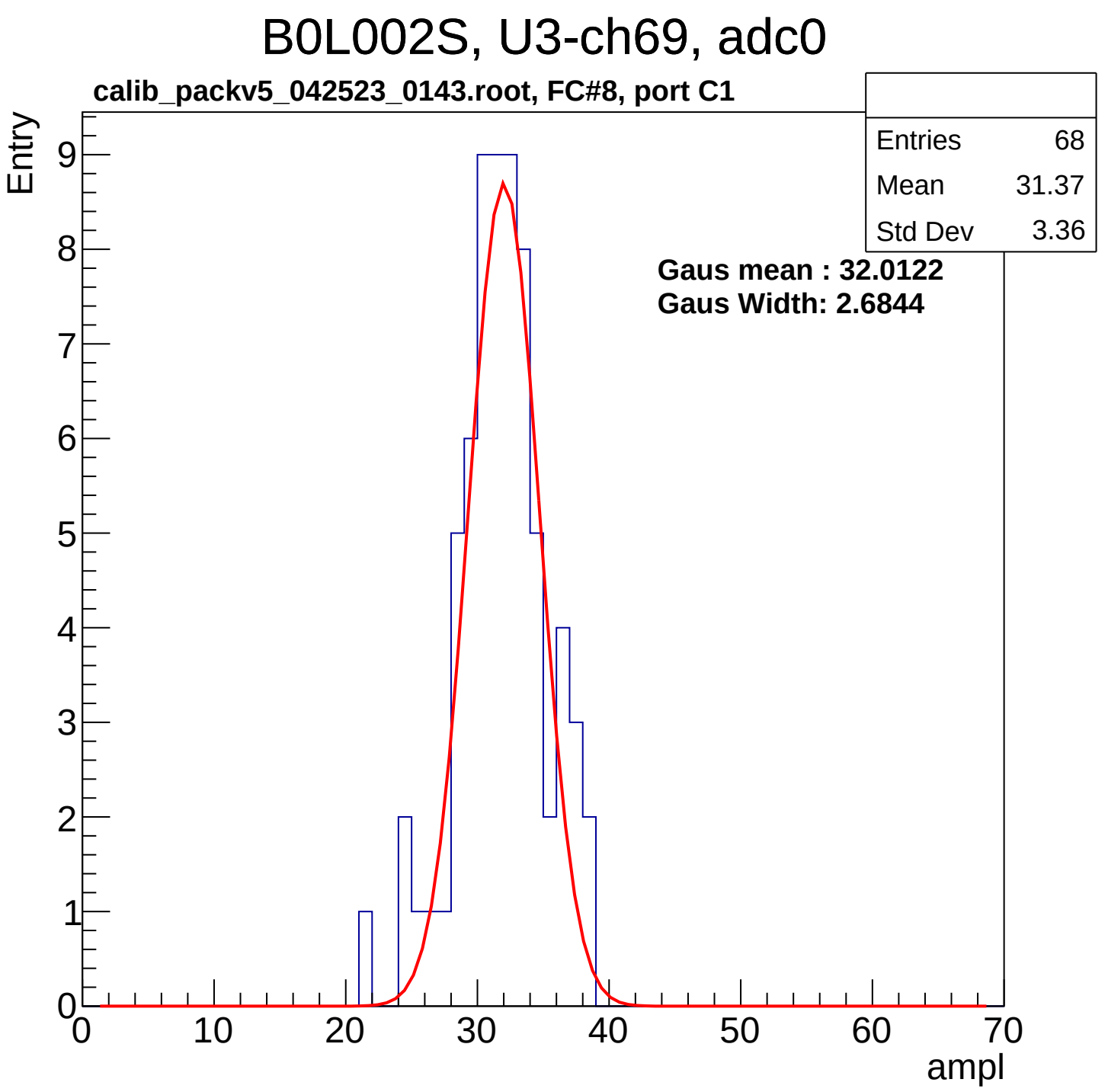
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch69, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31.37
Std Dev 3.36
Gaus mean : 32.0122
Gaus Width: 2.6844
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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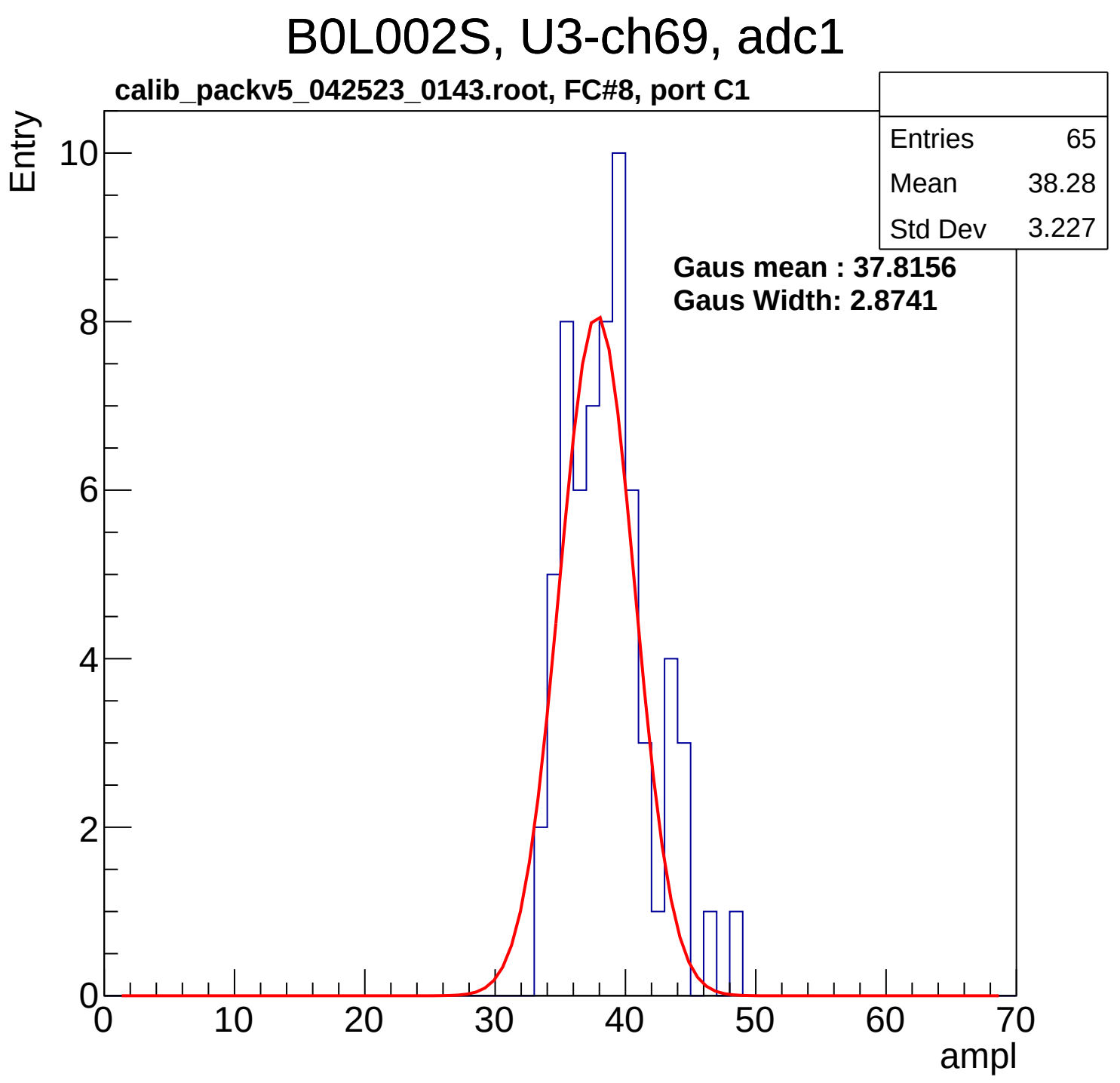
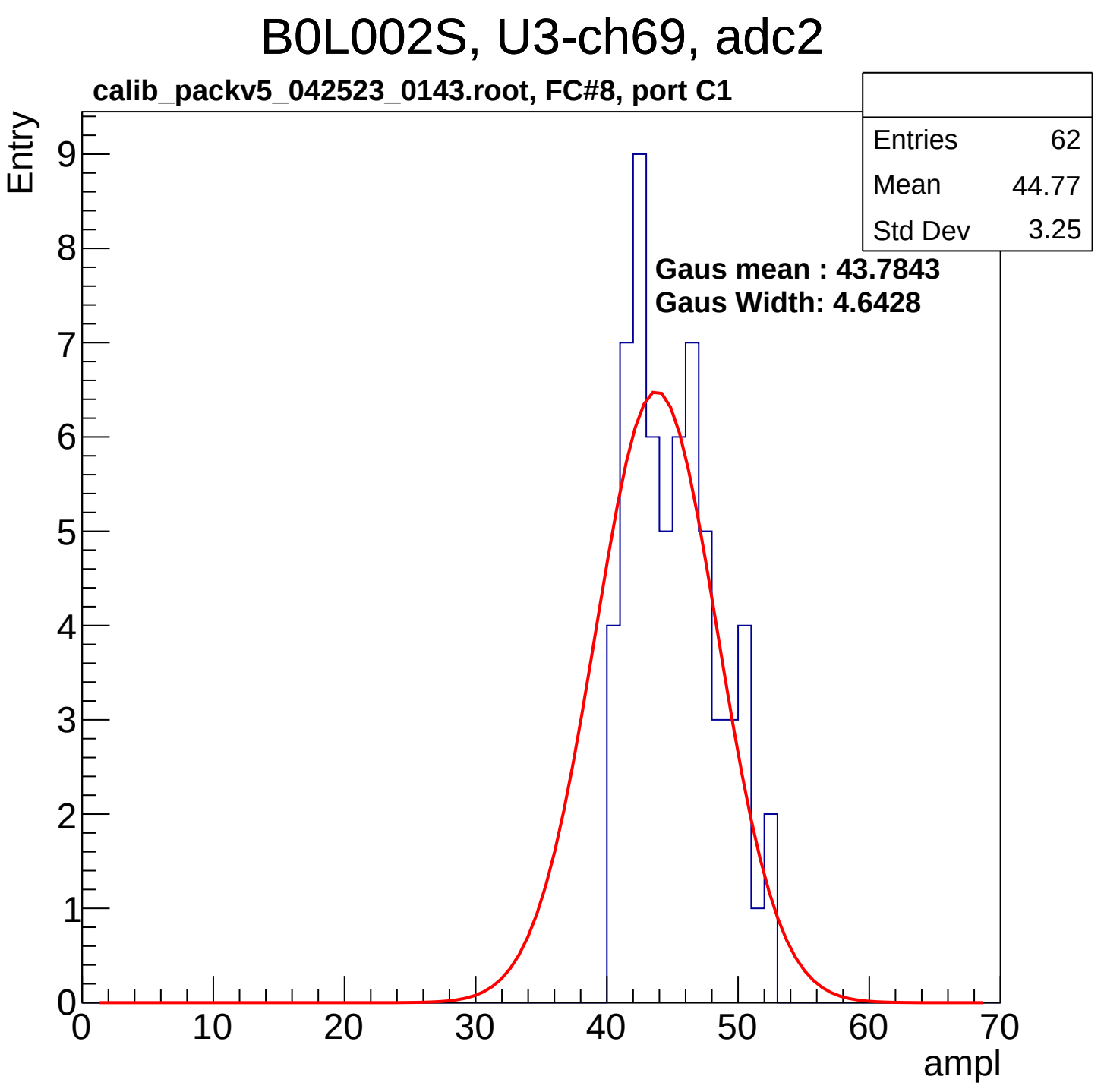
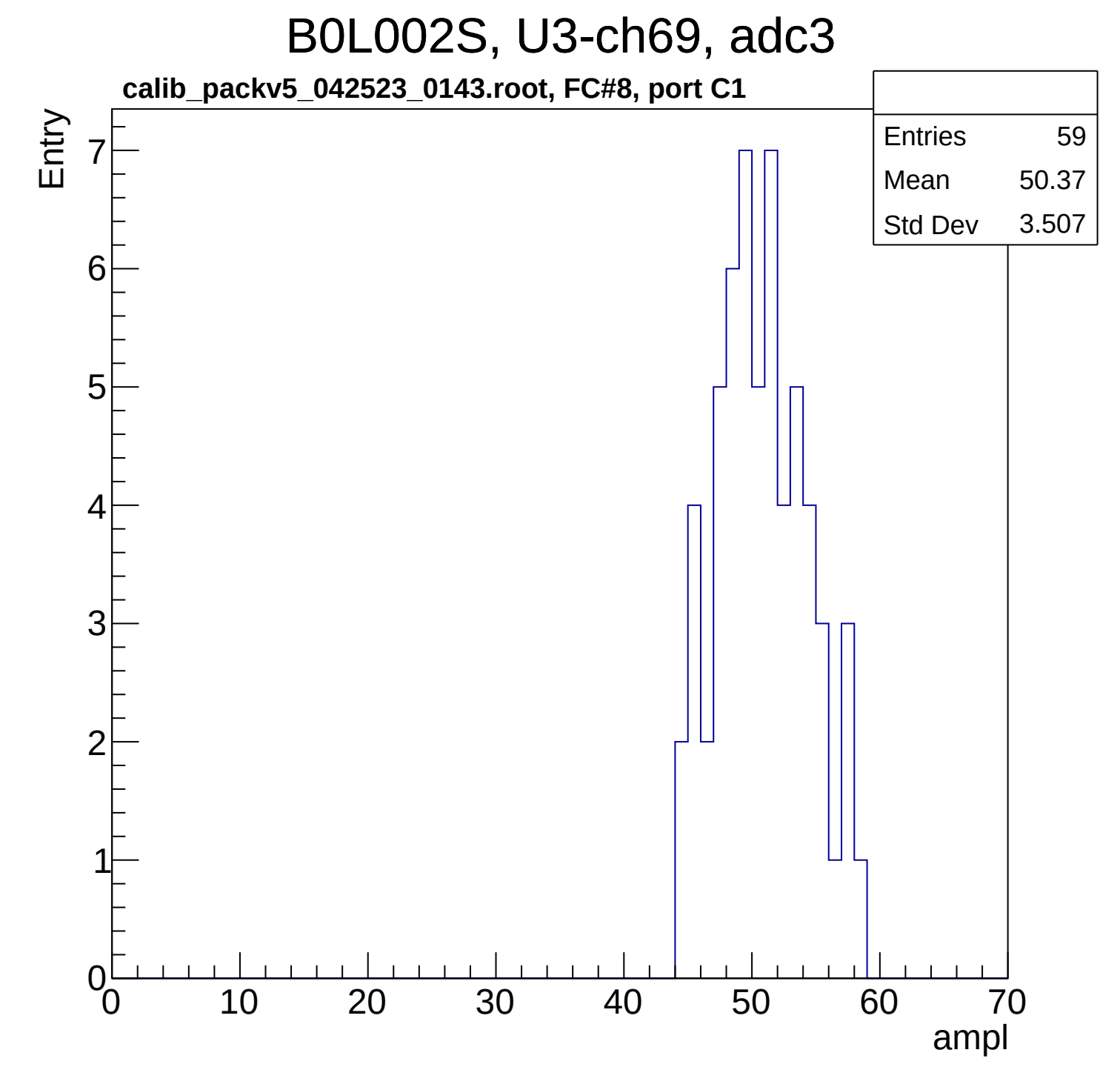

B0L002S, U3-ch69, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 38.28
Std Dev 3.227
Gaus mean : 37.8156
Gaus Width: 2.8741
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch69, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.77
Std Dev 3.25
Gaus mean : 43.7843
Gaus Width: 4.6428
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch69, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.37
Std Dev 3.507
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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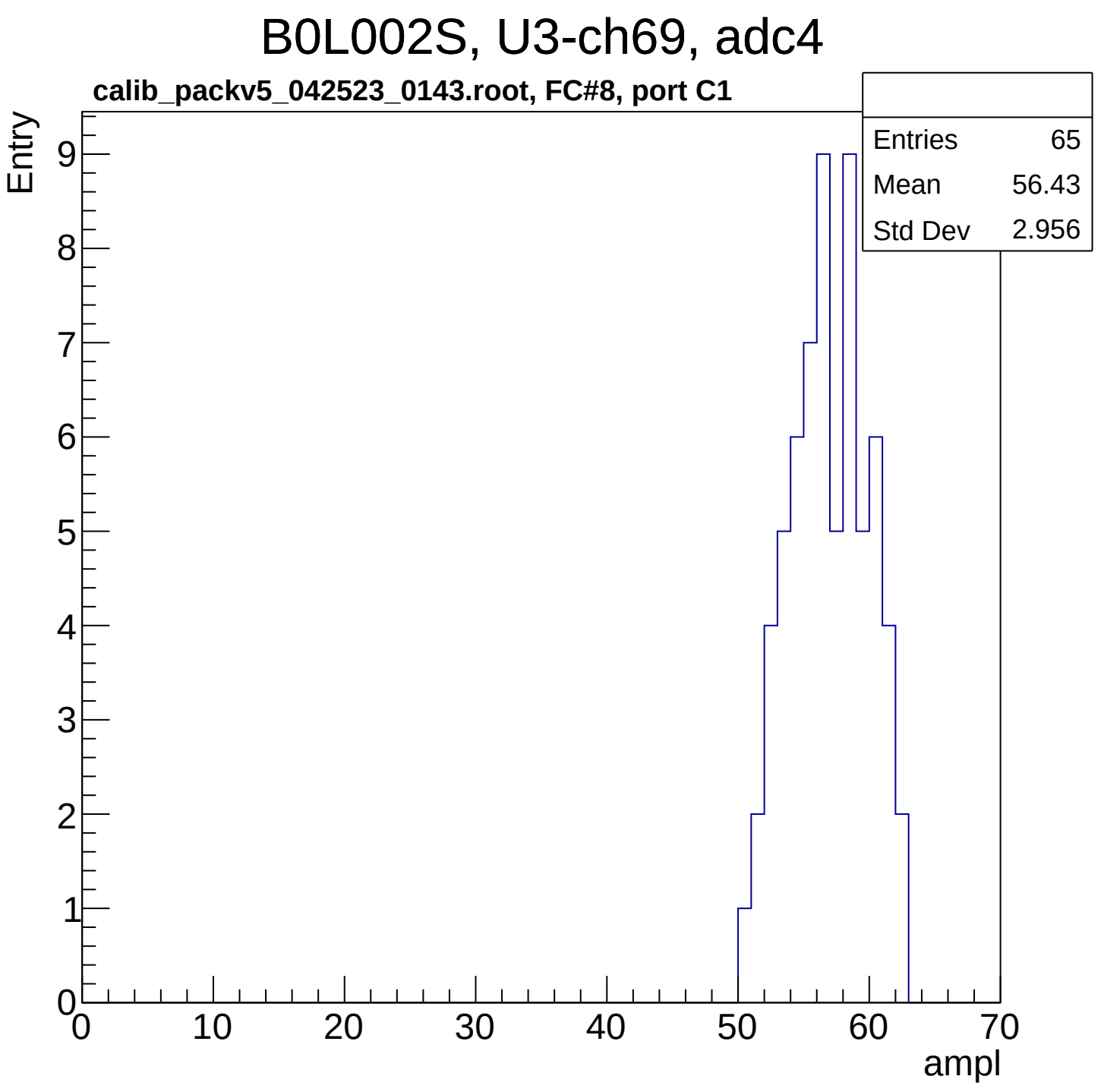
B0L002S, U3-ch69, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.43
Std Dev 2.956
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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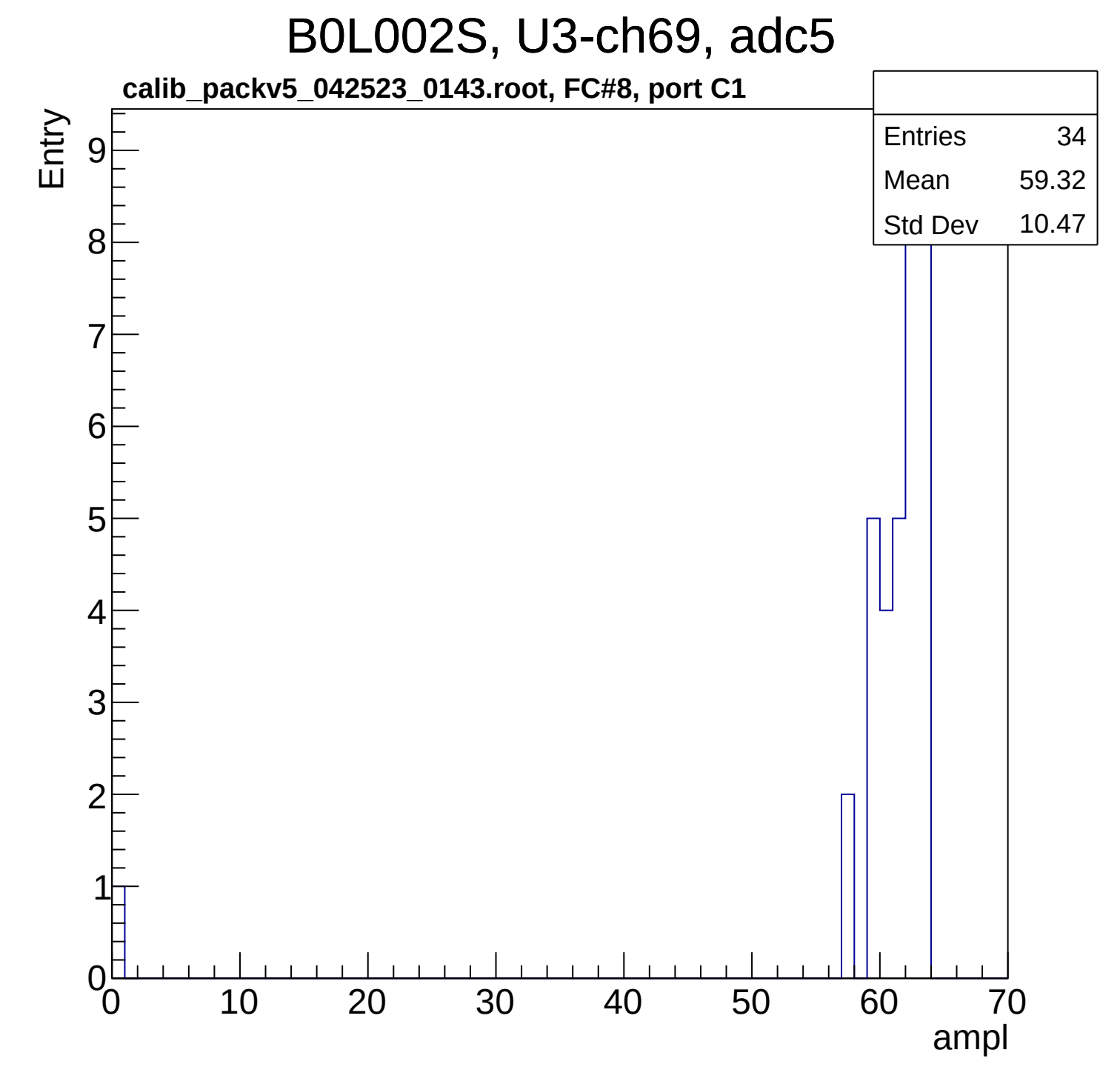
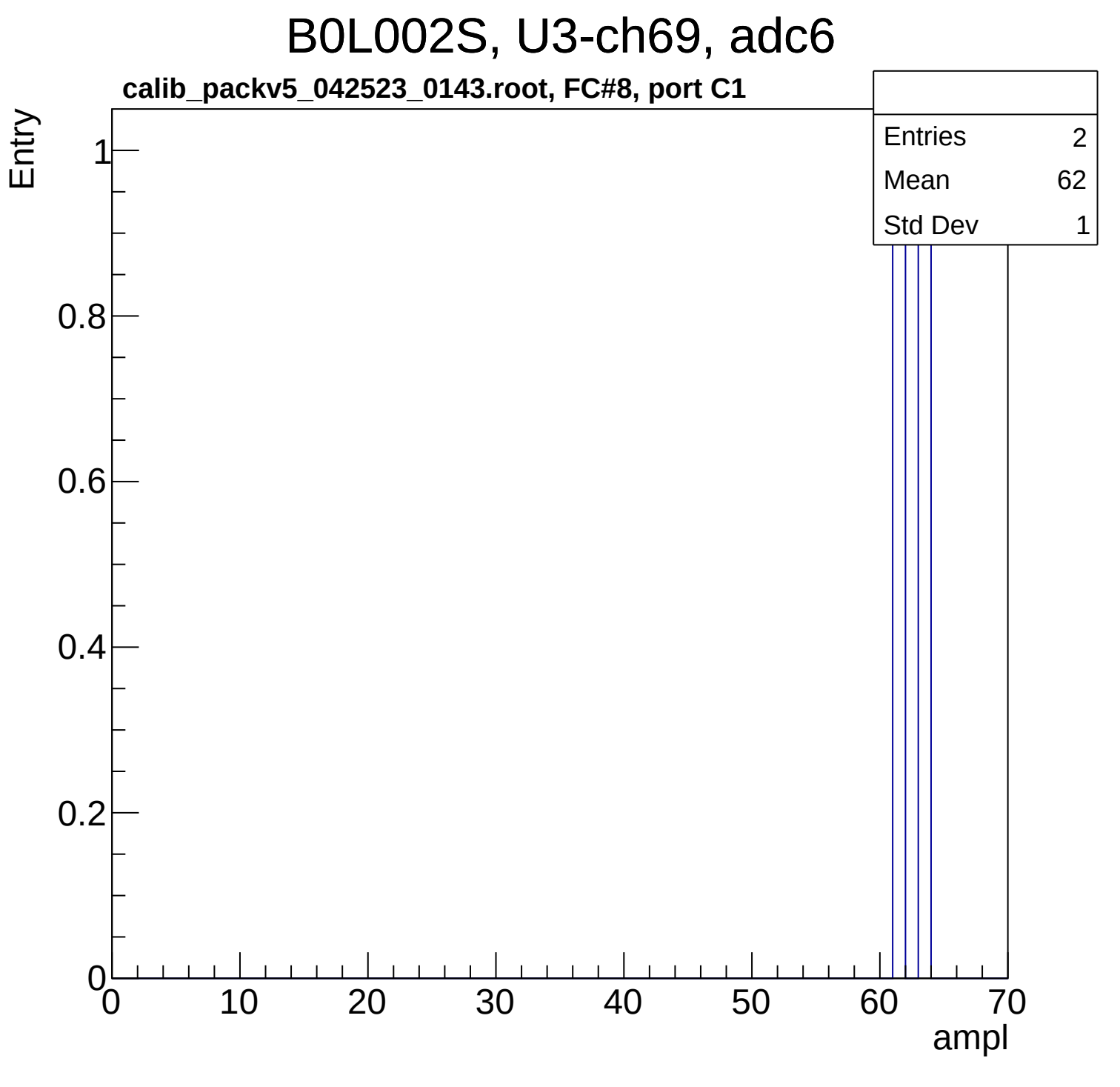
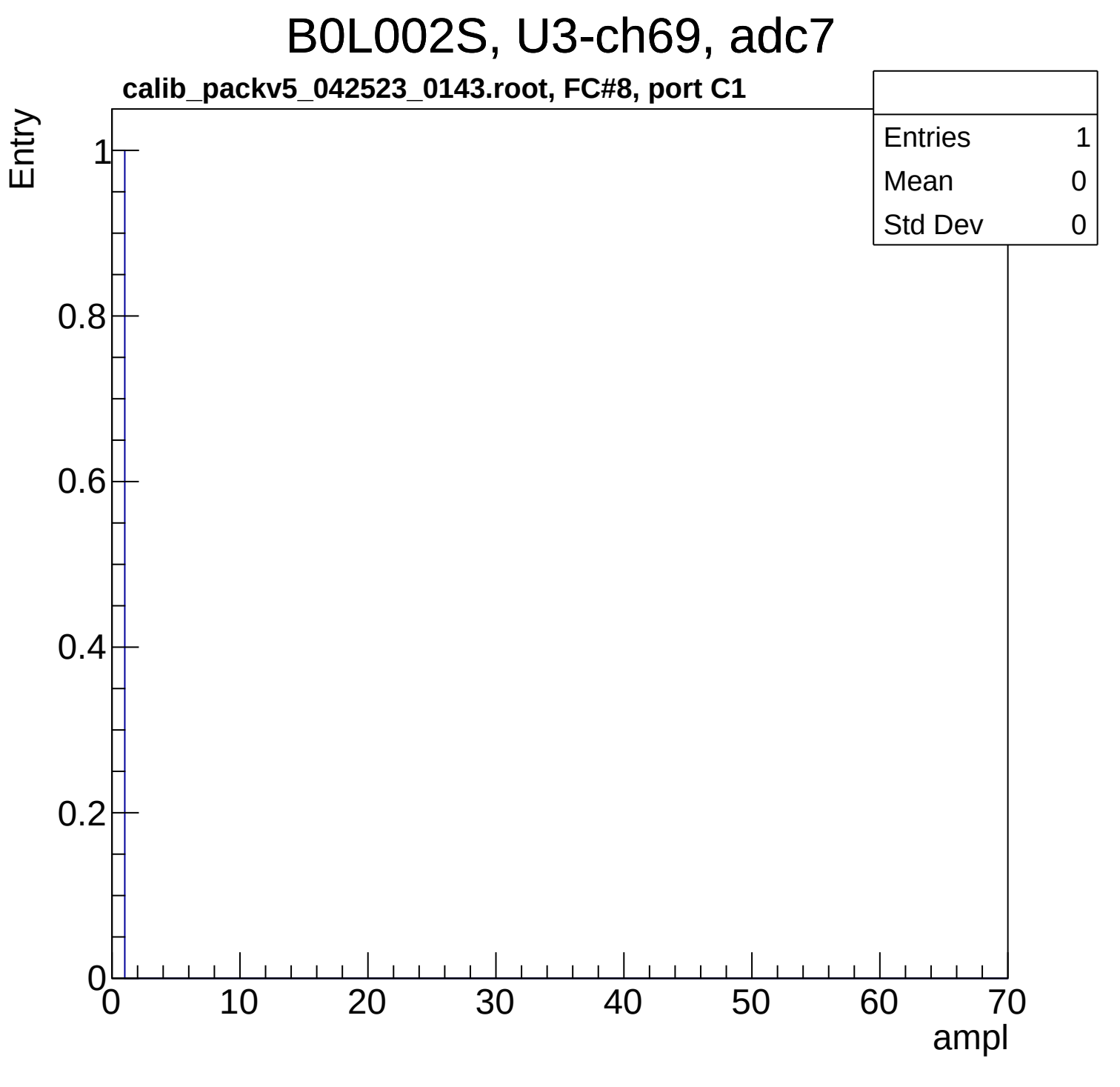

B0L002S, U3-ch69, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.32
Std Dev 10.47
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch69, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch69, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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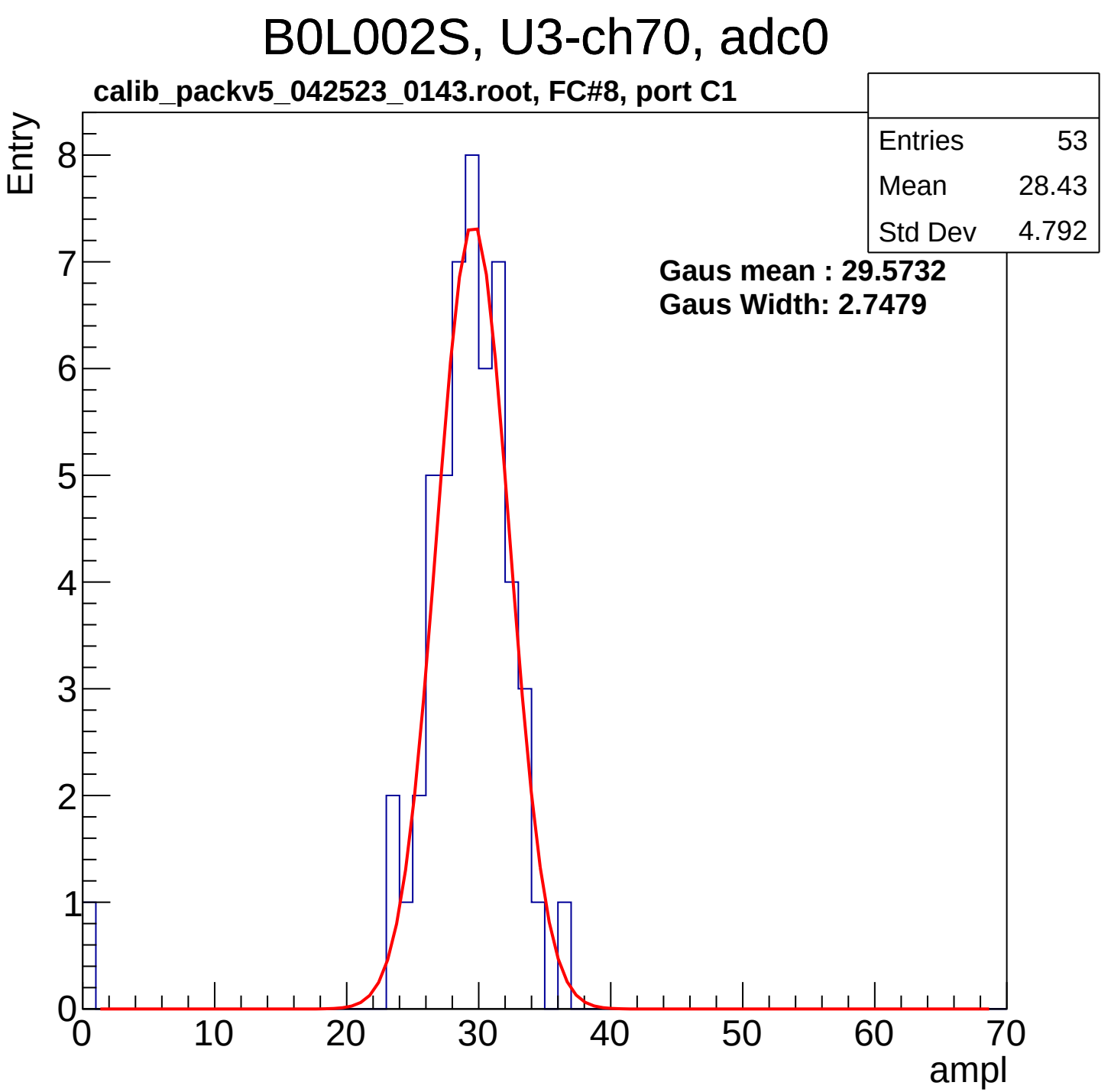
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch70, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 28.43
Std Dev 4.792
Gaus mean : 29.5732
Gaus Width: 2.7479
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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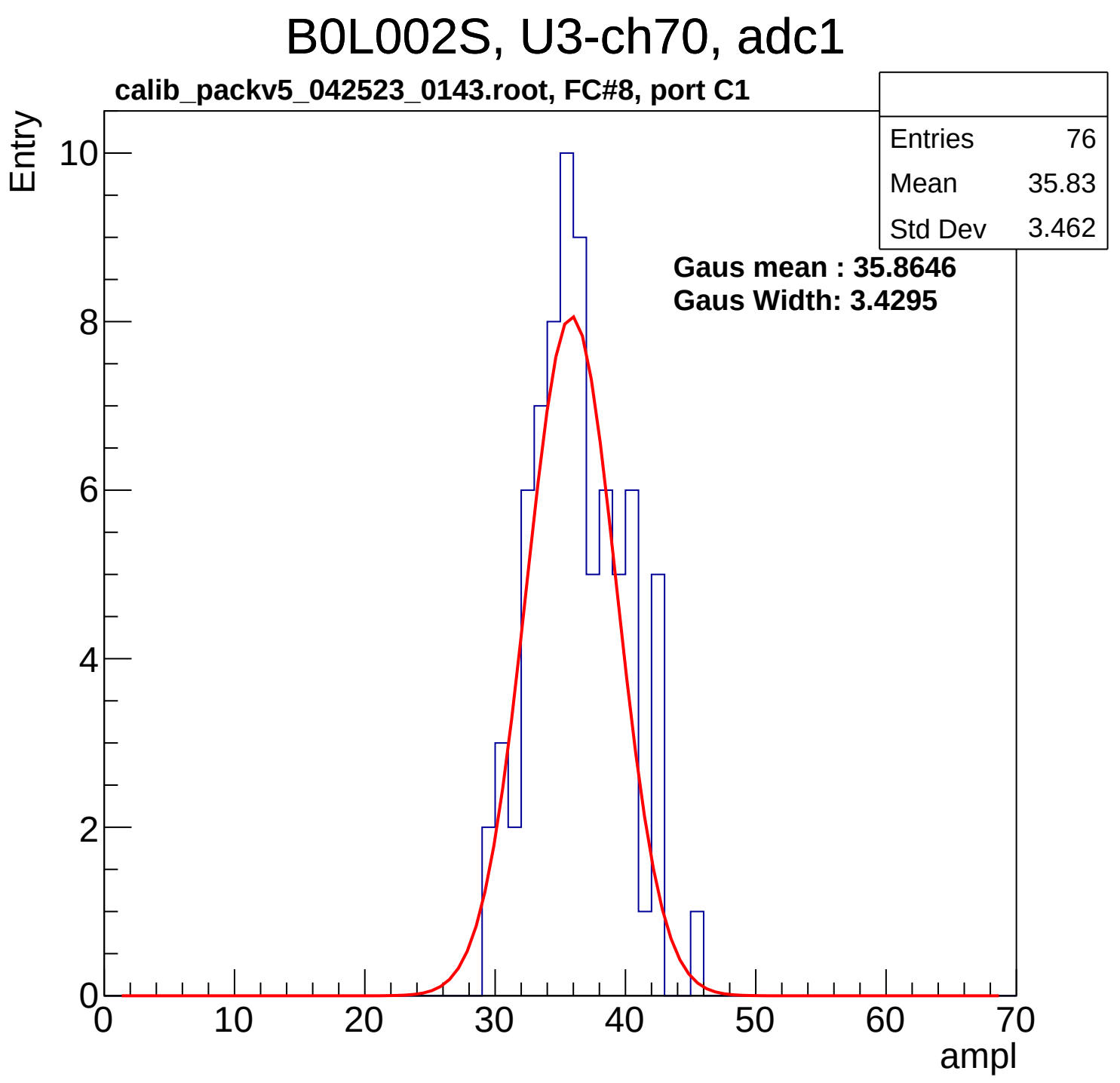
B0L002S, U3-ch70, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 35.83
Std Dev 3.462
Gaus mean : 35.8646
Gaus Width: 3.4295
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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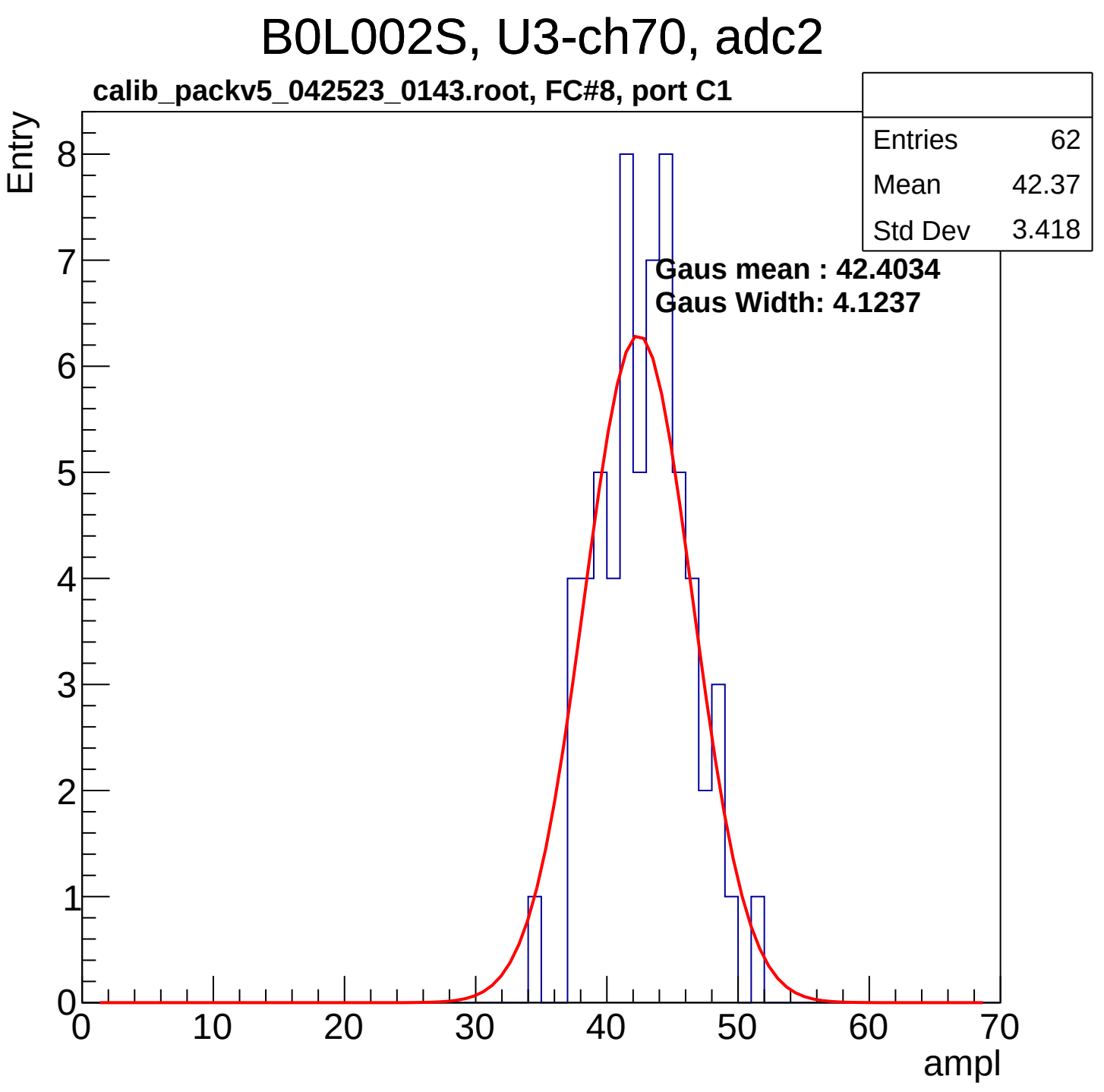
B0L002S, U3-ch70, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 42.37
Std Dev 3.418
Gaus mean : 42.4034
Gaus Width: 4.1237
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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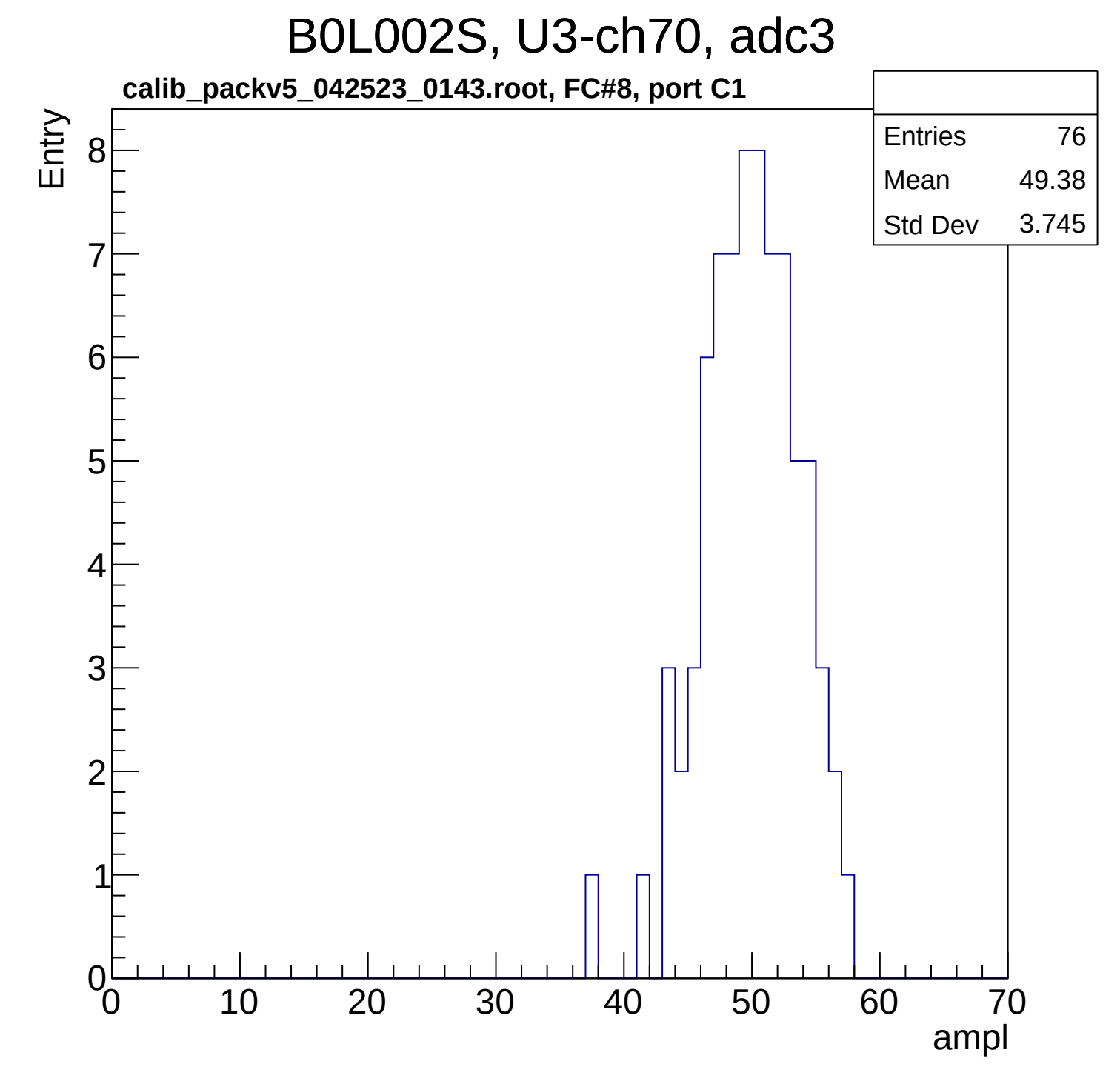
B0L002S, U3-ch70, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 49.38
Std Dev 3.745
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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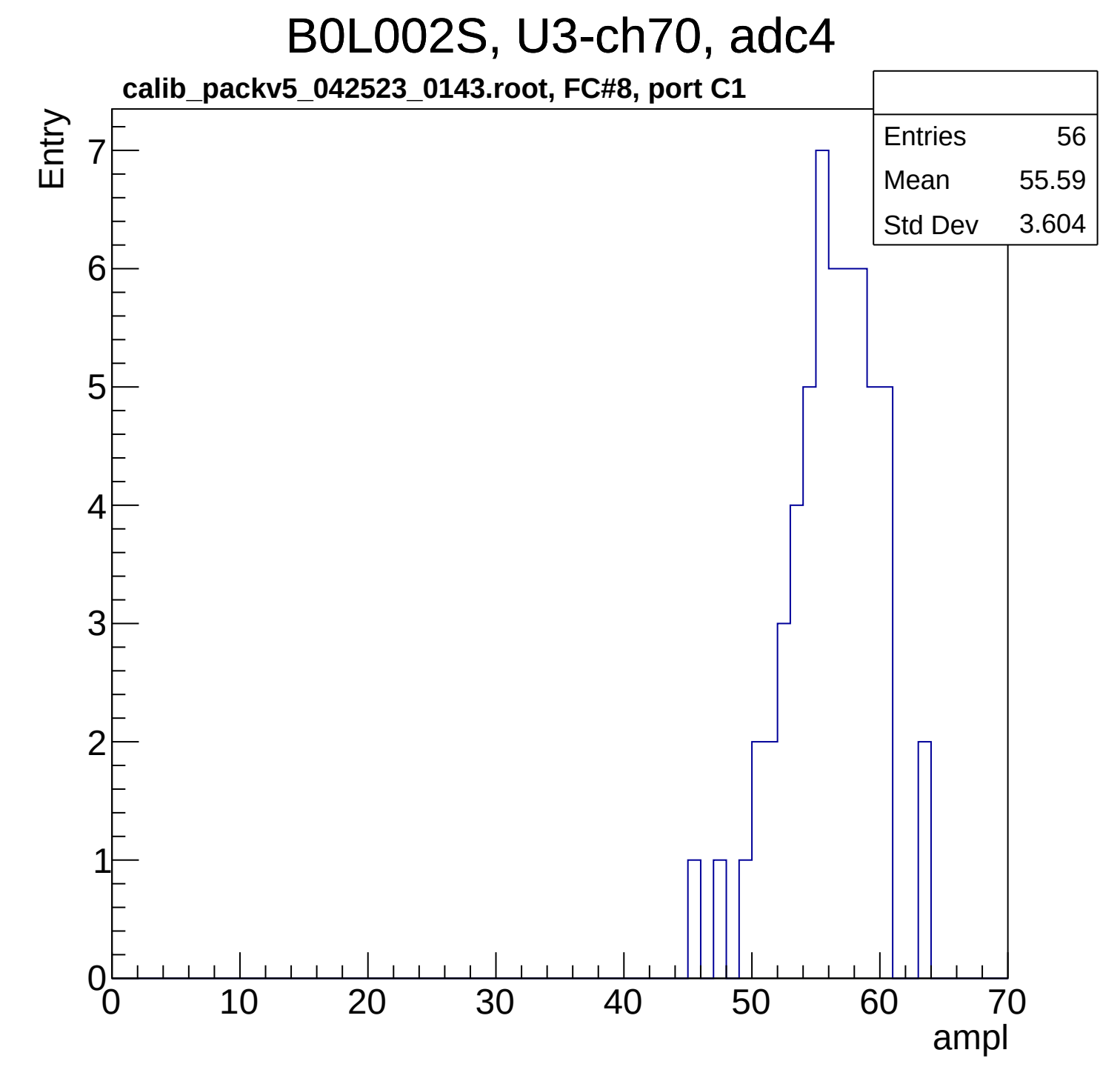
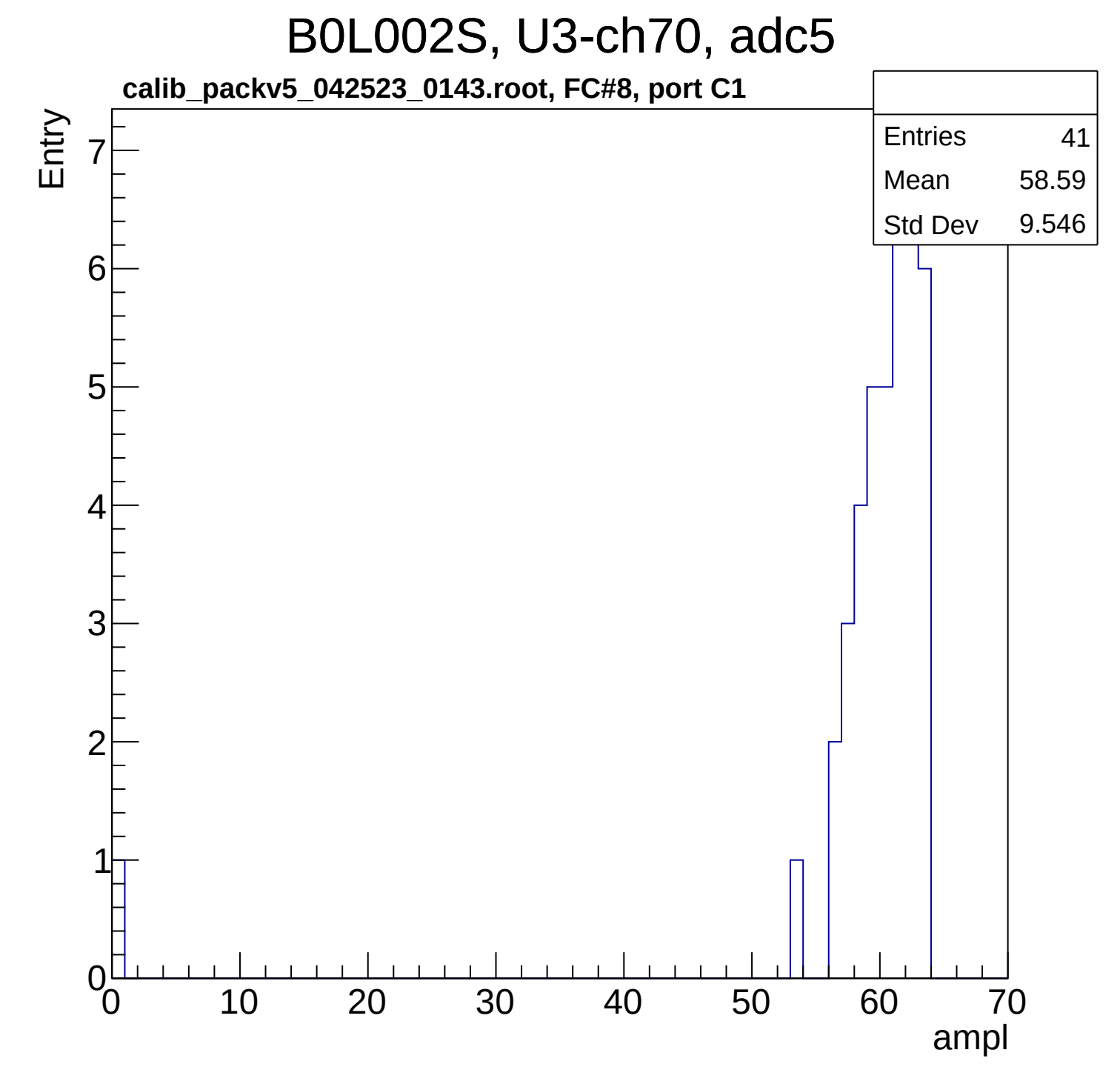

B0L002S, U3-ch70, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.59
Std Dev 3.604
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch70, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.59
Std Dev 9.546
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
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60
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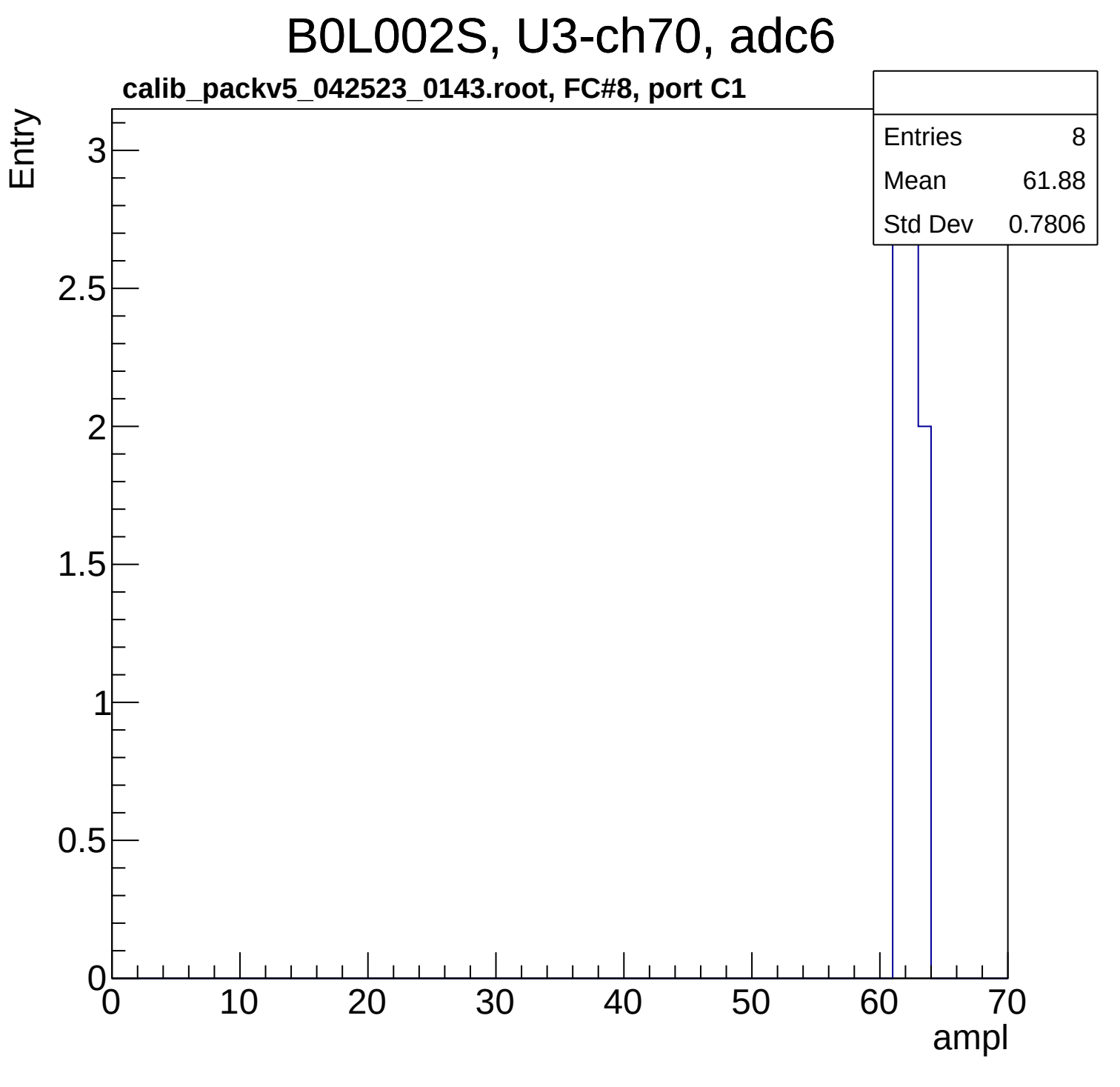
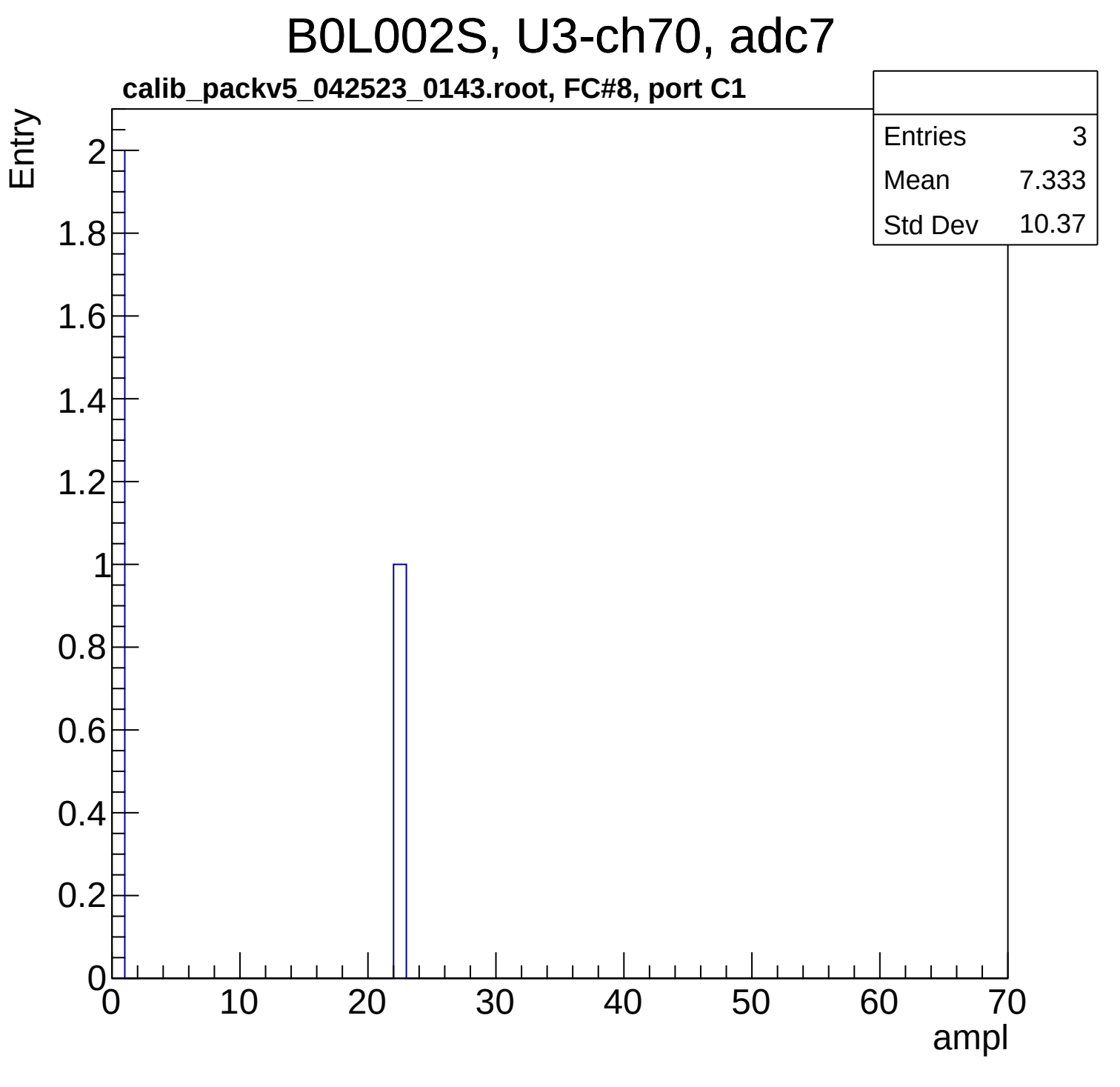

B0L002S, U3-ch70, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 61.88
Std Dev 0.7806
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch70, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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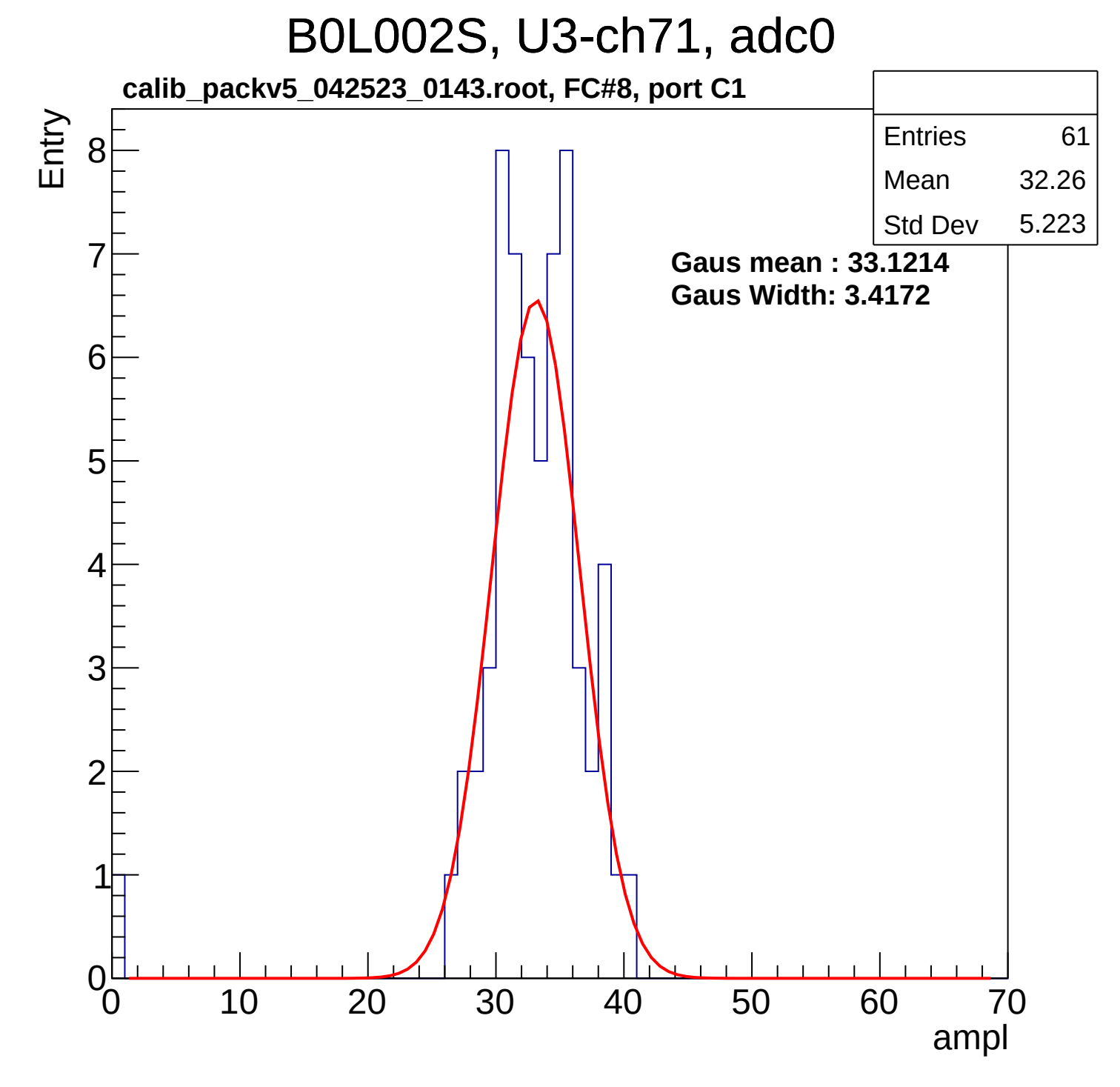
B0L002S, U3-ch71, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 32.26
Std Dev 5.223
Gaus mean : 33.1214
Gaus Width: 3.4172
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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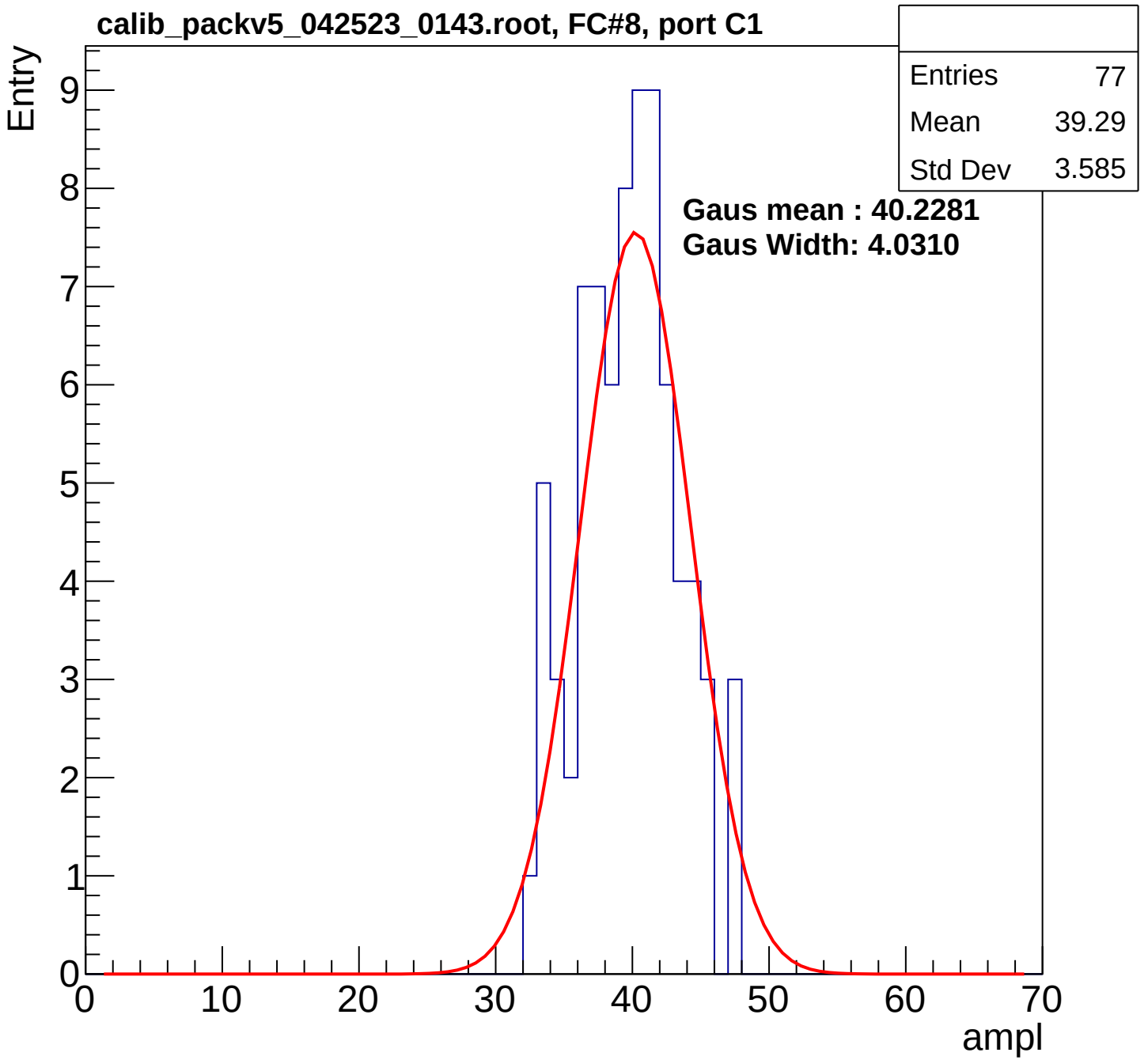
B0L002S, U3-ch71, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 39.29
Std Dev 3.585
Gaus mean : 40.2281
Gaus Width: 4.0310
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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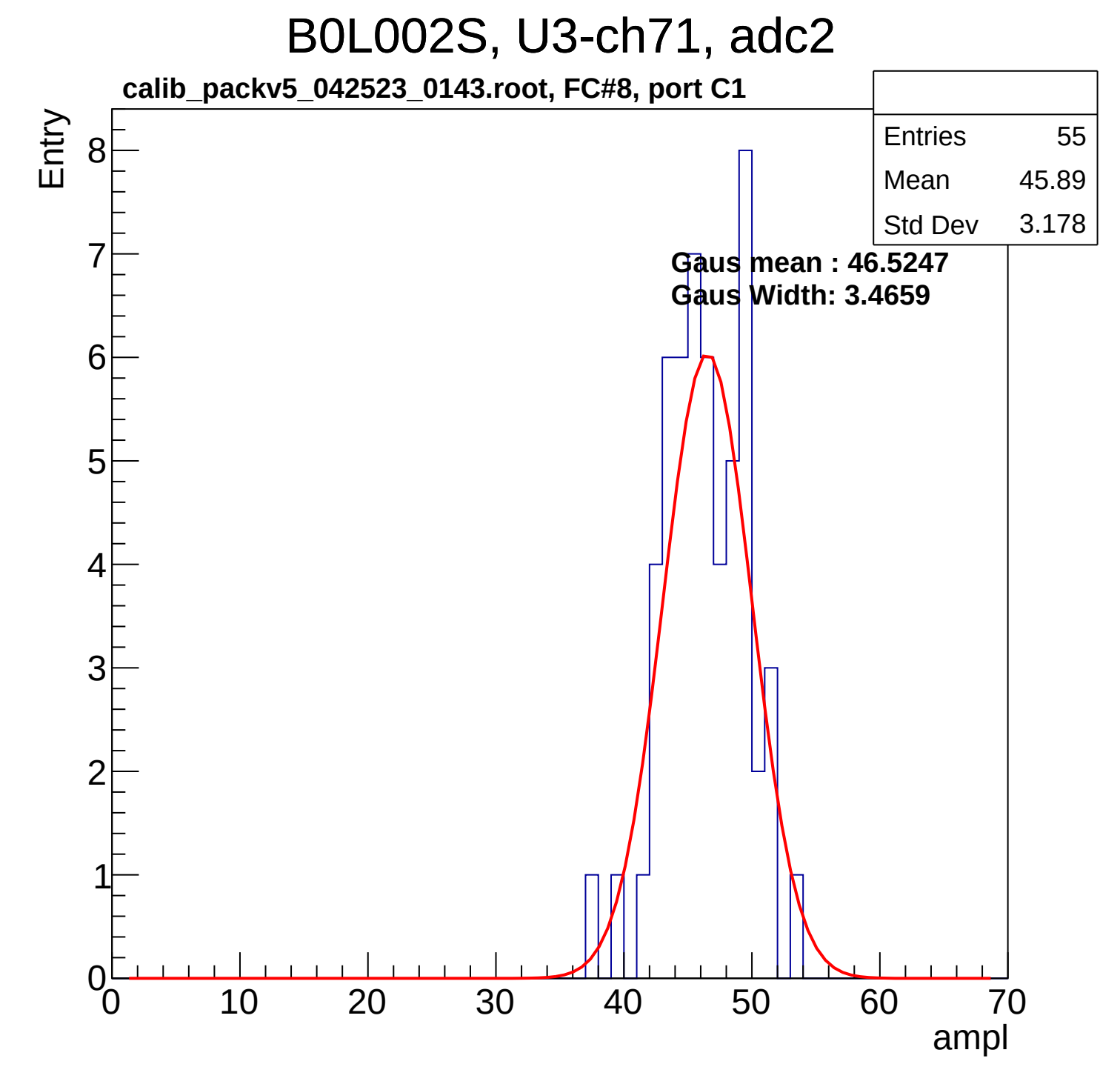
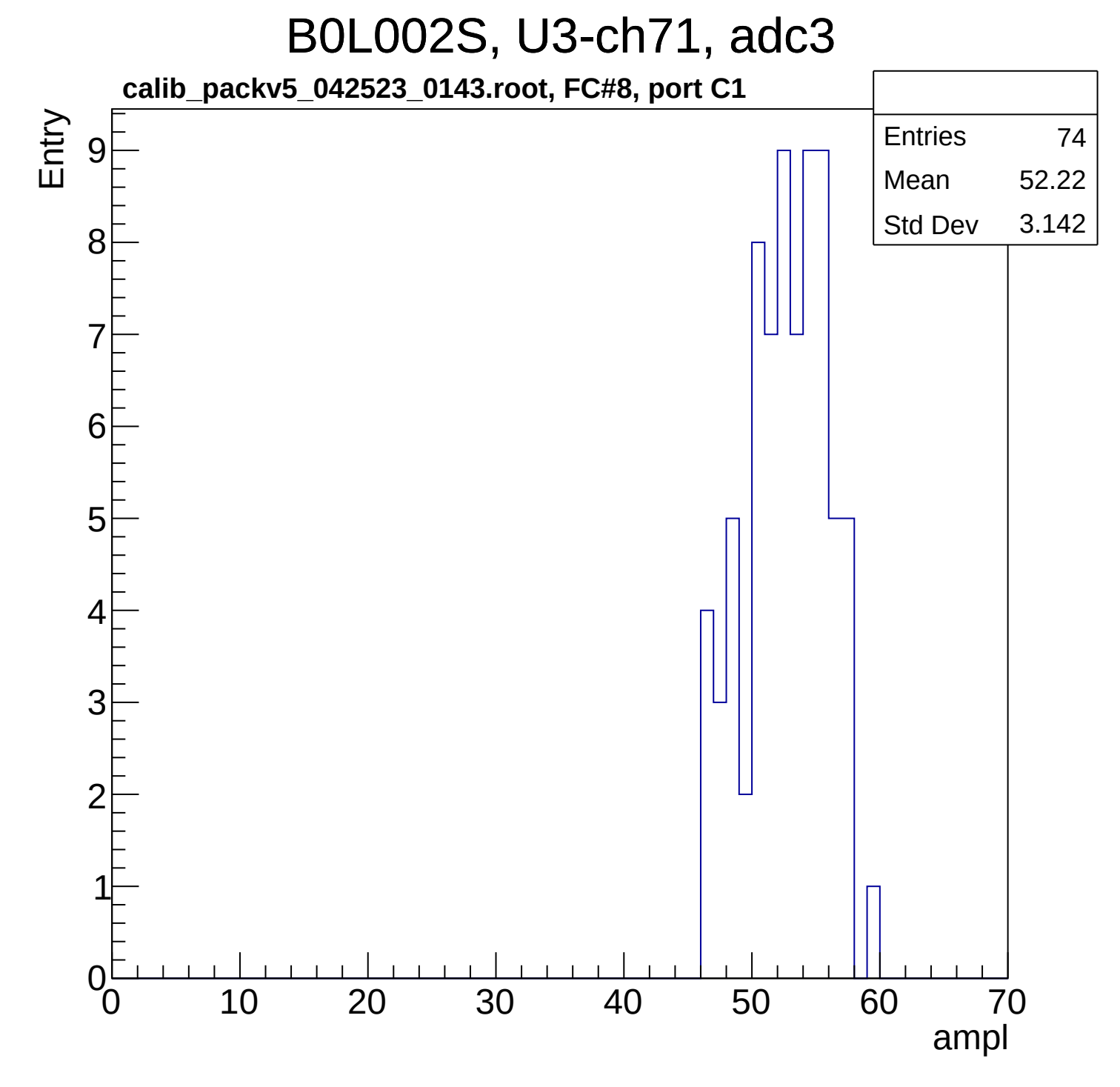

B0L002S, U3-ch71, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.89
Std Dev 3.178
Gaus mean : 46.5247
Gaus Width: 3.4659
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch71, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 52.22
Std Dev 3.142
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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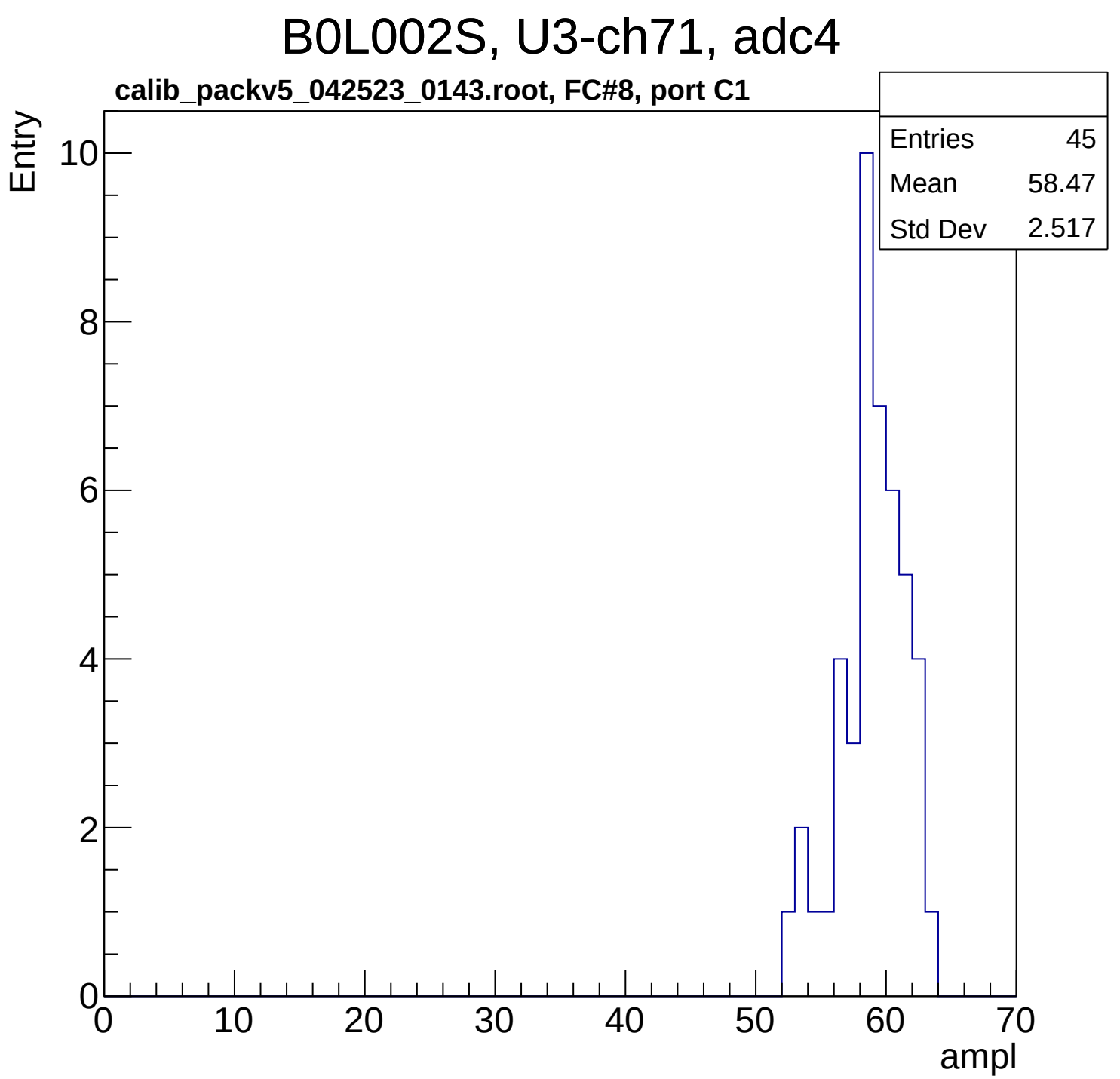
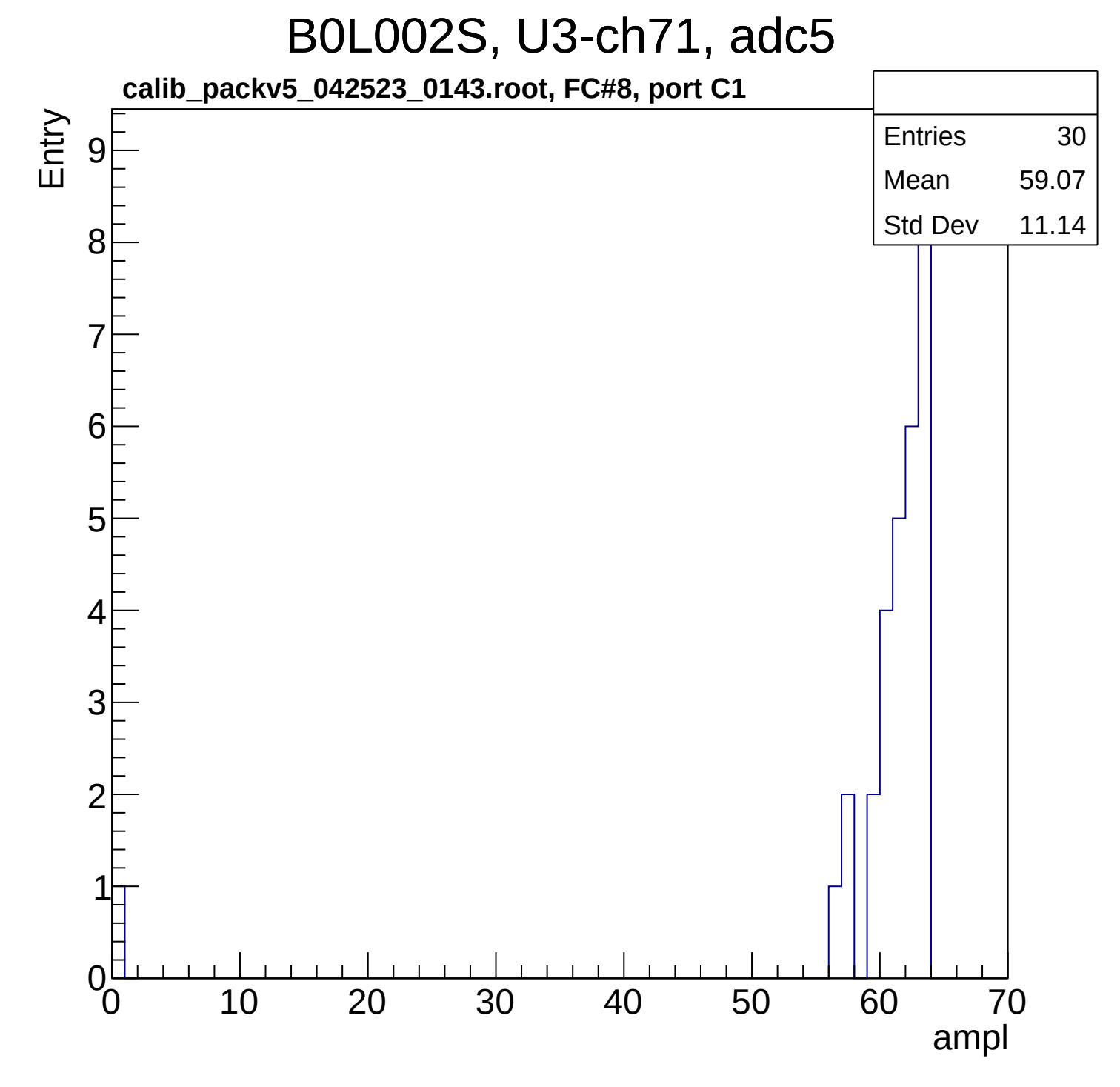

B0L002S, U3-ch71, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 58.47
Std Dev 2.517
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch71, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 59.07
Std Dev 11.14
Entry
0
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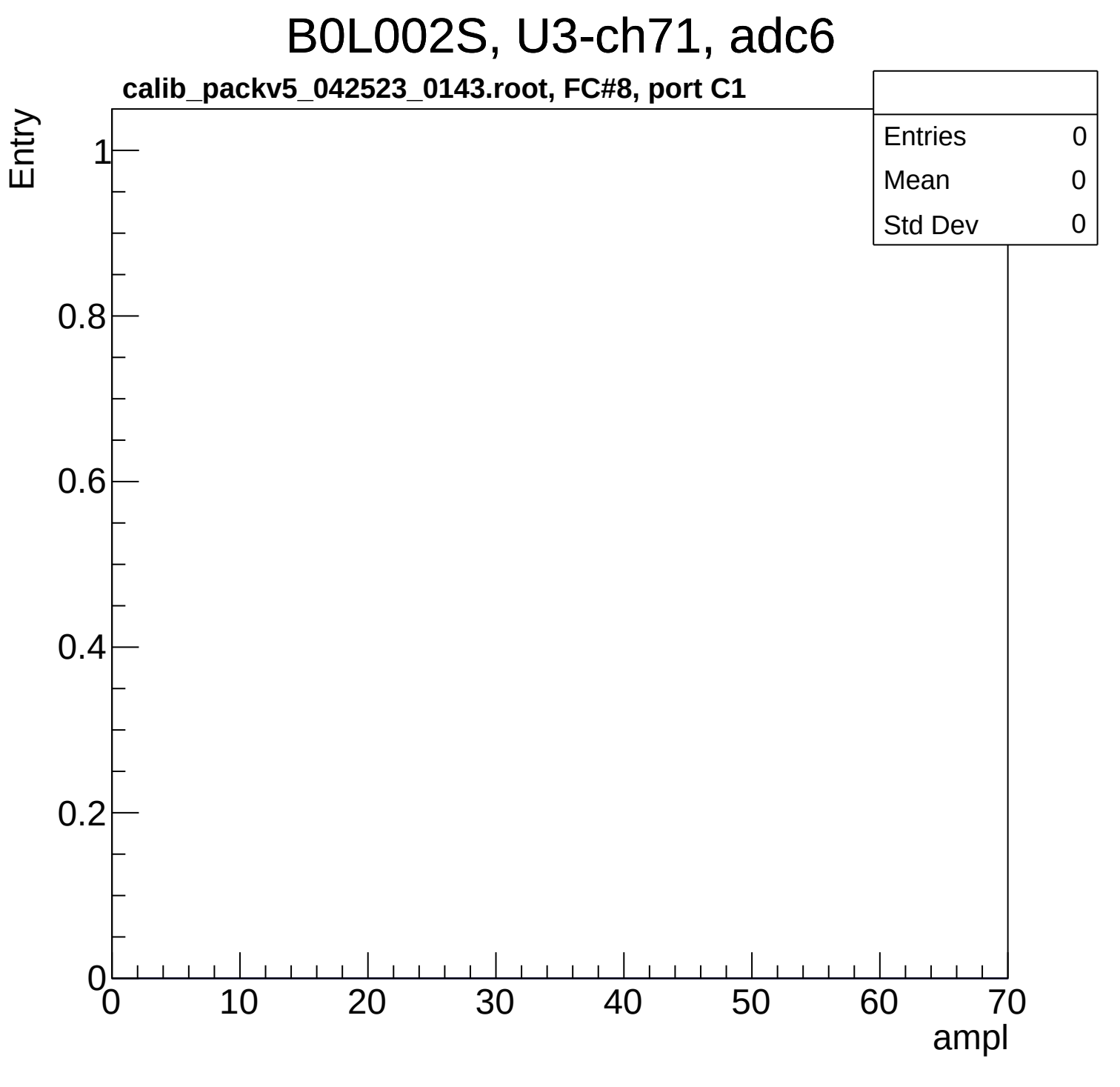
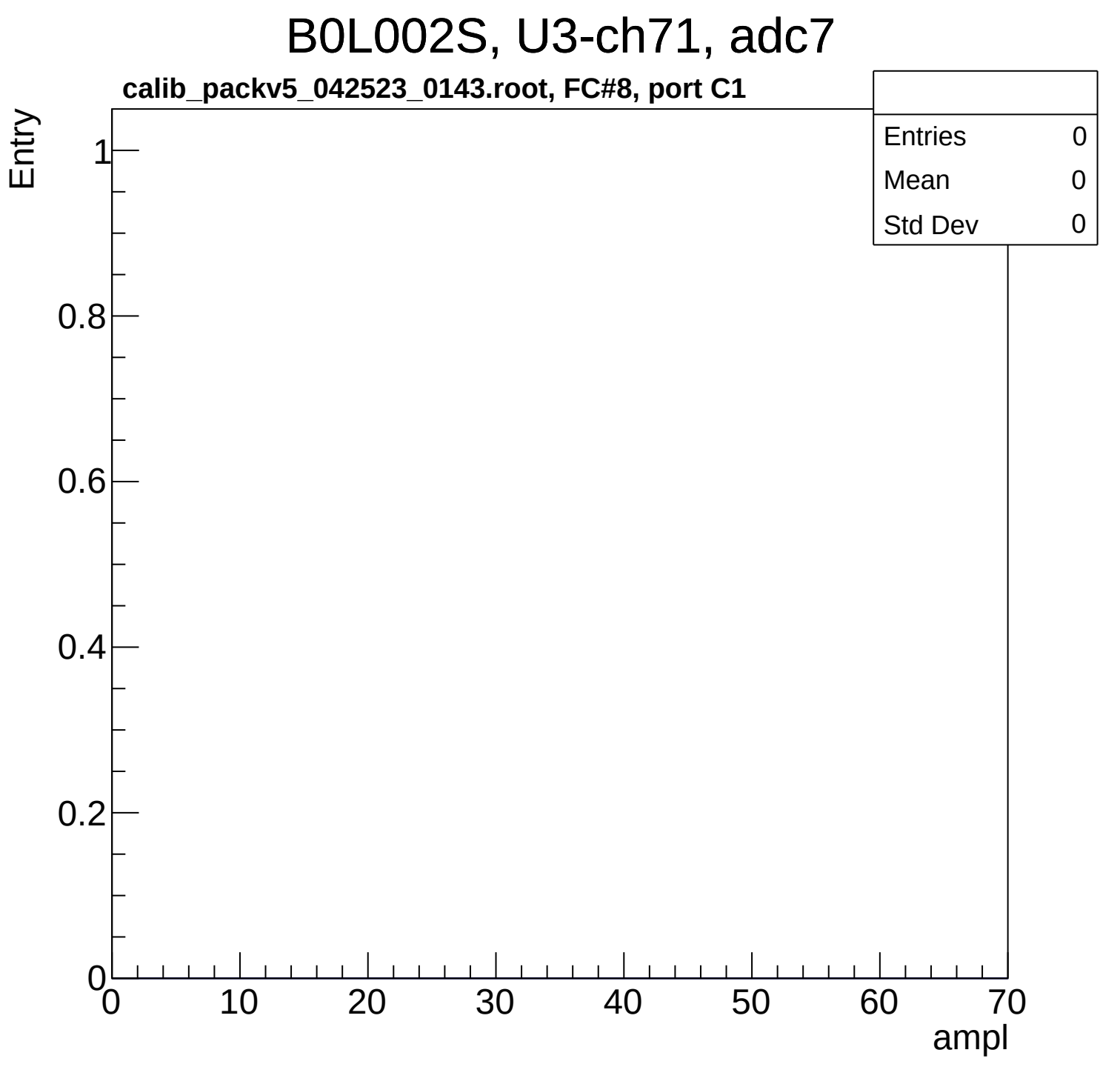
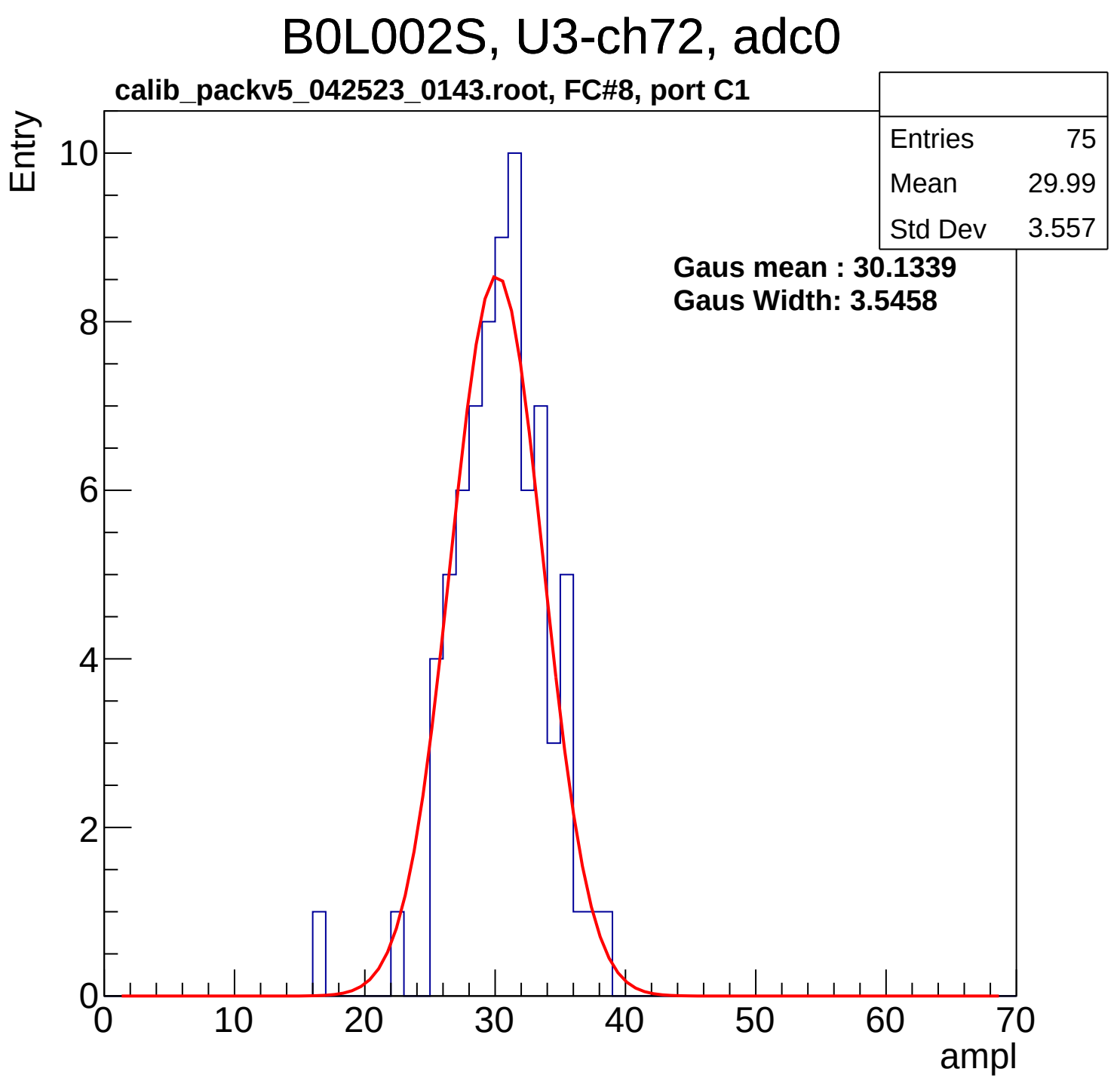
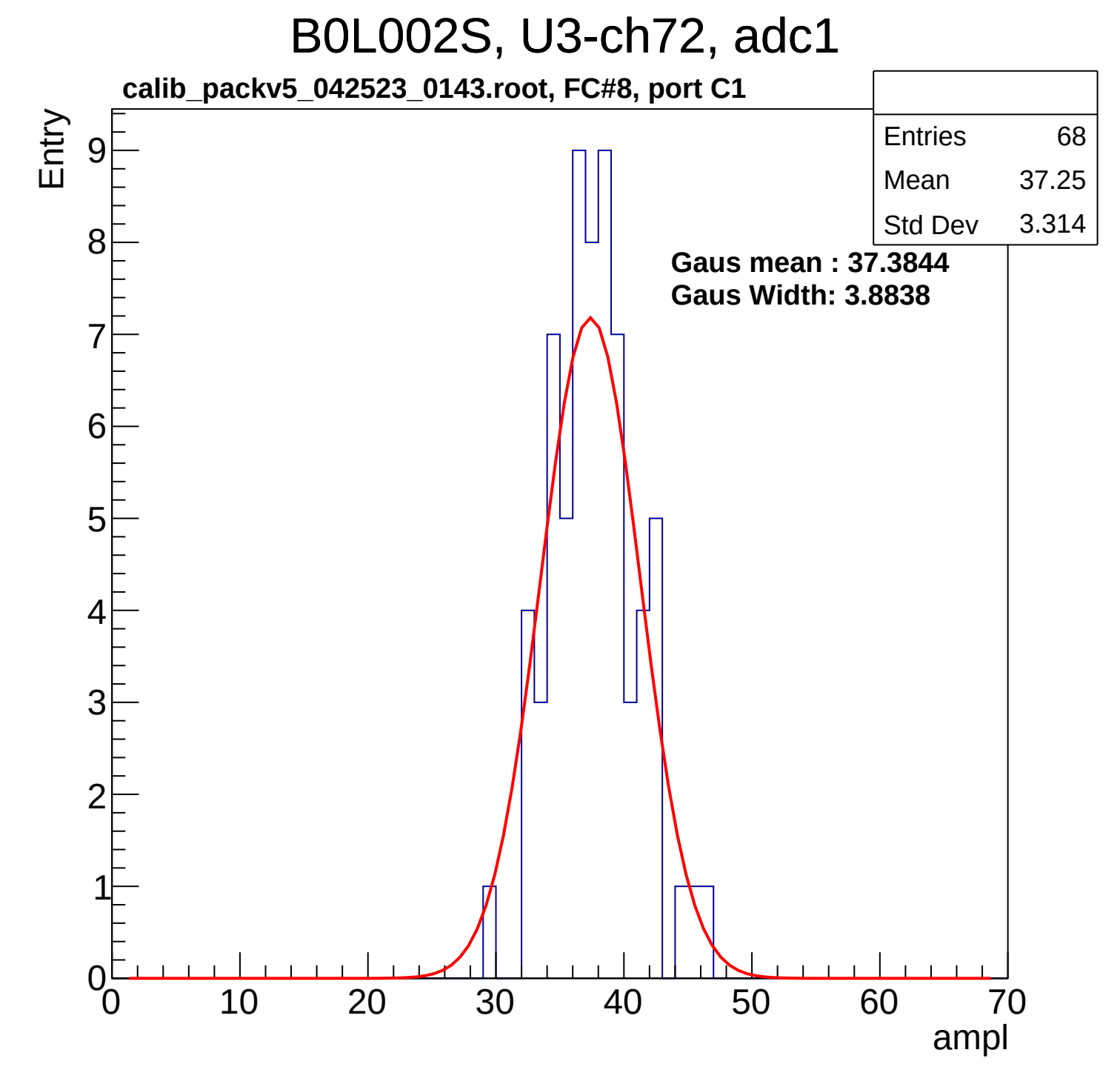
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch71, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch71, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch72, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.99
Std Dev 3.557
Gaus mean : 30.1339
Gaus Width: 3.5458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch72, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.25
Std Dev 3.314
Gaus mean : 37.3844
Gaus Width: 3.8838
Entry
ampl
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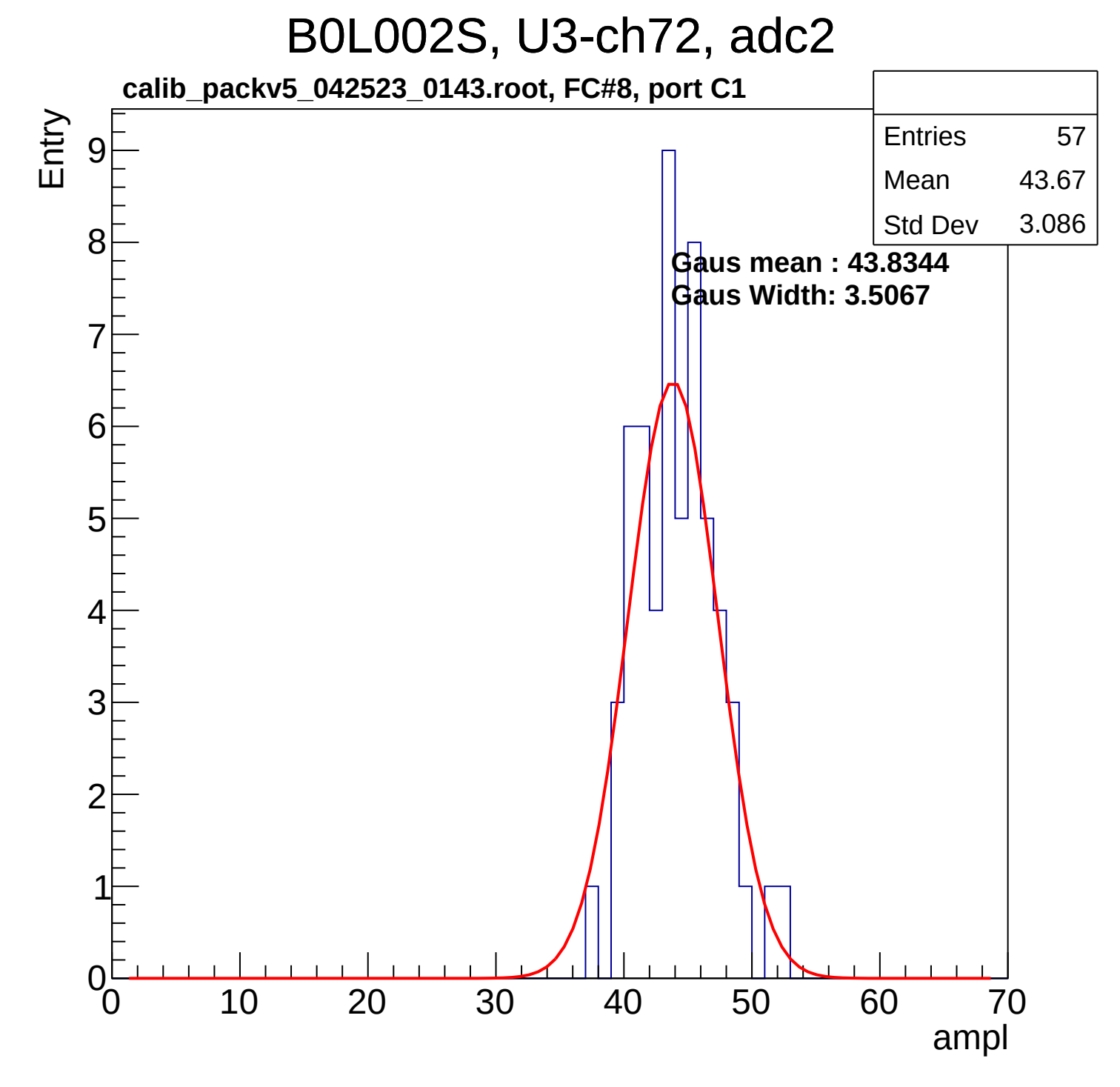
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch72, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 43.67
Std Dev 3.086
Gaus mean : 43.8344
Gaus Width: 3.5067
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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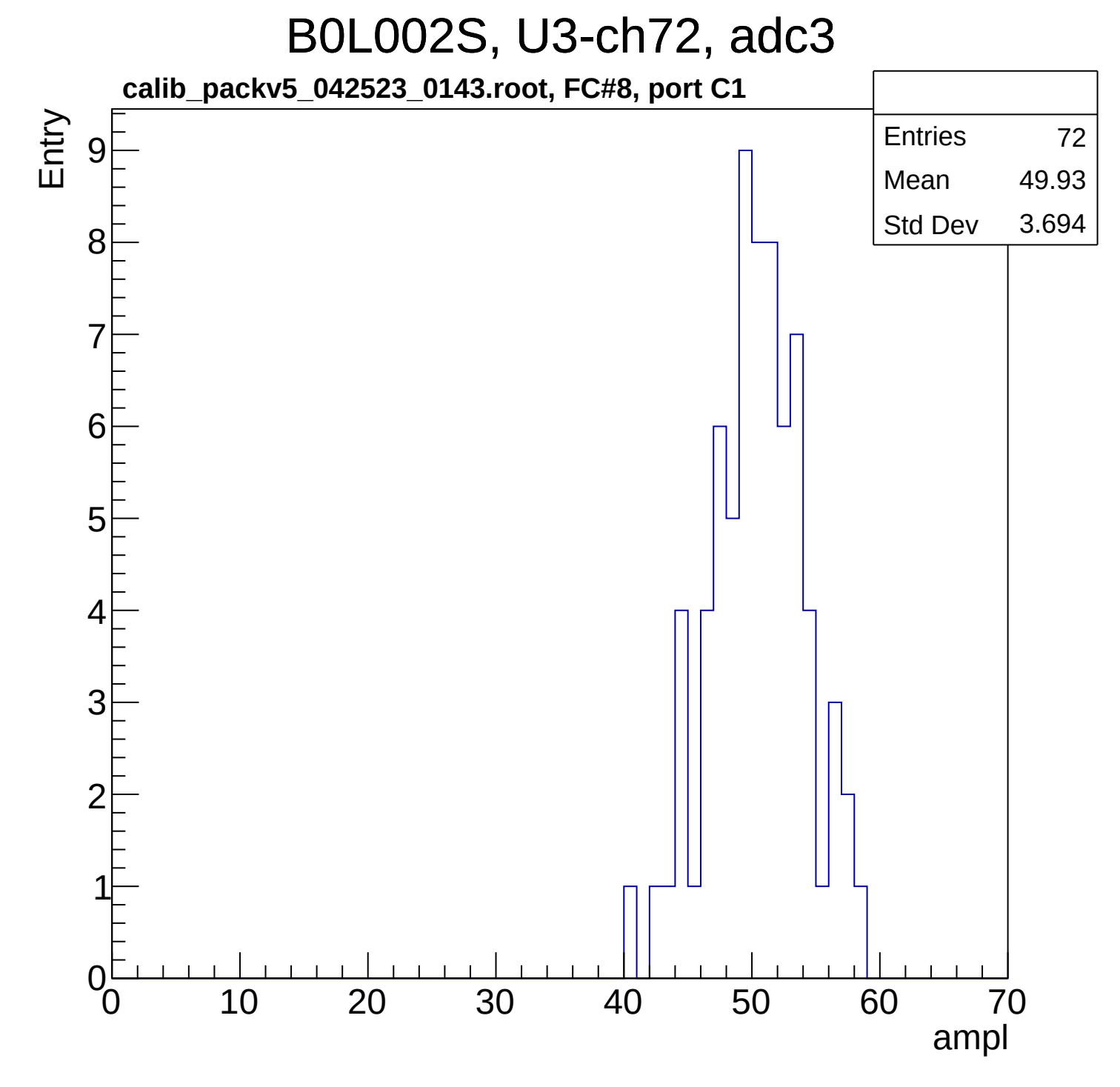
B0L002S, U3-ch72, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 49.93
Std Dev 3.694
Entry
ampl
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4
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6
7
8
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0
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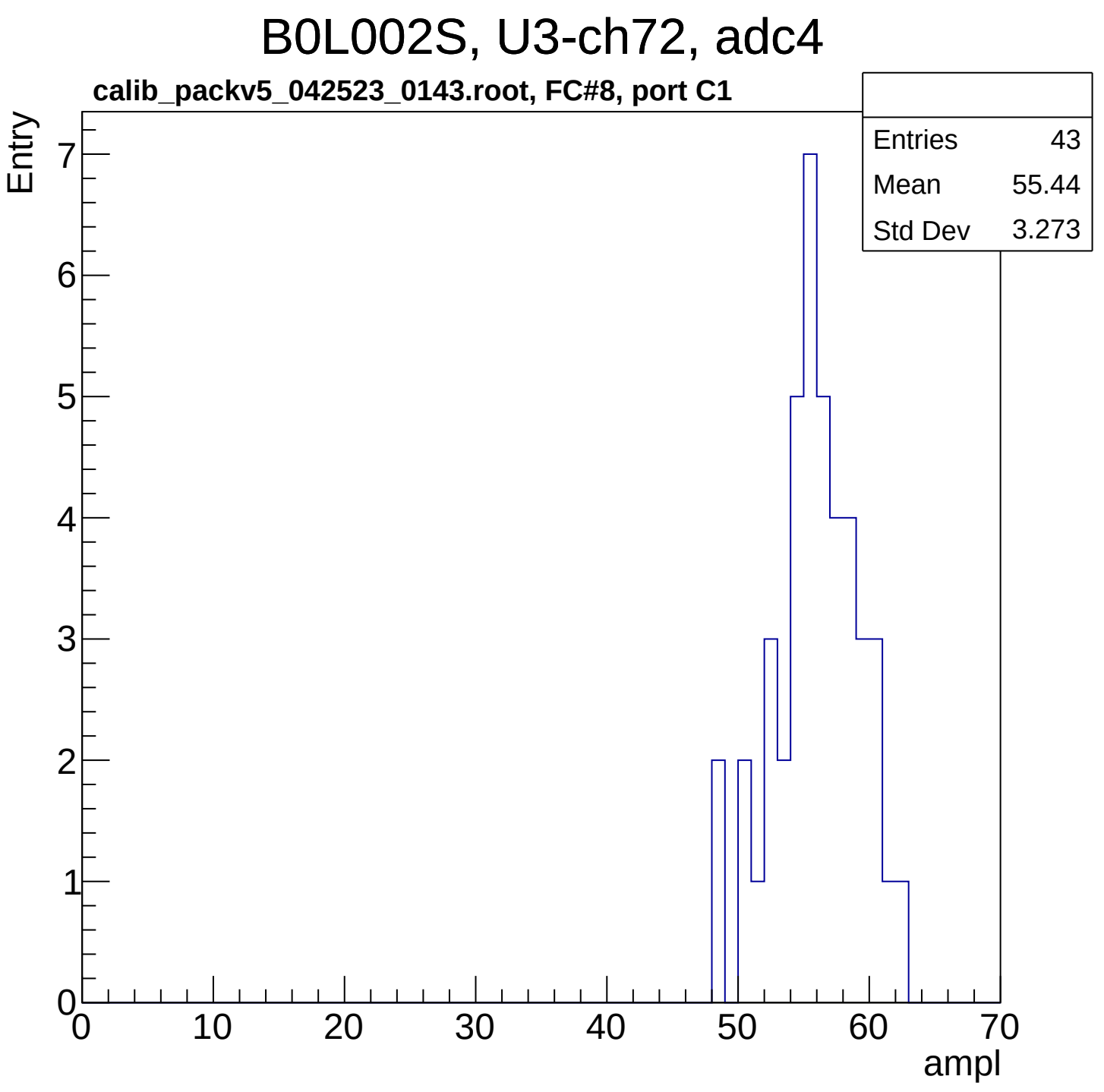
B0L002S, U3-ch72, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 55.44
Std Dev 3.273
Entry
ampl
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3
4
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6
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0
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20
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40
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60
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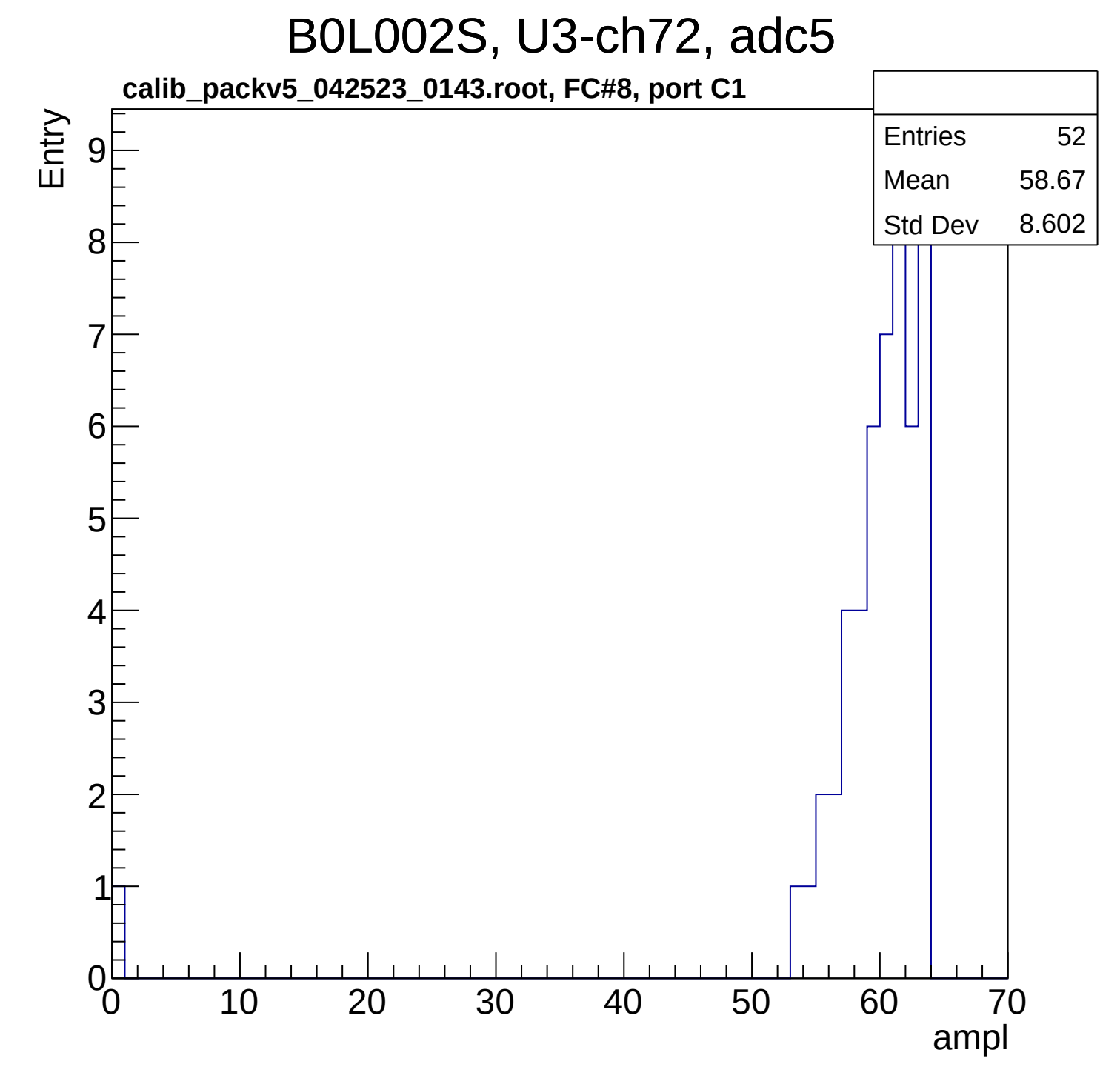
B0L002S, U3-ch72, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.67
Std Dev 8.602
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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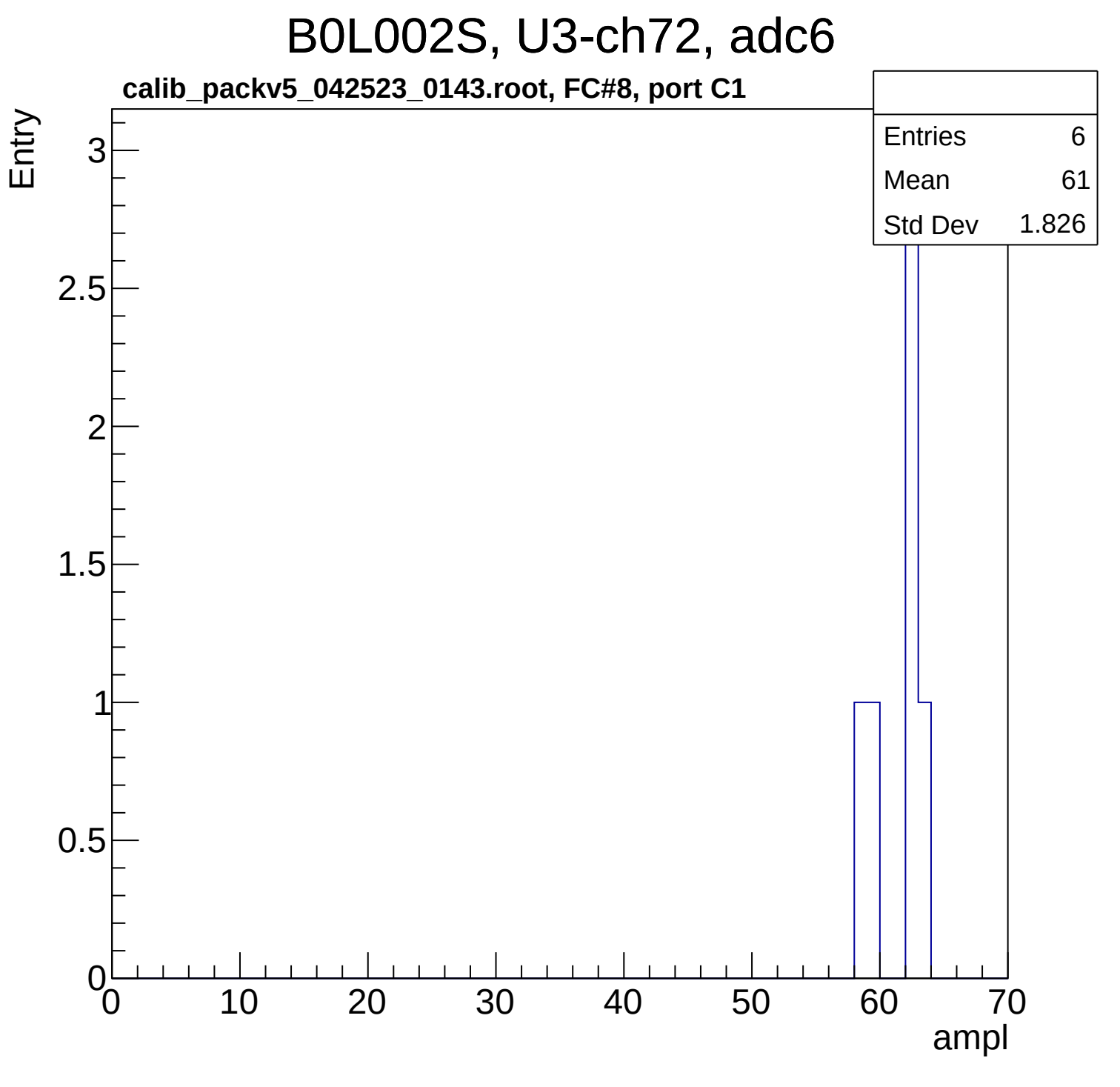
B0L002S, U3-ch72, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61
Std Dev 1.826
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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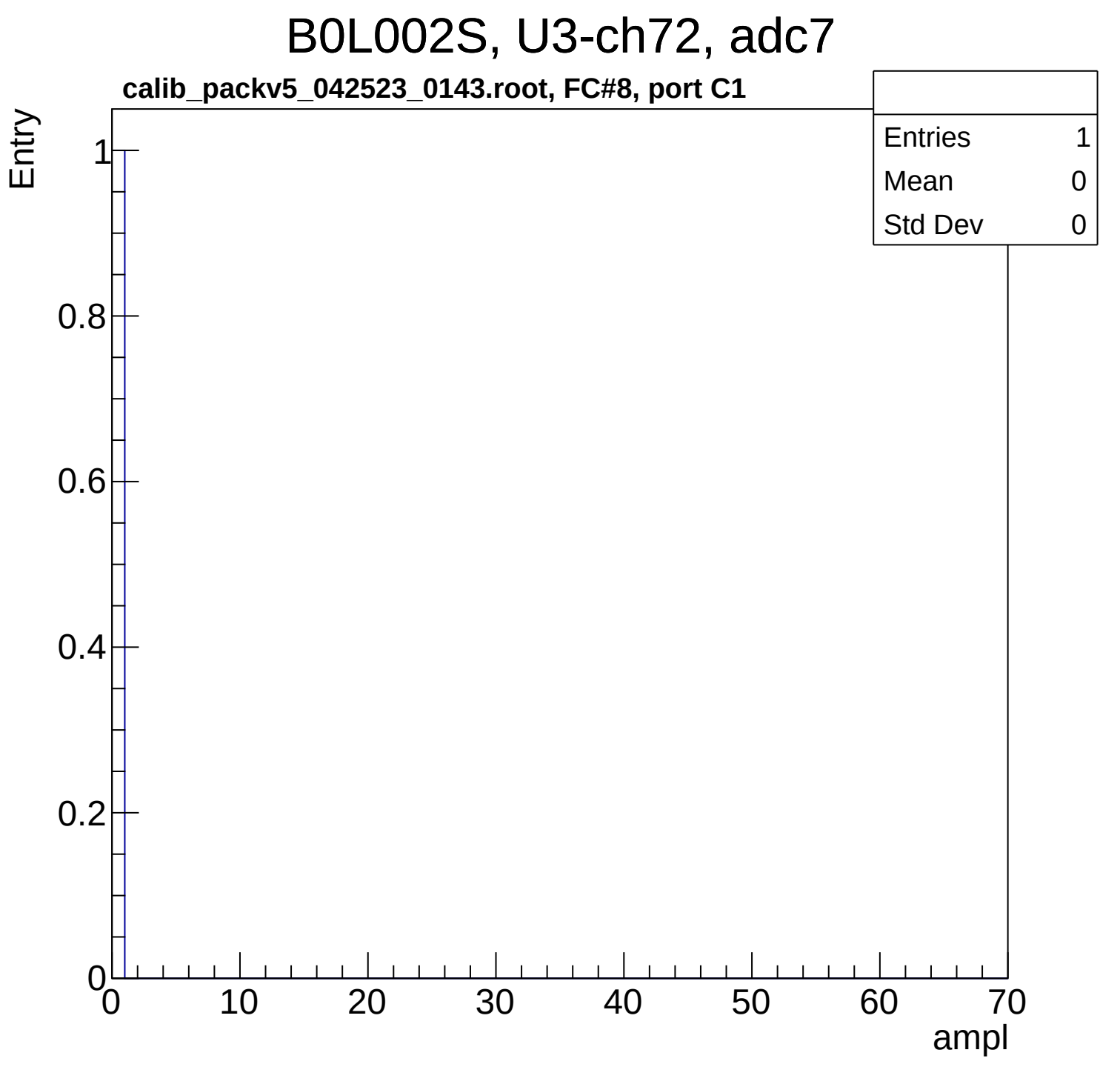
B0L002S, U3-ch72, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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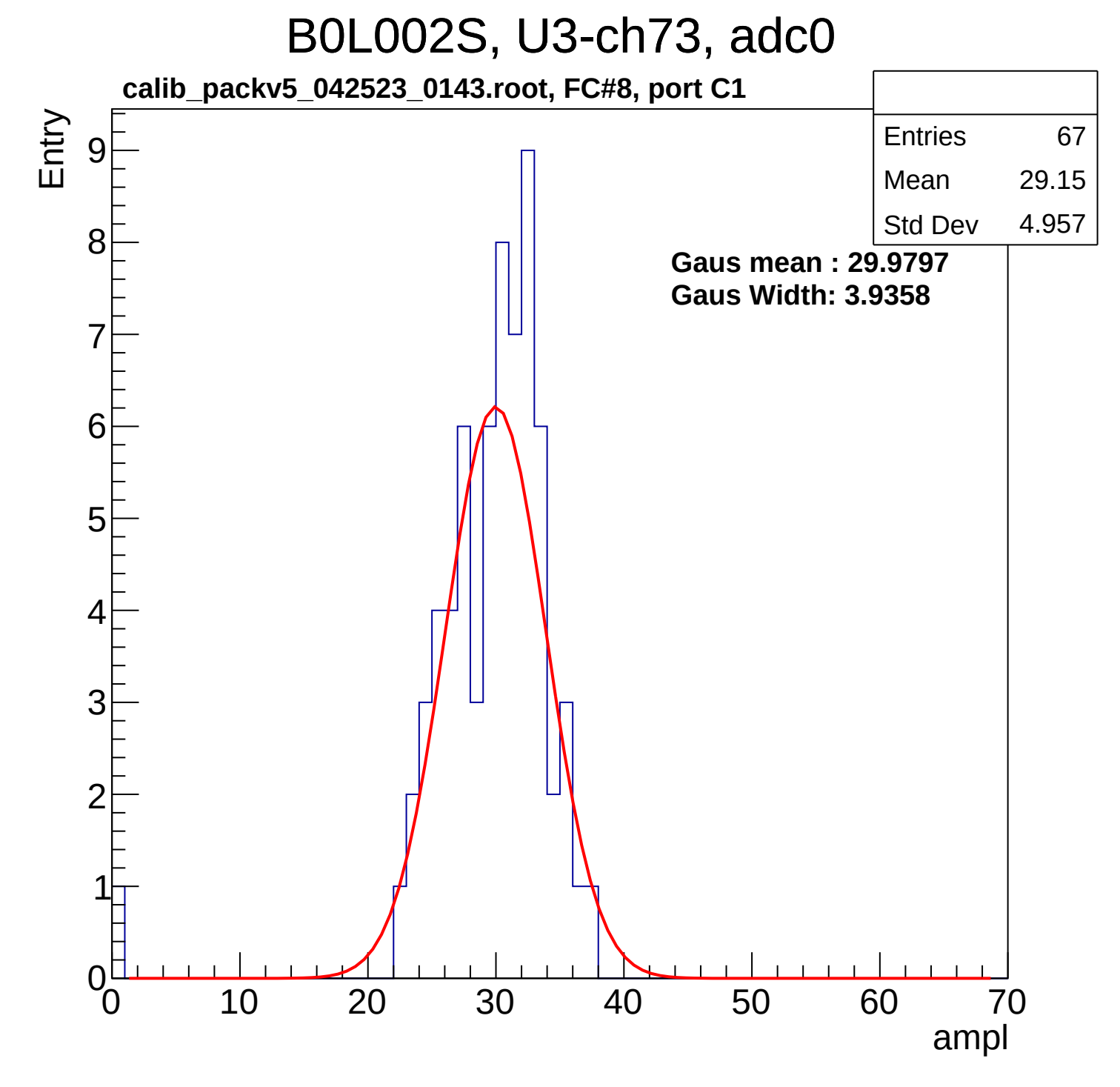
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch73, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.15
Std Dev 4.957
Gaus mean : 29.9797
Gaus Width: 3.9358
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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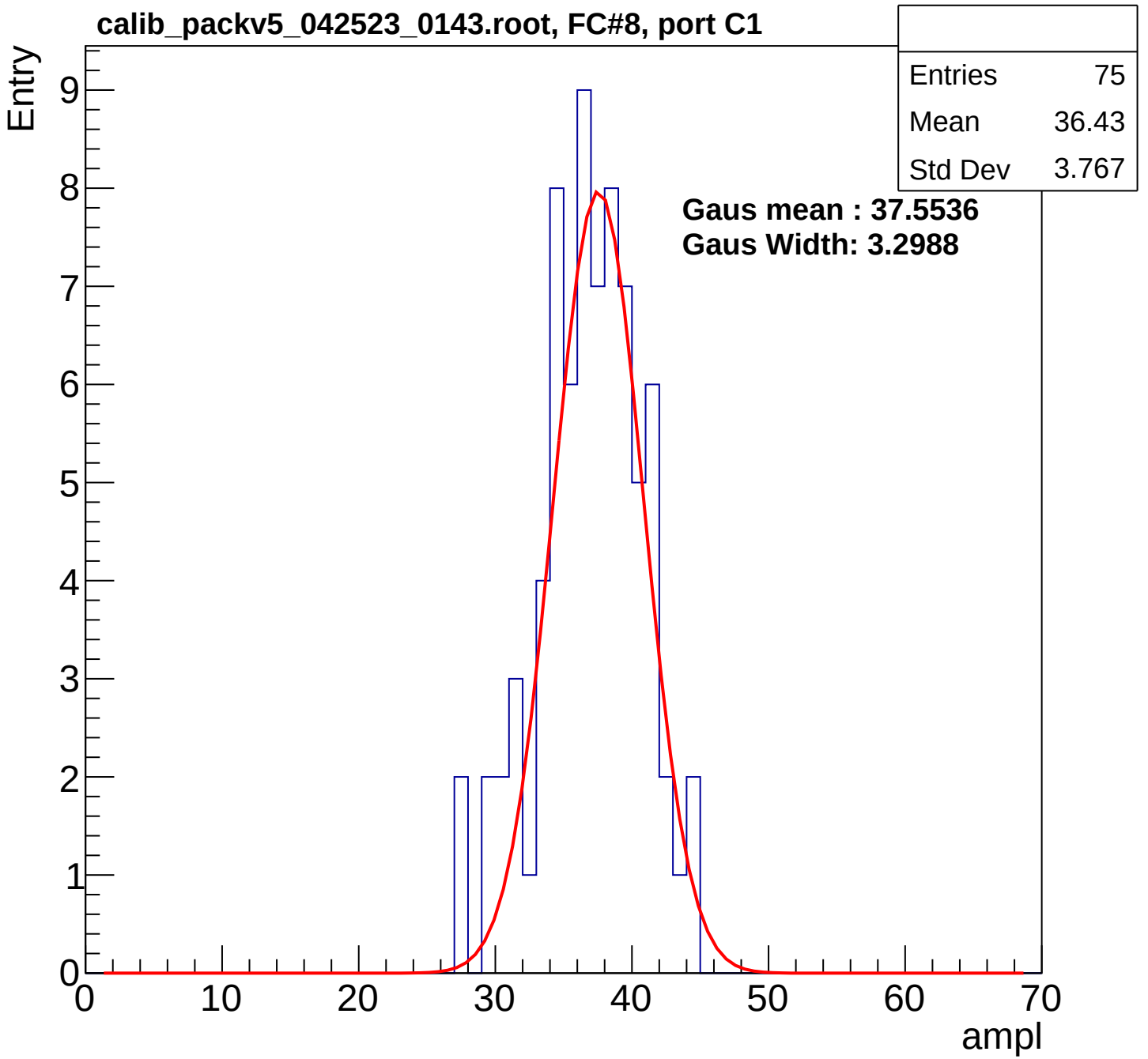
B0L002S, U3-ch73, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 36.43
Std Dev 3.767
Gaus mean : 37.5536
Gaus Width: 3.2988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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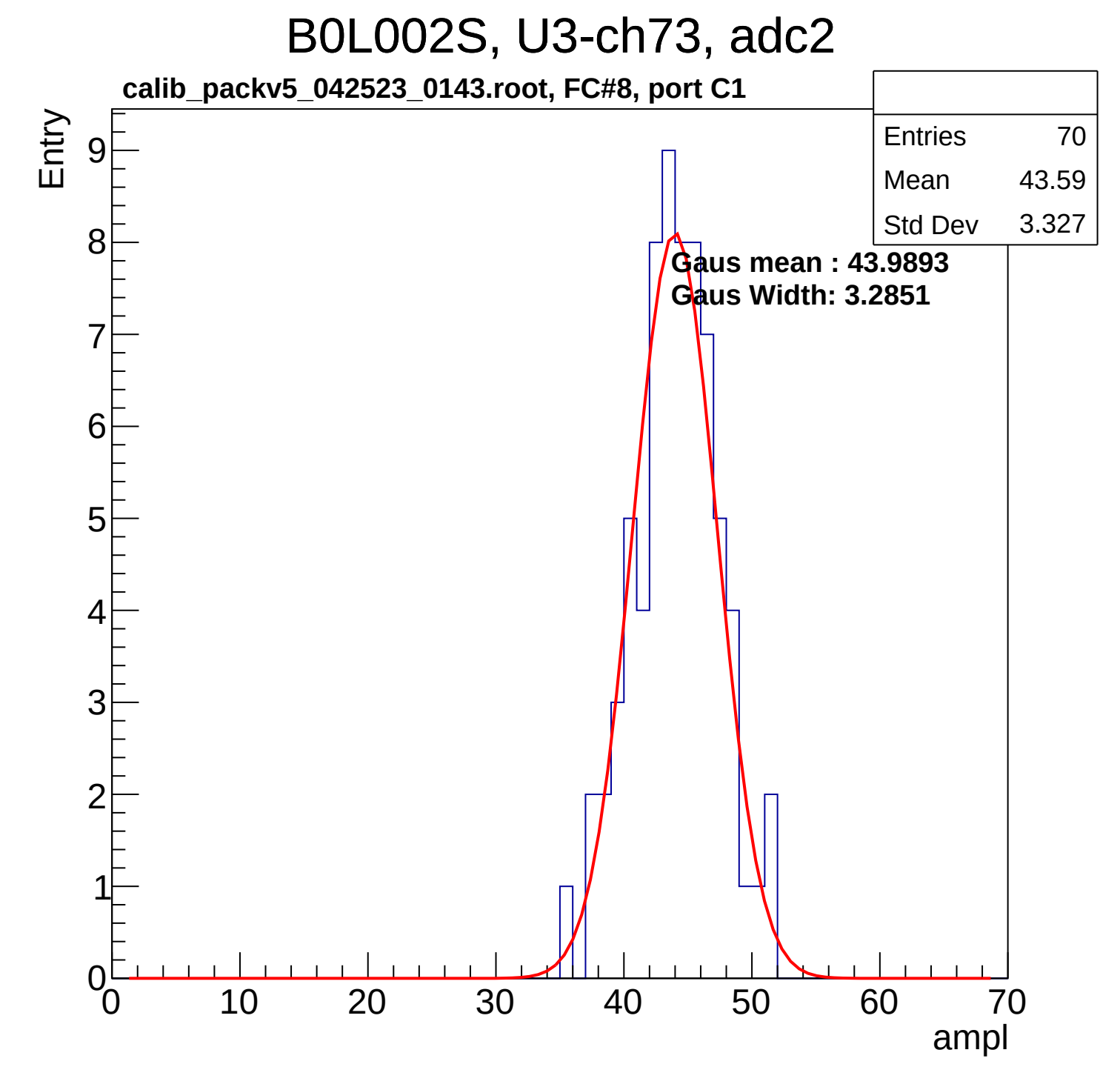
B0L002S, U3-ch73, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.59
Std Dev 3.327
Gaus mean : 43.9893
Gaus Width: 3.2851
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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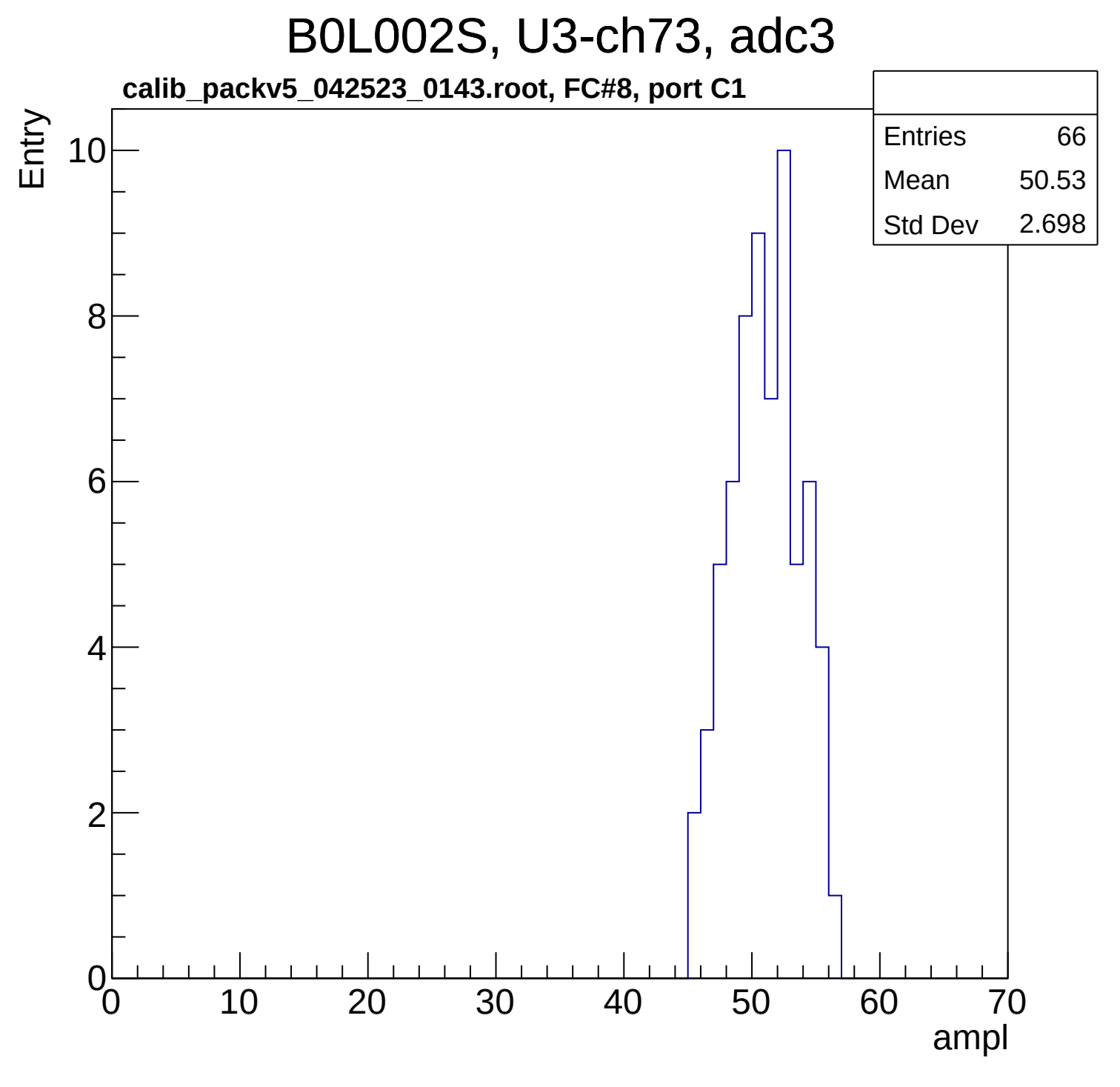
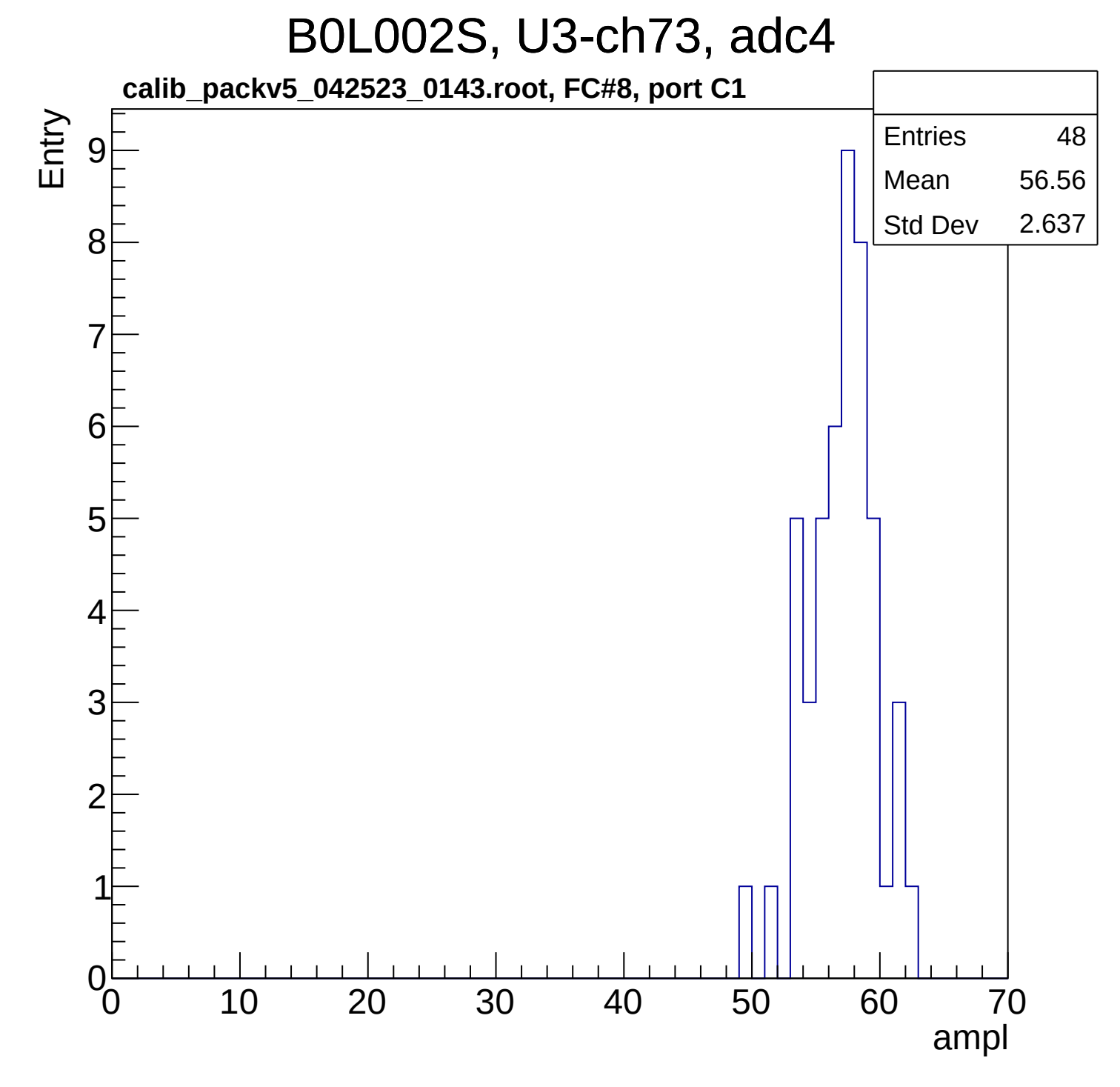
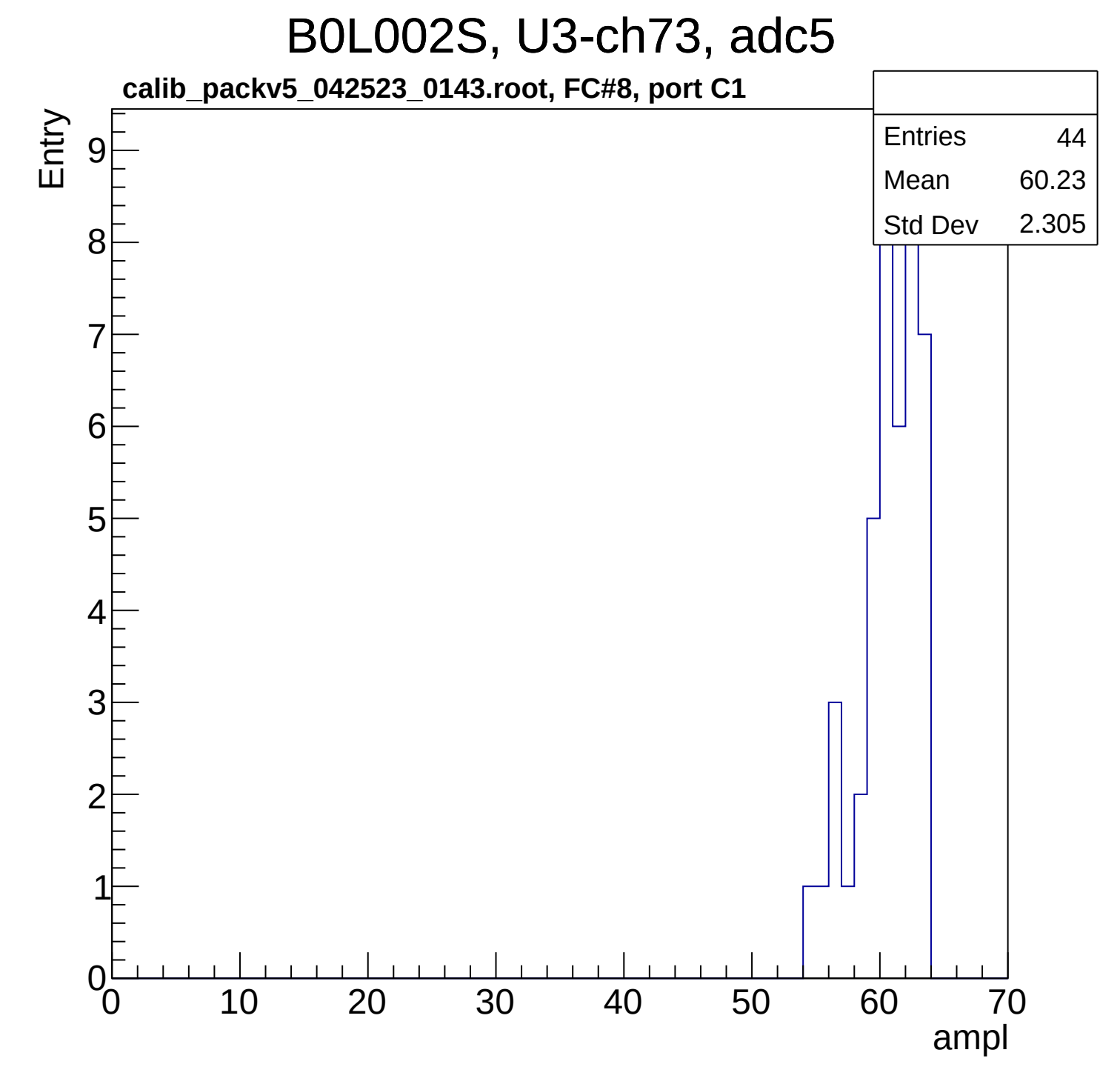

B0L002S, U3-ch73, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.53
Std Dev 2.698
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch73, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 56.56
Std Dev 2.637
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch73, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 60.23
Std Dev 2.305
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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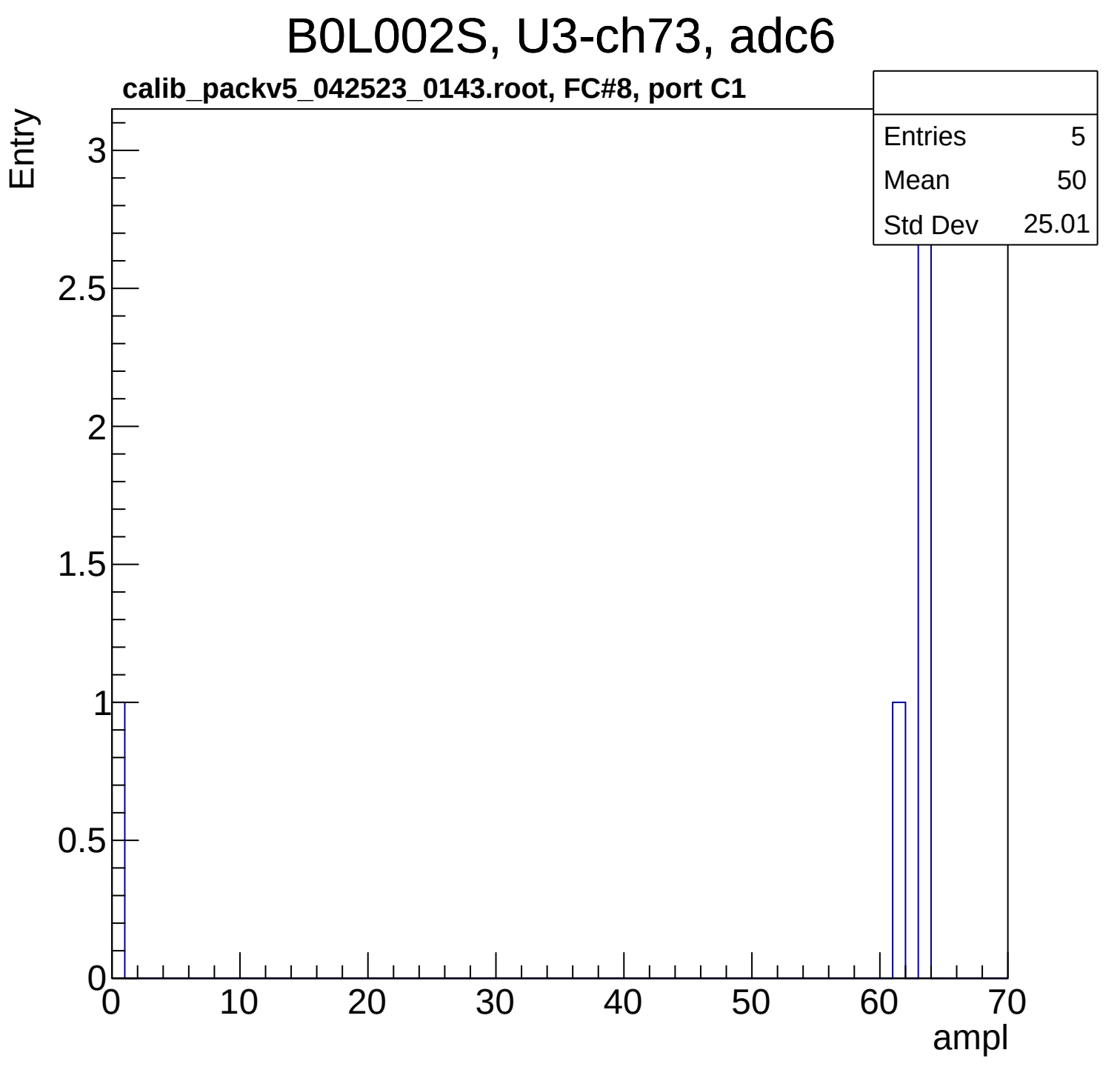
B0L002S, U3-ch73, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 50
Std Dev 25.01
Entry
ampl
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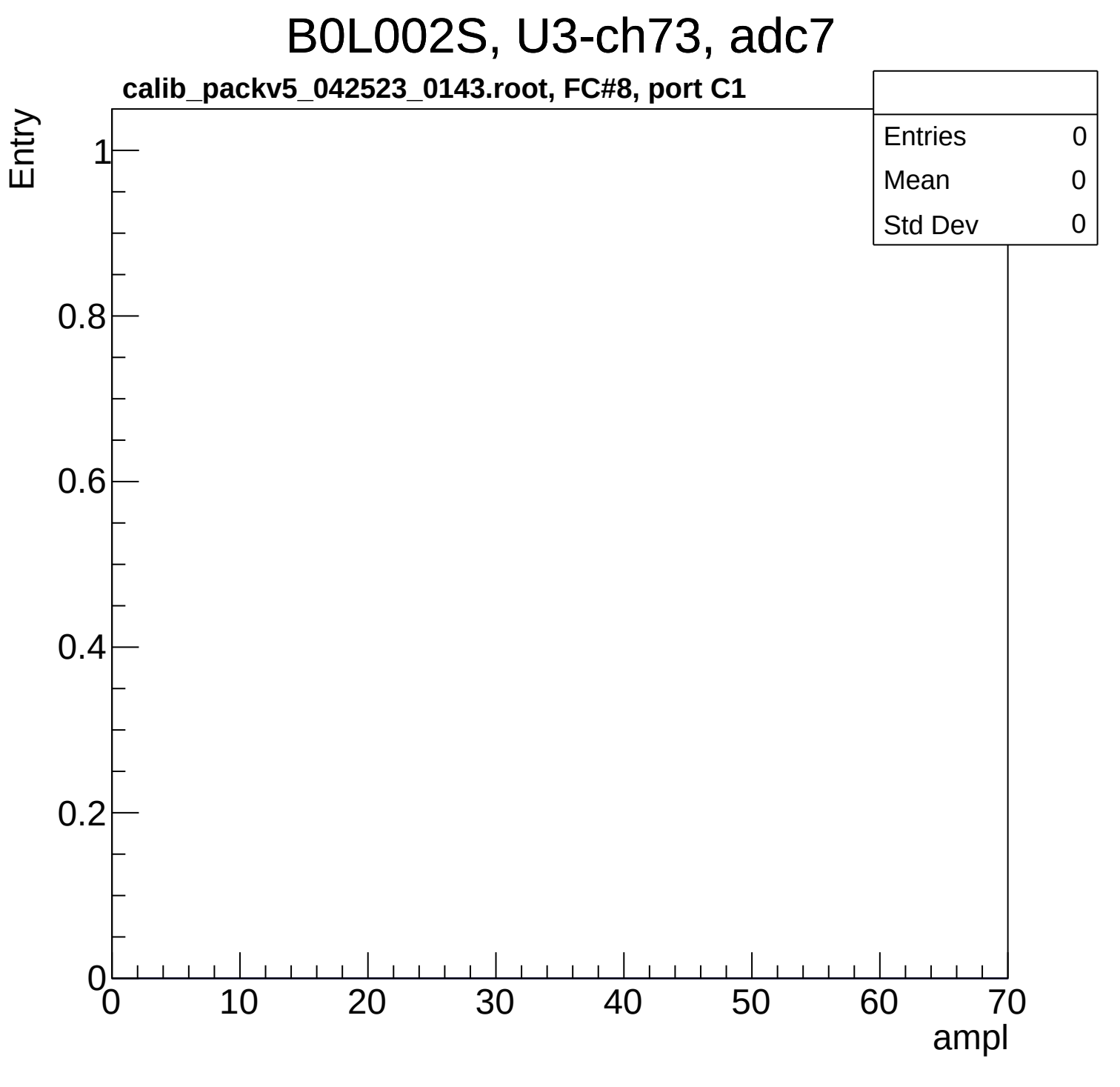
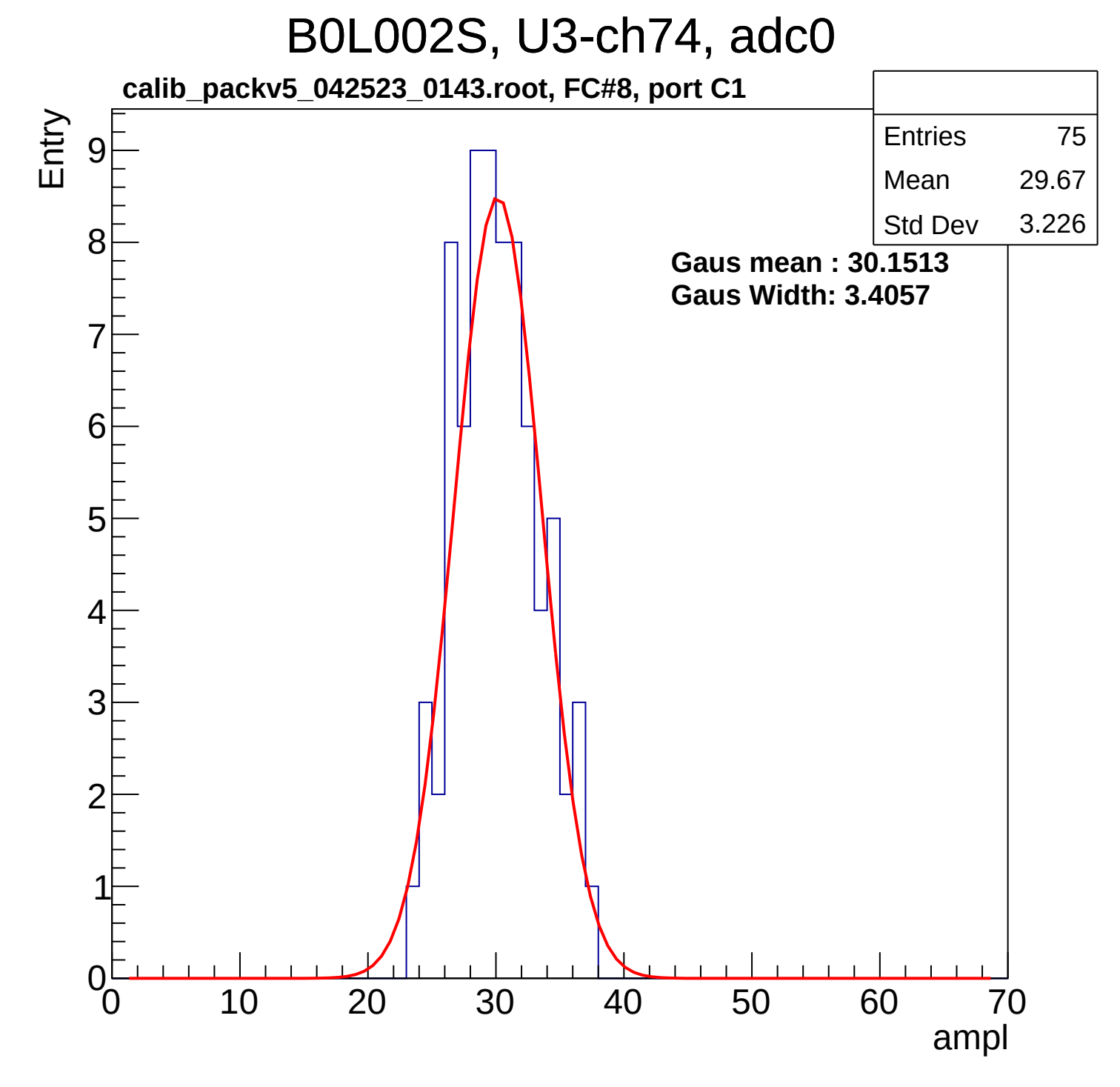
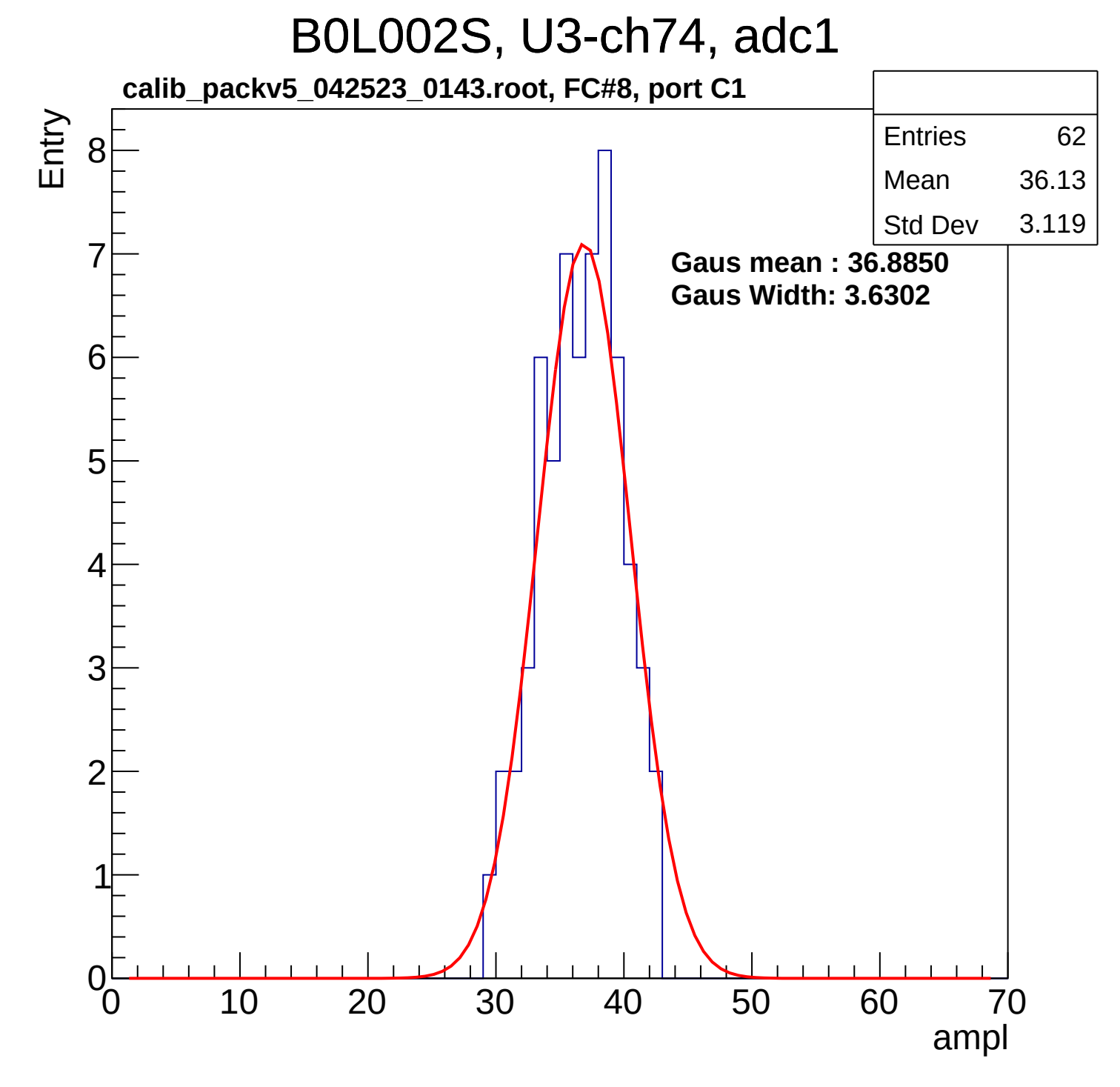
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch73, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch74, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.67
Std Dev 3.226
Gaus mean : 30.1513
Gaus Width: 3.4057
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U3-ch74, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 36.13
Std Dev 3.119
Gaus mean : 36.8850
Gaus Width: 3.6302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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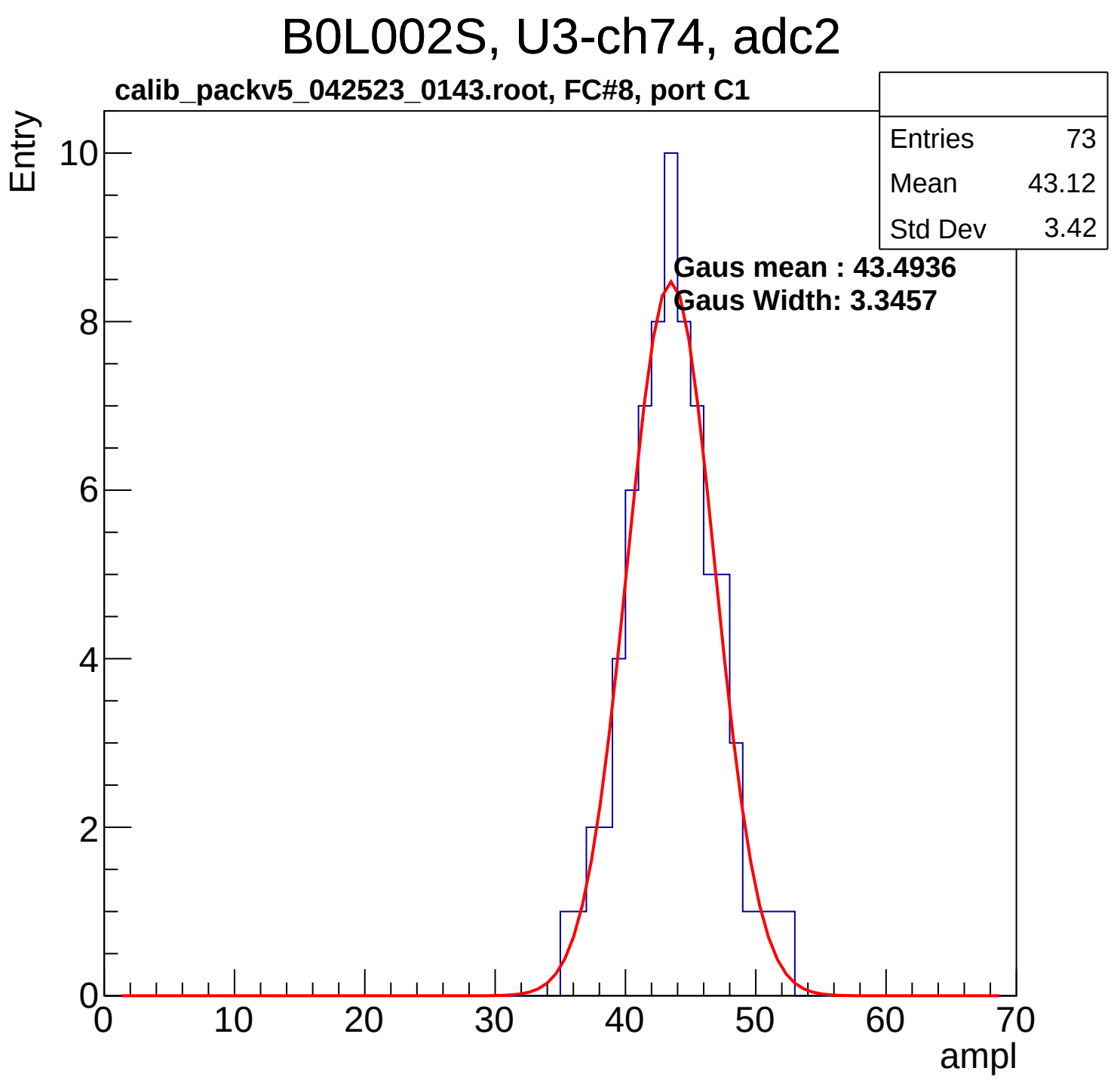
B0L002S, U3-ch74, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.12
Std Dev 3.42
Gaus mean : 43.4936
Gaus Width: 3.3457
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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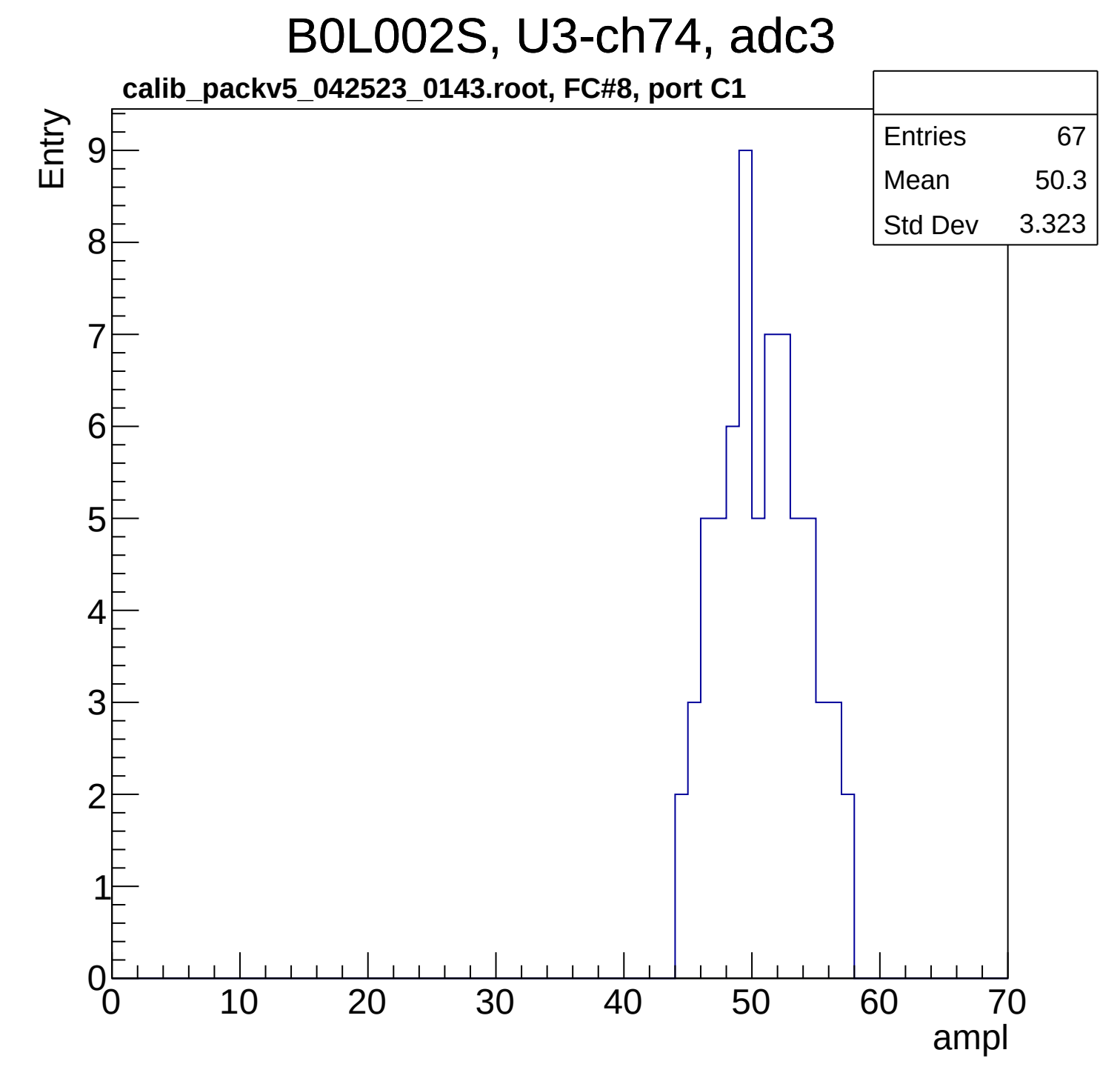
B0L002S, U3-ch74, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.3
Std Dev 3.323
Entry
ampl
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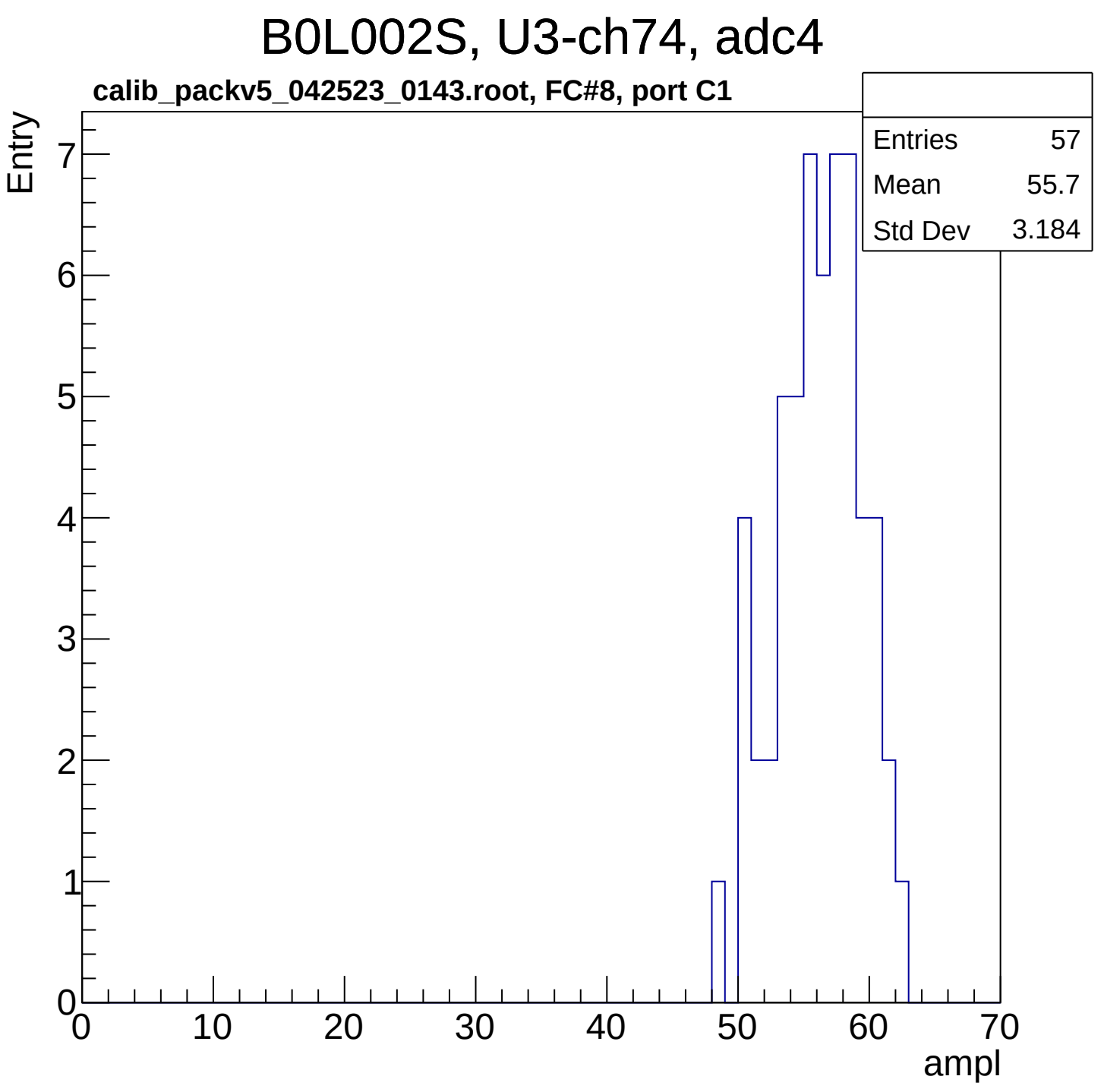
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch74, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.7
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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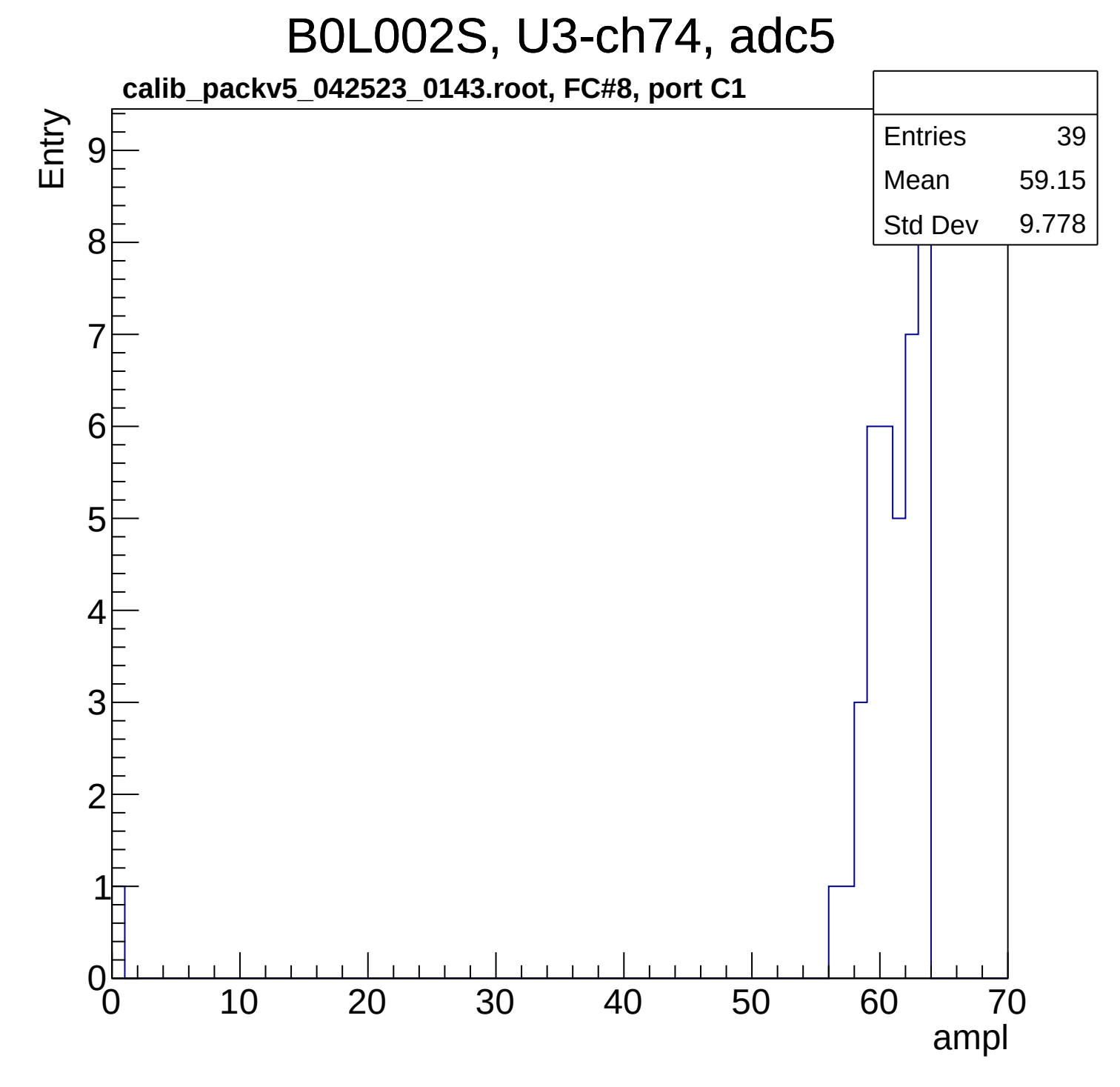
B0L002S, U3-ch74, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.15
Std Dev 9.778
Entry
ampl
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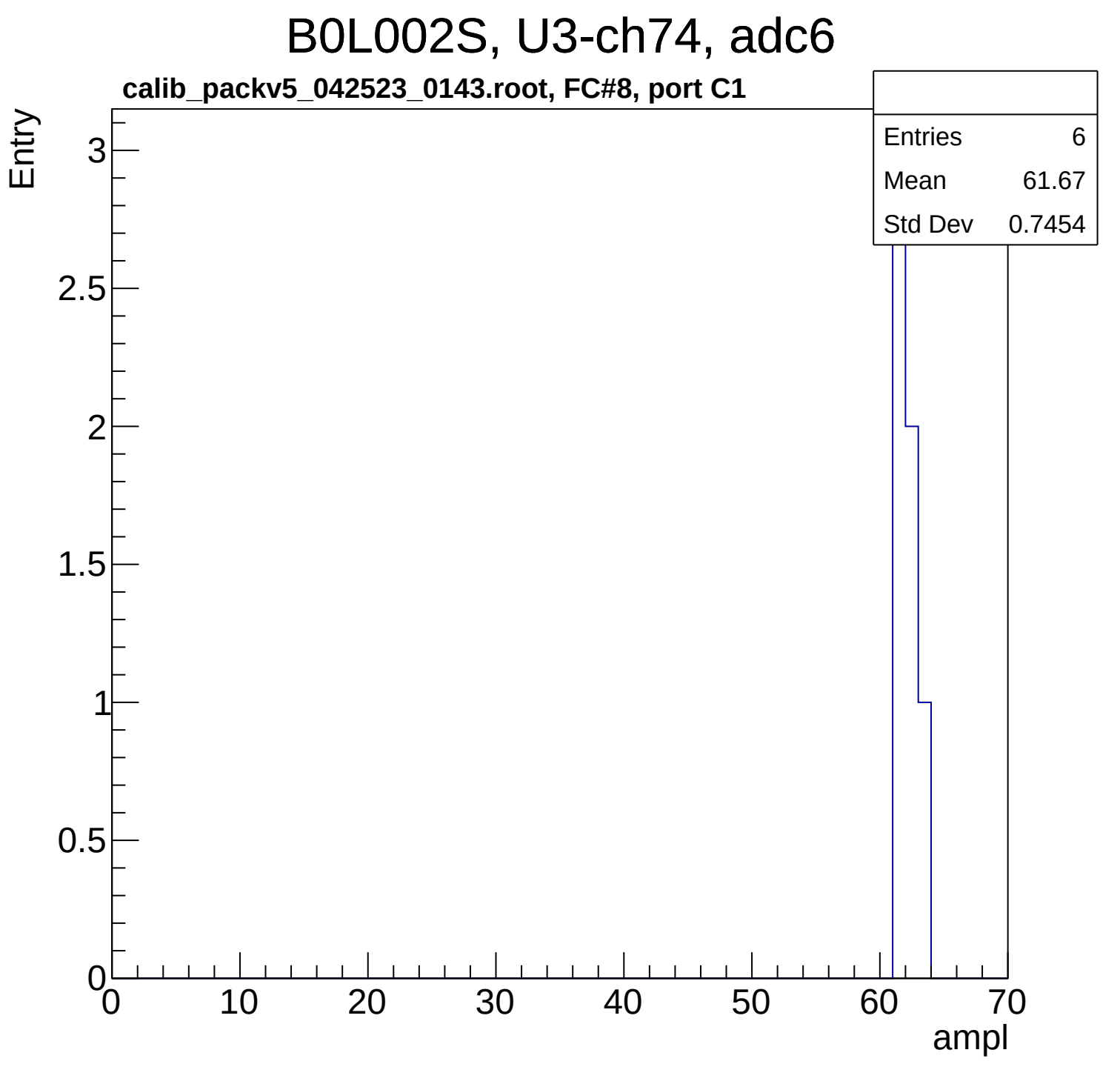
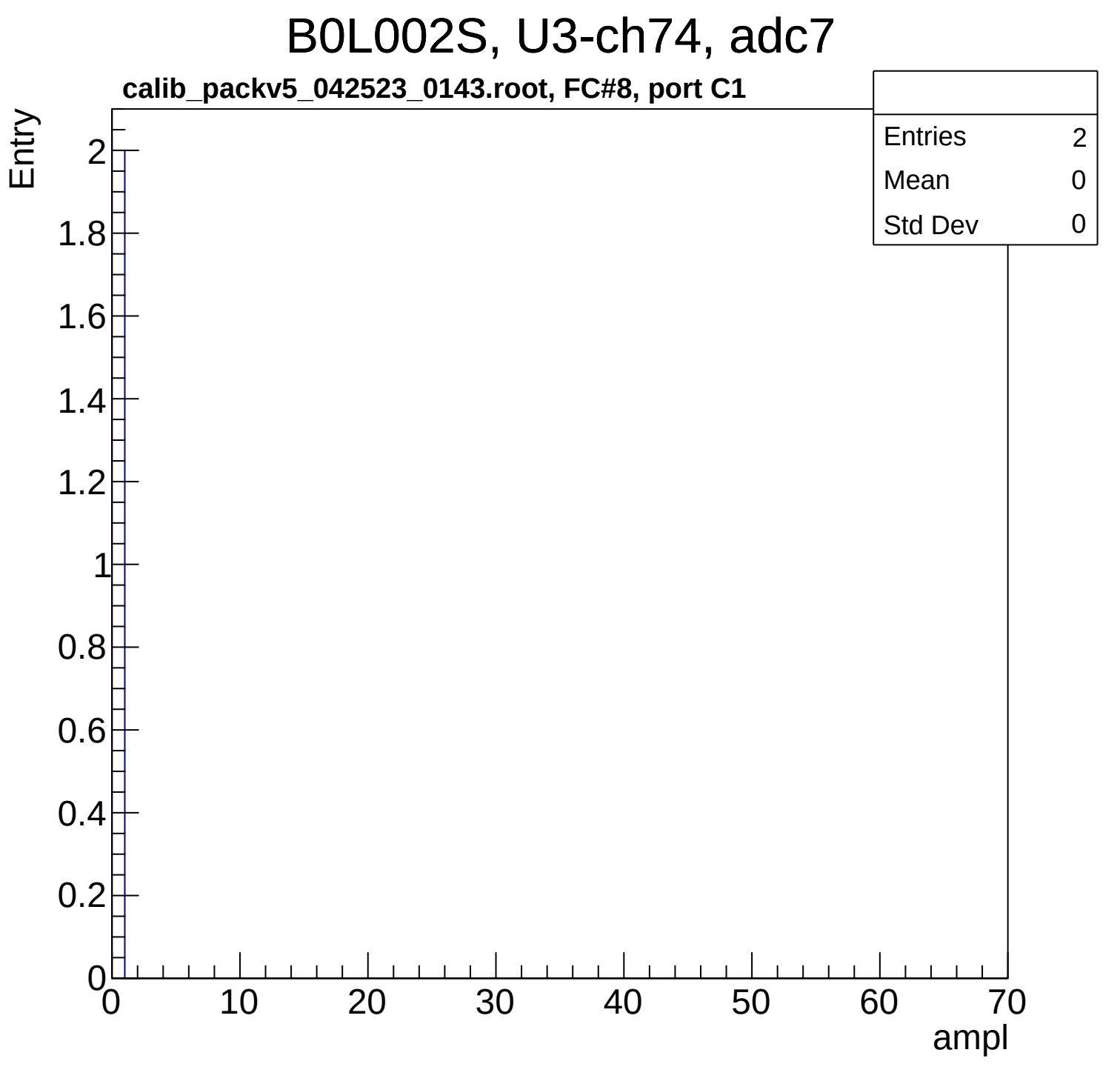
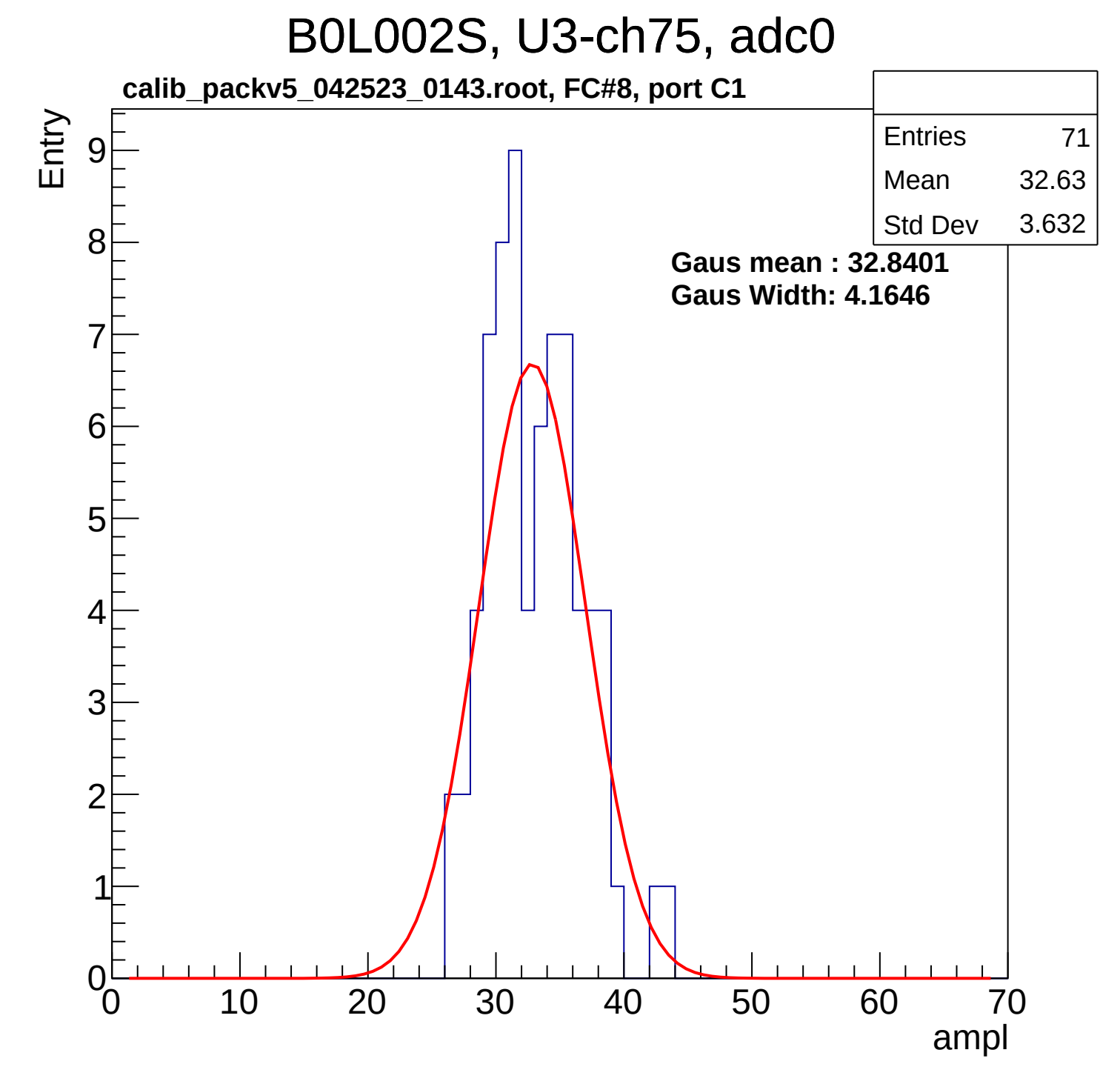
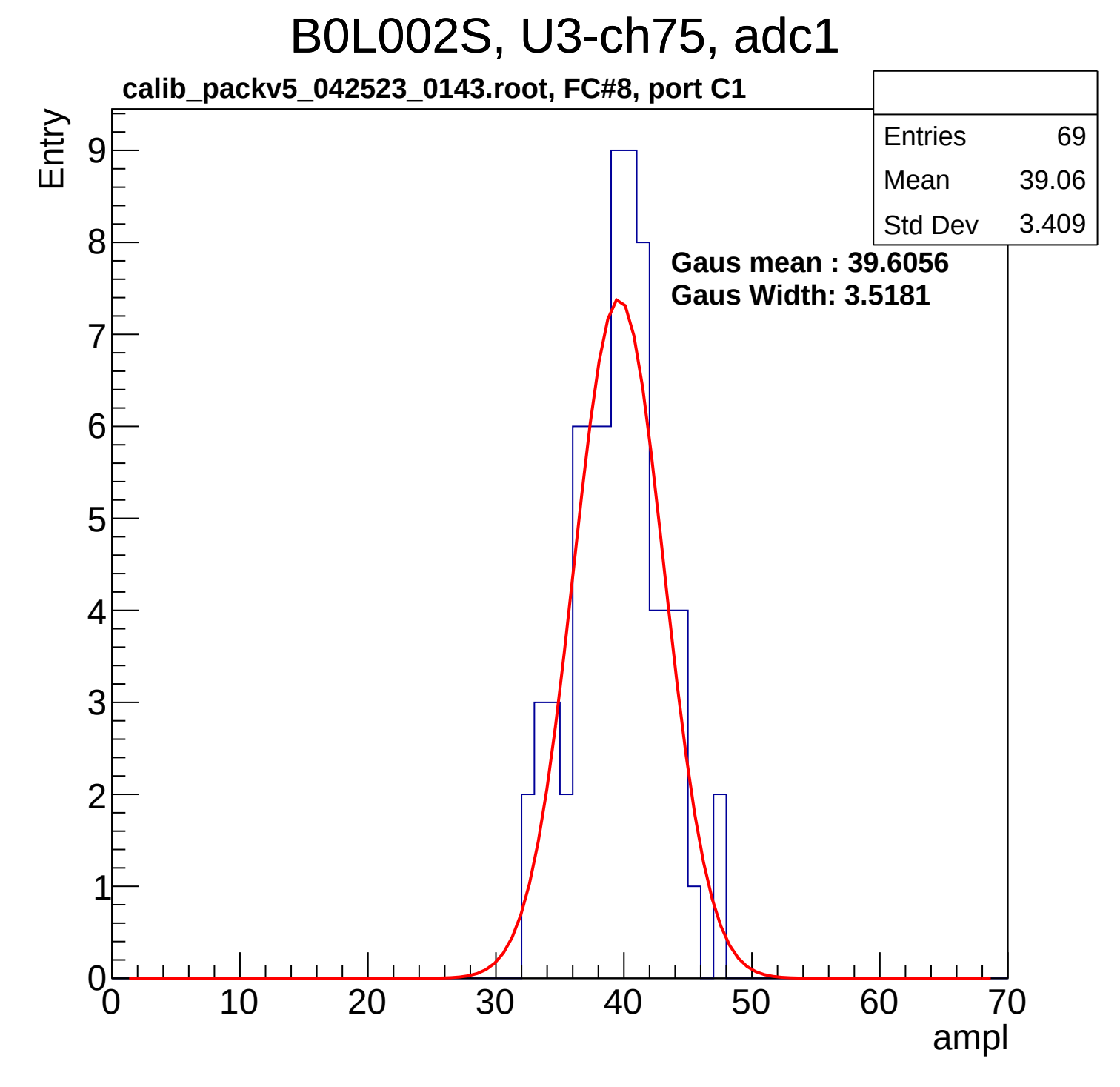
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch74, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.67
Std Dev 0.7454
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch74, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch75, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 32.63
Std Dev 3.632
Gaus mean : 32.8401
Gaus Width: 4.1646
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch75, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 39.06
Std Dev 3.409
Gaus mean : 39.6056
Gaus Width: 3.5181
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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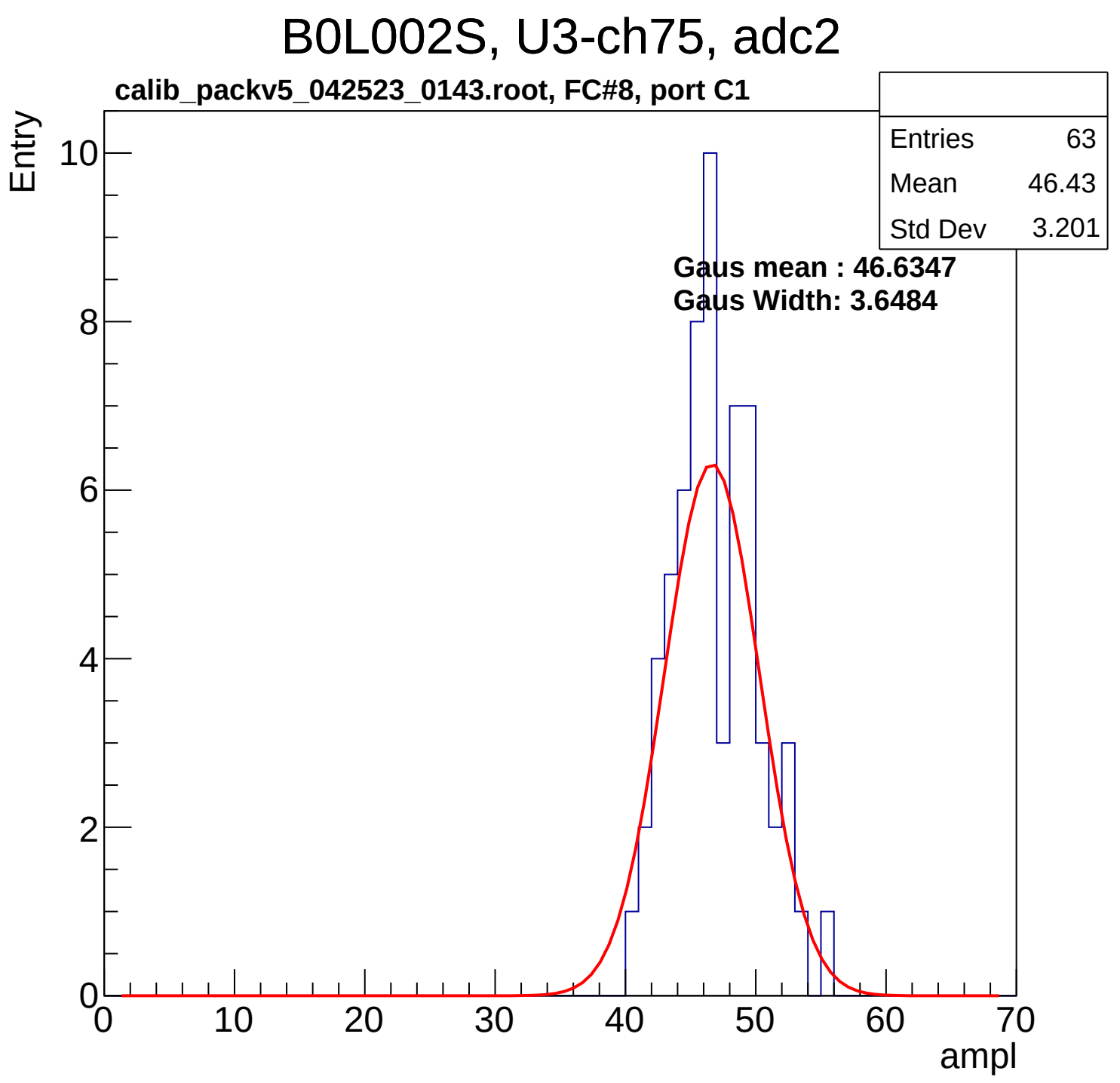
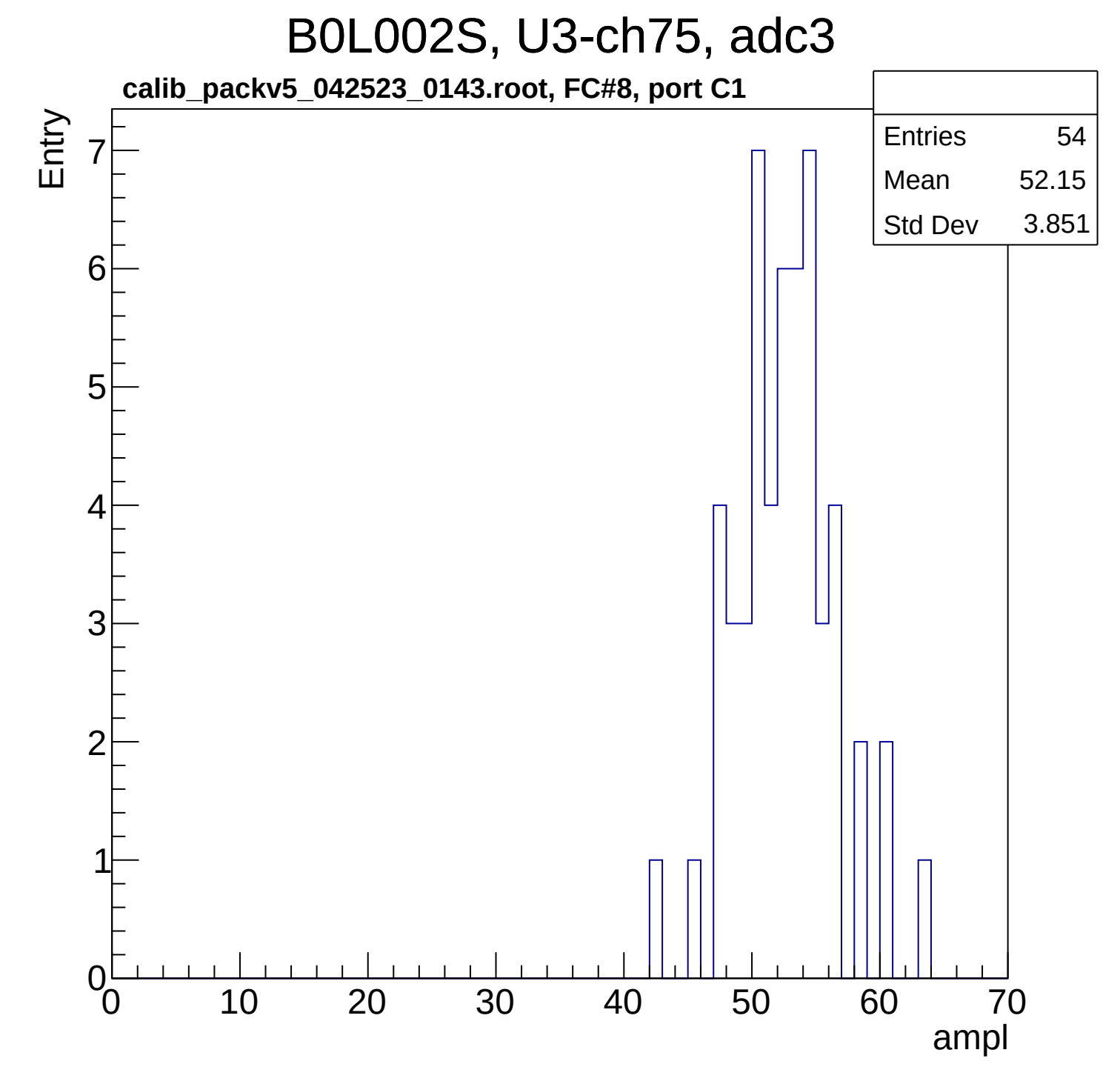

B0L002S, U3-ch75, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 46.43
Std Dev 3.201
Gaus mean : 46.6347
Gaus Width: 3.6484
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch75, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 52.15
Std Dev 3.851
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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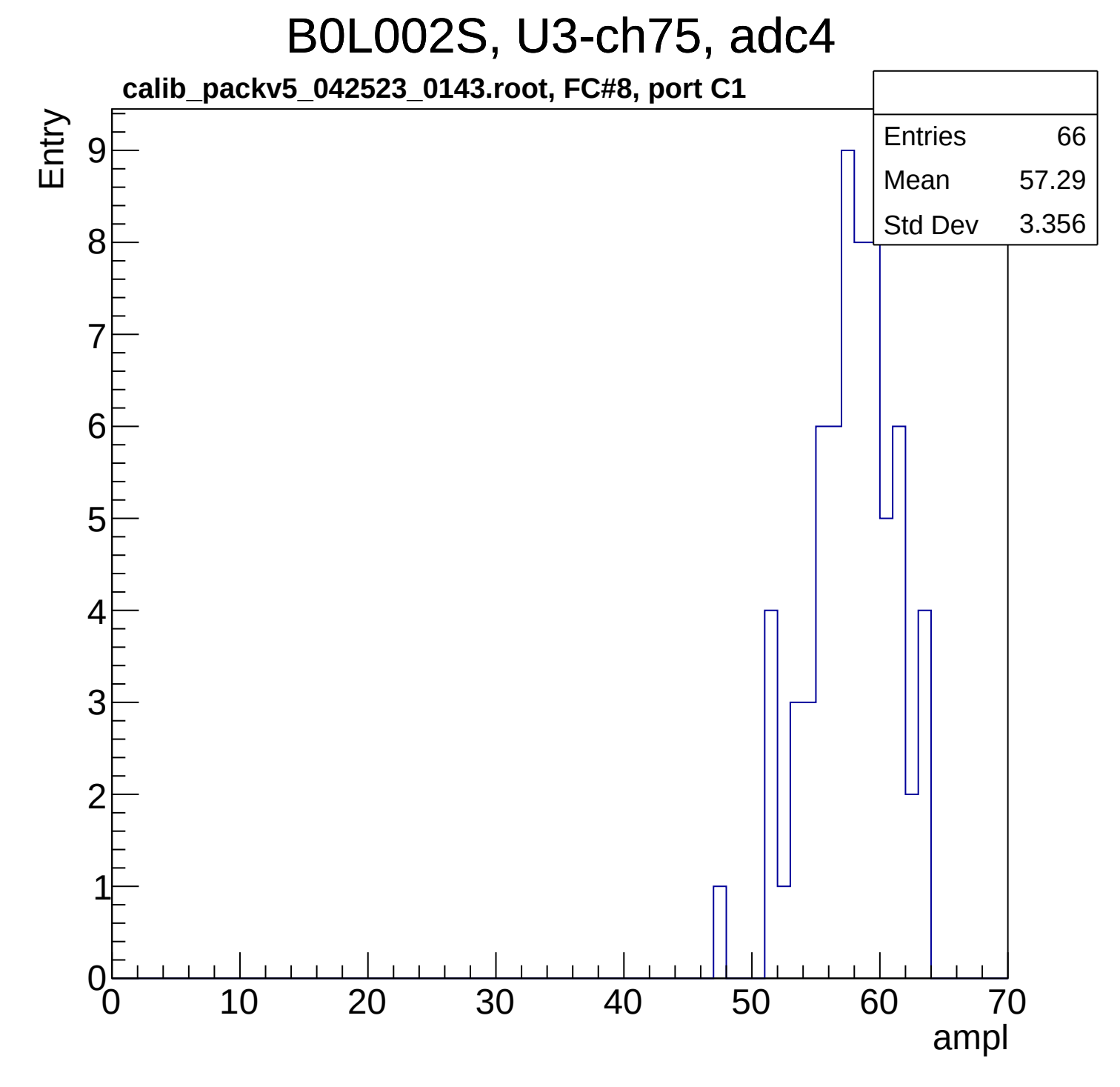
B0L002S, U3-ch75, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 57.29
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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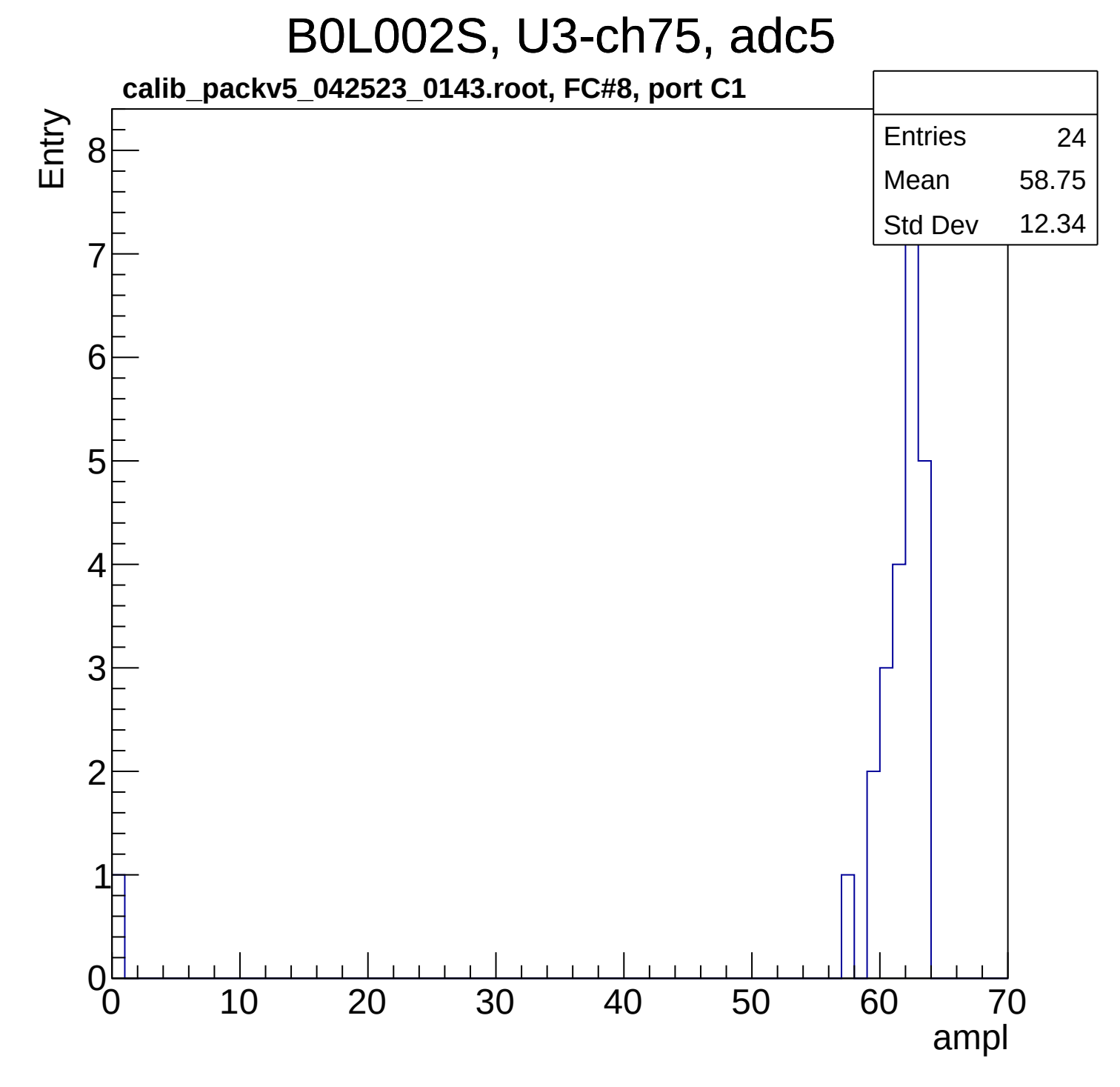
B0L002S, U3-ch75, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 58.75
Std Dev 12.34
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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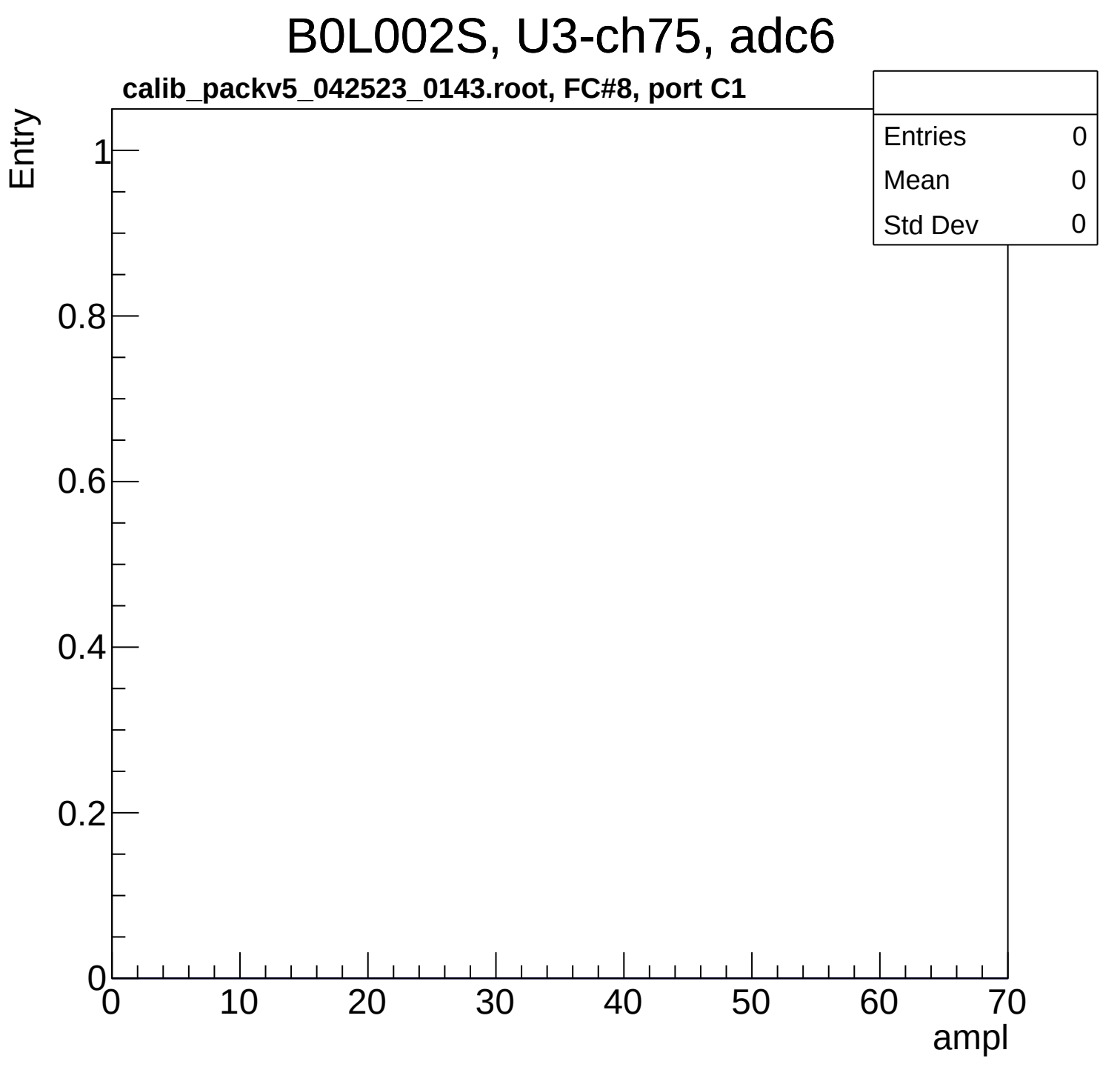
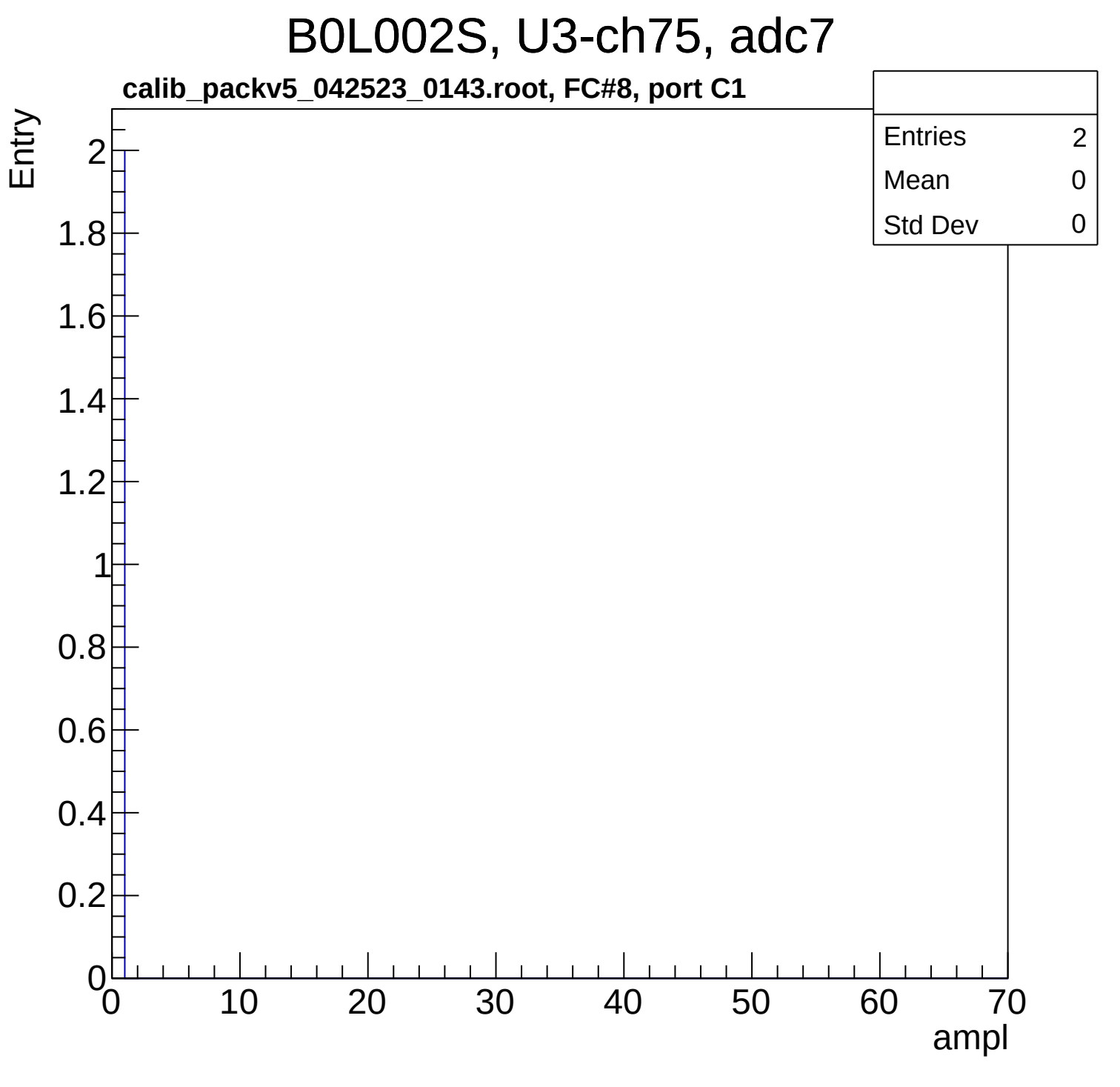

B0L002S, U3-ch75, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch75, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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30
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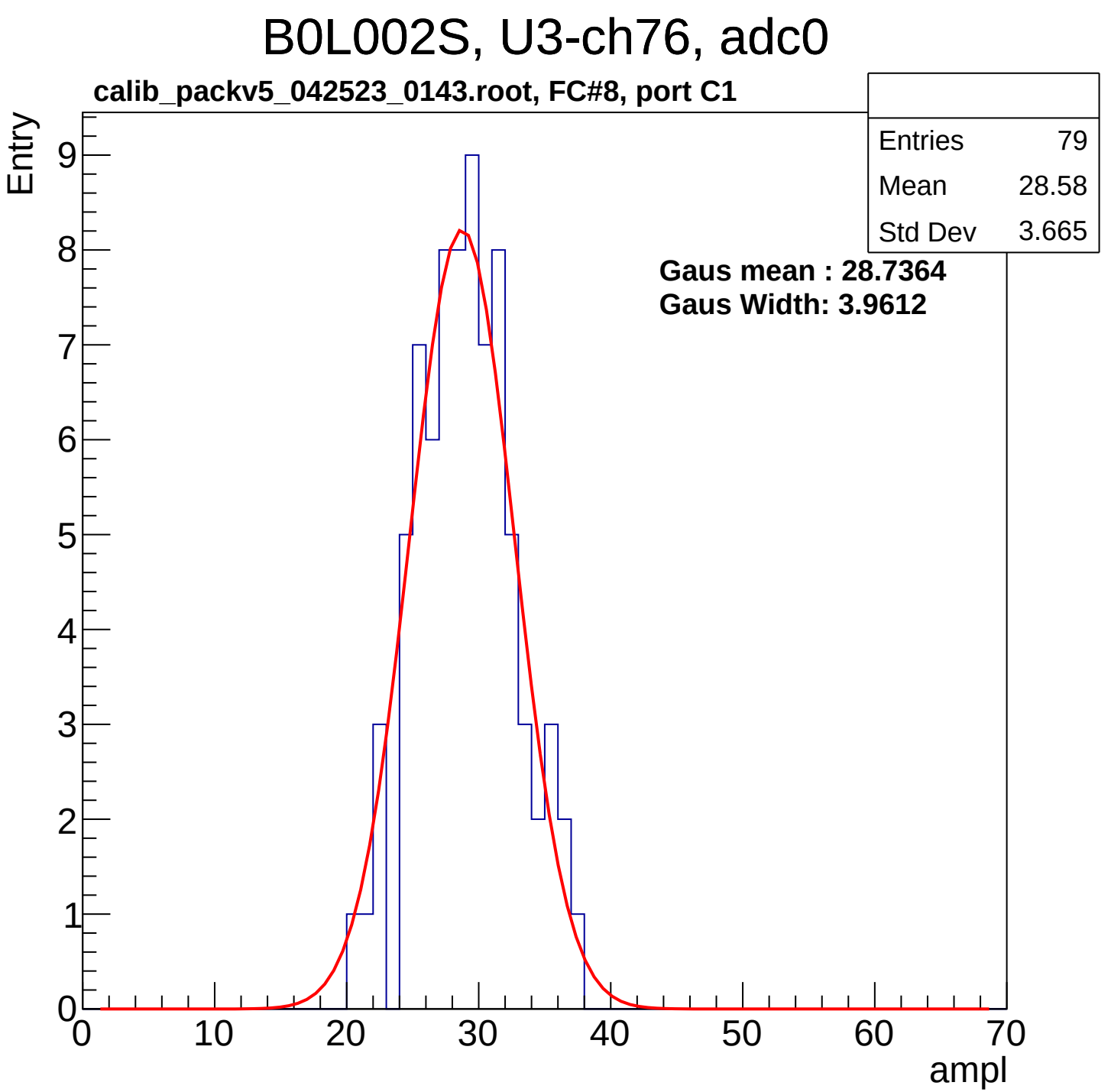
50
60
70
ampl


B0L002S, U3-ch76, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 28.58
Std Dev 3.665
Gaus mean : 28.7364
Gaus Width: 3.9612
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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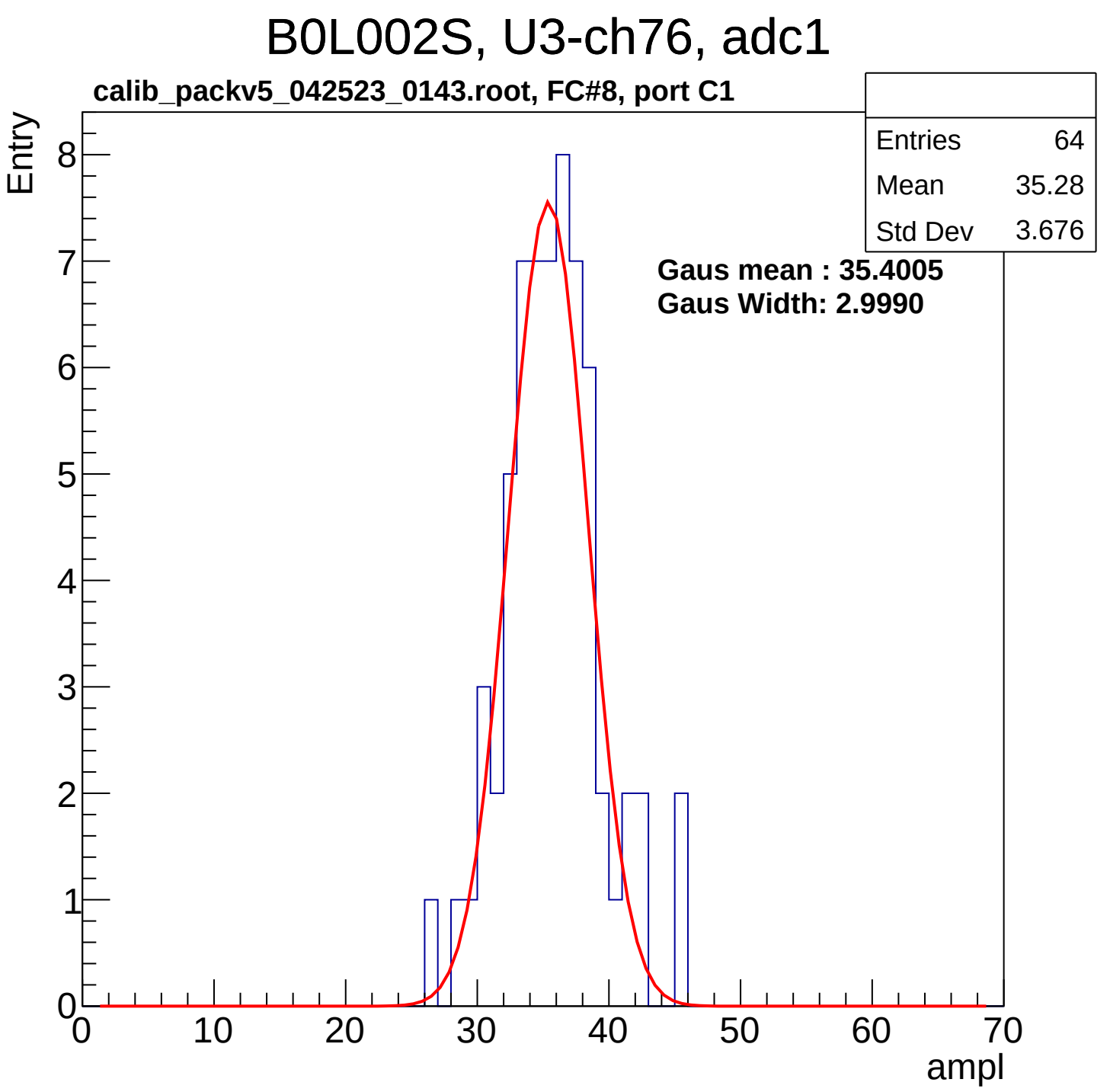
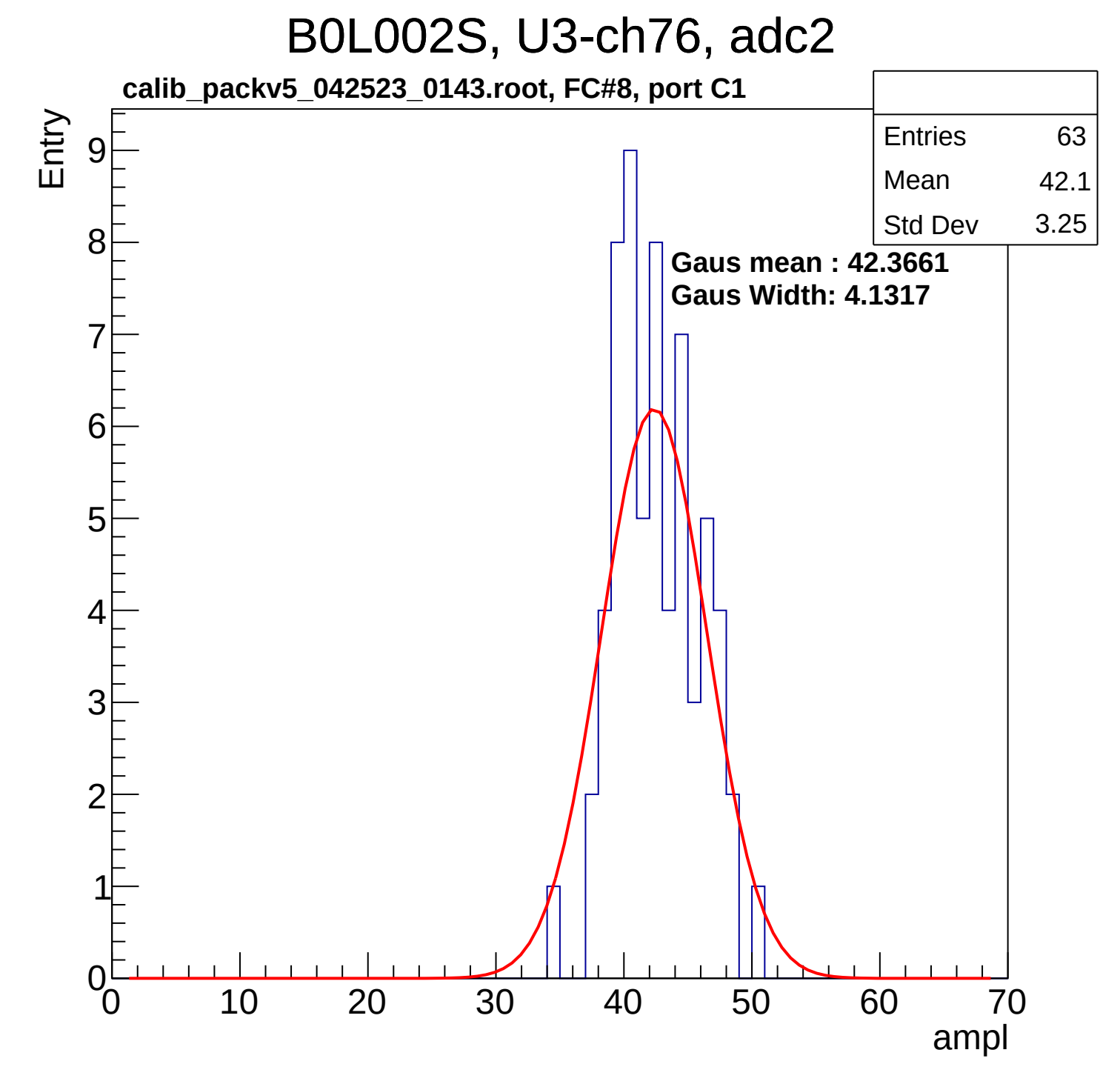

B0L002S, U3-ch76, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 35.28
Std Dev 3.676
Gaus mean : 35.4005
Gaus Width: 2.9990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch76, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 42.1
Std Dev 3.25
Gaus mean : 42.3661
Gaus Width: 4.1317
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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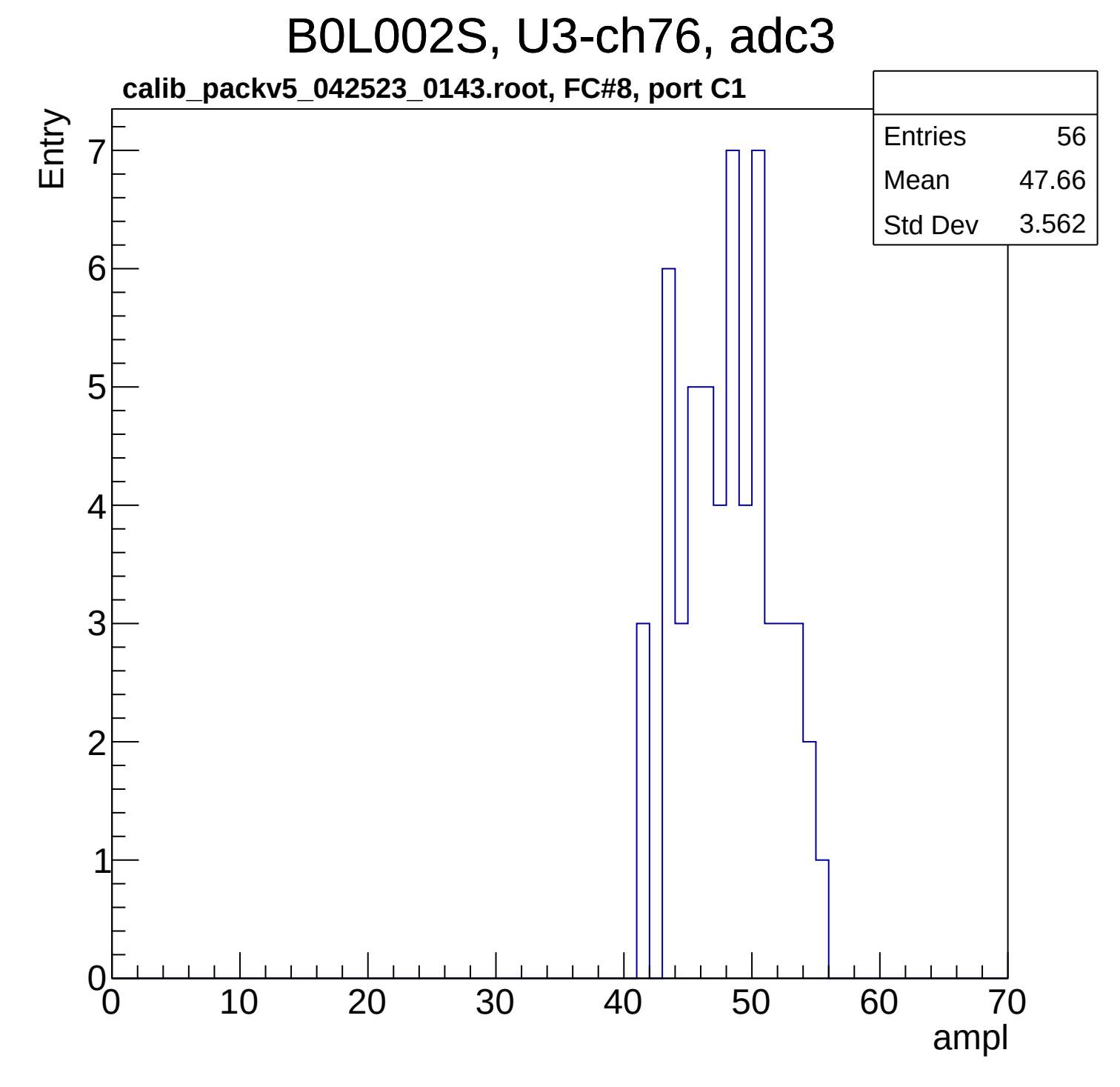
B0L002S, U3-ch76, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 47.66
Std Dev 3.562
Entry
ampl
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3
4
5
6
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0
10
20
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40
50
60
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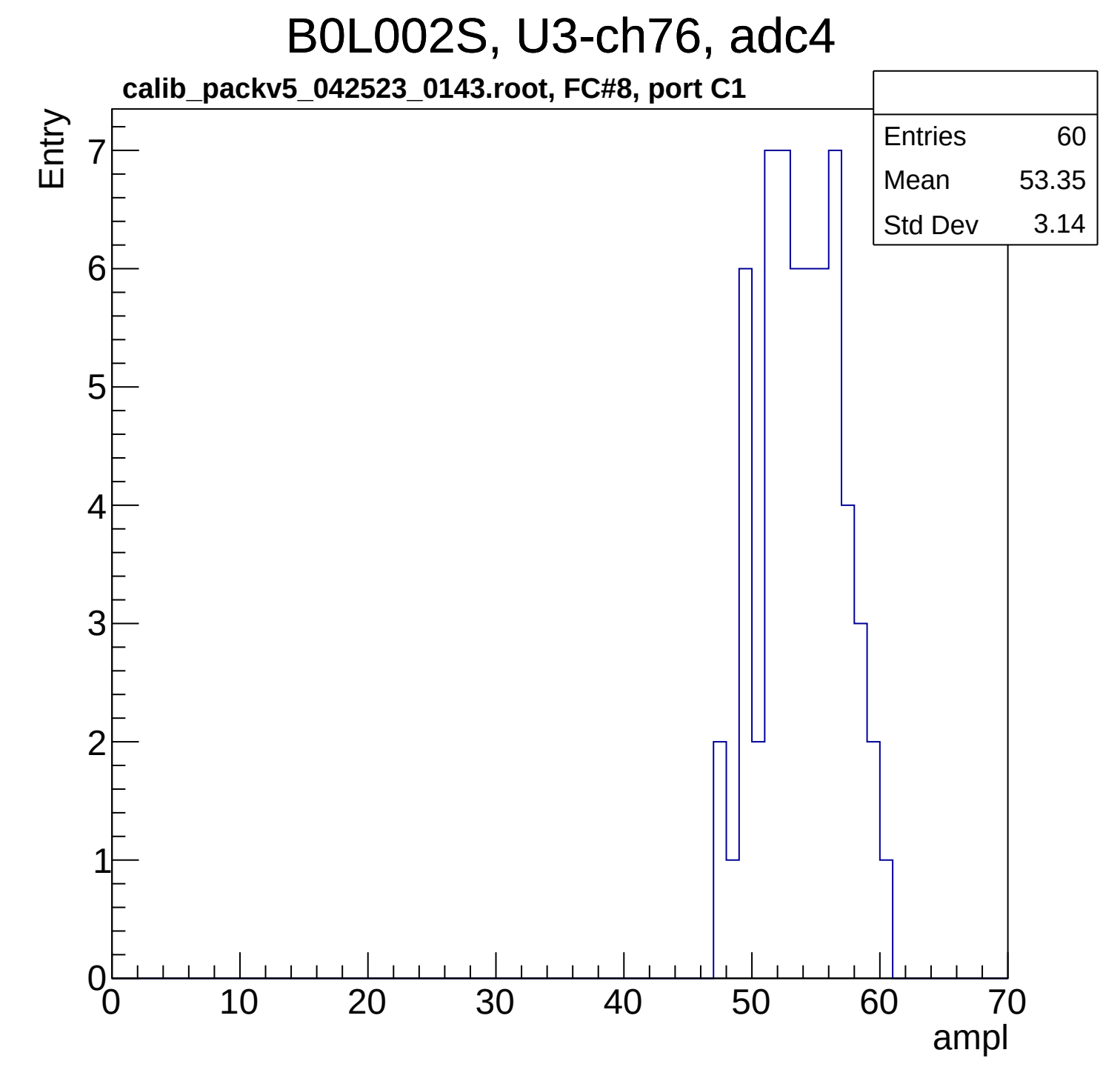
B0L002S, U3-ch76, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 53.35
Std Dev 3.14
Entry
ampl
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1
2
3
4
5
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0
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40
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60
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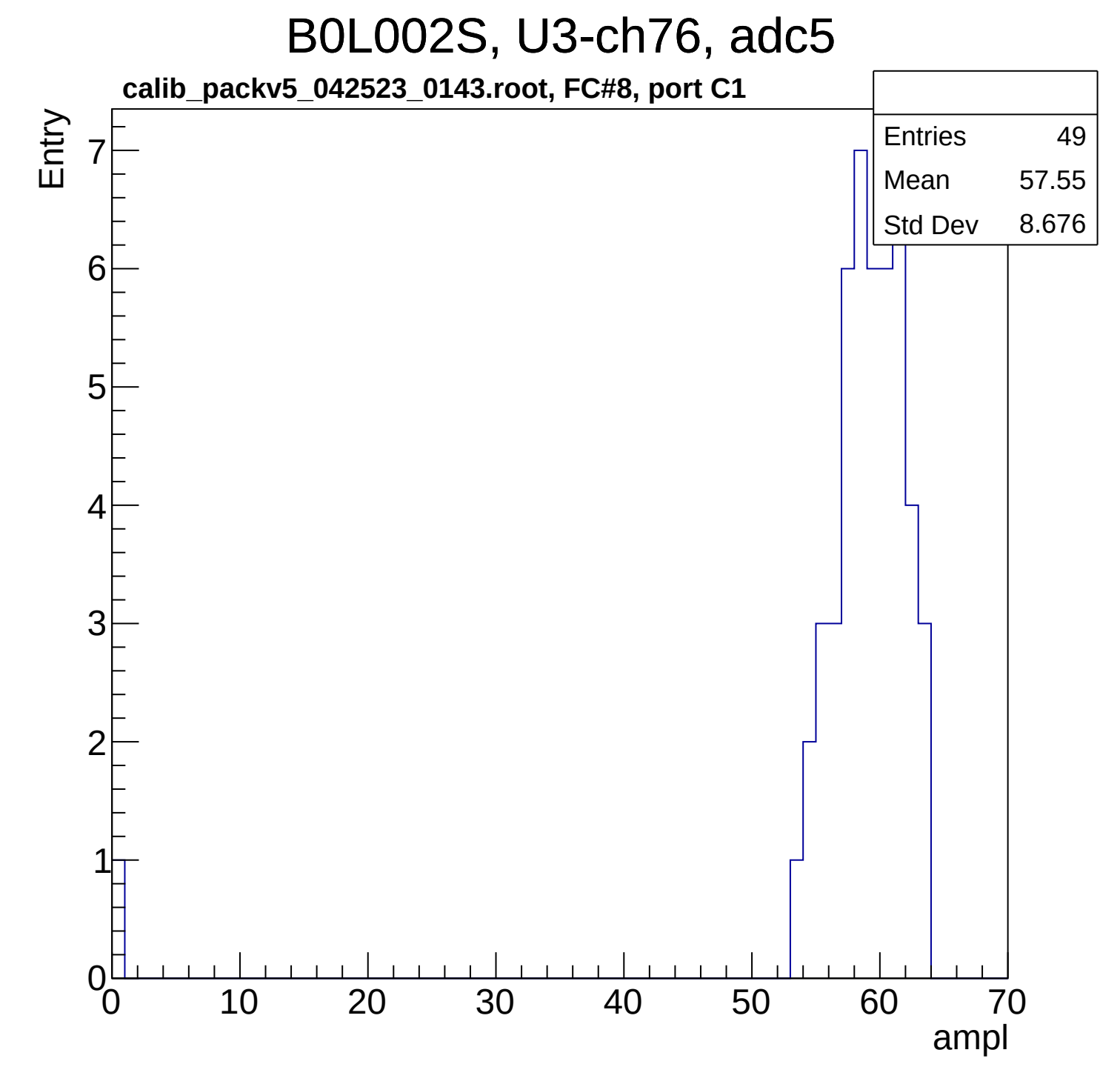
B0L002S, U3-ch76, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 57.55
Std Dev 8.676
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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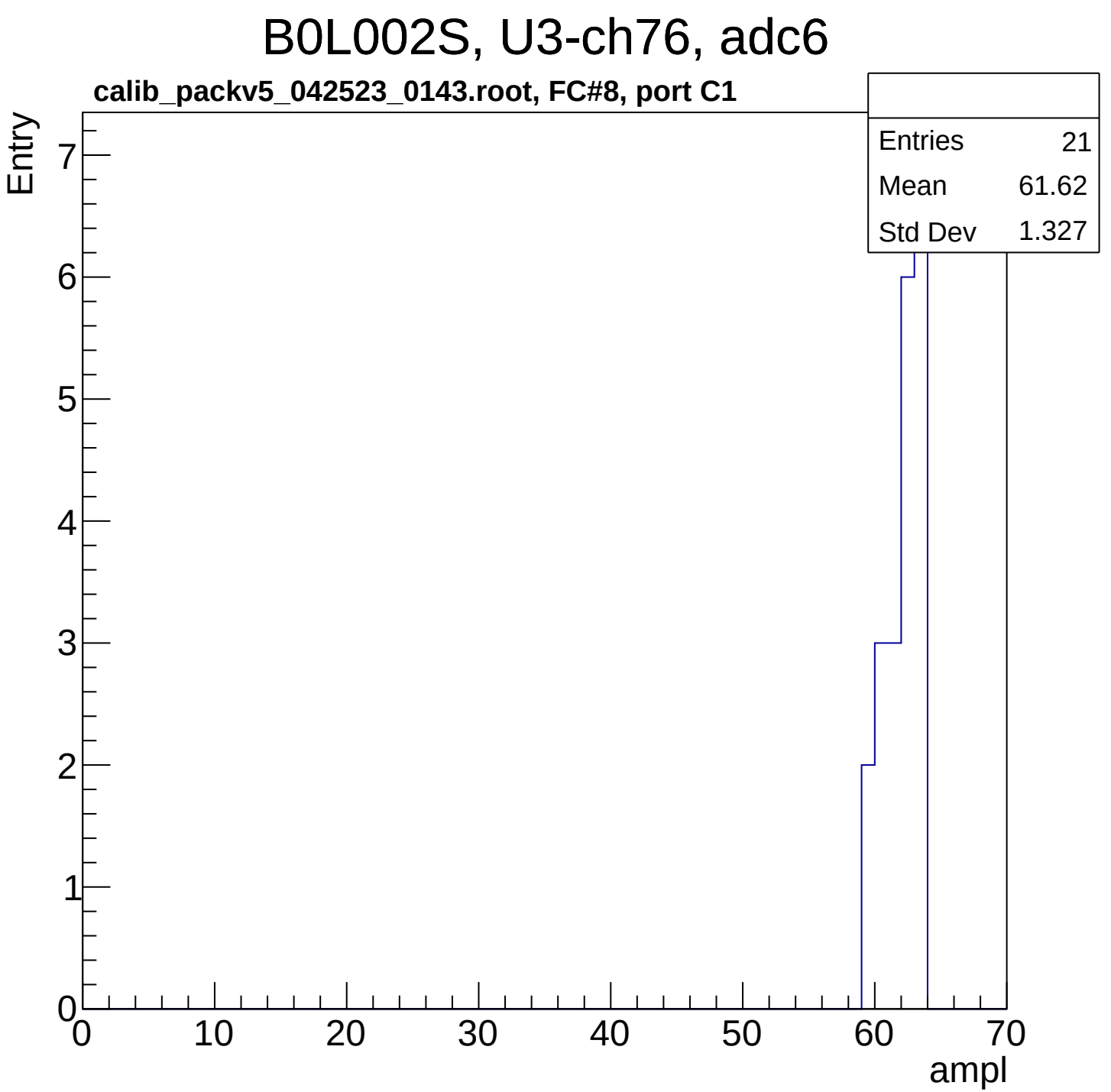
B0L002S, U3-ch76, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 61.62
Std Dev 1.327
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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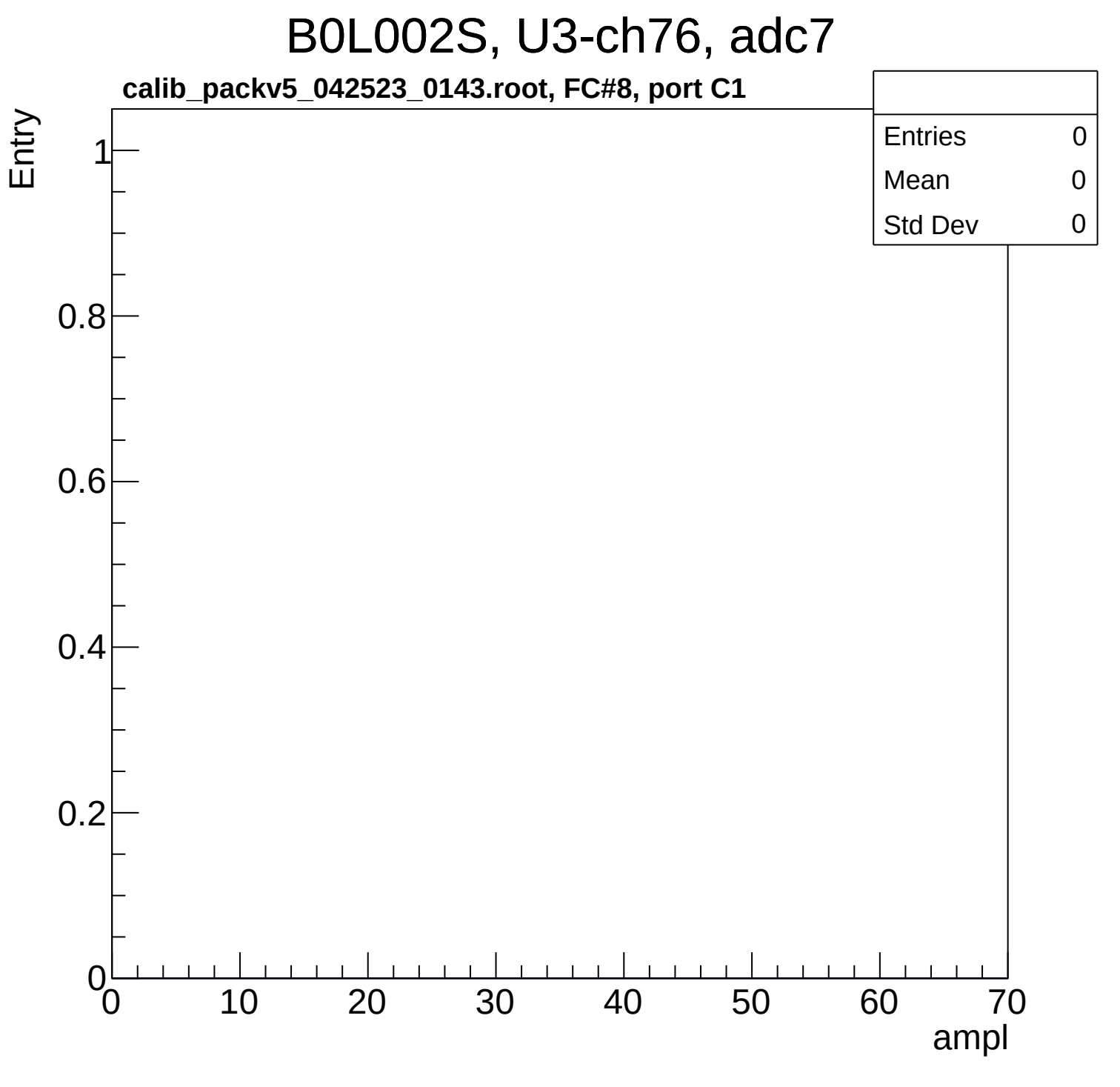
B0L002S, U3-ch76, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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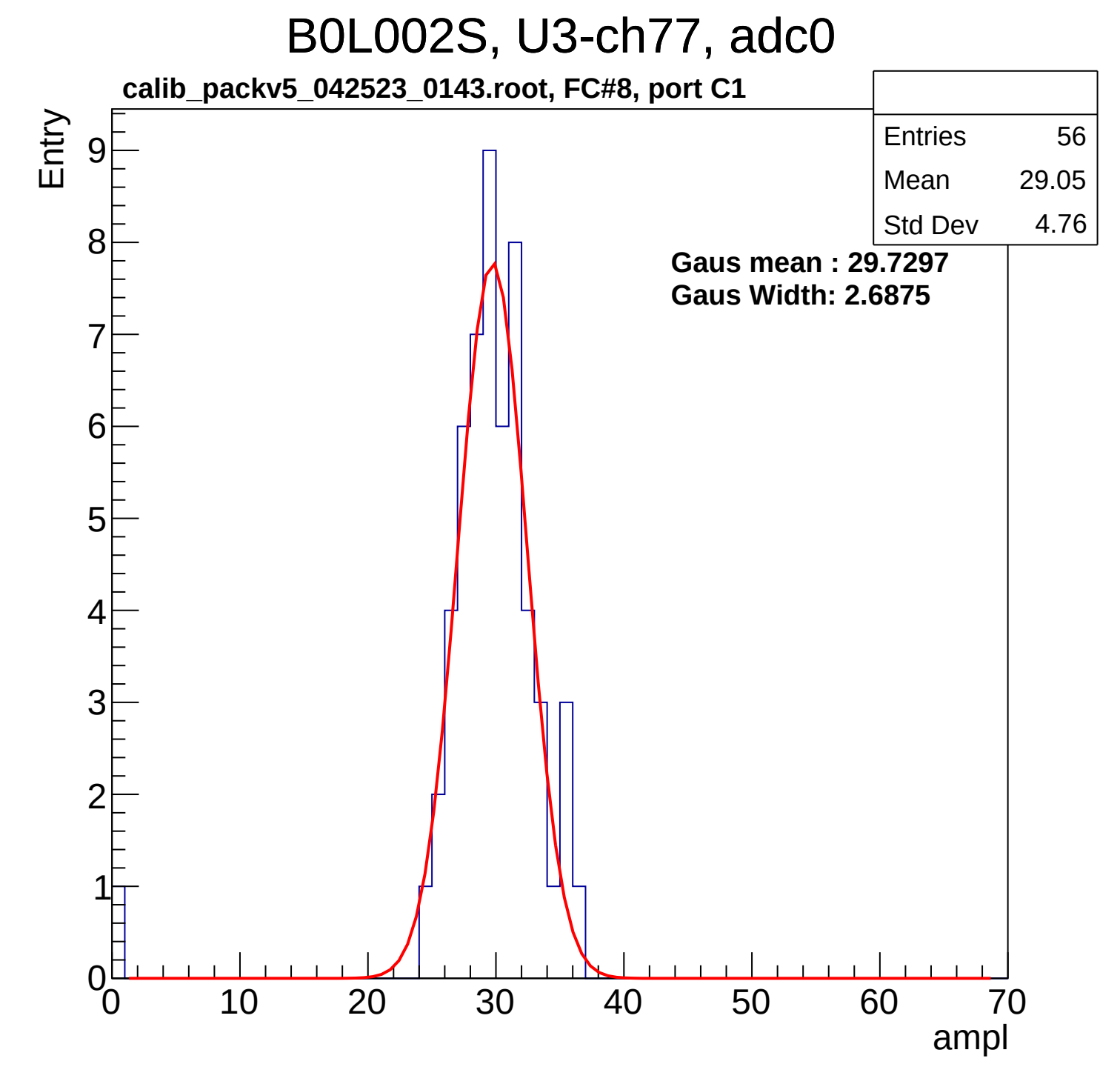
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch77, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 29.05
Std Dev 4.76
Gaus mean : 29.7297
Gaus Width: 2.6875
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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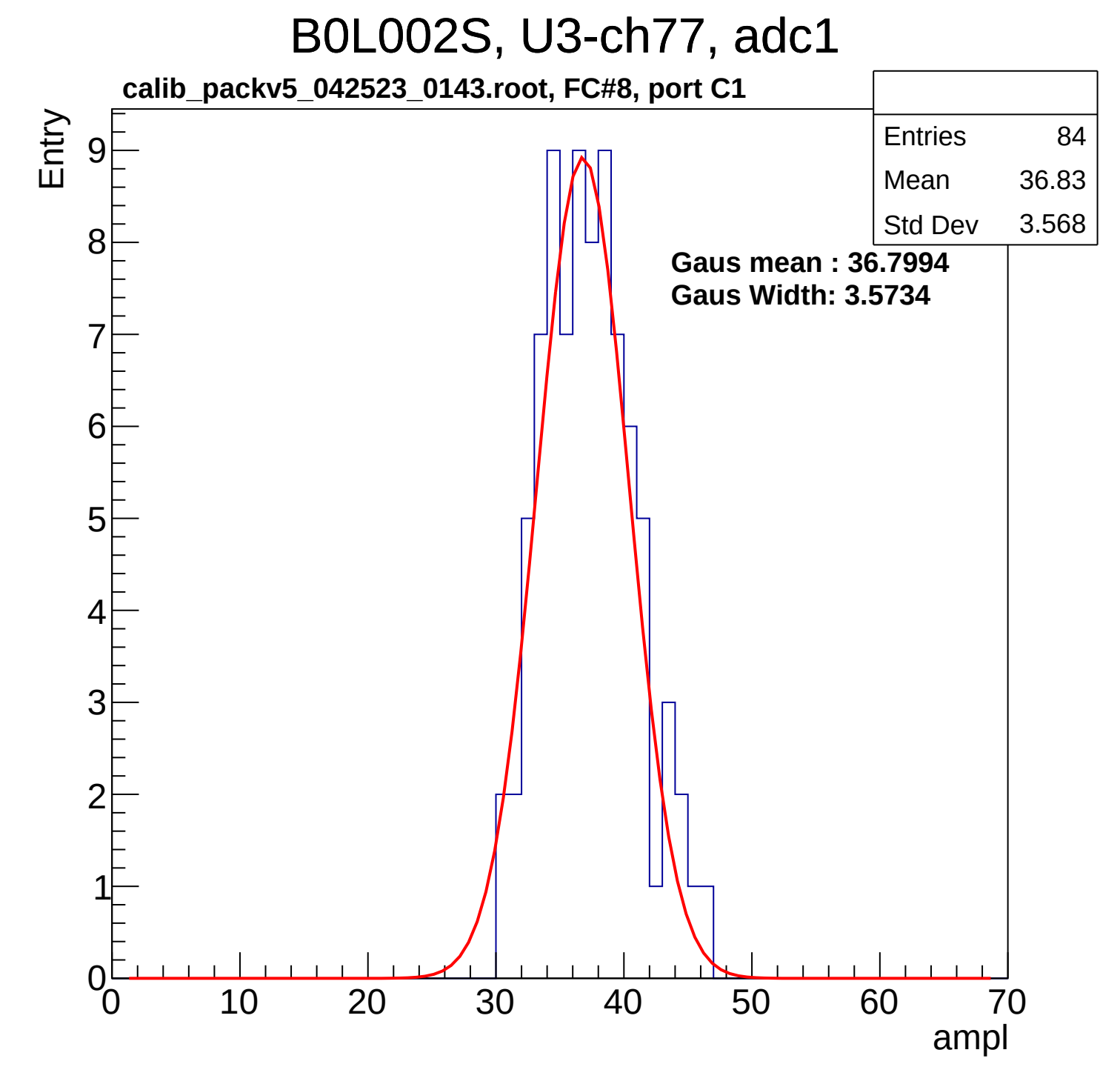
B0L002S, U3-ch77, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 36.83
Std Dev 3.568
Gaus mean : 36.7994
Gaus Width: 3.5734
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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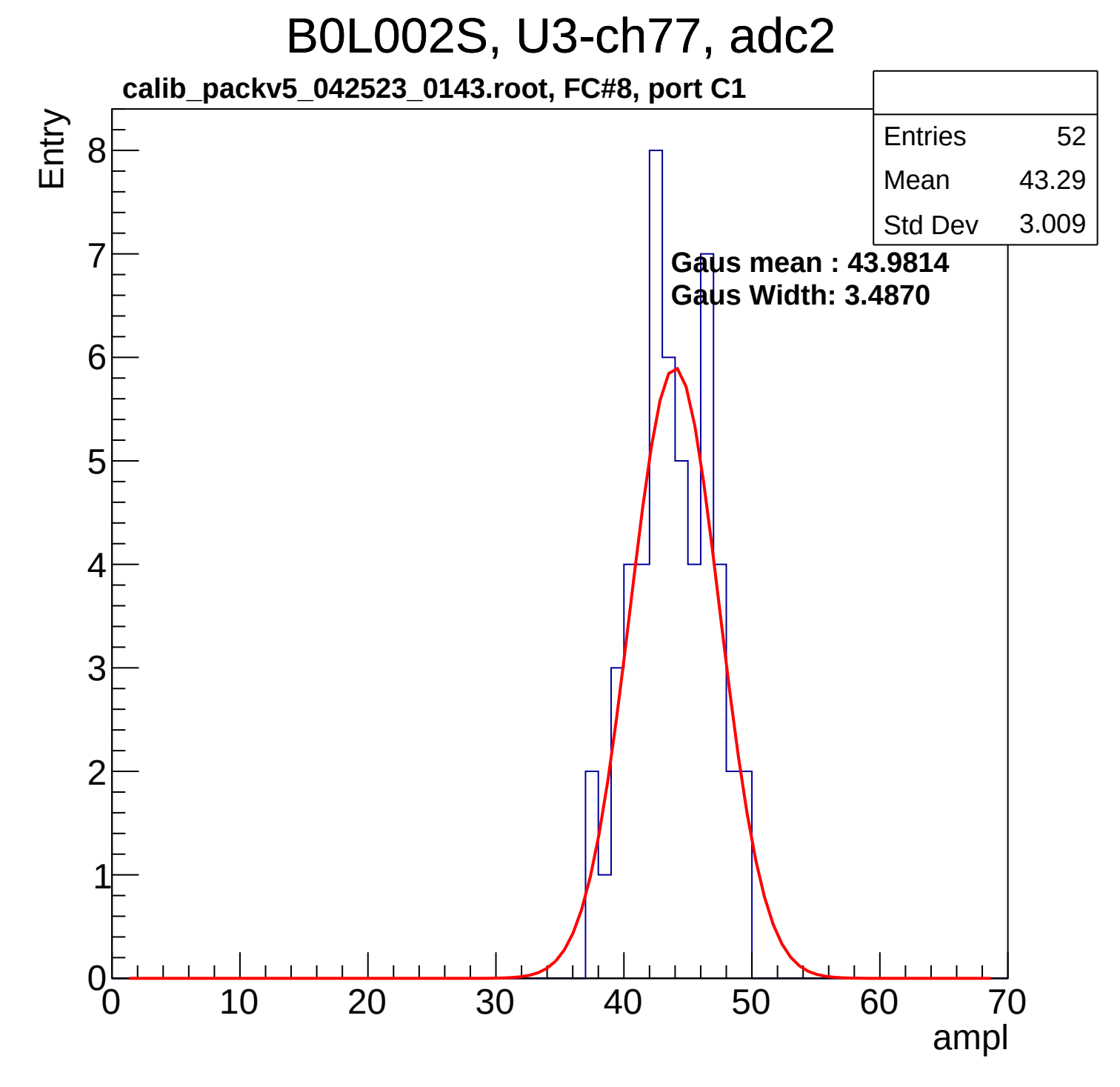
B0L002S, U3-ch77, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 43.29
Std Dev 3.009
Gaus mean : 43.9814
Gaus Width: 3.4870
Entry
ampl
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2
3
4
5
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7
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0
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60
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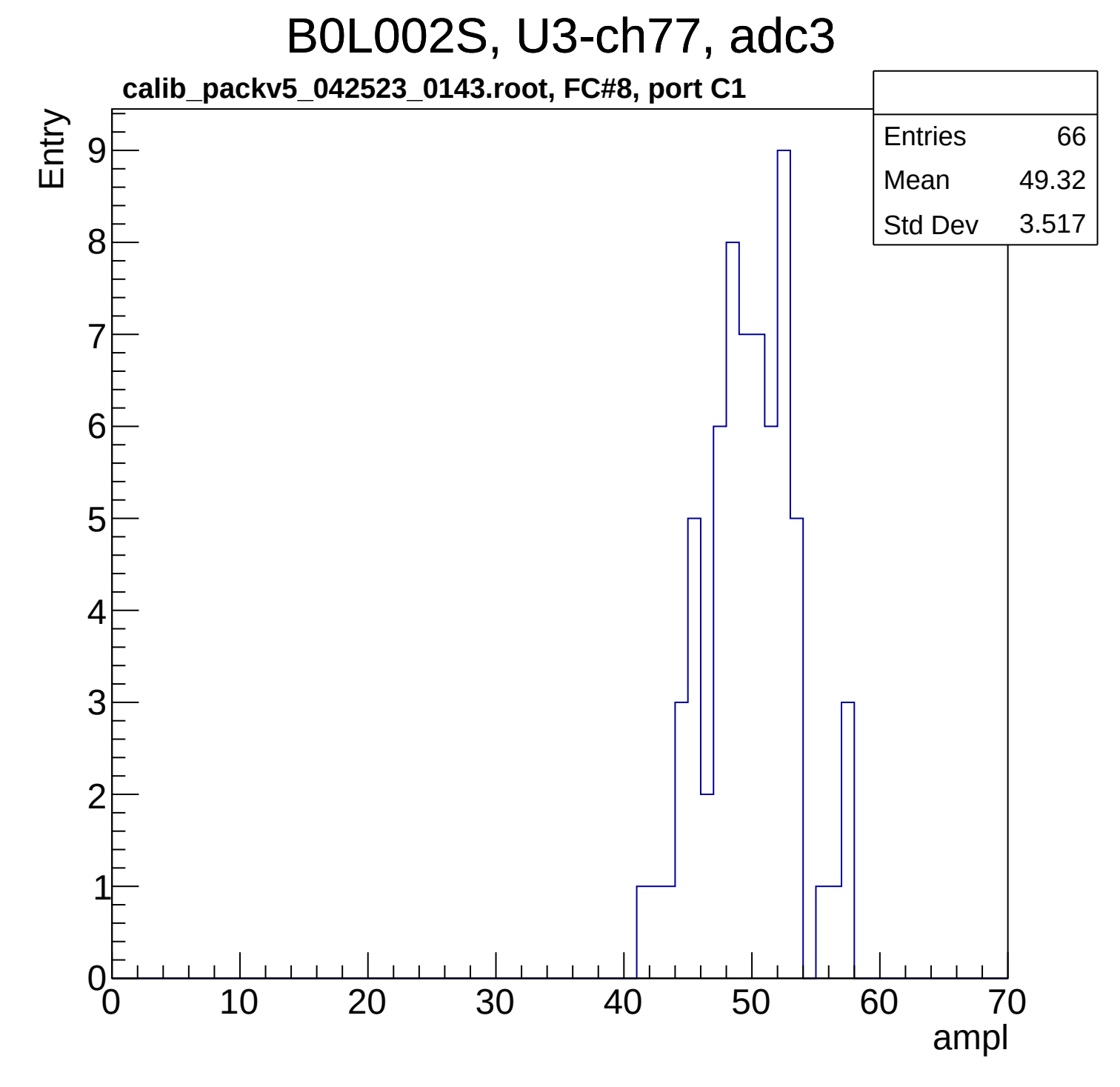
B0L002S, U3-ch77, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 49.32
Std Dev 3.517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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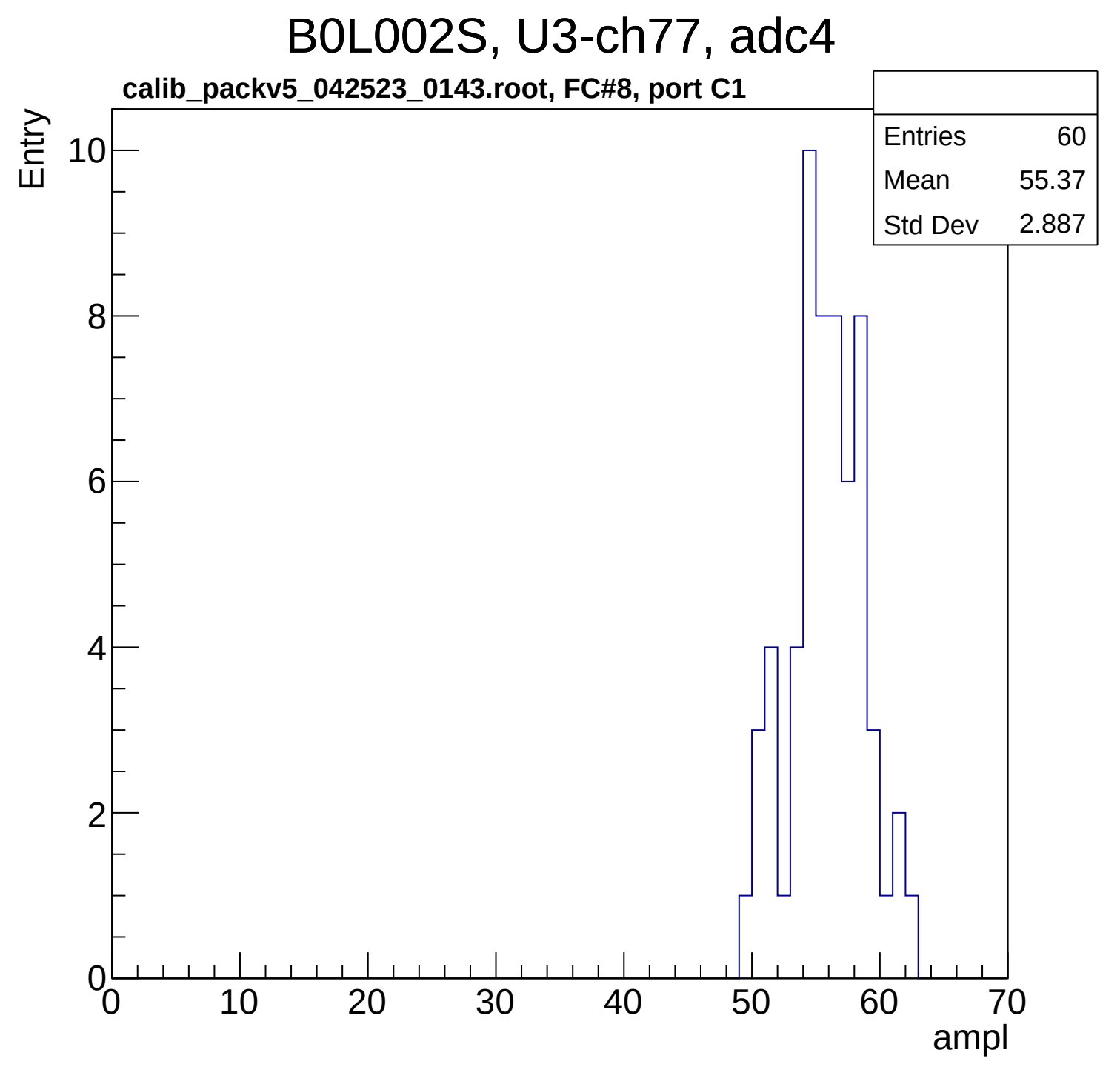
B0L002S, U3-ch77, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.37
Std Dev 2.887
Entry
ampl
0
2
4
6
8
10
0
10
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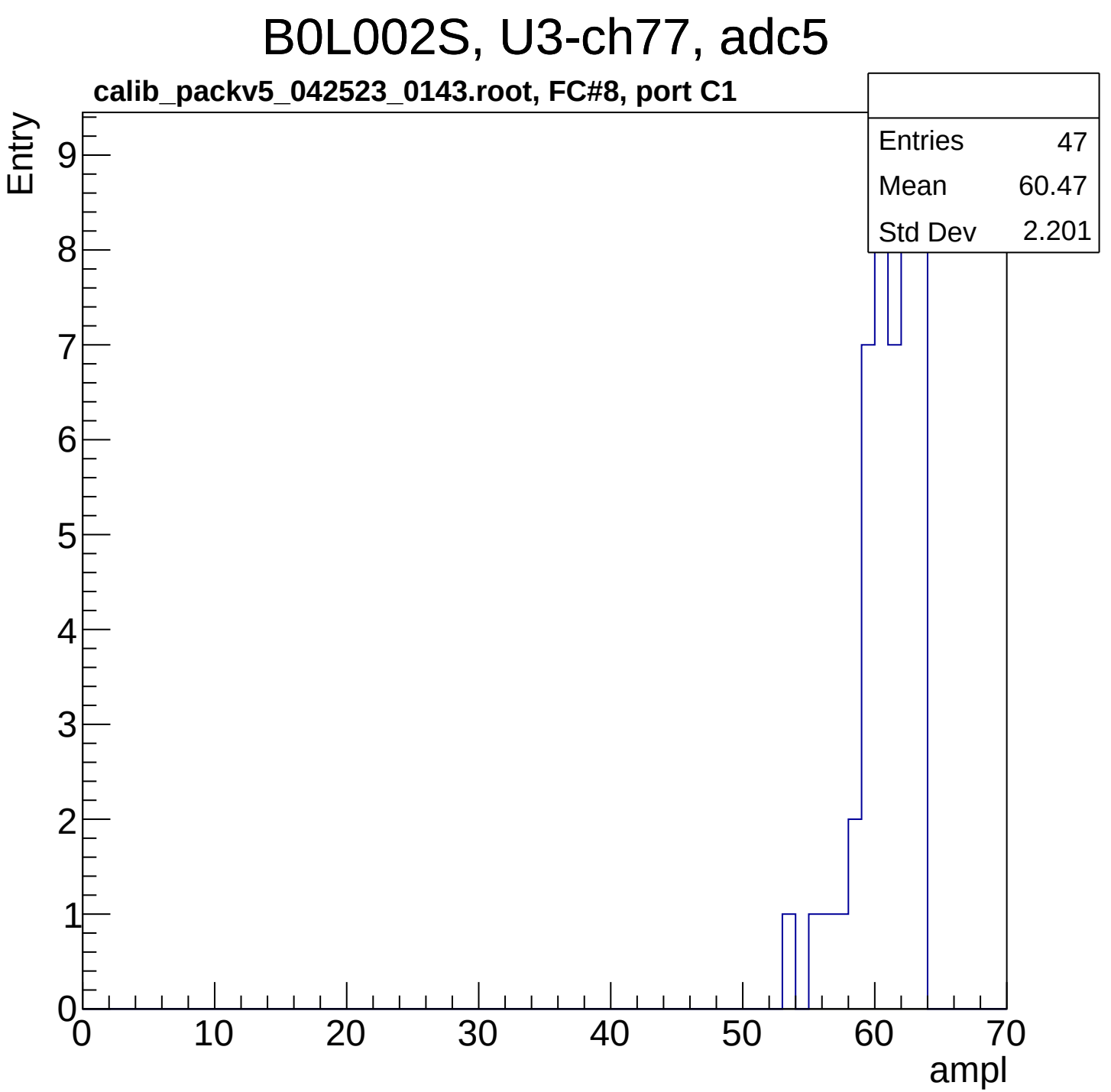
30
40
50
60
70


B0L002S, U3-ch77, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 60.47
Std Dev 2.201
Entry
0
1
2
3
4
5
6
7
8
9
0
10
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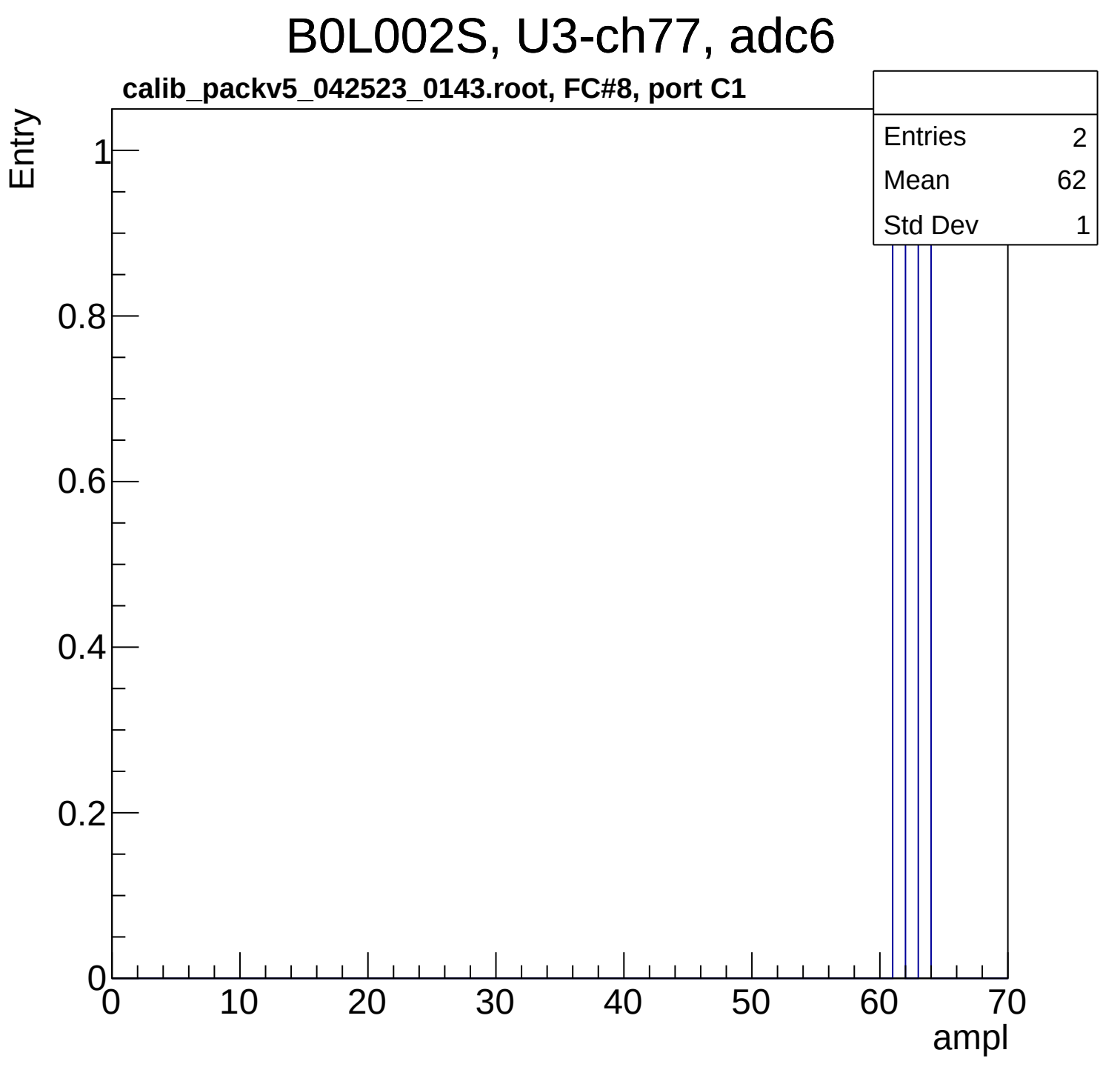
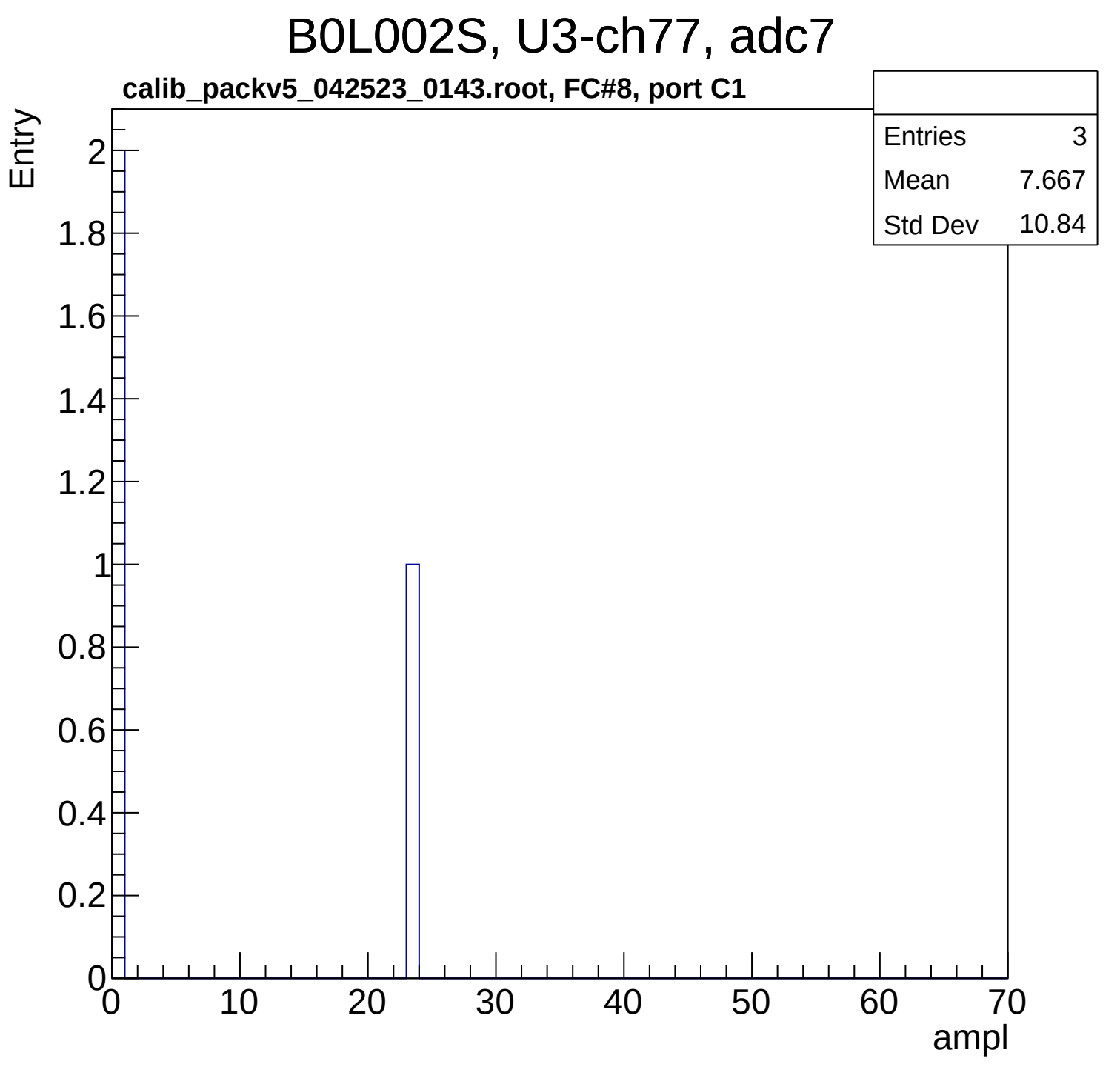
50
60
70
ampl


B0L002S, U3-ch77, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch77, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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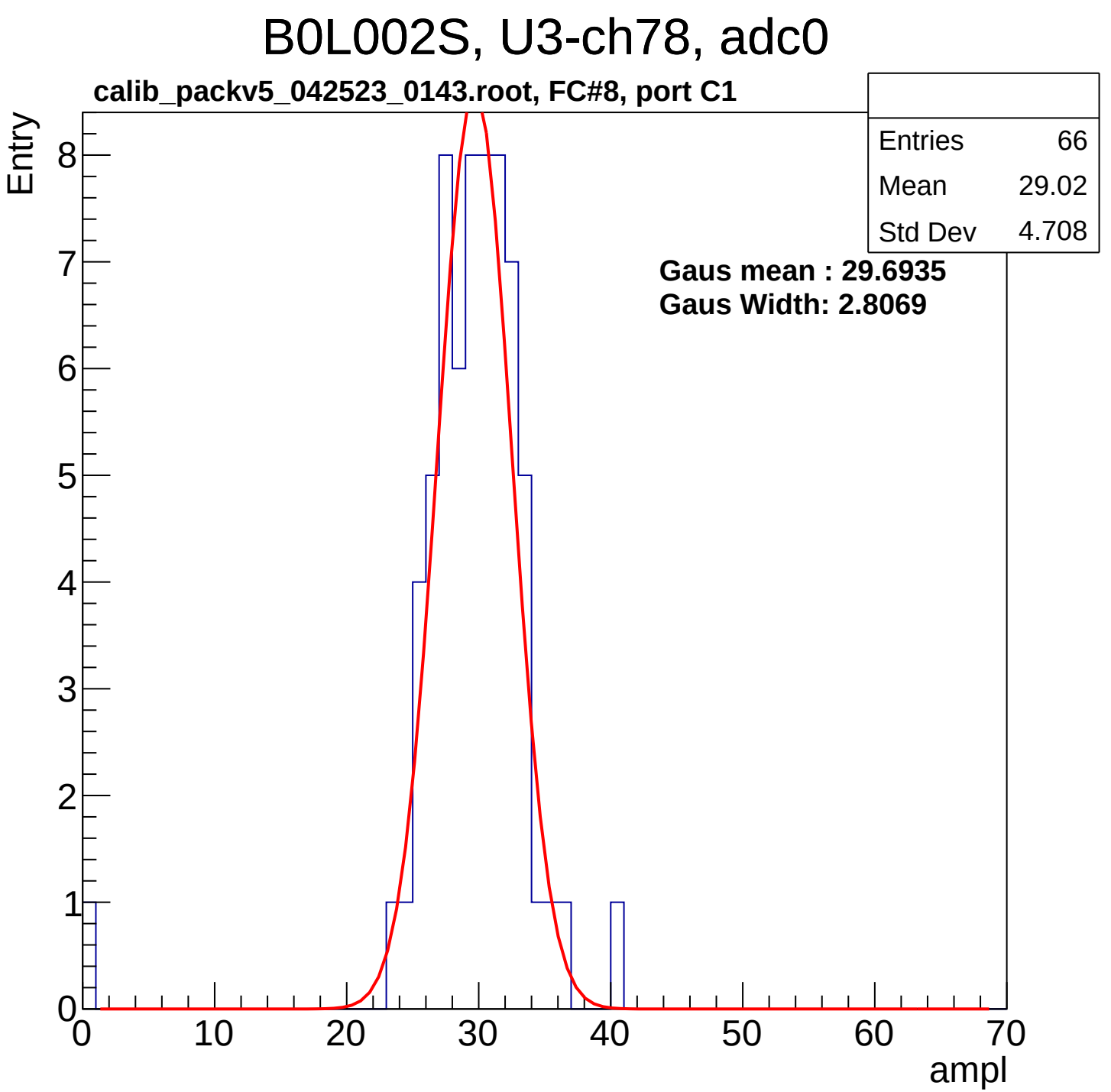
B0L002S, U3-ch78, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 29.02
Std Dev 4.708
Gaus mean : 29.6935
Gaus Width: 2.8069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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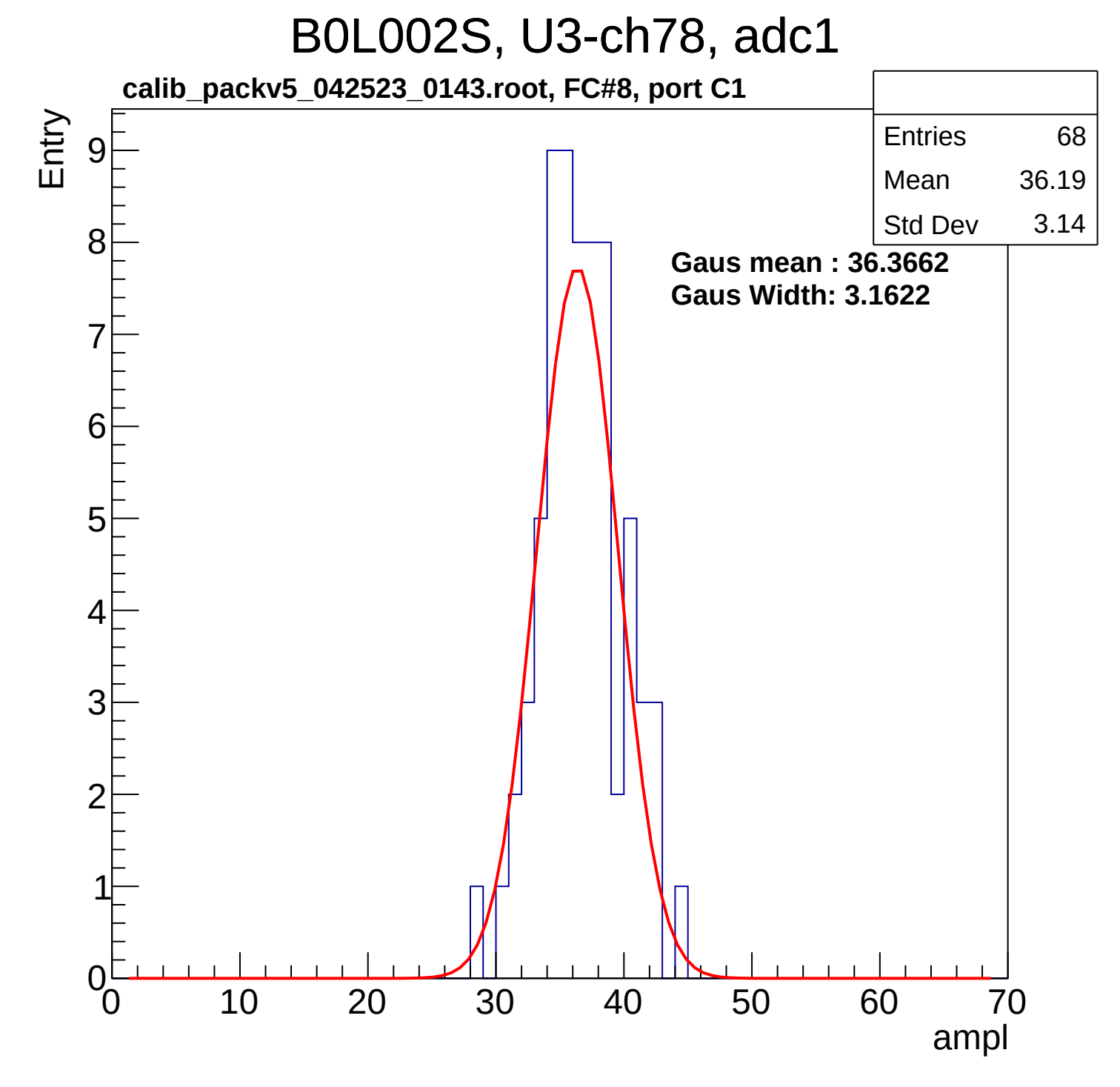
B0L002S, U3-ch78, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 36.19
Std Dev 3.14
Gaus mean : 36.3662
Gaus Width: 3.1622
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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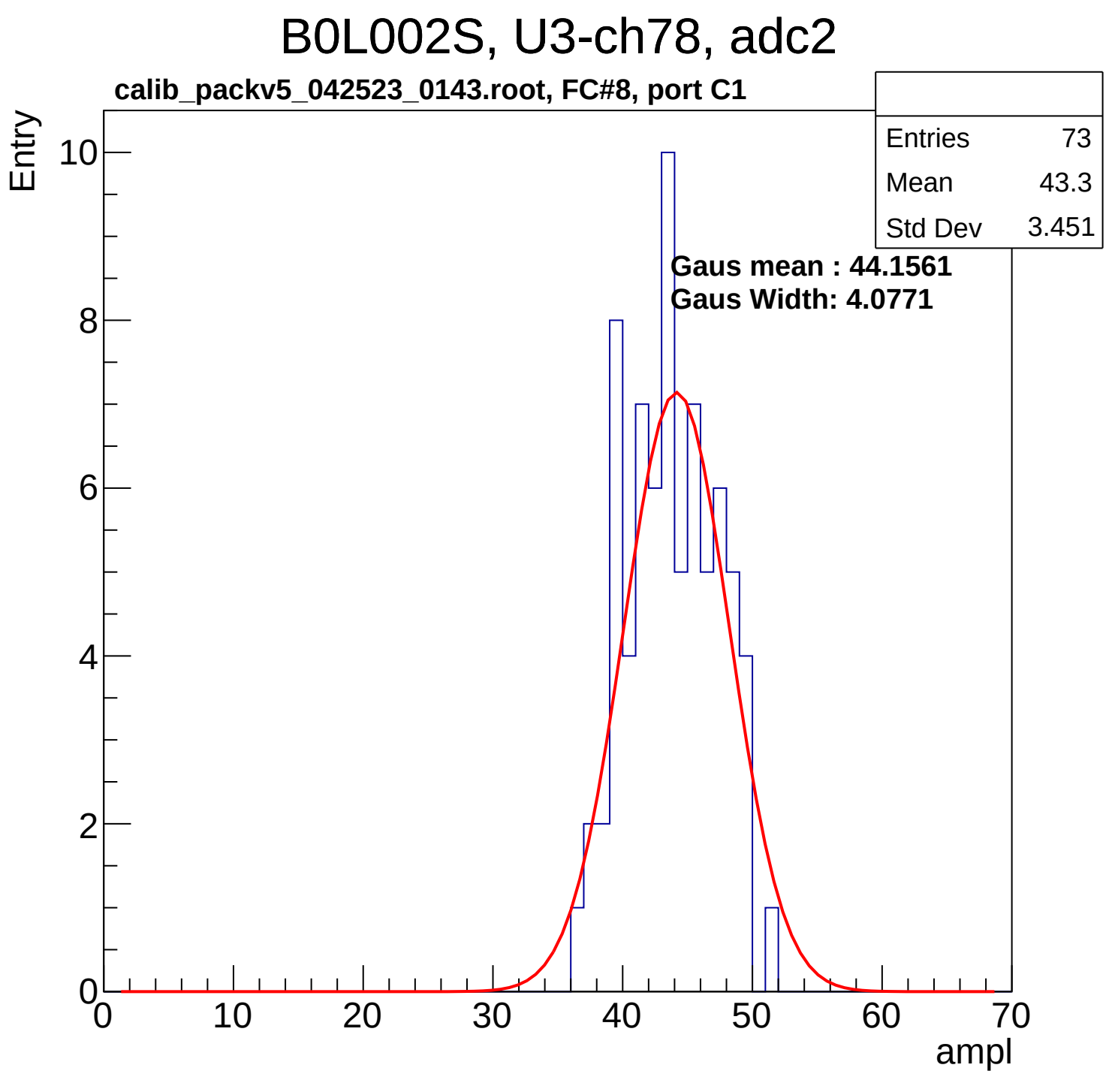
B0L002S, U3-ch78, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.3
Std Dev 3.451
Gaus mean : 44.1561
Gaus Width: 4.0771
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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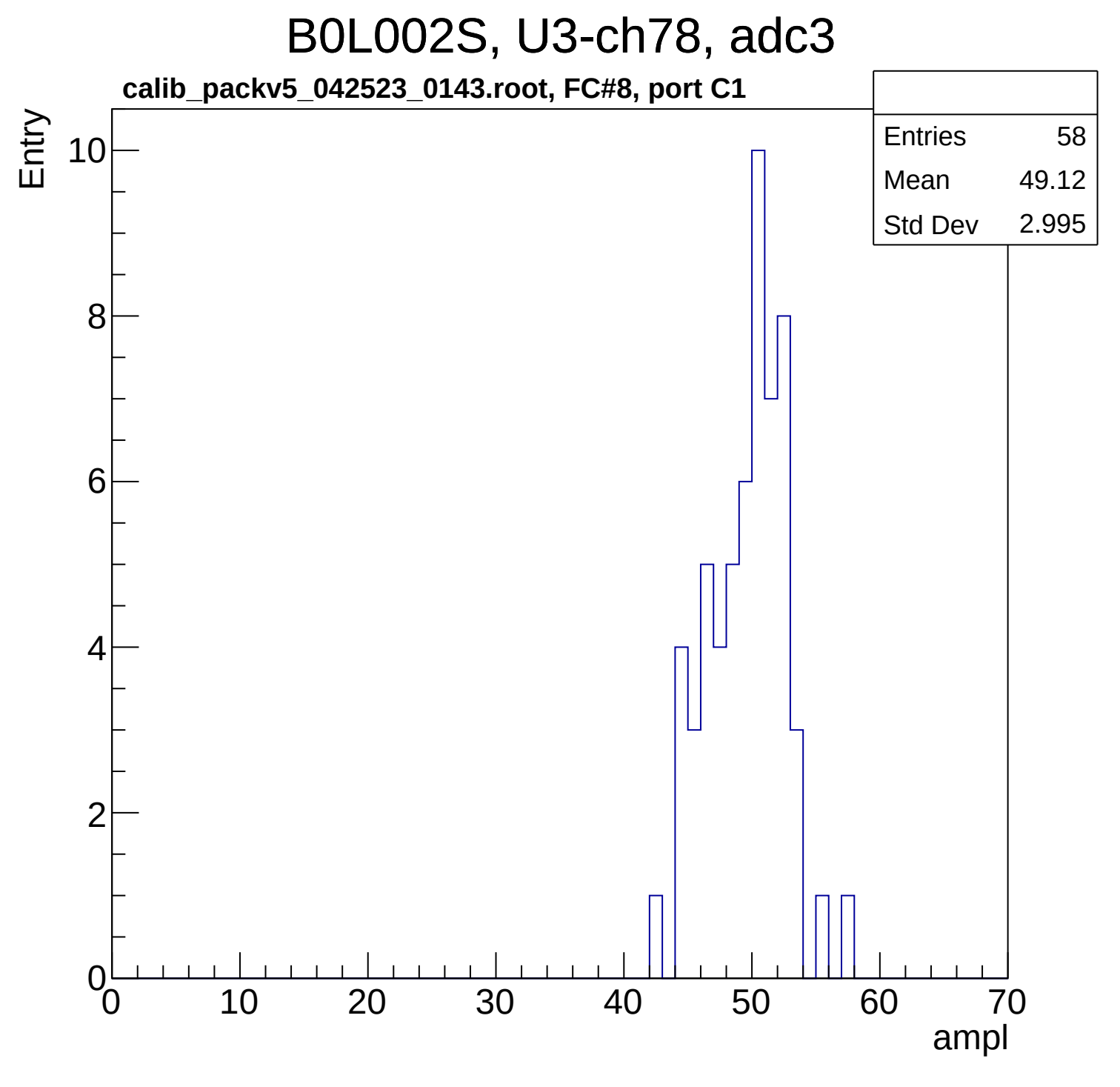
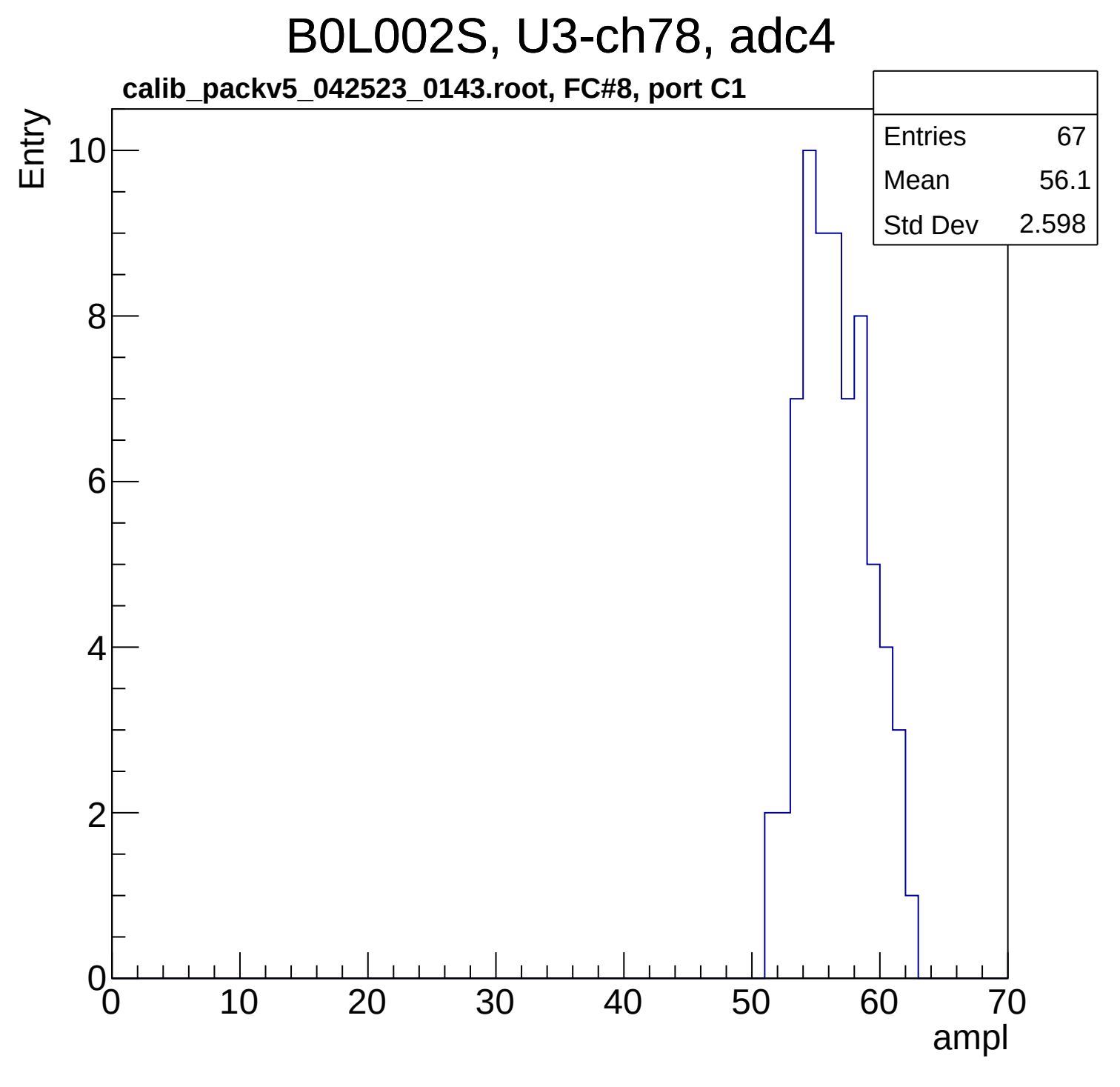
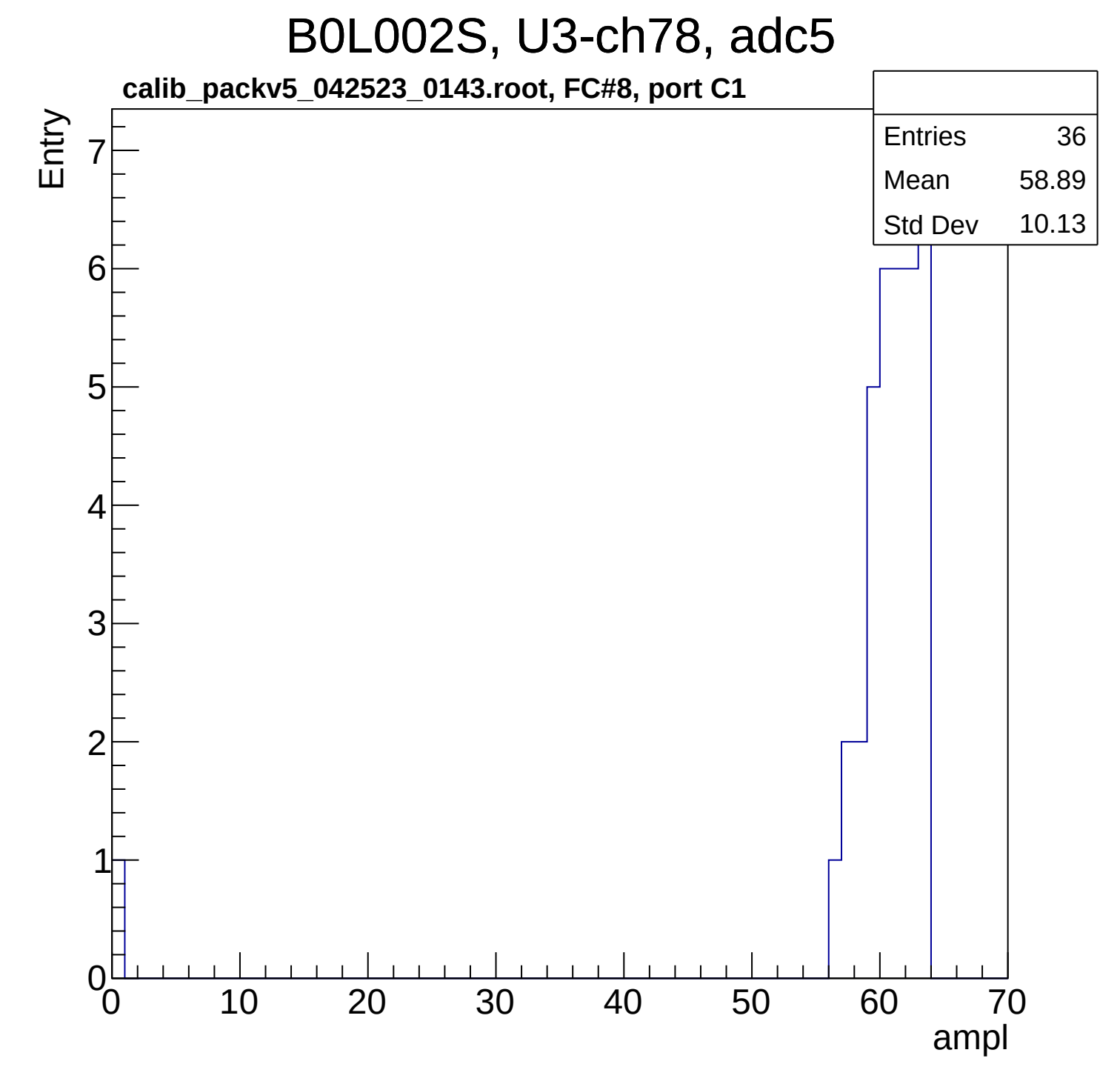
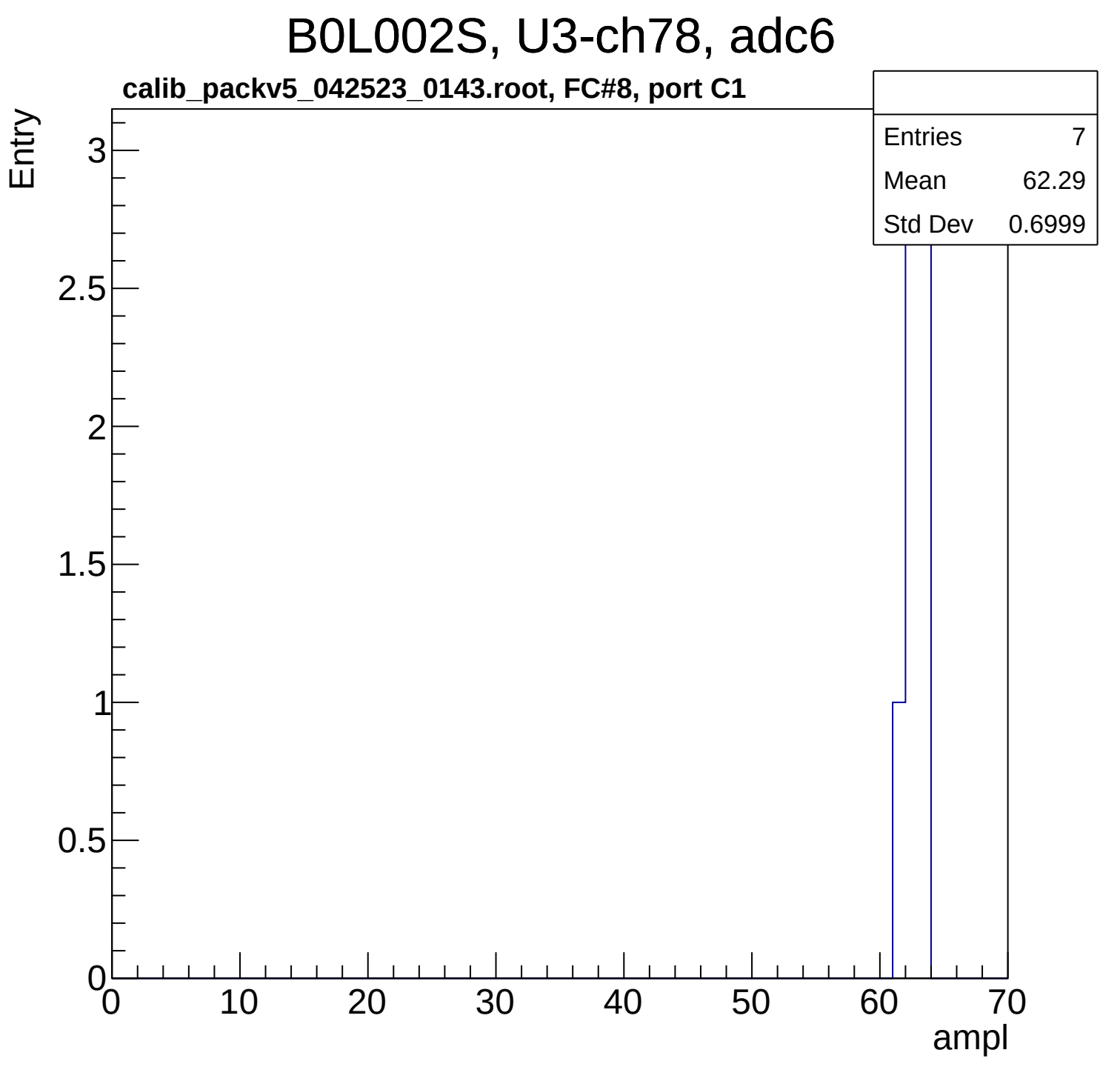
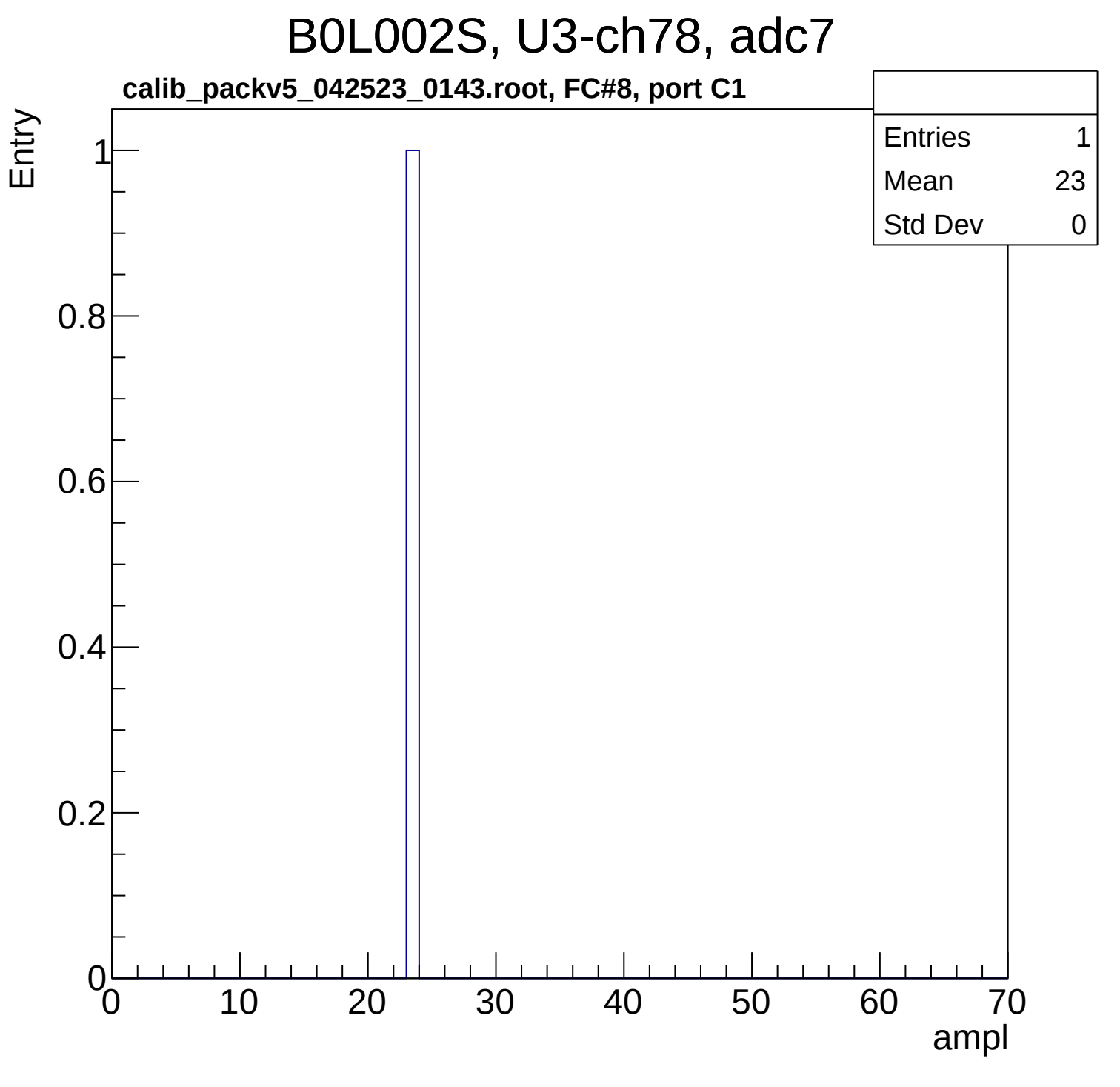
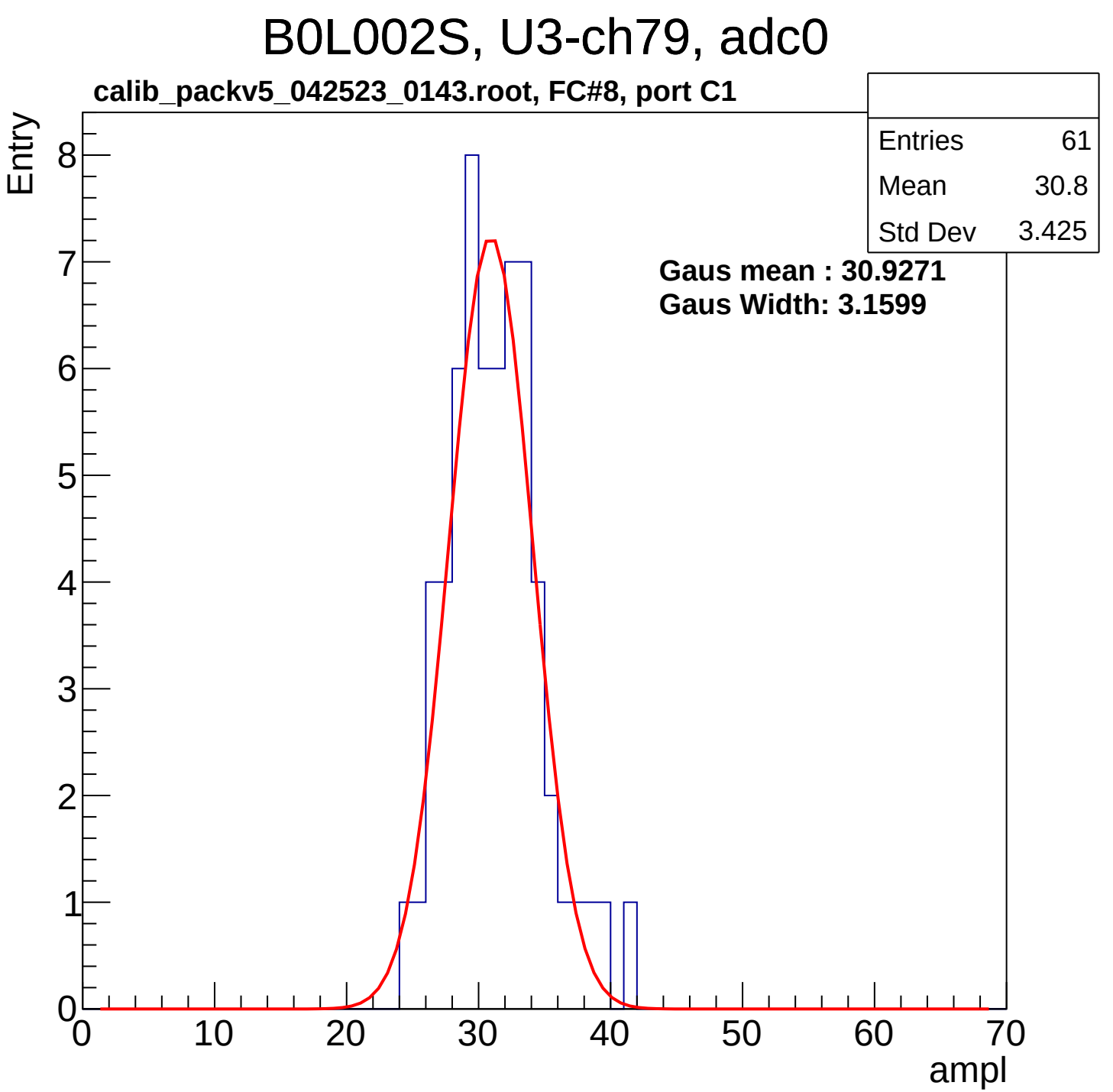

B0L002S, U3-ch78, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 49.12
Std Dev 2.995
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

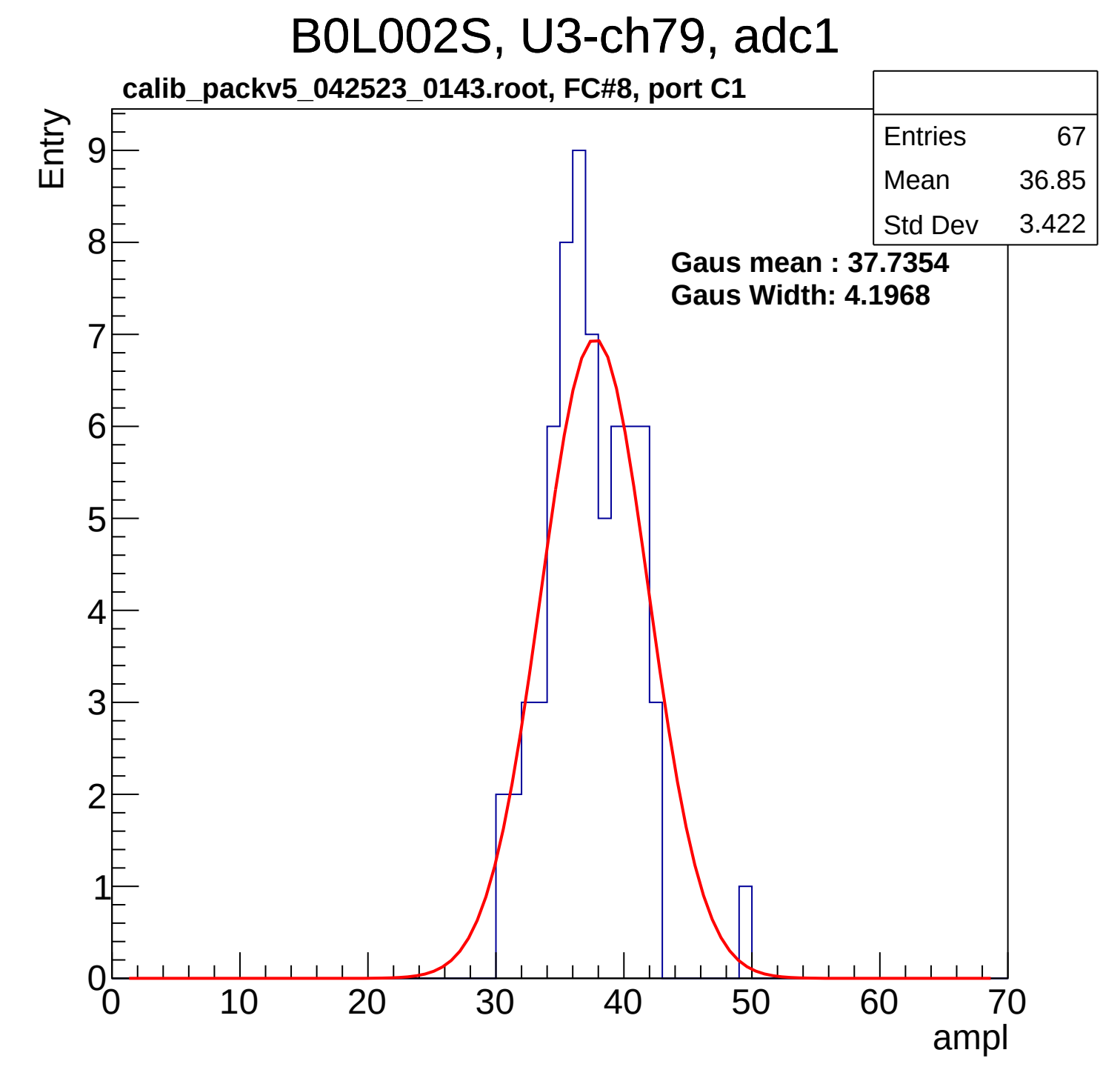

B0L002S, U3-ch78, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.1
Std Dev 2.598
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

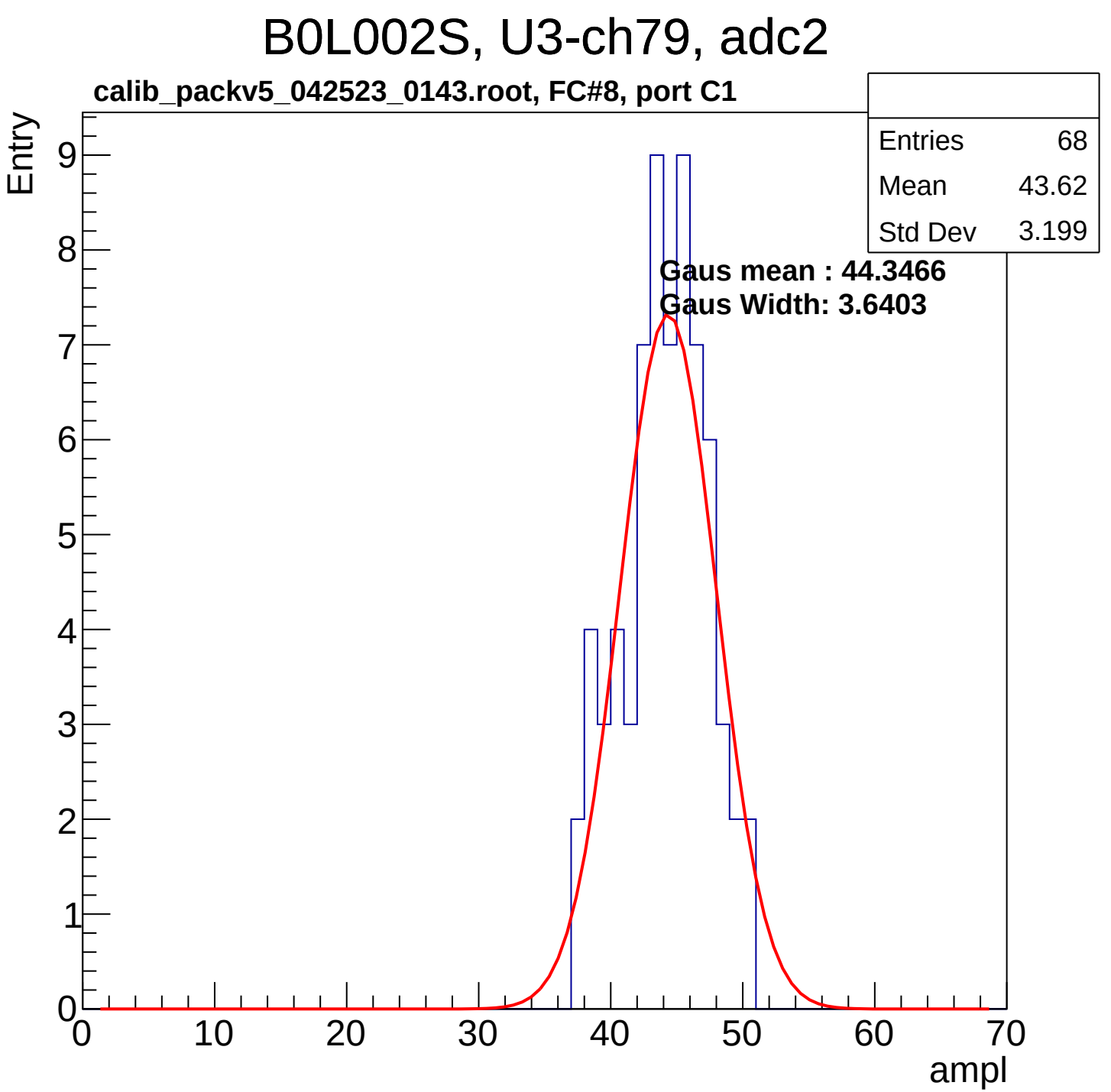

B0L002S, U3-ch78, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.89
Std Dev 10.13
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

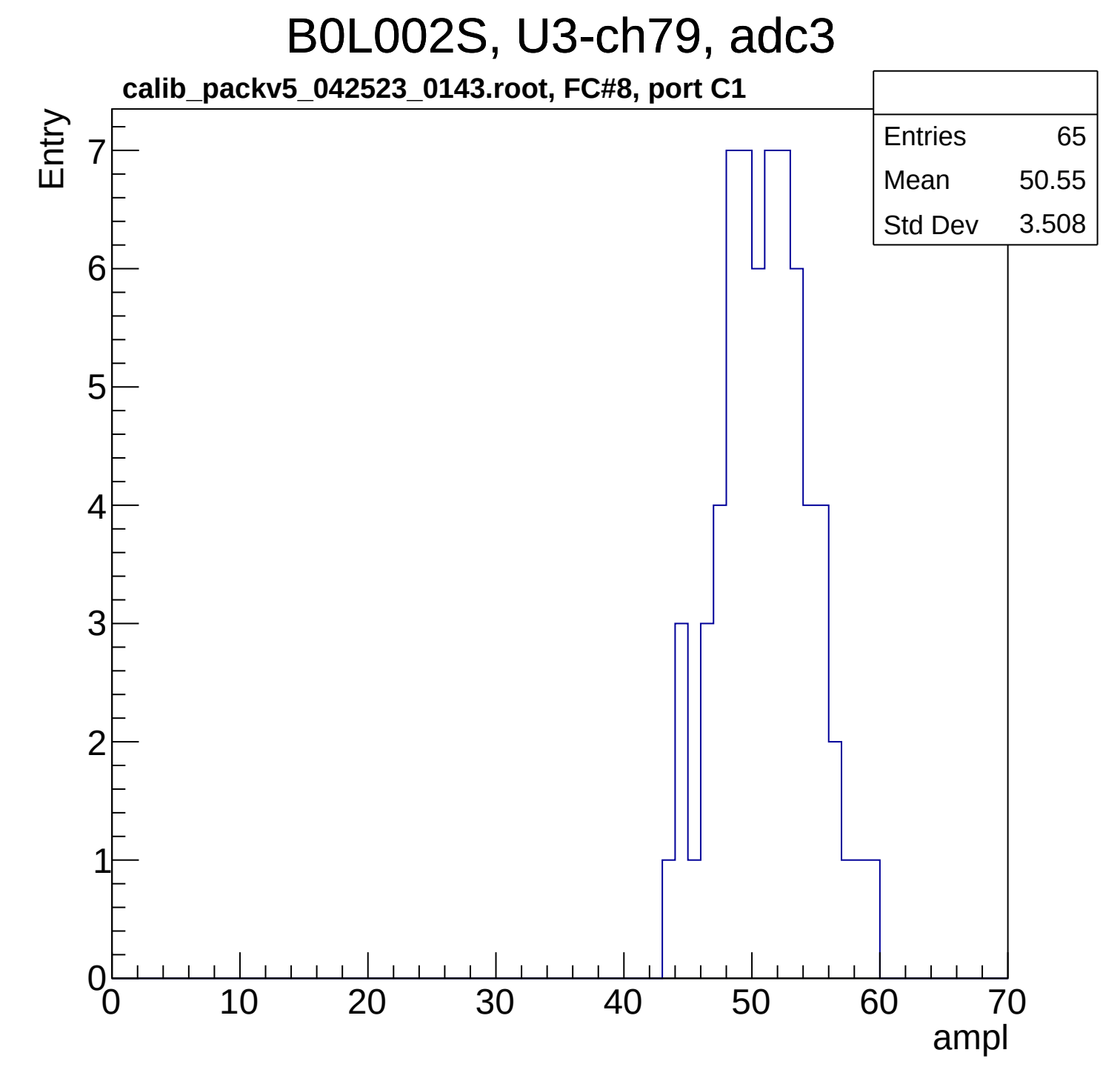

B0L002S, U3-ch78, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

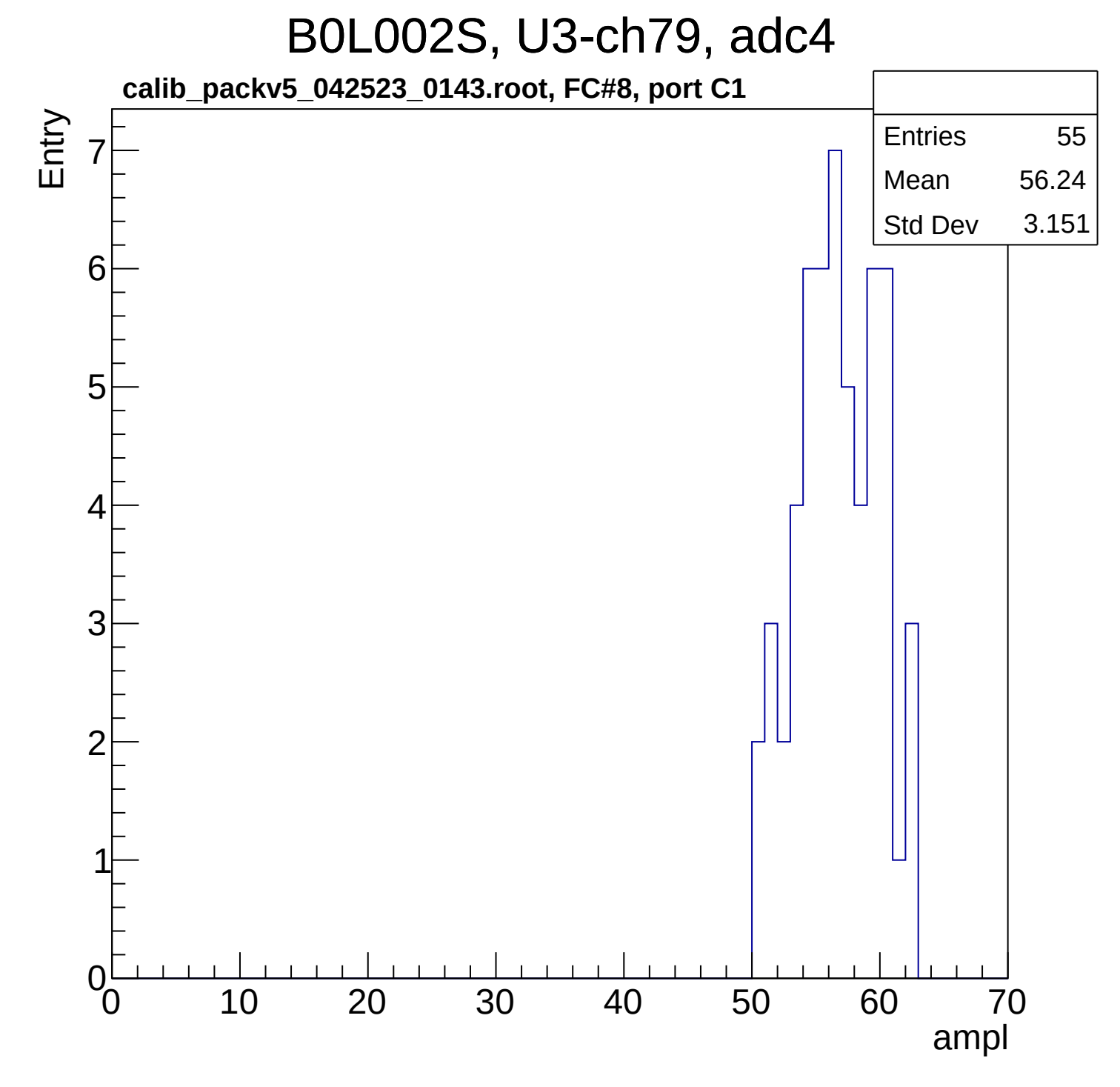

B0L002S, U3-ch78, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

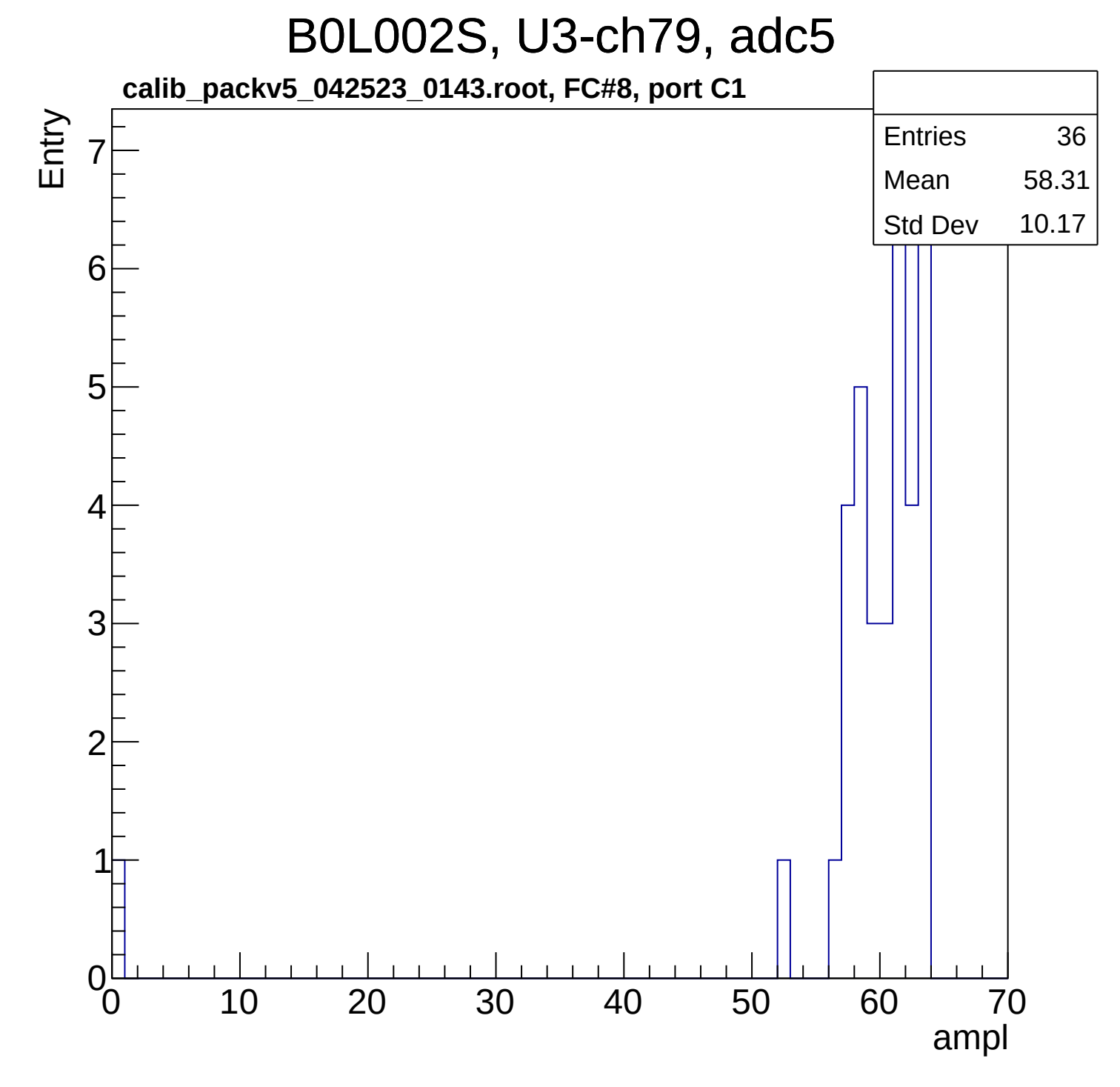

B0L002S, U3-ch79, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.8
Std Dev 3.425
Gaus mean : 30.9271
Gaus Width: 3.1599
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

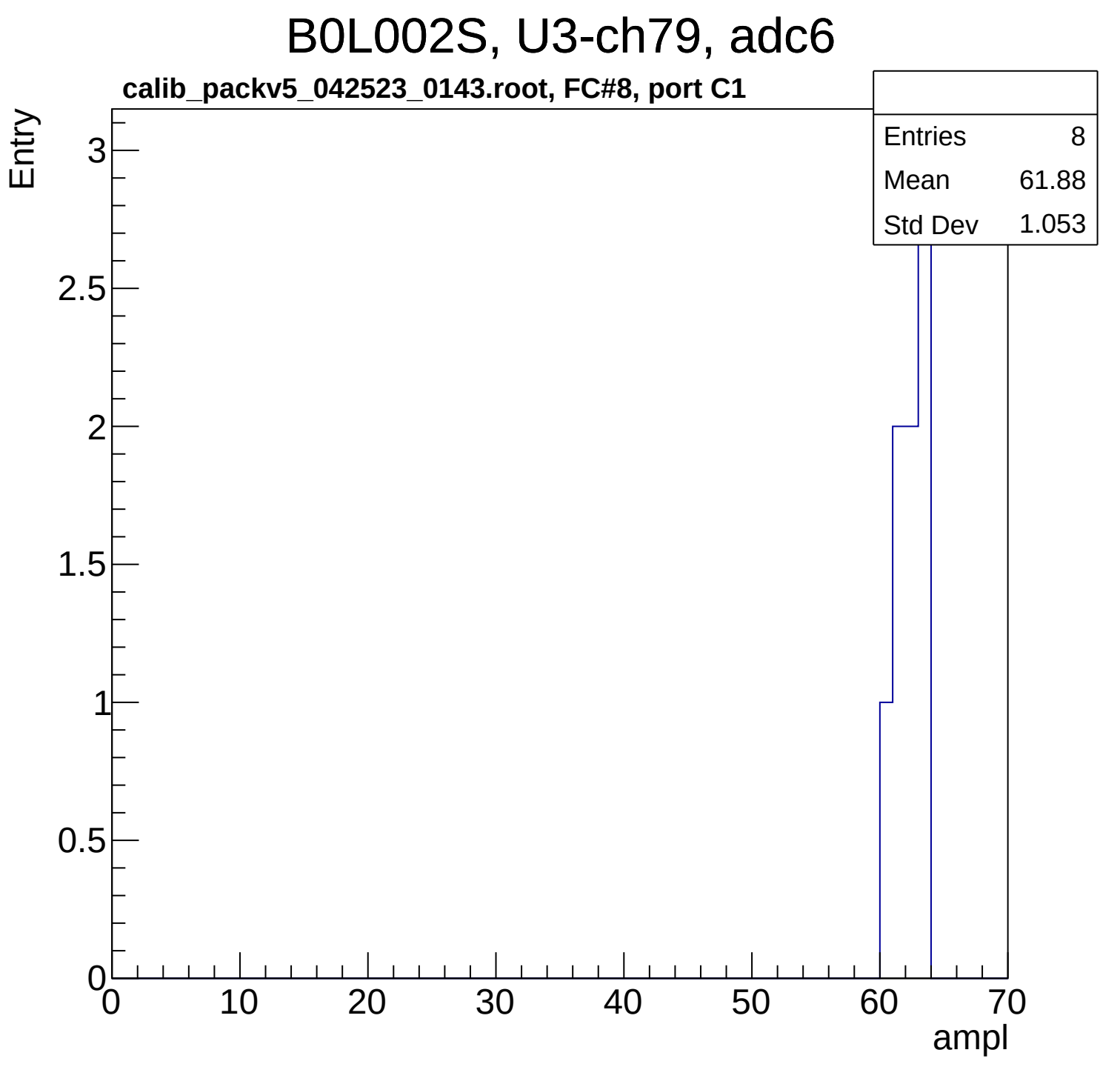

B0L002S, U3-ch79, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.85
Std Dev 3.422
Gaus mean : 37.7354
Gaus Width: 4.1968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch79, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.62
Std Dev 3.199
Gaus mean : 44.3466
Gaus Width: 3.6403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch79, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.55
Std Dev 3.508
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch79, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.24
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch79, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.31
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch79, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 61.88
Std Dev 1.053
Entry
ampl
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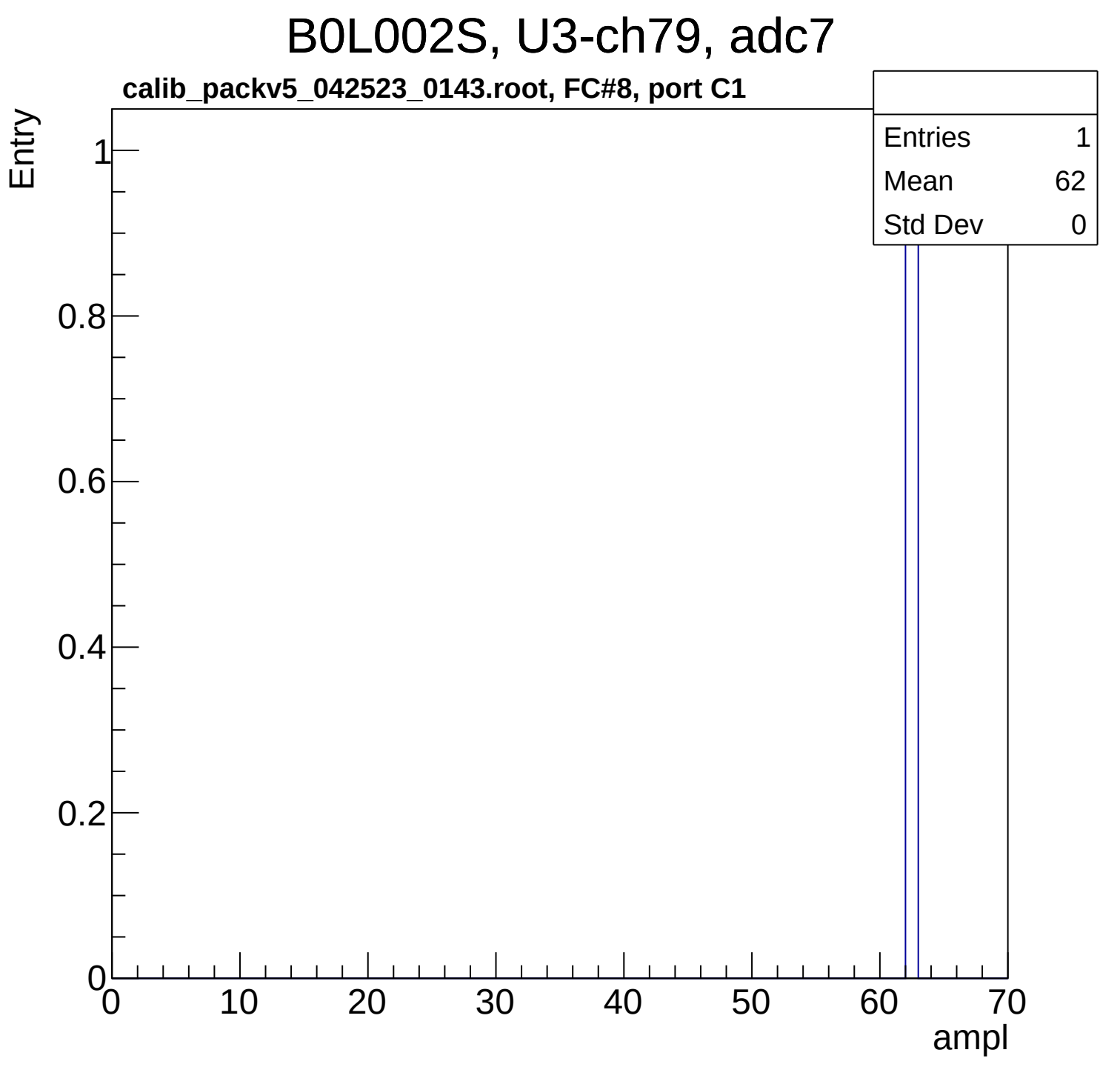
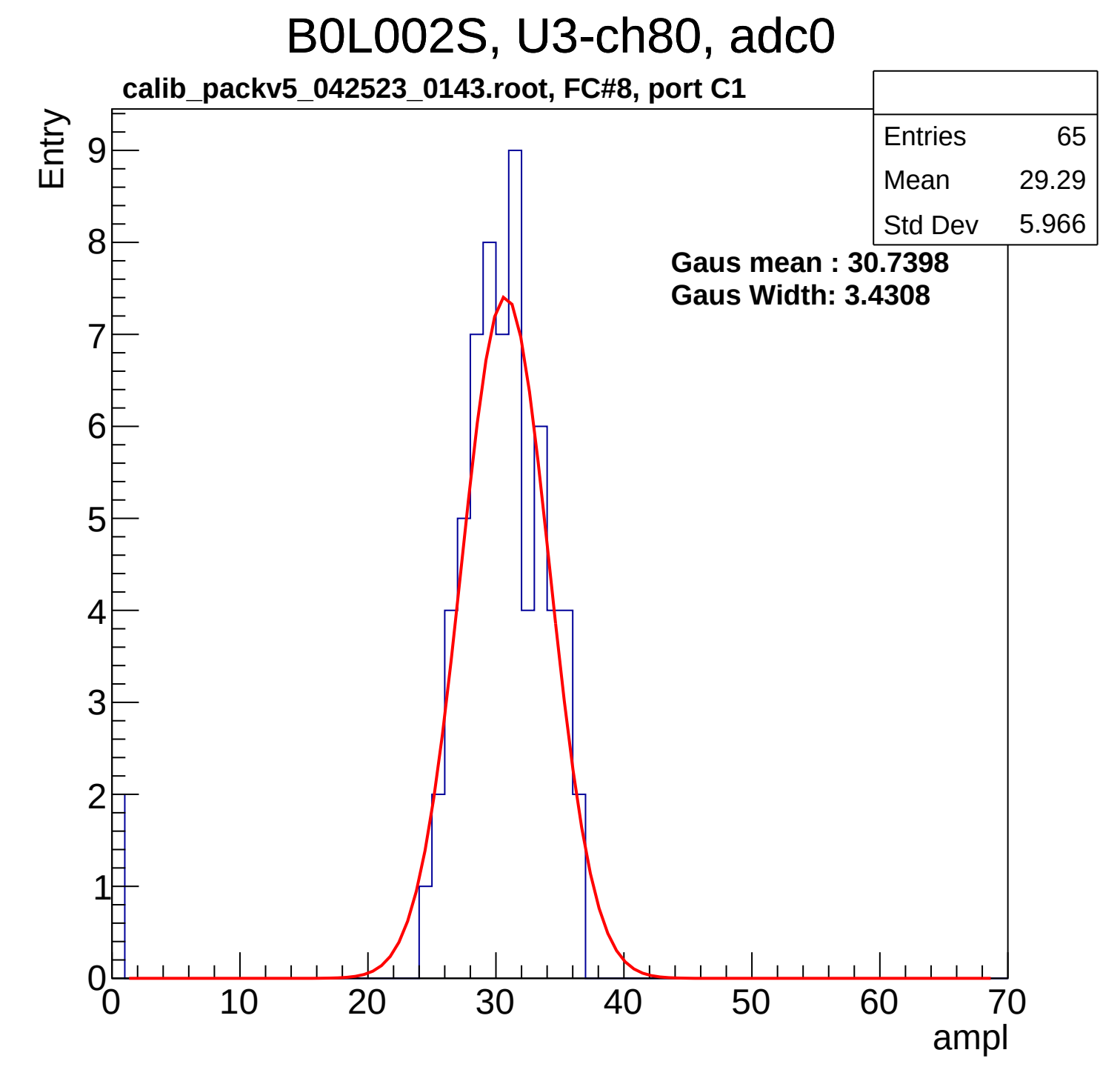
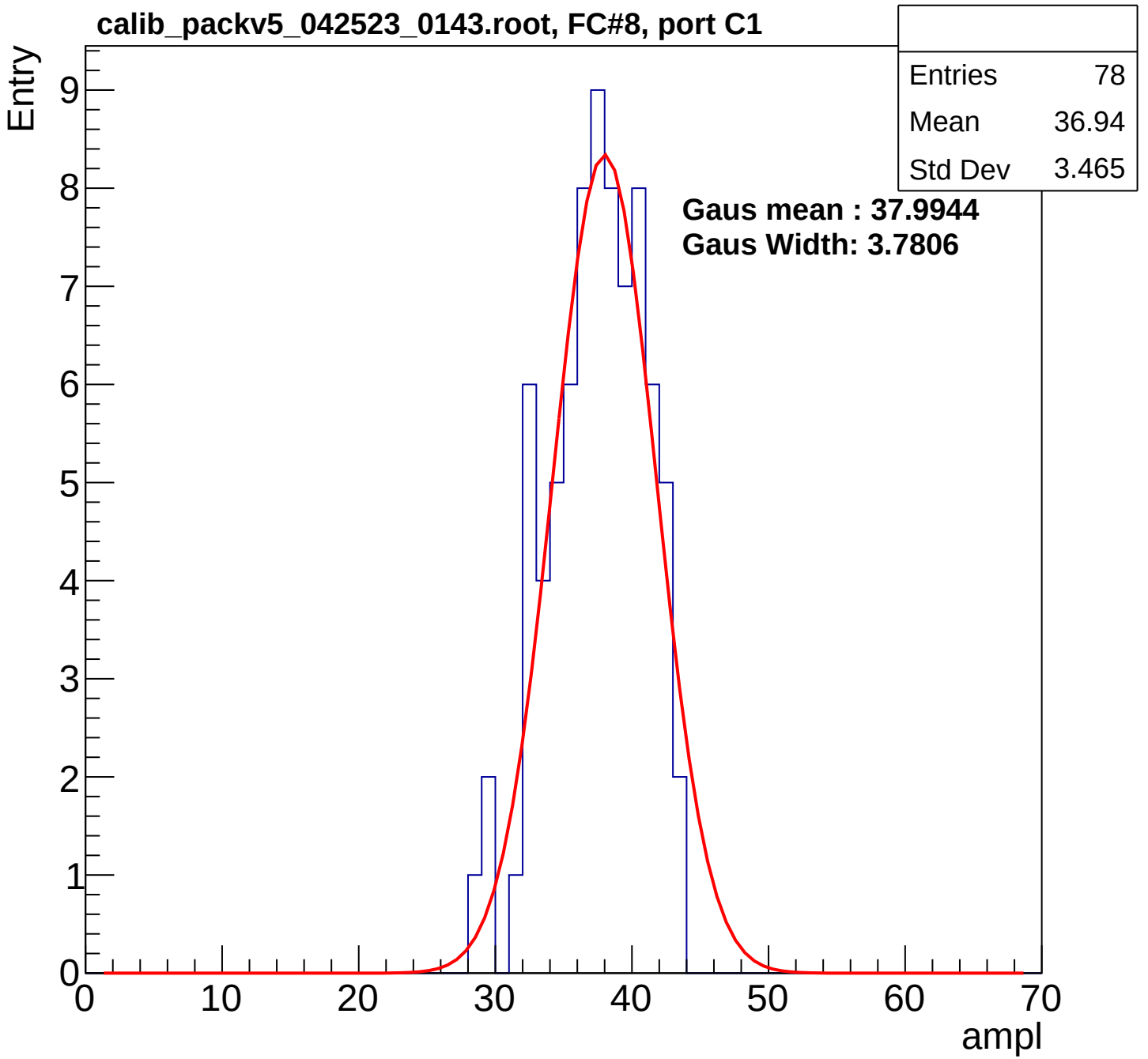
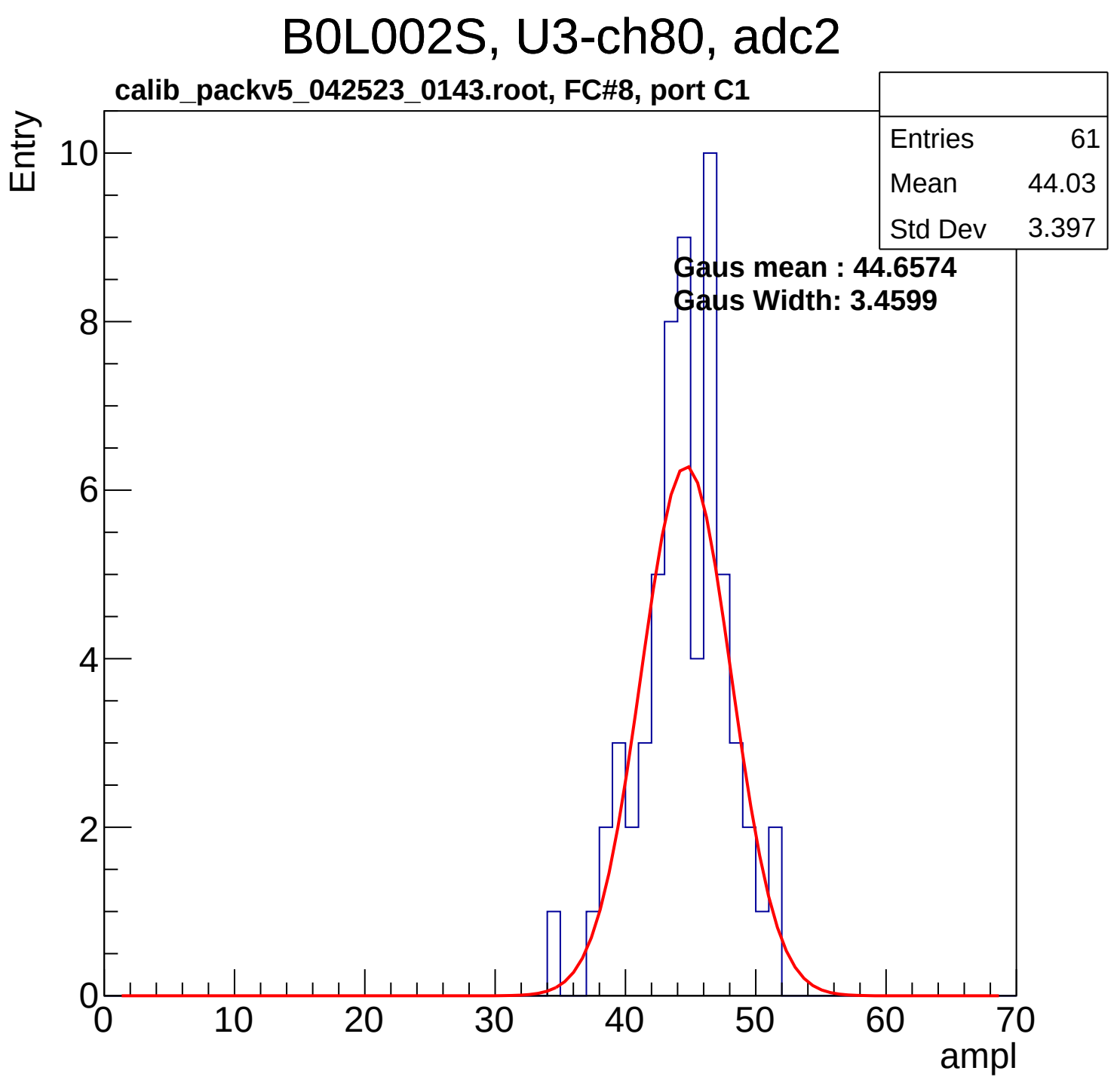
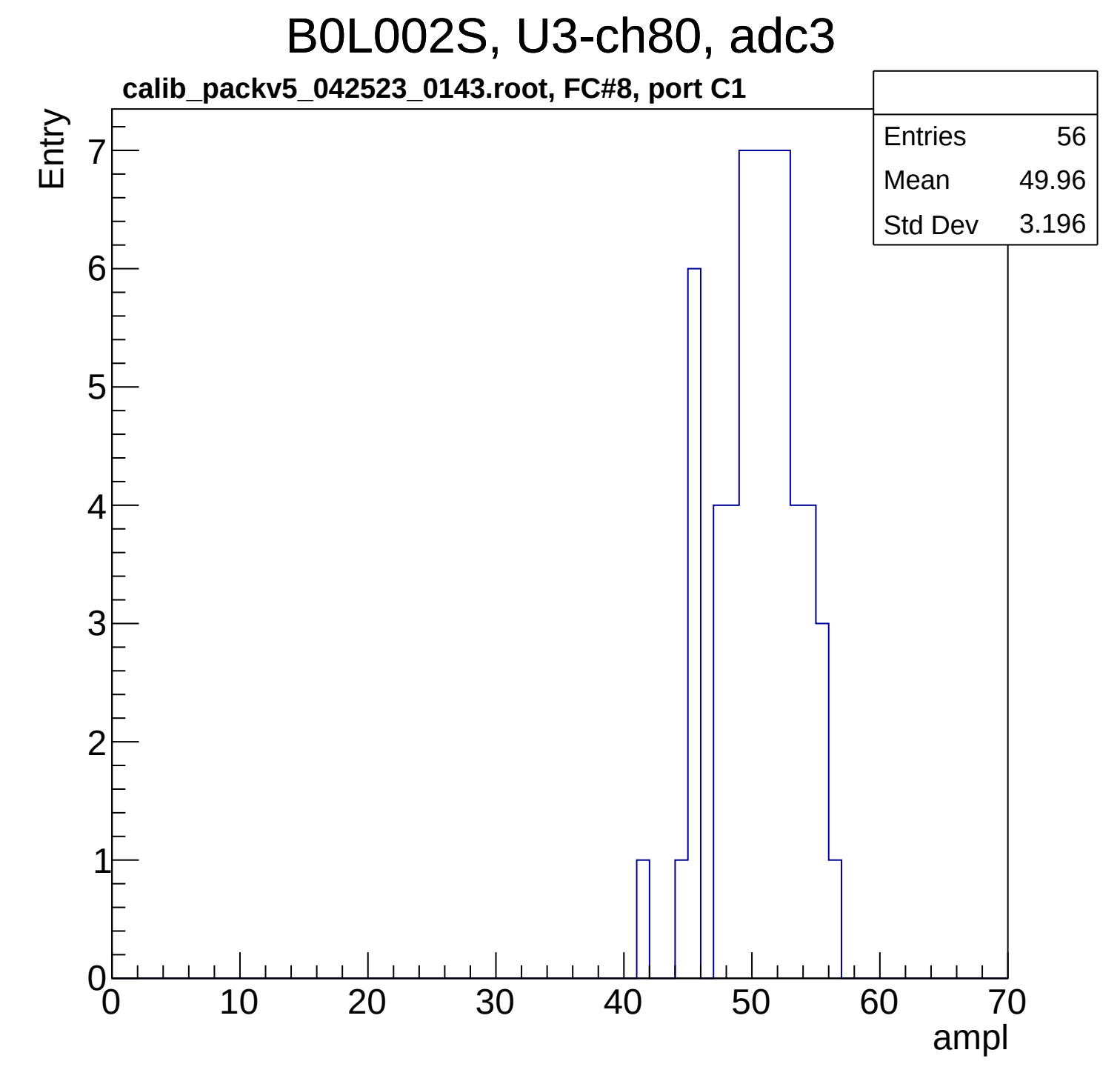
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

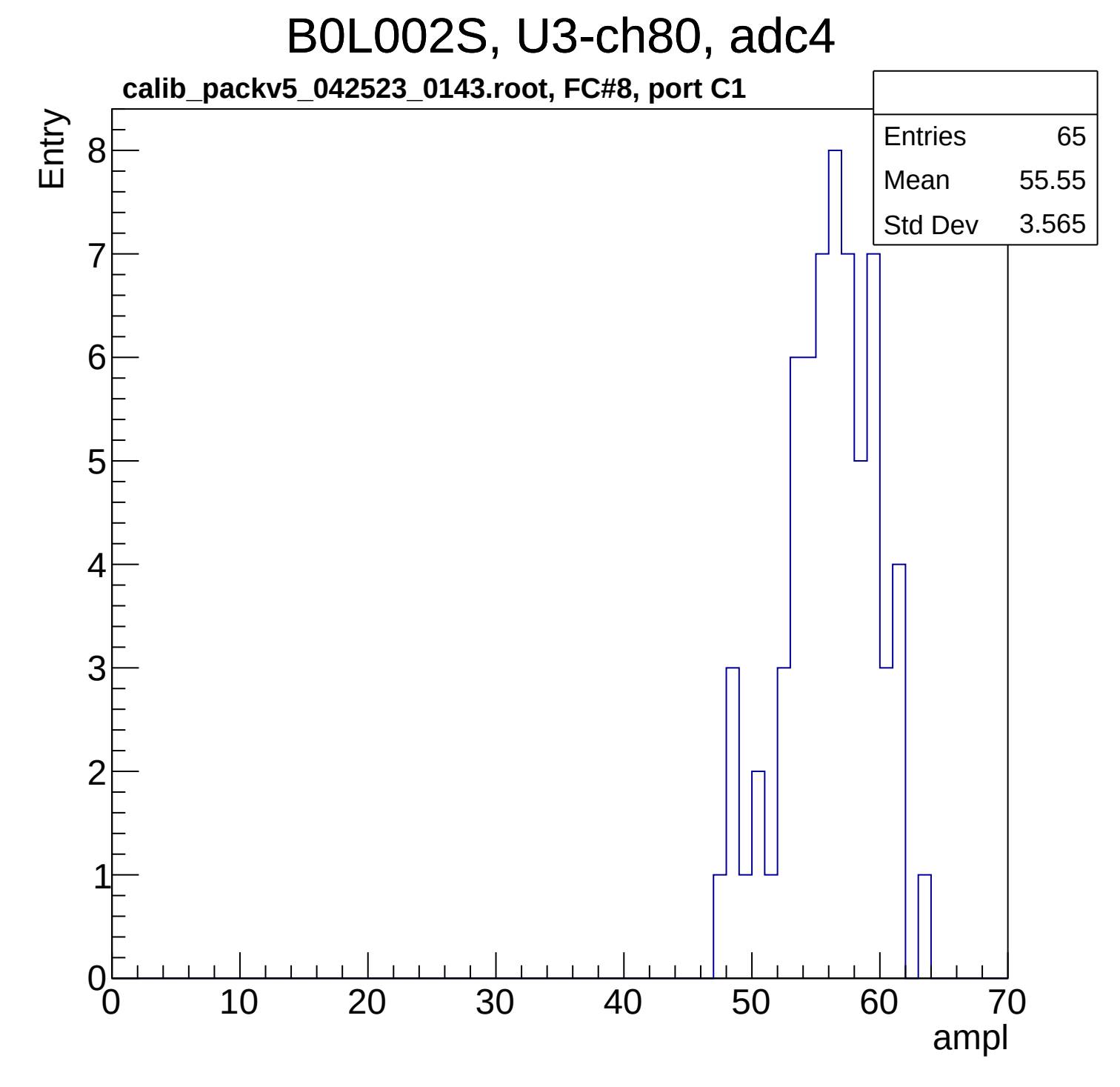

B0L002S, U3-ch79, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch80, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 29.29
Std Dev 5.966
Gaus mean : 30.7398
Gaus Width: 3.4308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch80, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 36.94
Std Dev 3.465
Gaus mean : 37.9944
Gaus Width: 3.7806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch80, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.03
Std Dev 3.397
Gaus mean : 44.6574
Gaus Width: 3.4599
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch80, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 49.96
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch80, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 55.55
Std Dev 3.565
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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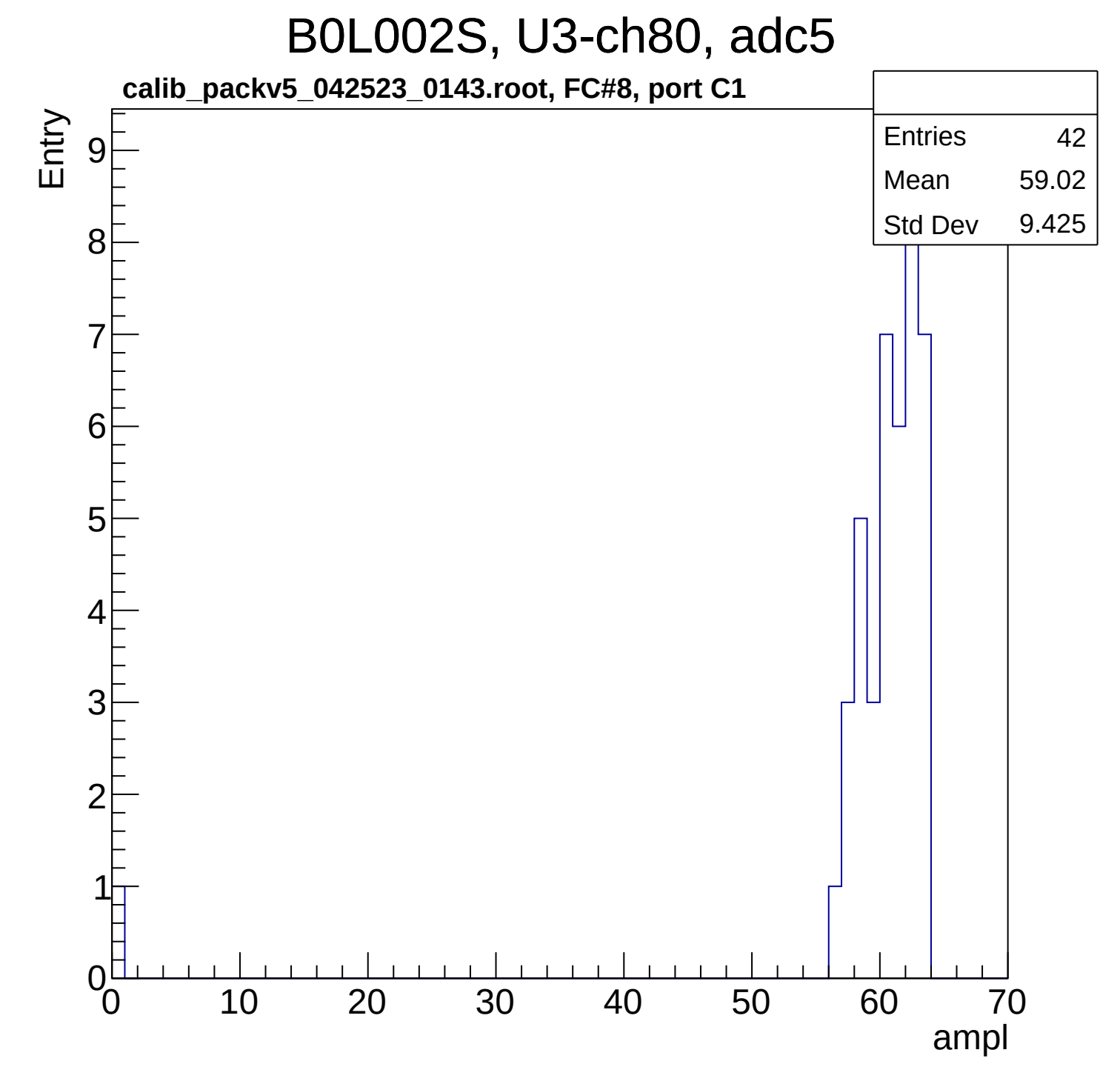
B0L002S, U3-ch80, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.02
Std Dev 9.425
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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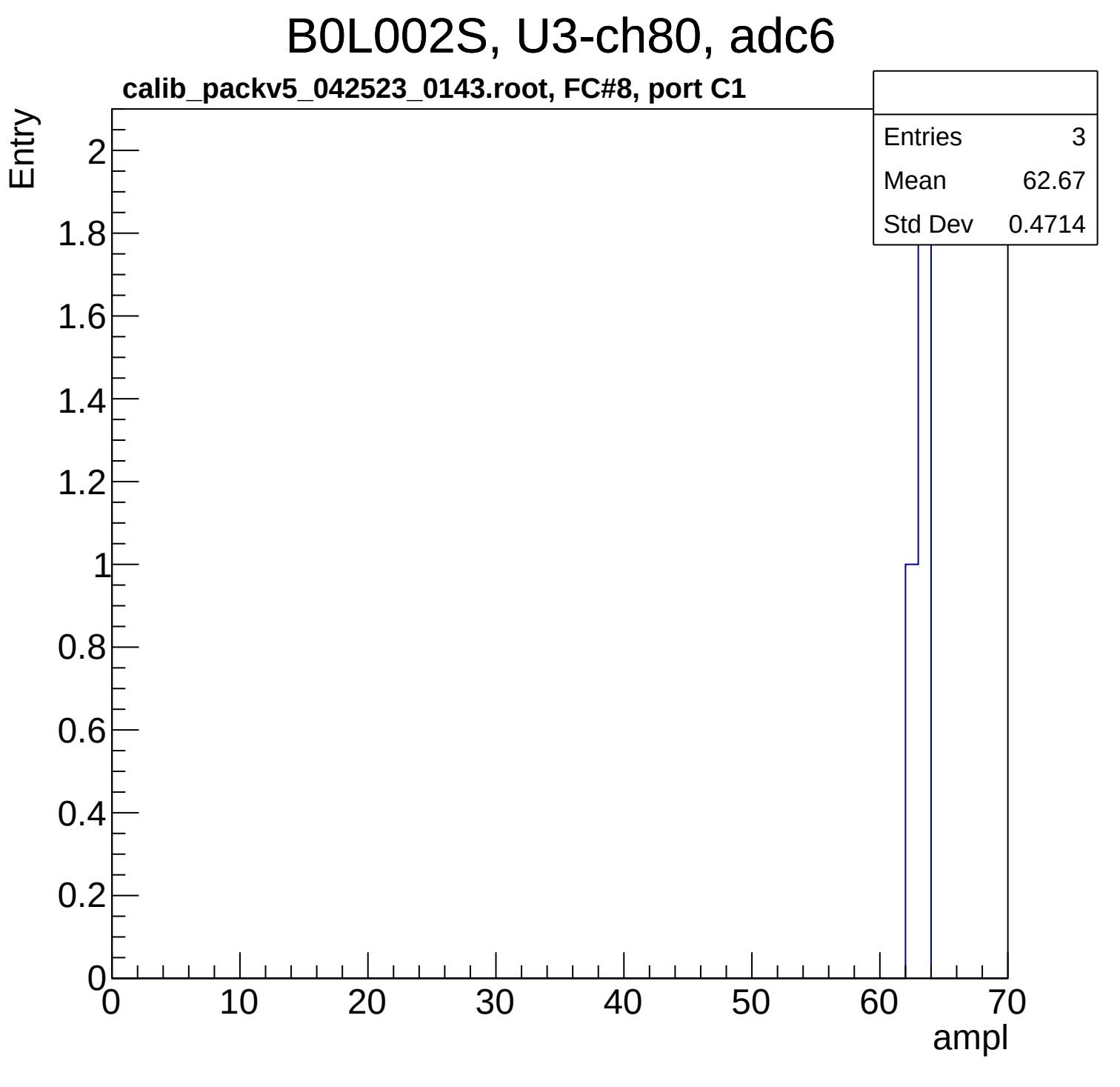
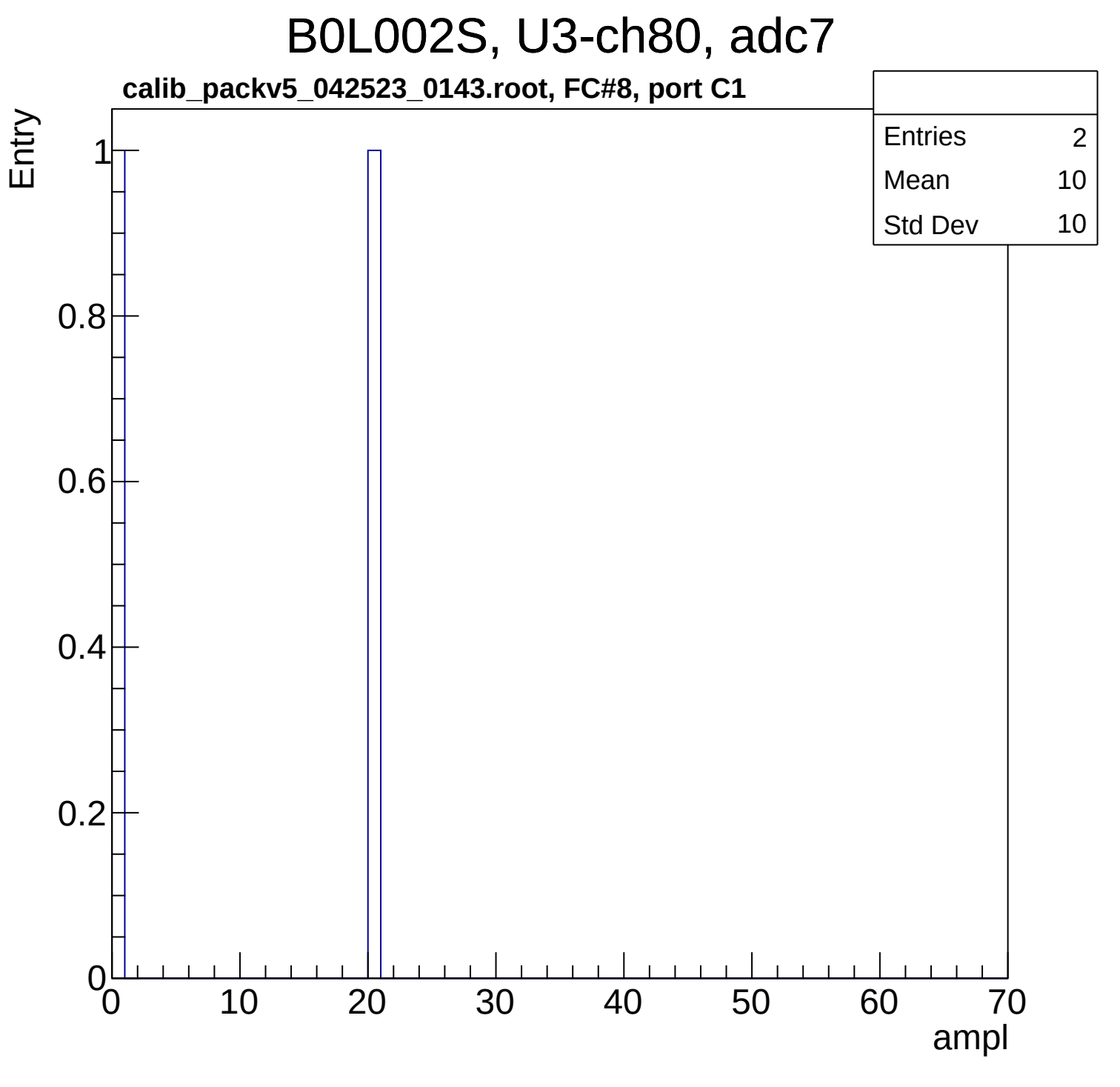
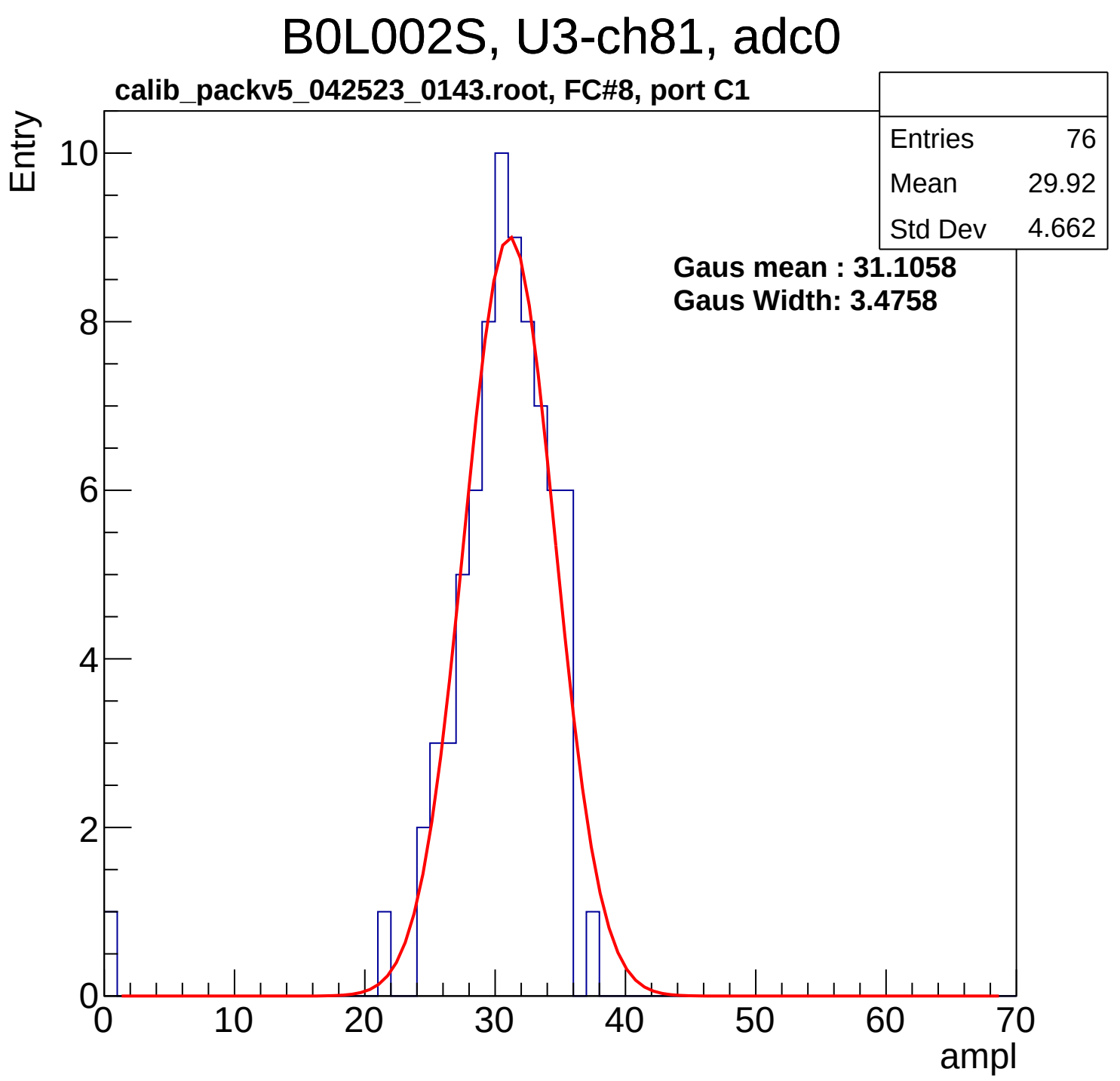
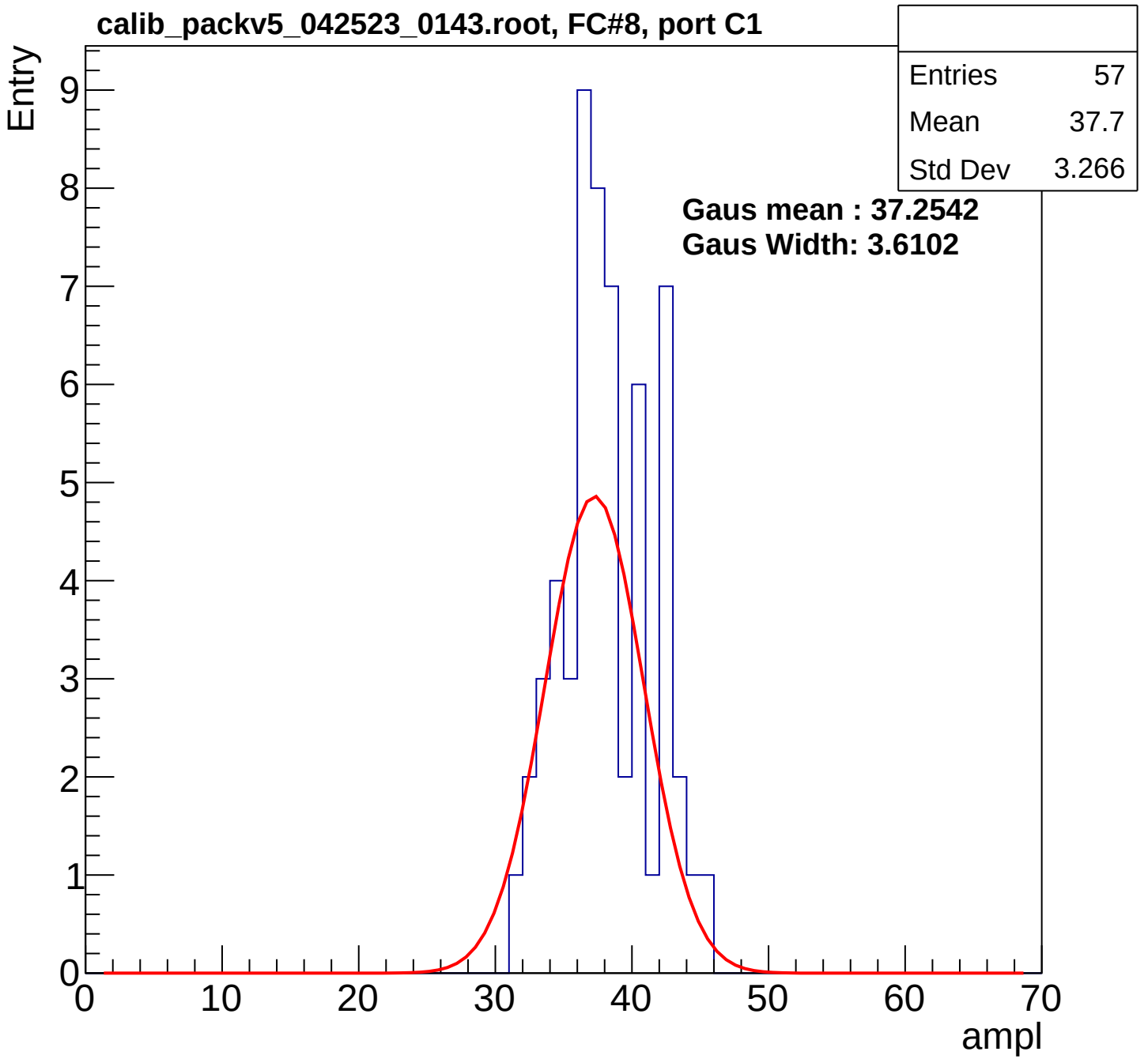

B0L002S, U3-ch80, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch80, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch81, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 29.92
Std Dev 4.662
Gaus mean : 31.1058
Gaus Width: 3.4758
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch81, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 37.7
Std Dev 3.266
Gaus mean : 37.2542
Gaus Width: 3.6102
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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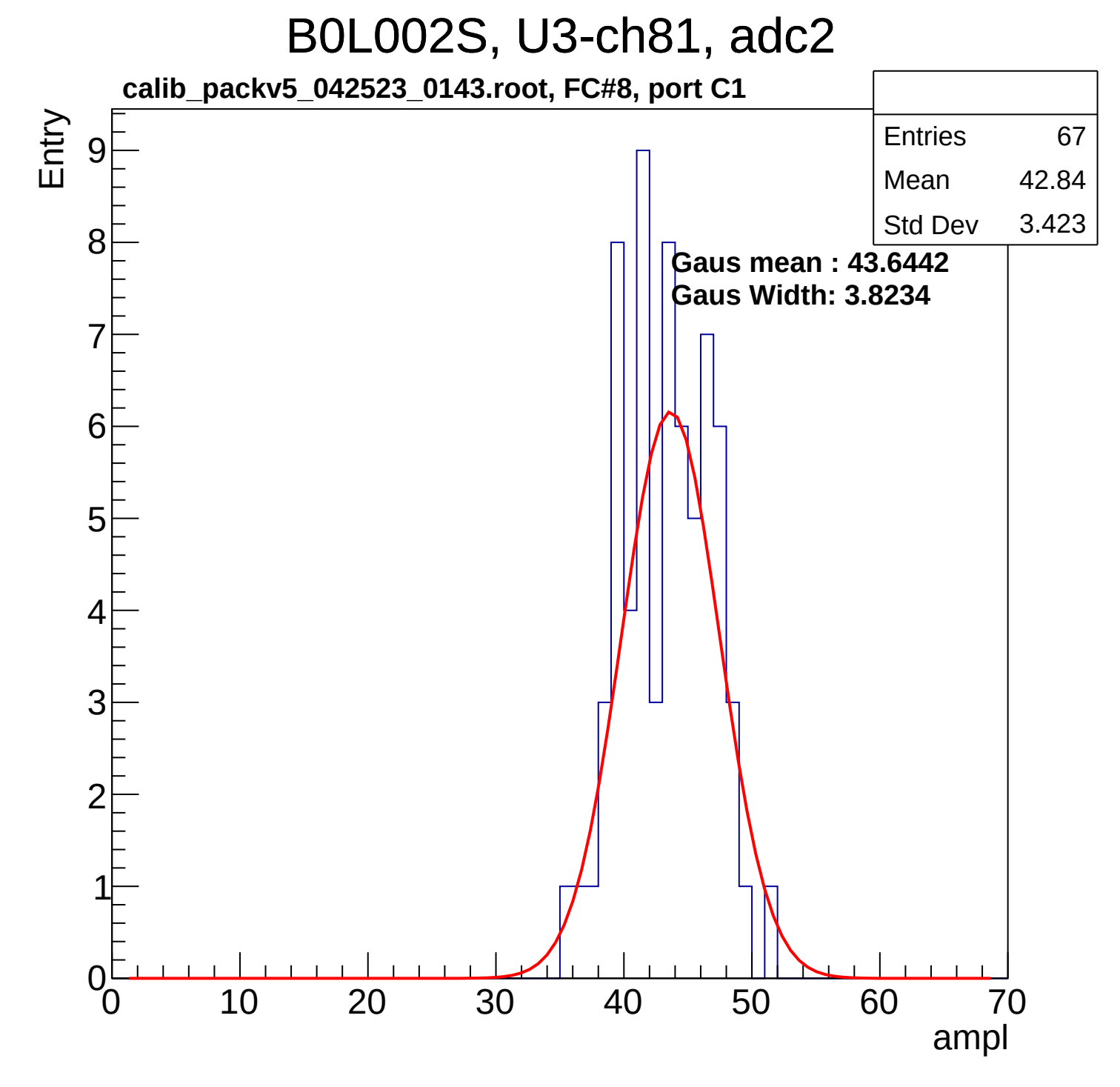
60
70


B0L002S, U3-ch81, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 42.84
Std Dev 3.423
Gaus mean : 43.6442
Gaus Width: 3.8234
Entry
ampl
0
1
2
3
4
5
6
7
8
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60
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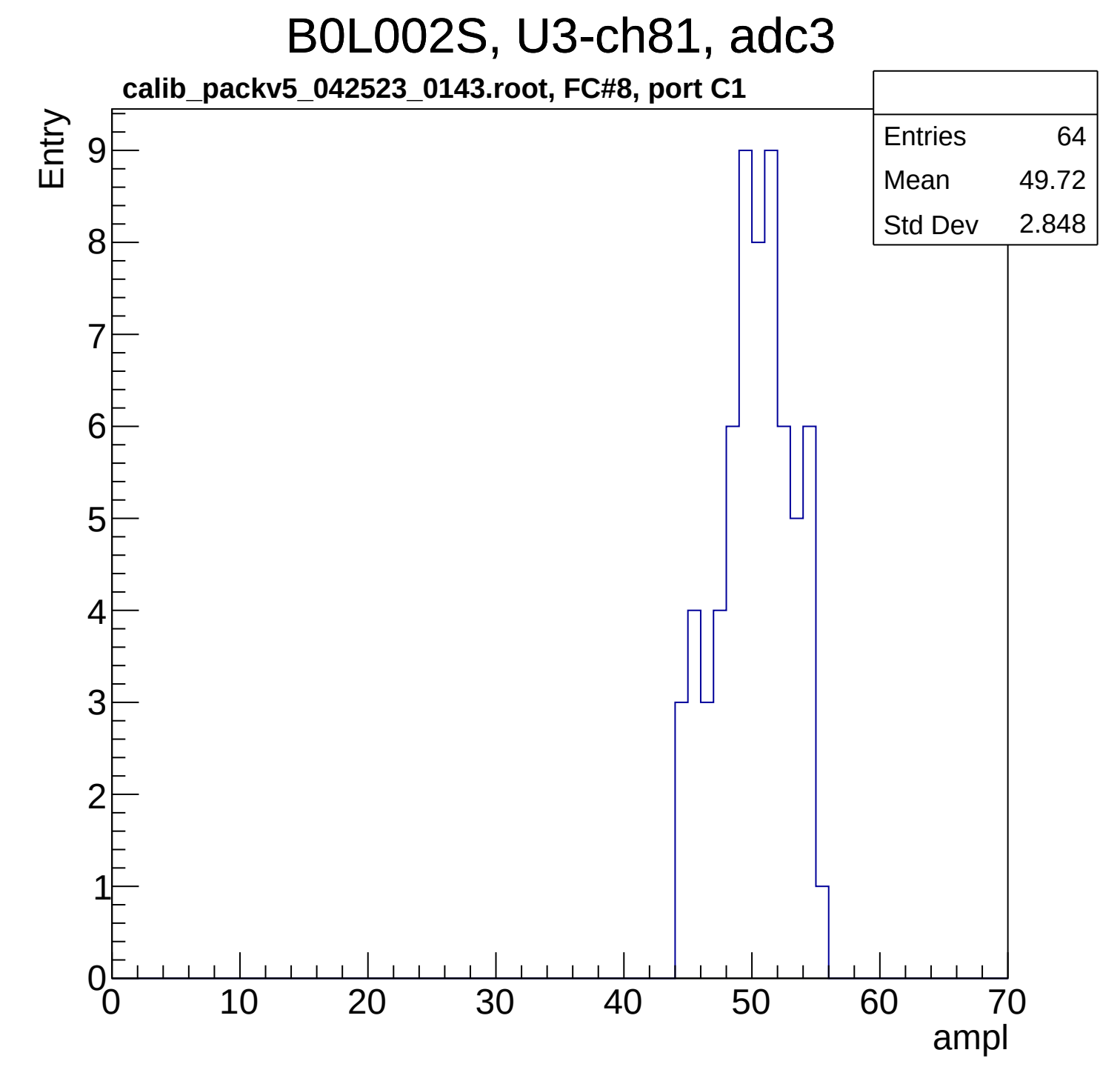
B0L002S, U3-ch81, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 49.72
Std Dev 2.848
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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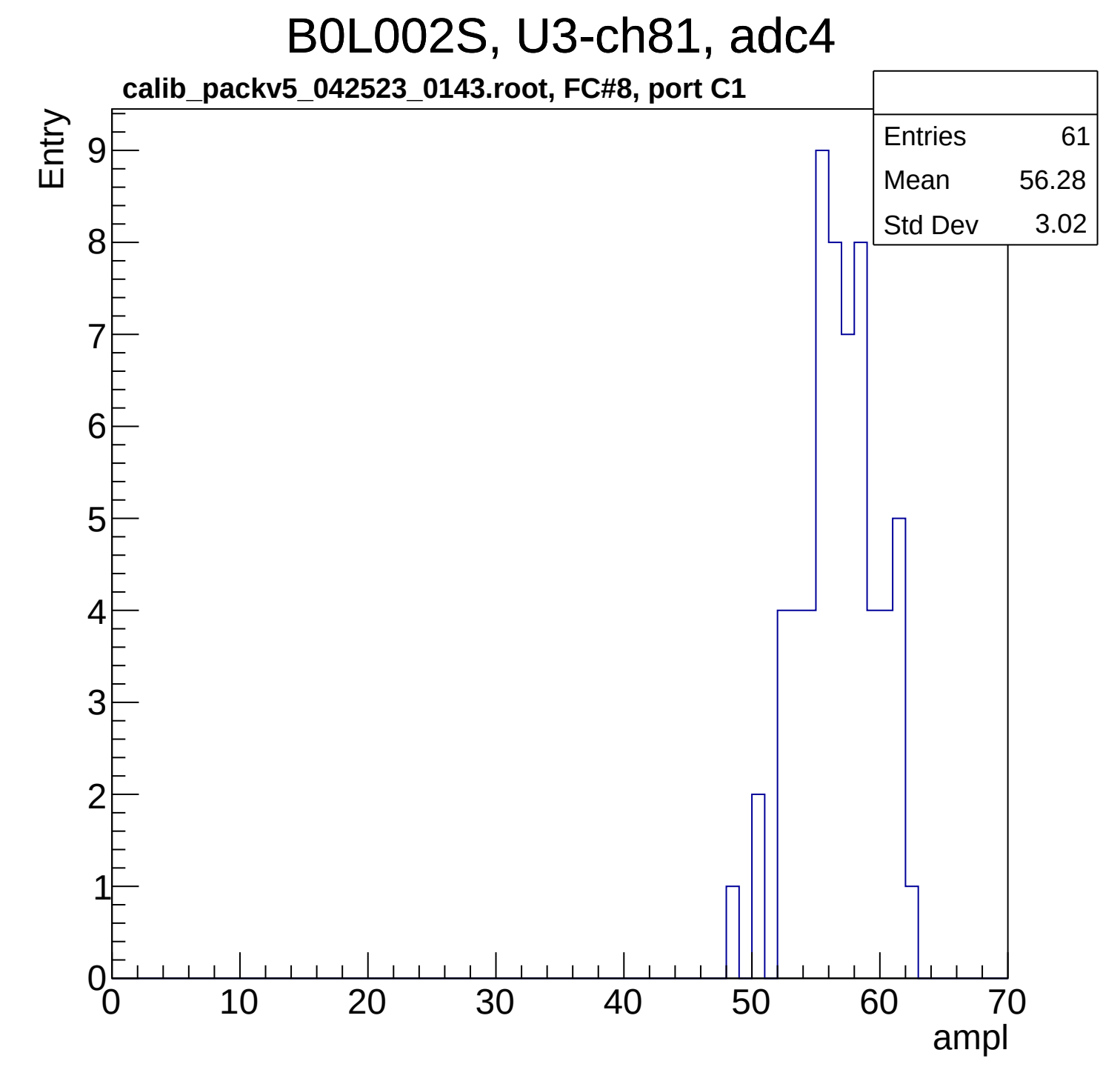
B0L002S, U3-ch81, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.28
Std Dev 3.02
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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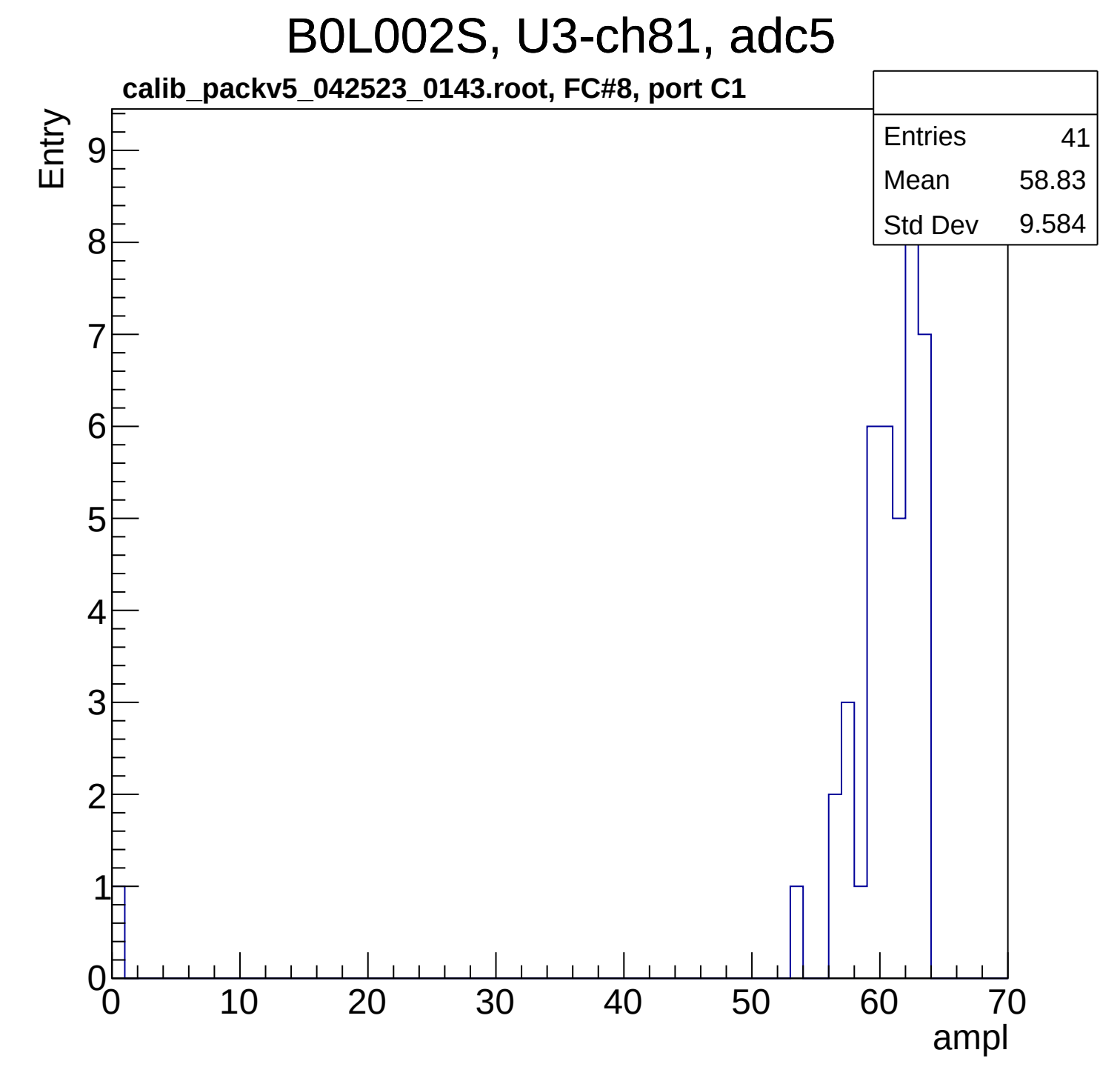
B0L002S, U3-ch81, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.83
Std Dev 9.584
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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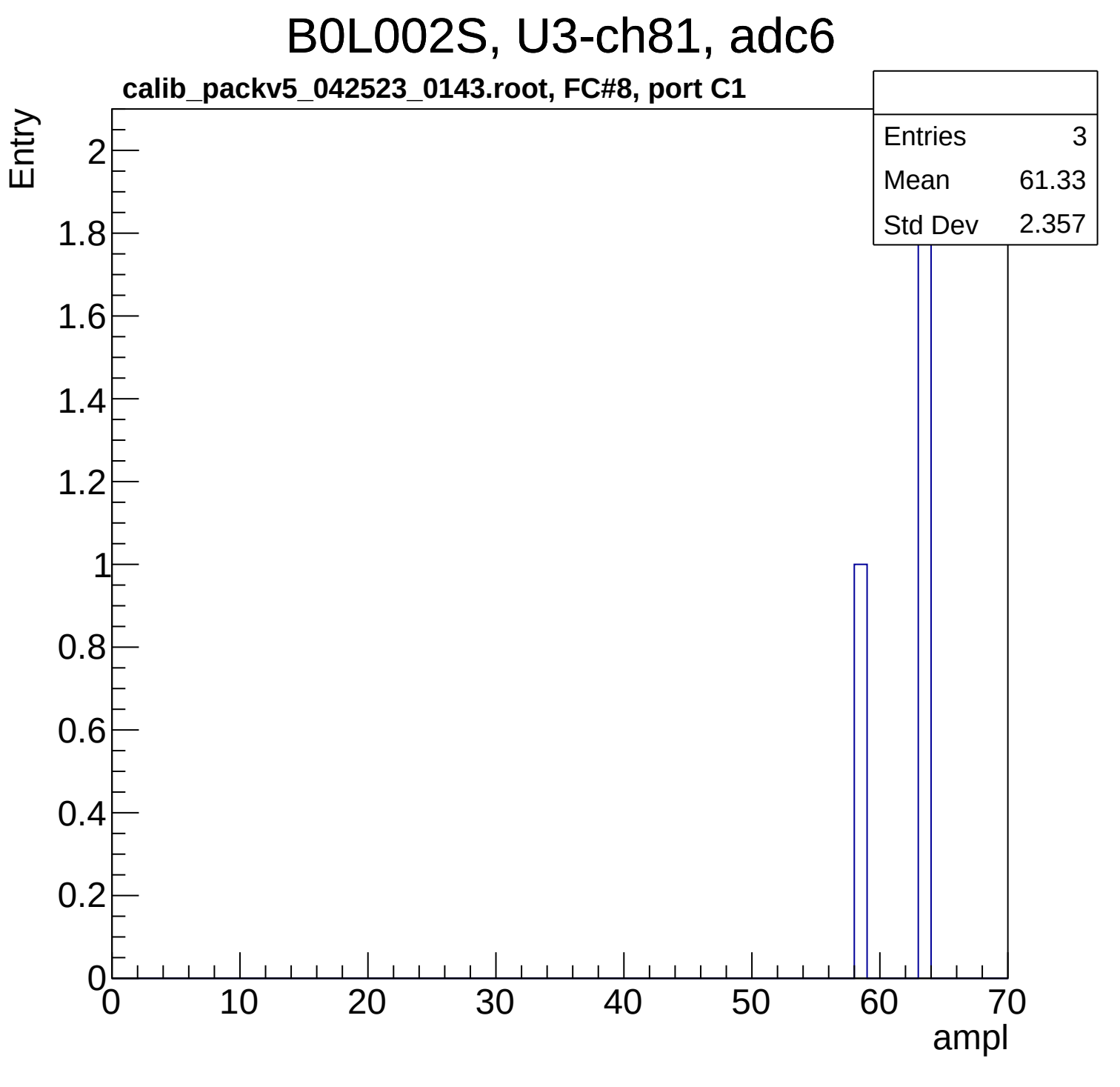
B0L002S, U3-ch81, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.33
Std Dev 2.357
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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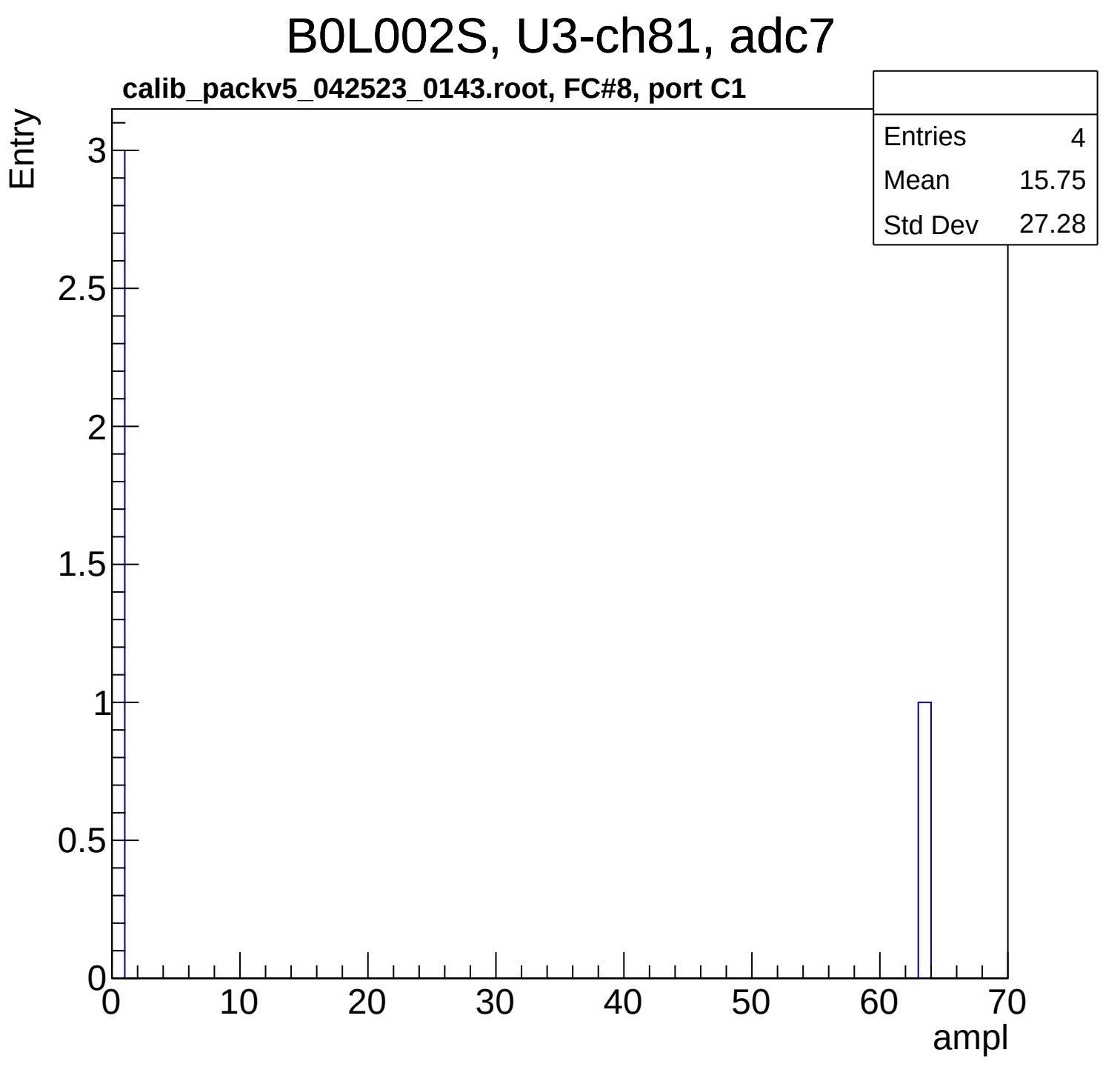
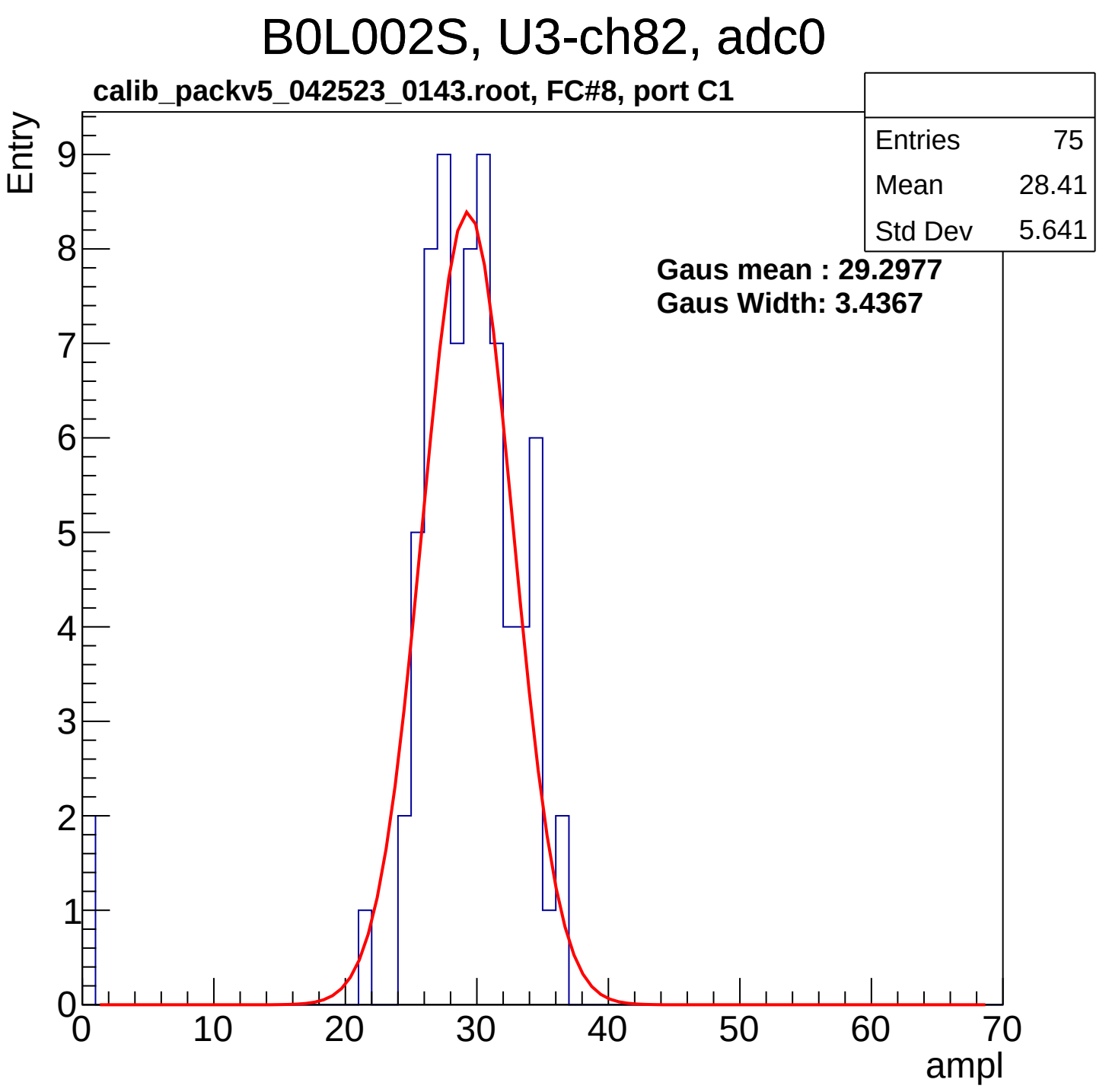

B0L002S, U3-ch81, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 15.75
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch82, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 28.41
Std Dev 5.641
Gaus mean : 29.2977
Gaus Width: 3.4367
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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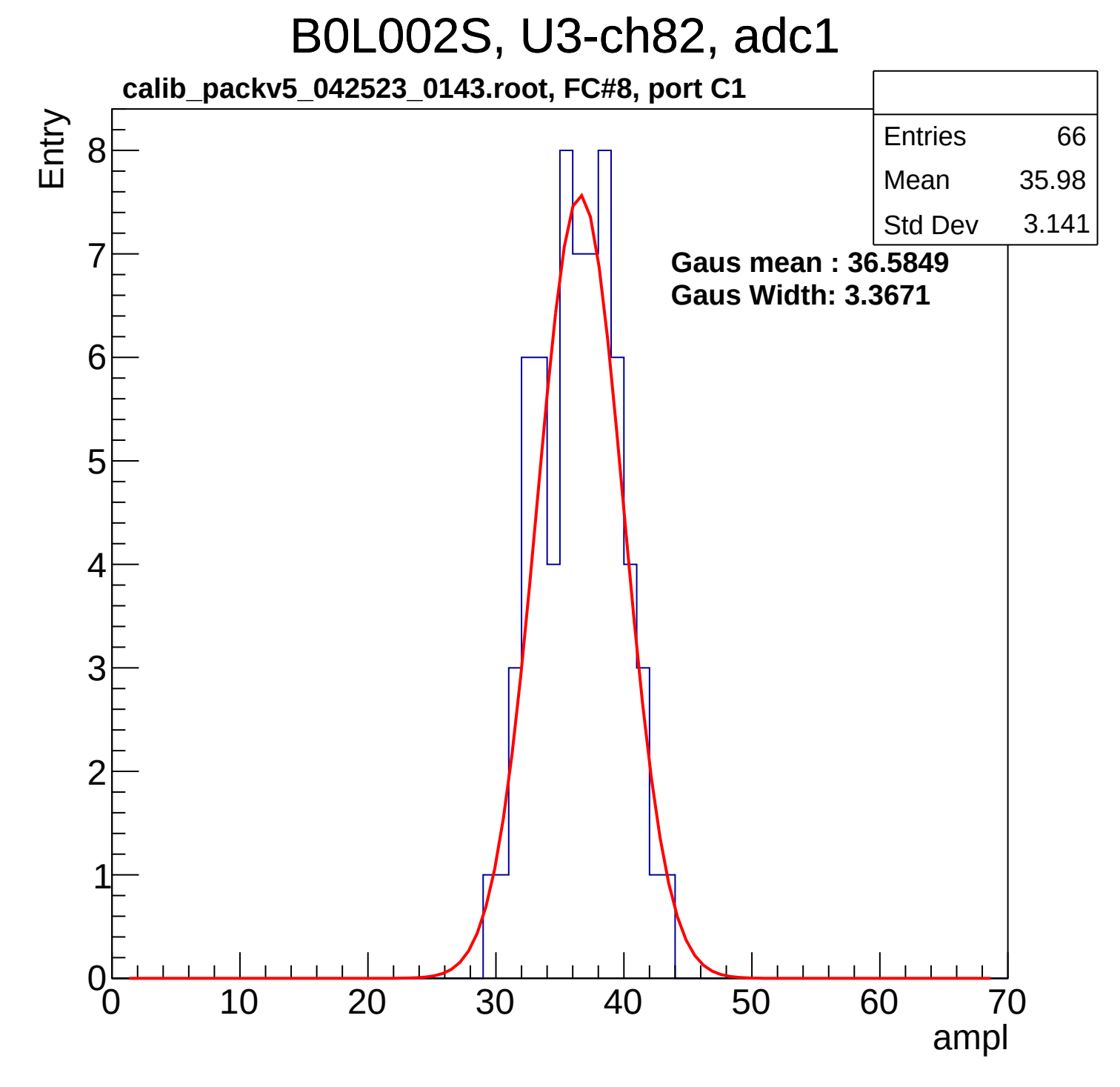
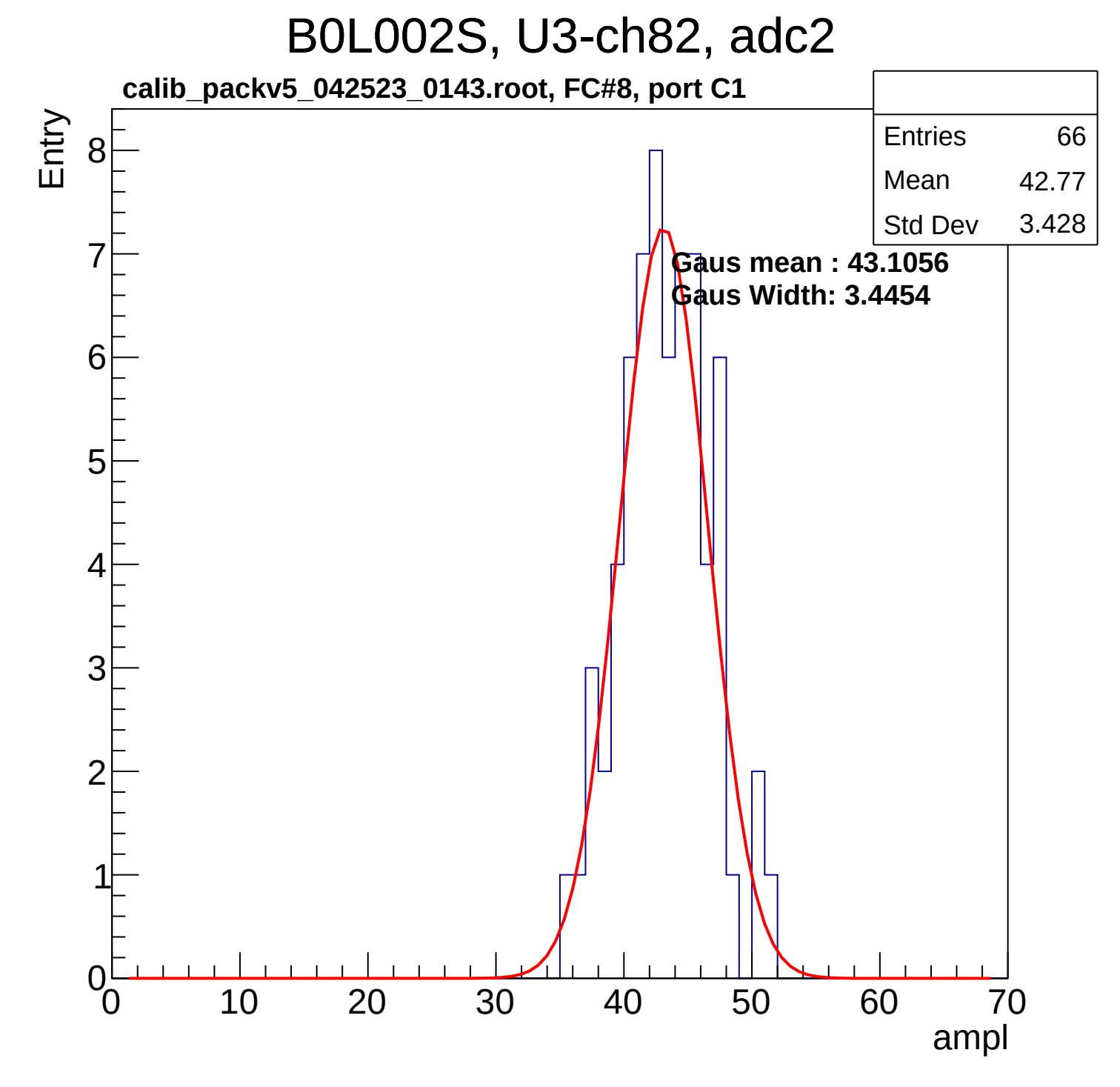

B0L002S, U3-ch82, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 35.98
Std Dev 3.141
Gaus mean : 36.5849
Gaus Width: 3.3671
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch82, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 42.77
Std Dev 3.428
Gaus mean : 43.1056
Gaus Width: 3.4454
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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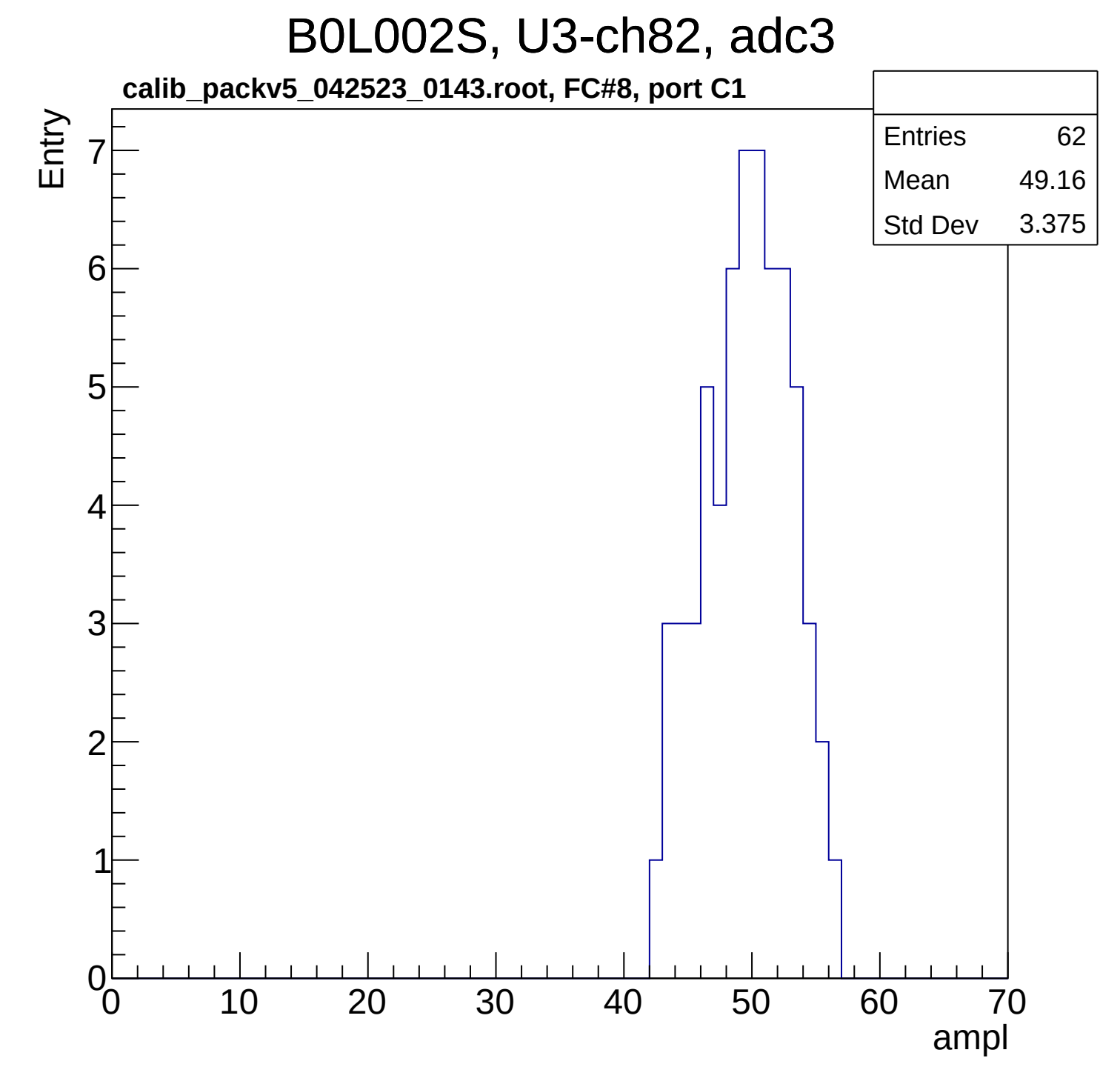
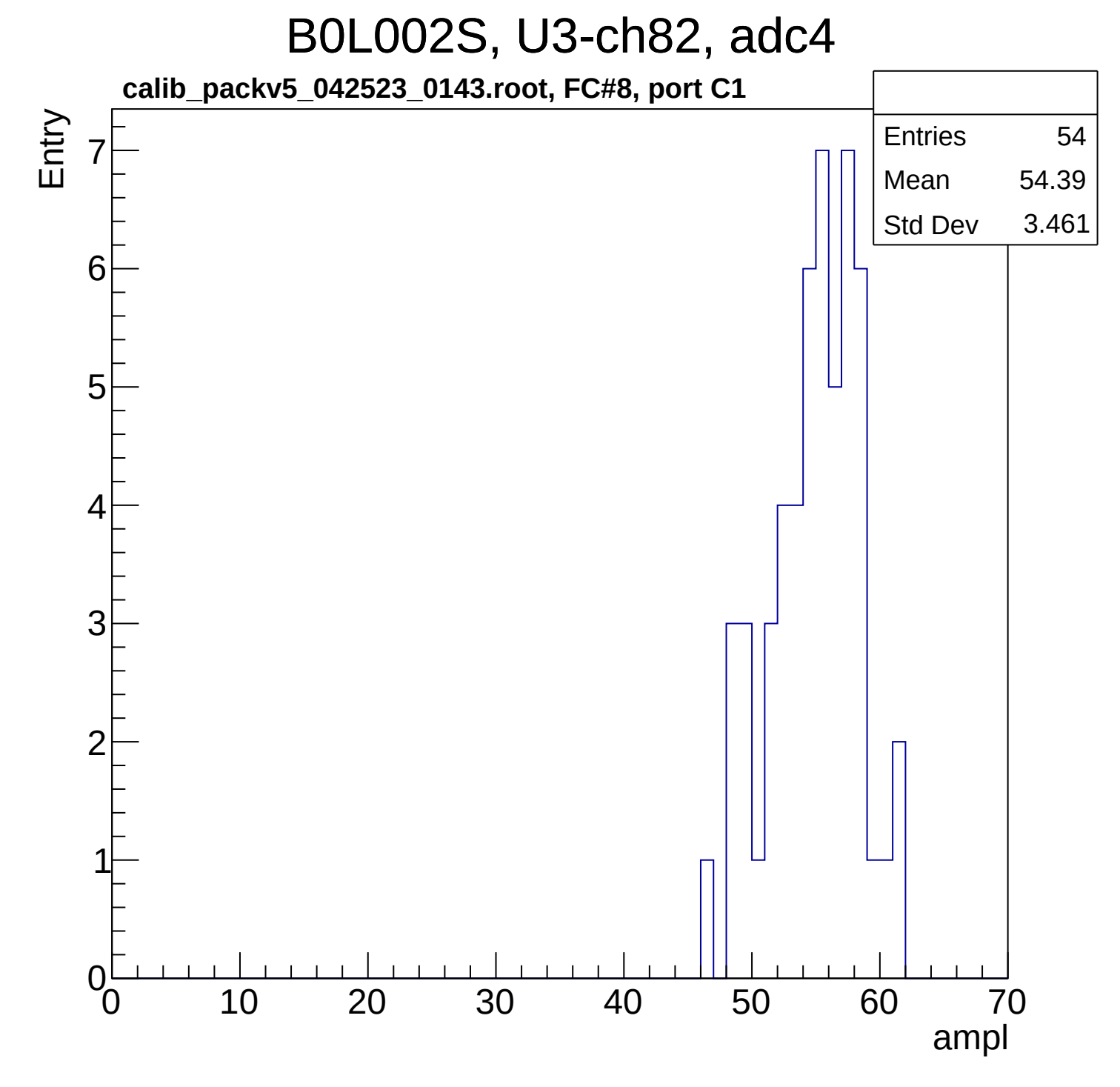
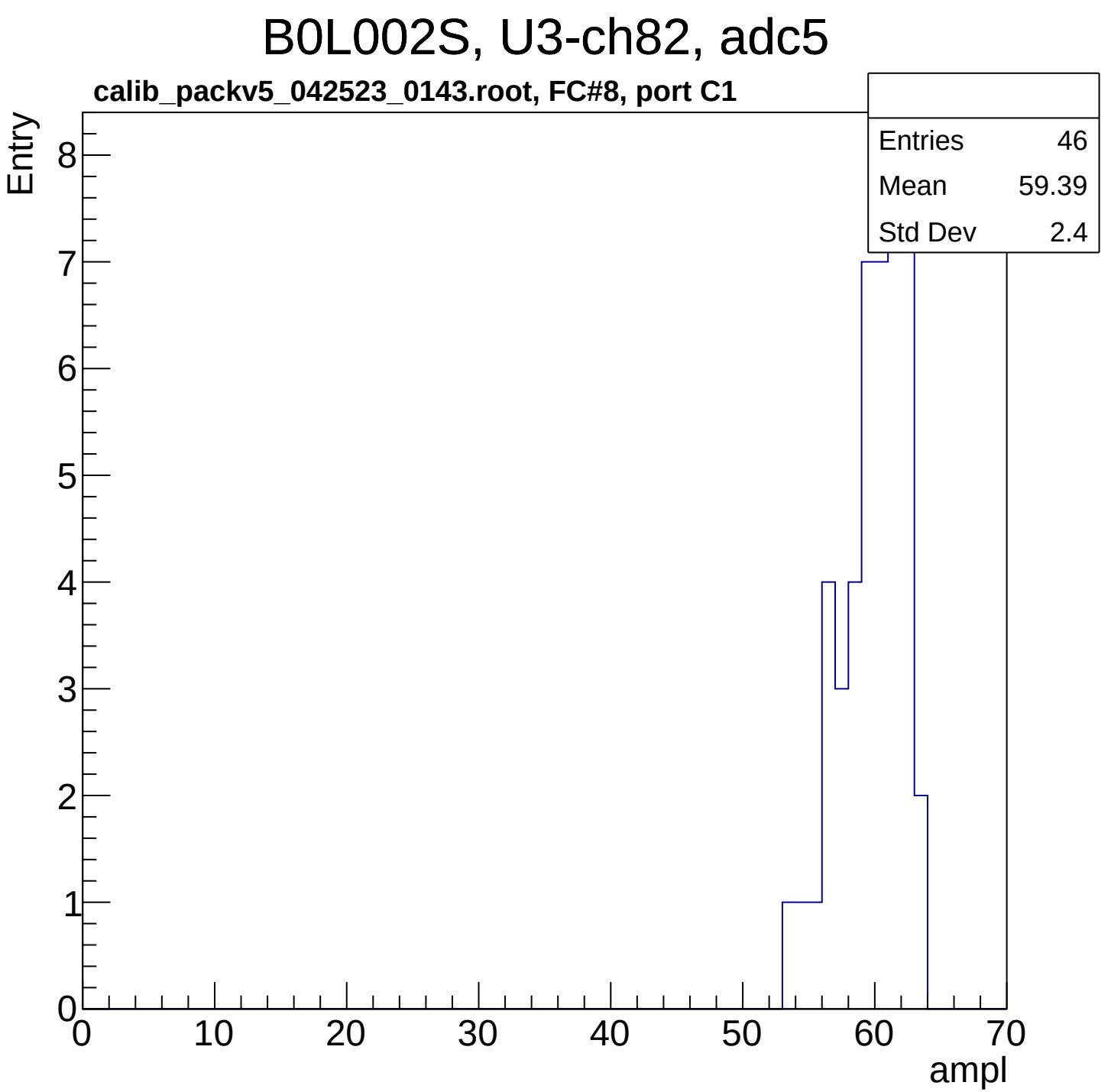

B0L002S, U3-ch82, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 49.16
Std Dev 3.375
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch82, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 54.39
Std Dev 3.461
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch82, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.39
Std Dev 2.4
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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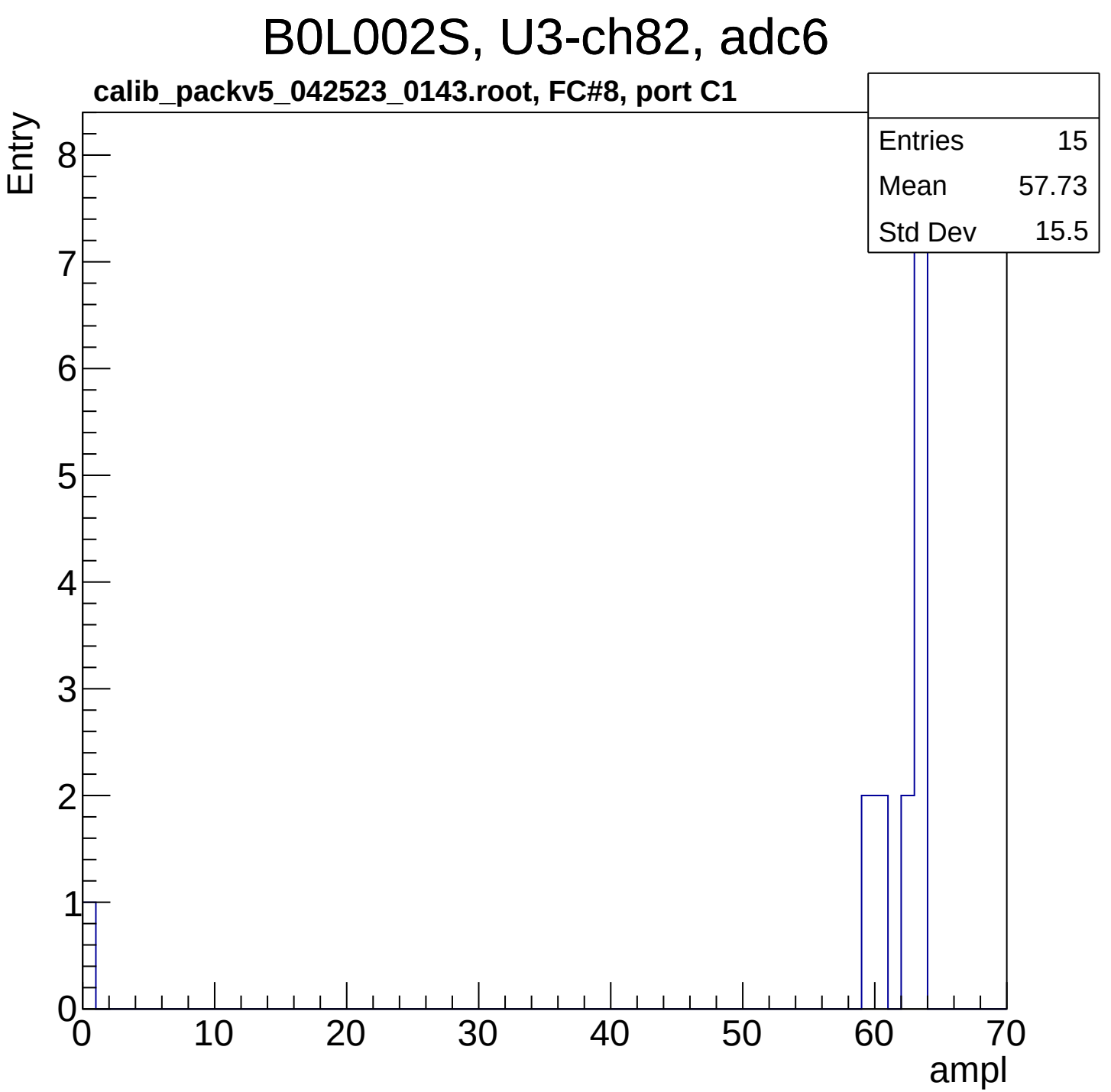
B0L002S, U3-ch82, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 15
Mean 57.73
Std Dev 15.5
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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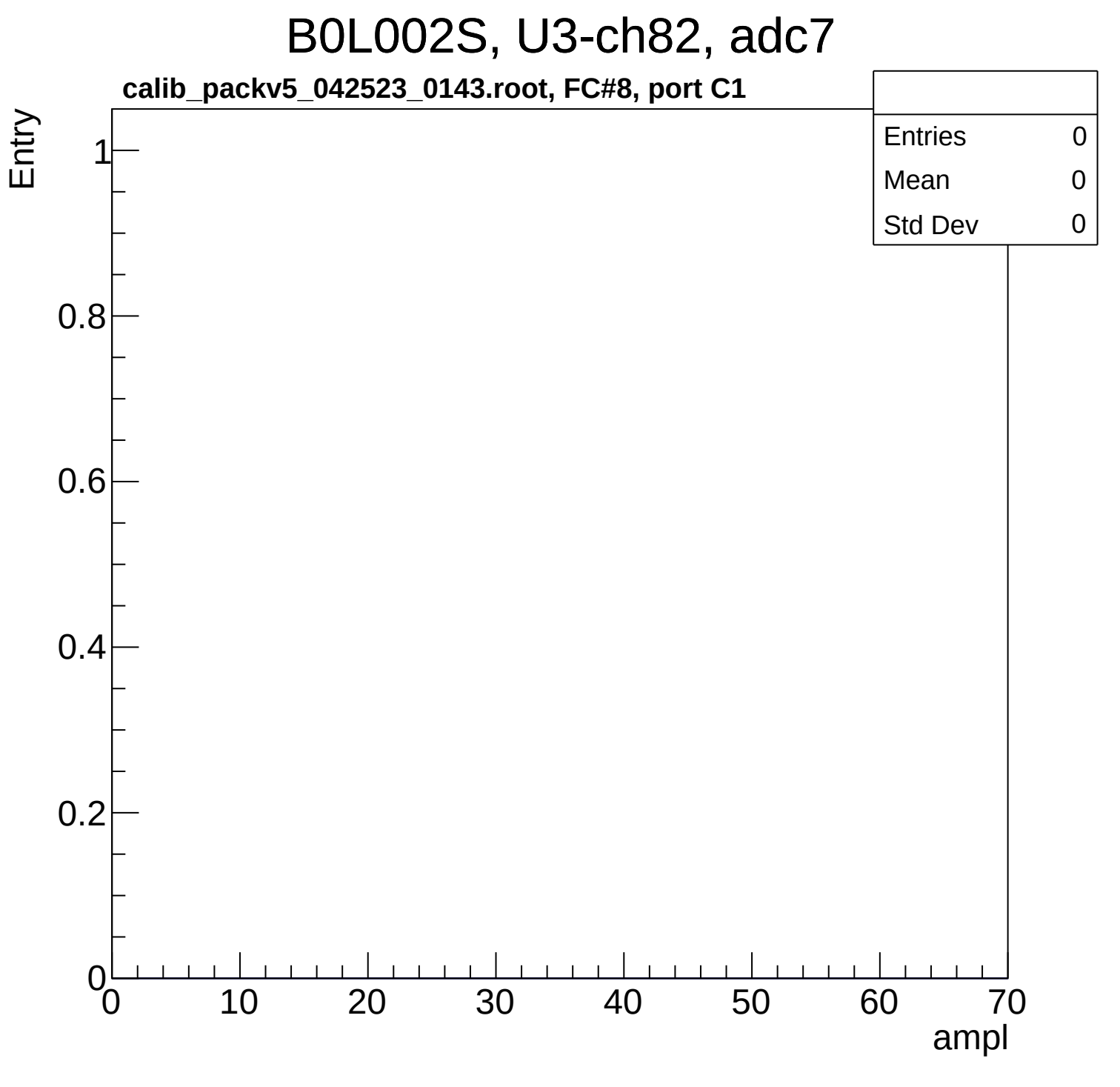
B0L002S, U3-ch82, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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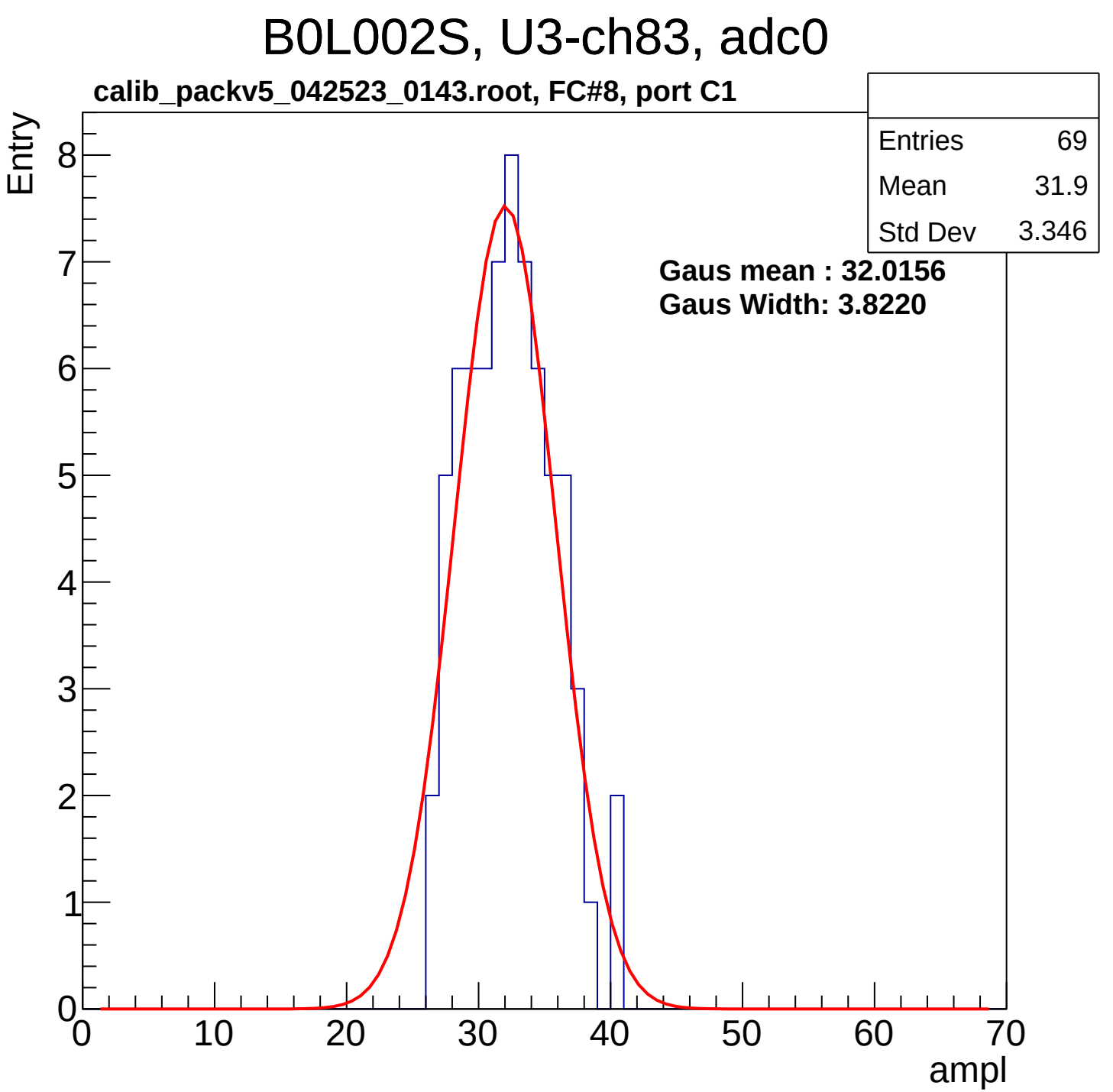
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch83, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 31.9
Std Dev 3.346
Gaus mean : 32.0156
Gaus Width: 3.8220
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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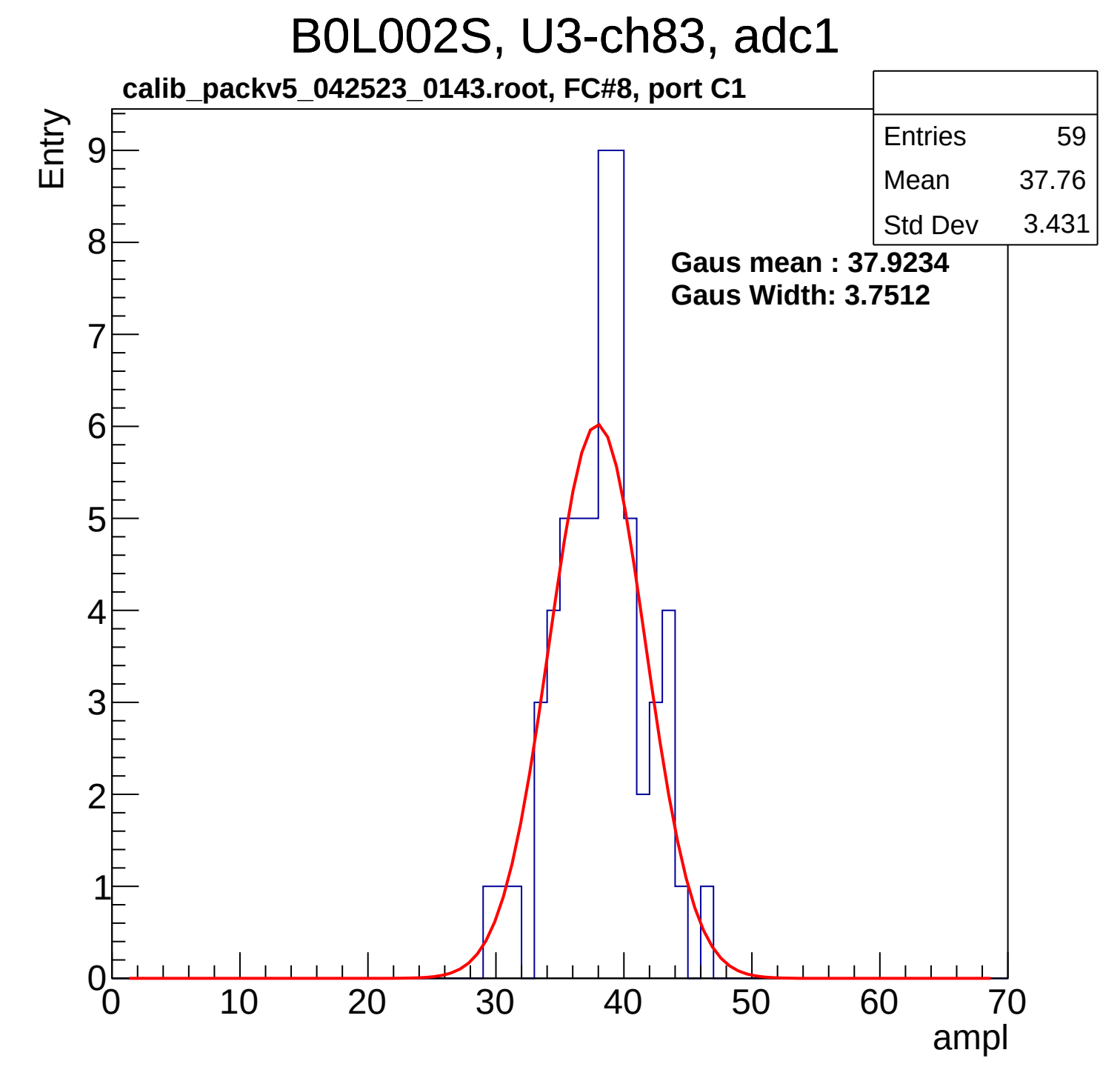
B0L002S, U3-ch83, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 37.76
Std Dev 3.431
Gaus mean : 37.9234
Gaus Width: 3.7512
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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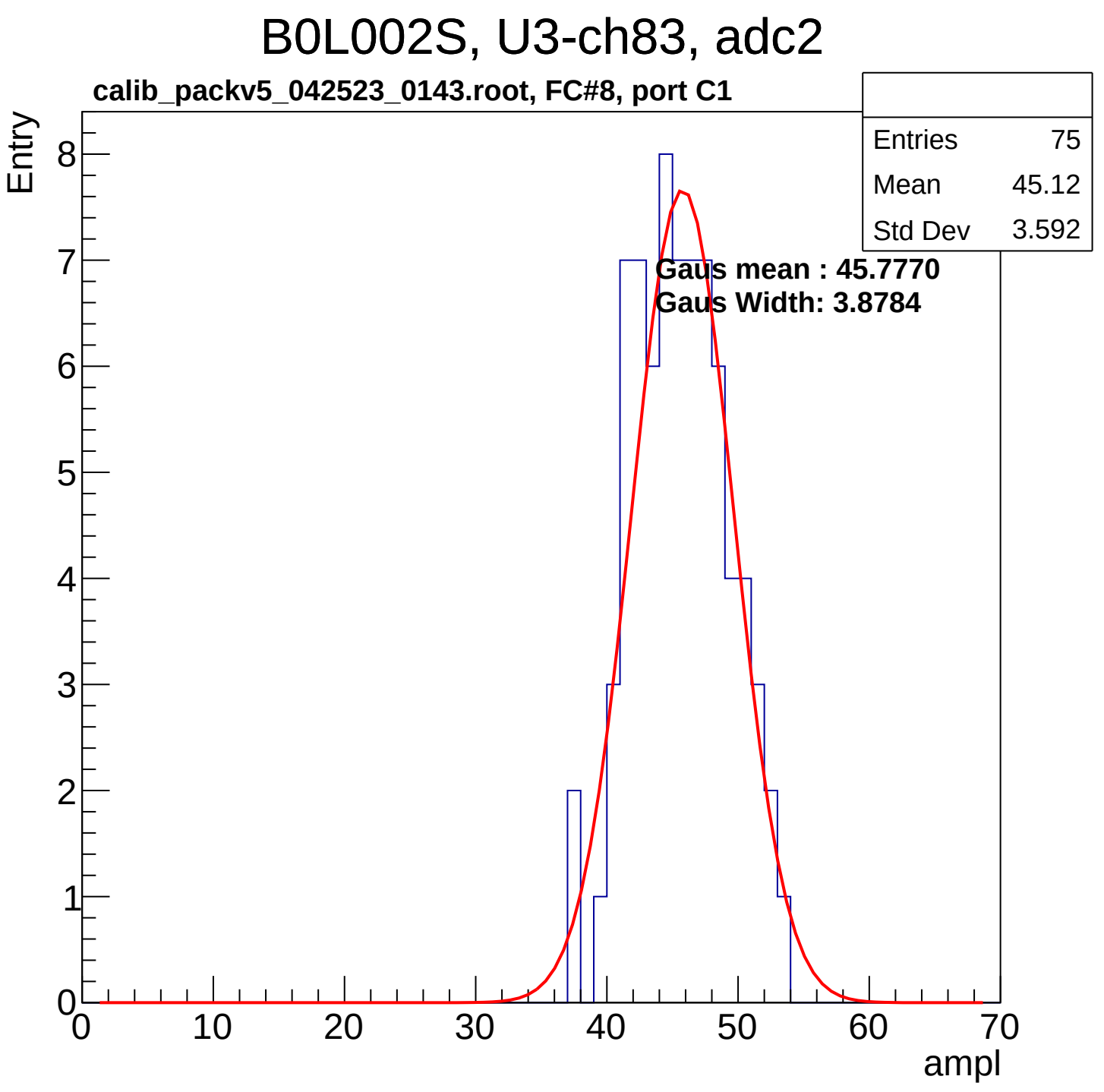
B0L002S, U3-ch83, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 45.12
Std Dev 3.592
Gaus mean : 45.7770
Gaus Width: 3.8784
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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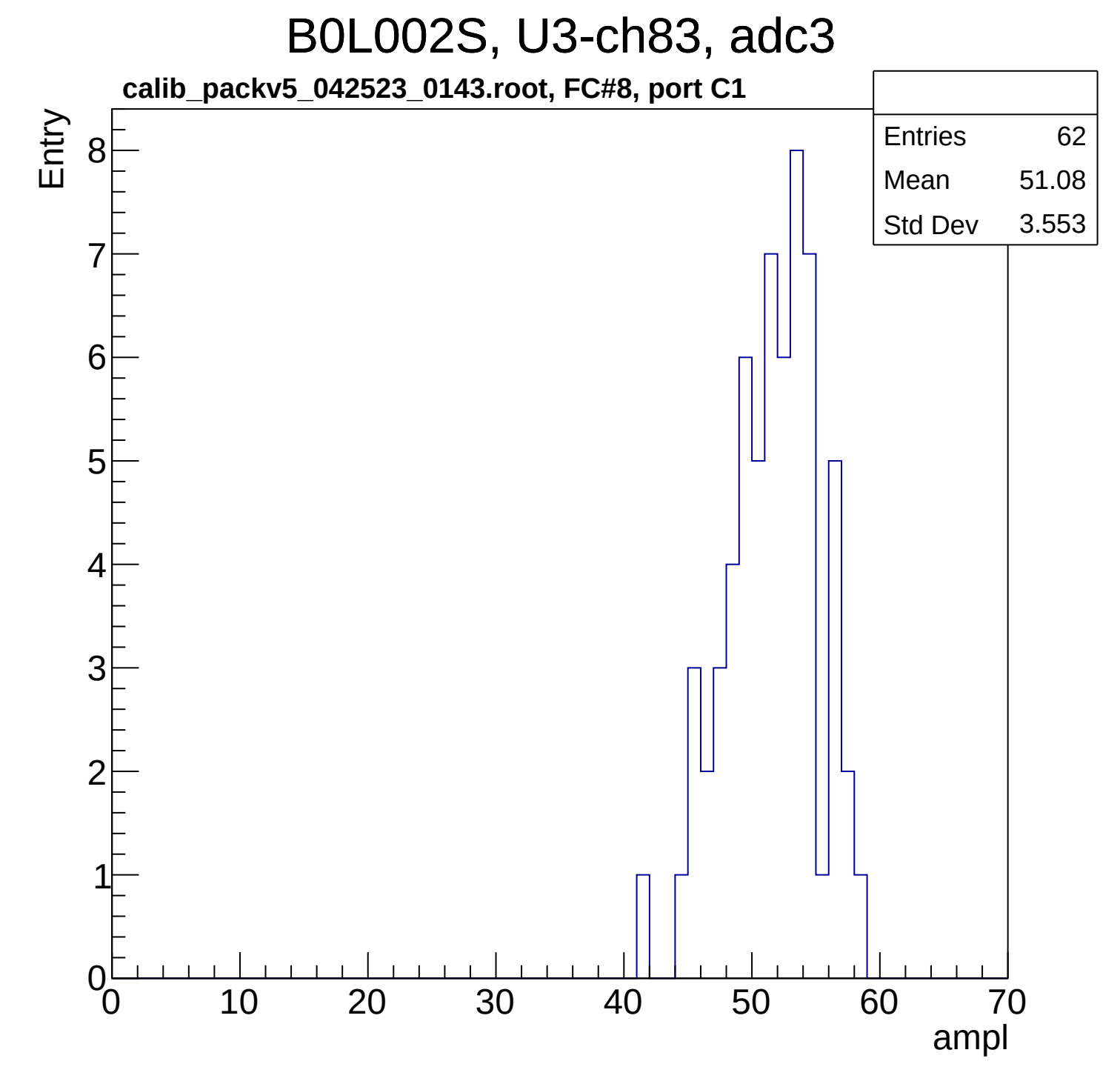
B0L002S, U3-ch83, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.08
Std Dev 3.553
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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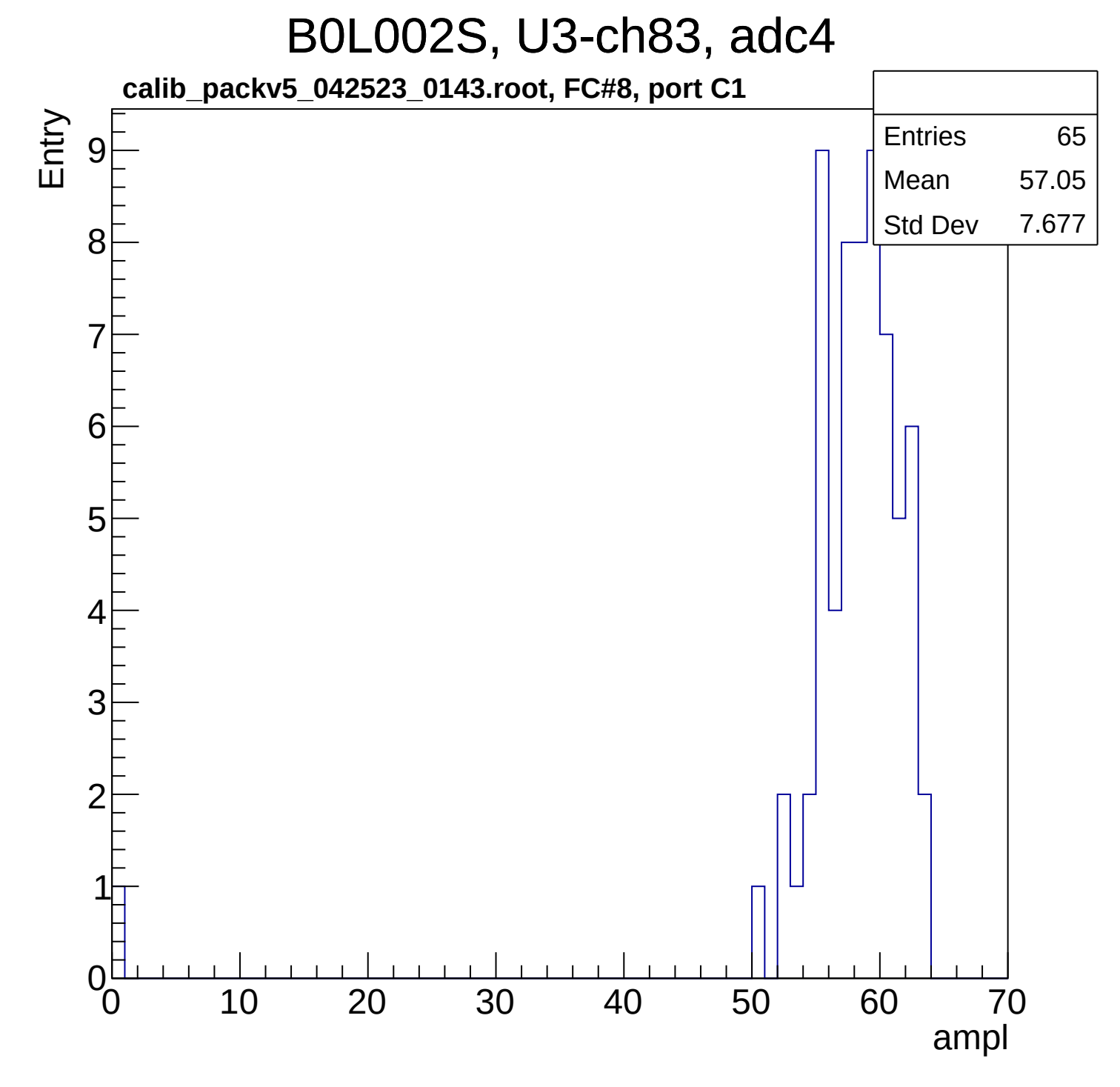
B0L002S, U3-ch83, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.05
Std Dev 7.677
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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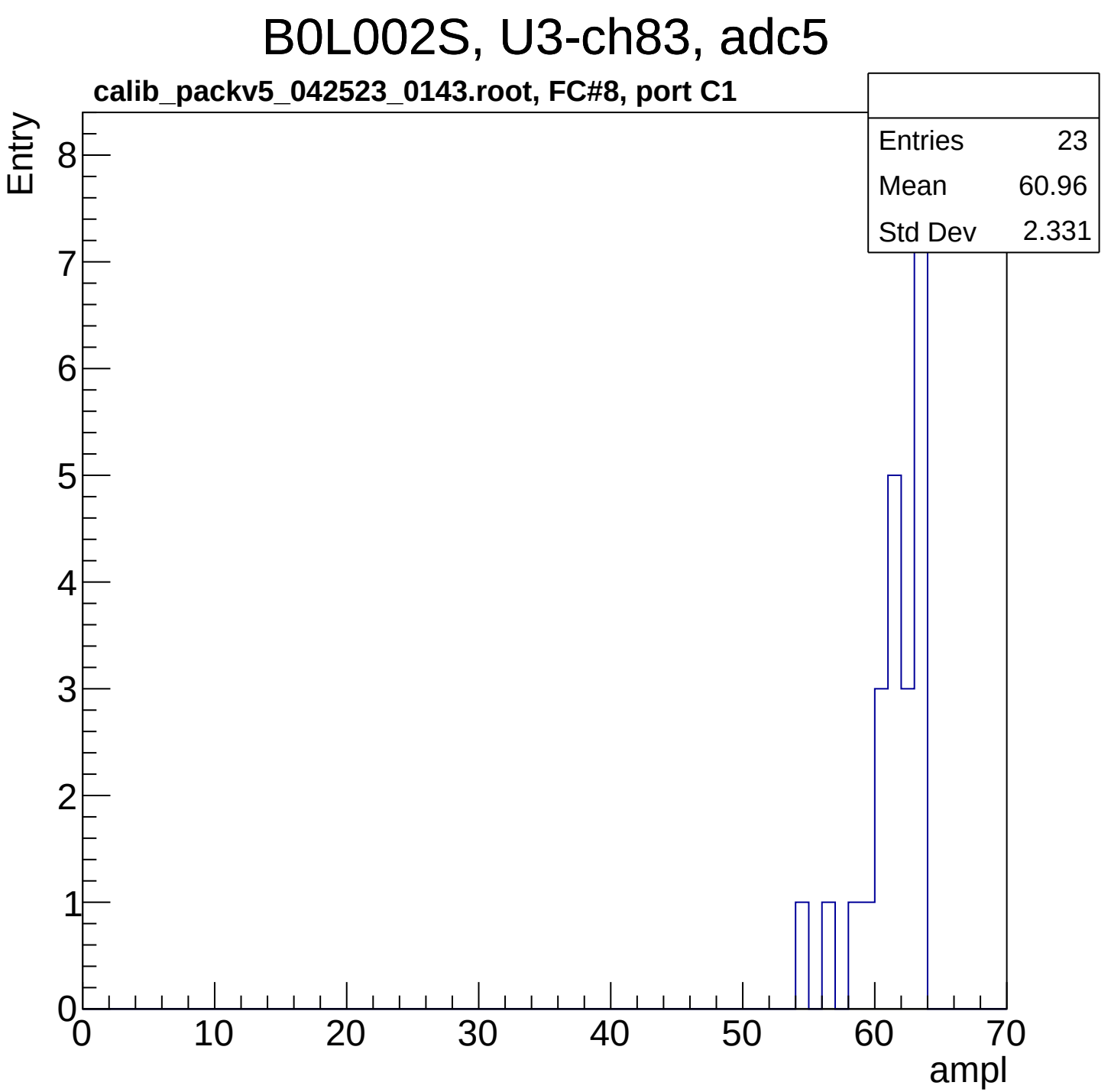
B0L002S, U3-ch83, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 60.96
Std Dev 2.331
Entry
ampl
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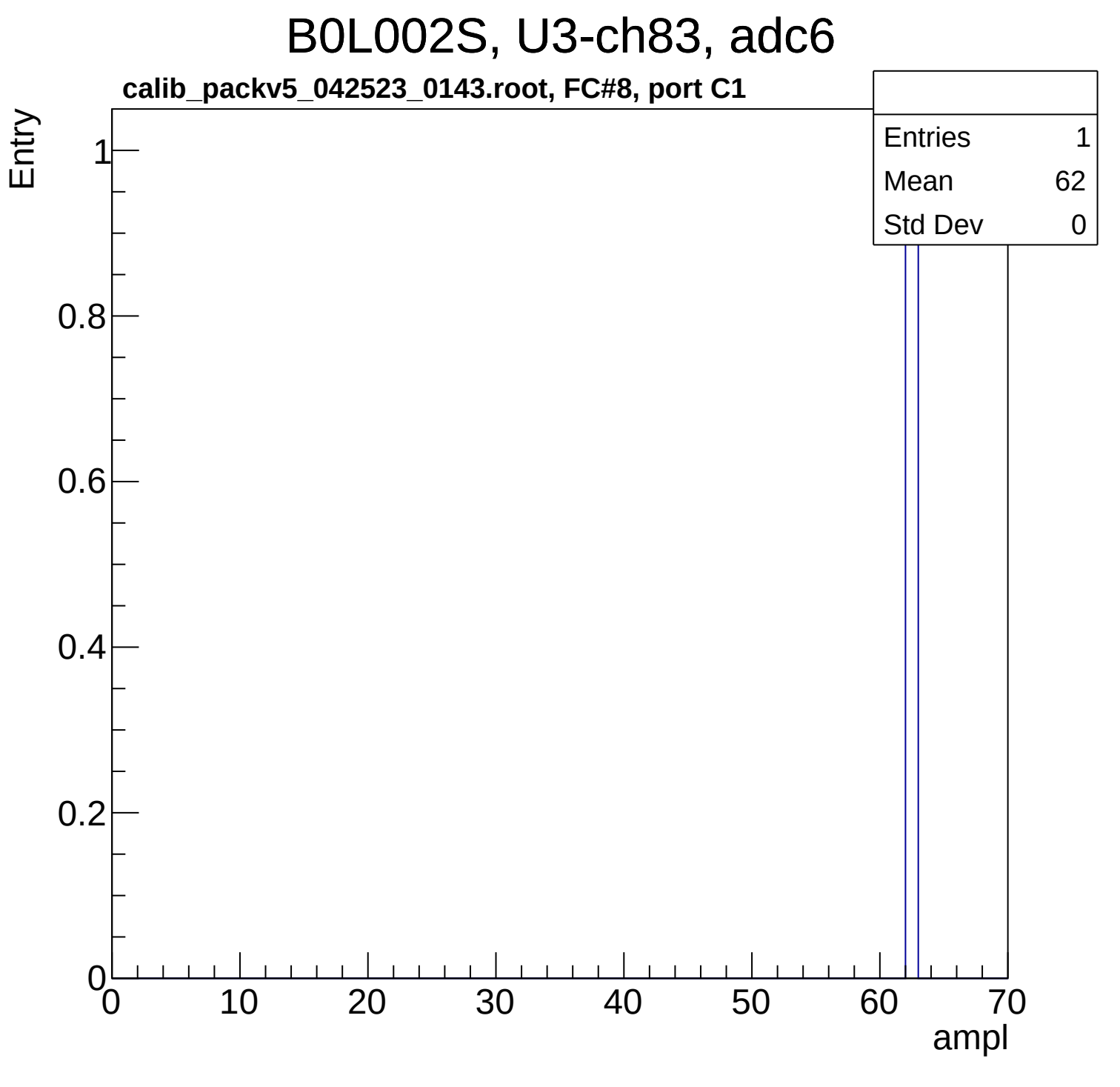
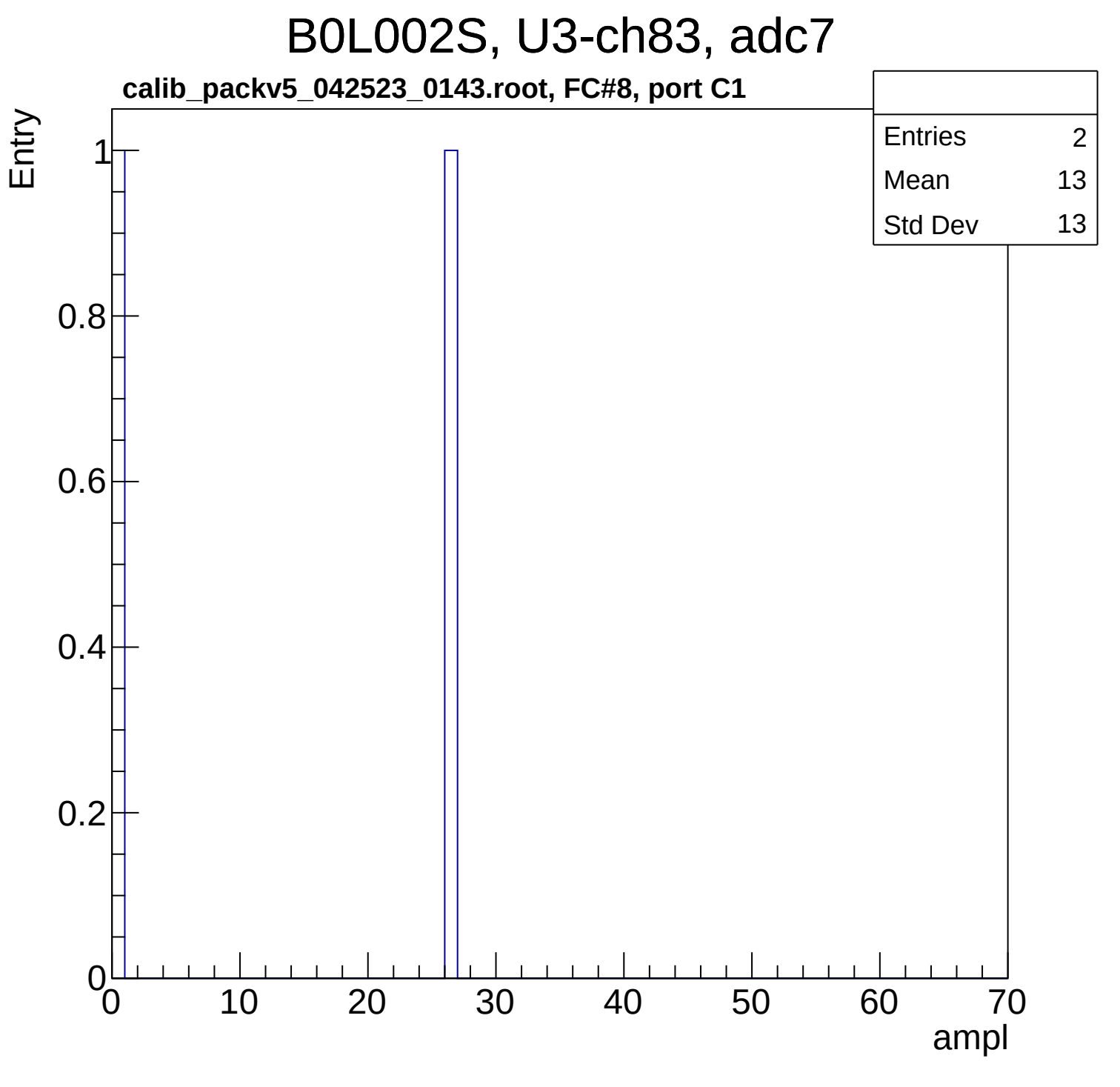
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch83, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch83, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 13
Std Dev 13
Entry
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0.6
0.4
0.2
0
0
10
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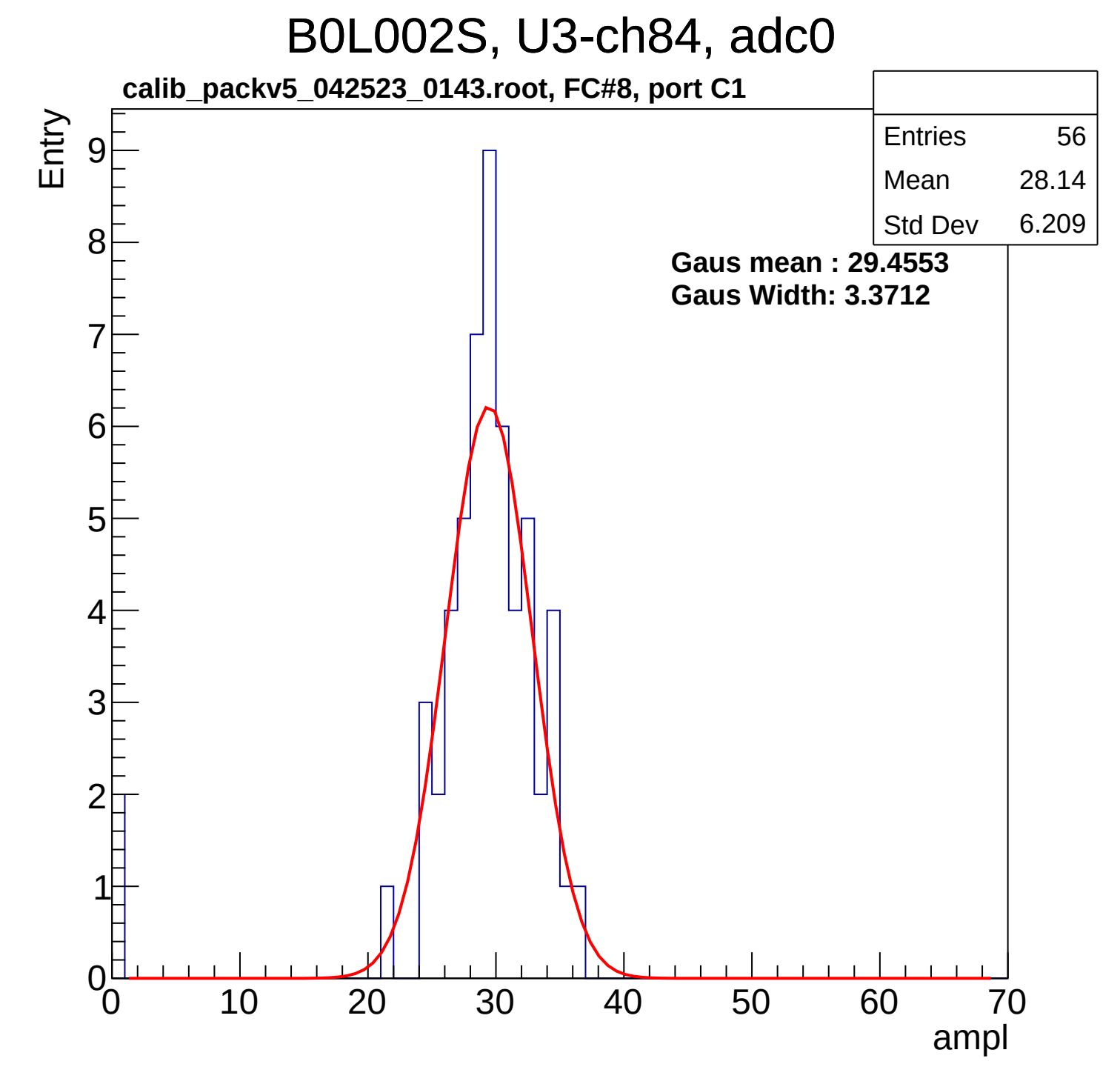
30
40
50
60
70
ampl


B0L002S, U3-ch84, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 28.14
Std Dev 6.209
Gaus mean : 29.4553
Gaus Width: 3.3712
Entry
ampl
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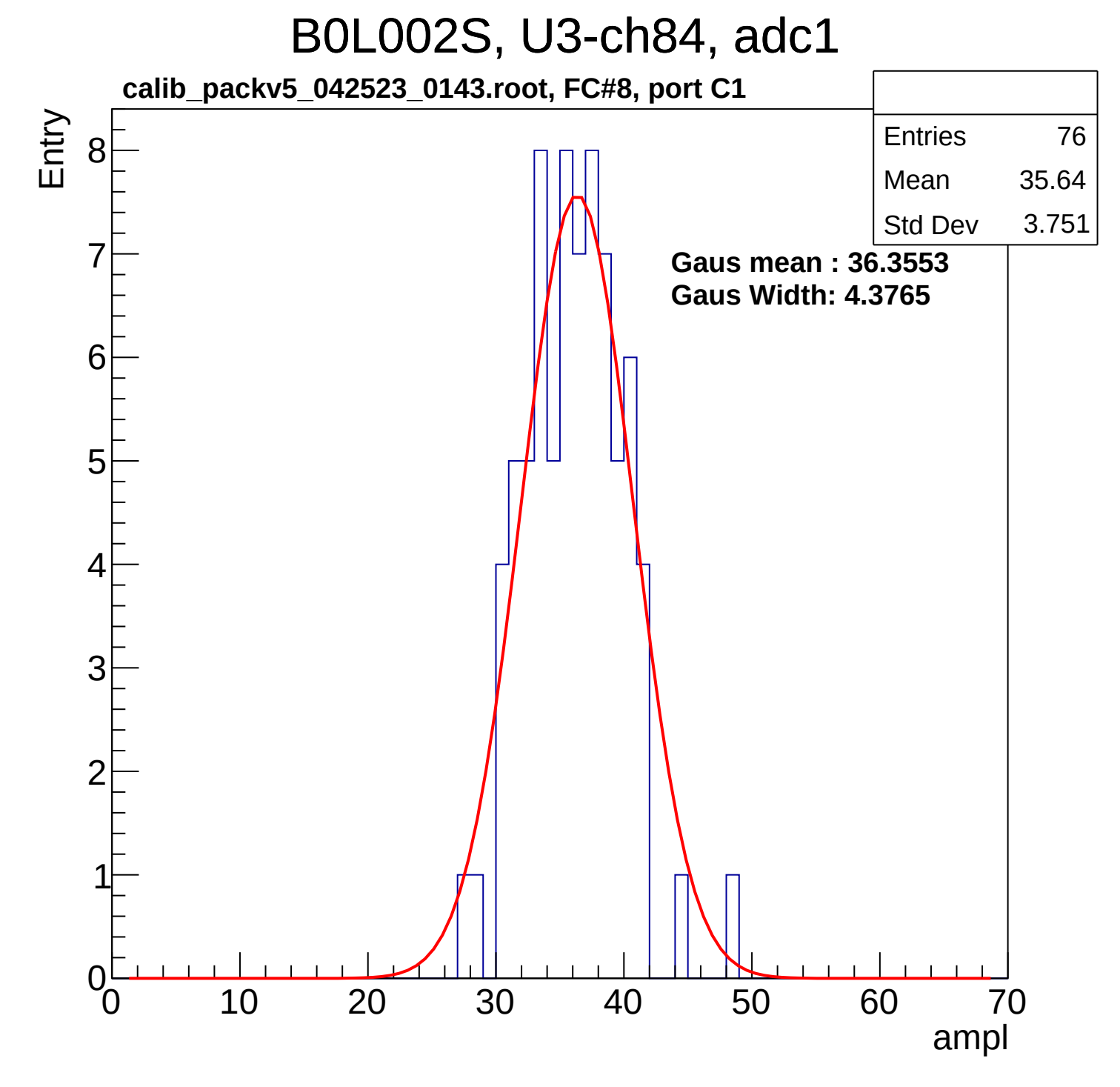
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch84, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 35.64
Std Dev 3.751
Gaus mean : 36.3553
Gaus Width: 4.3765
Entry
ampl
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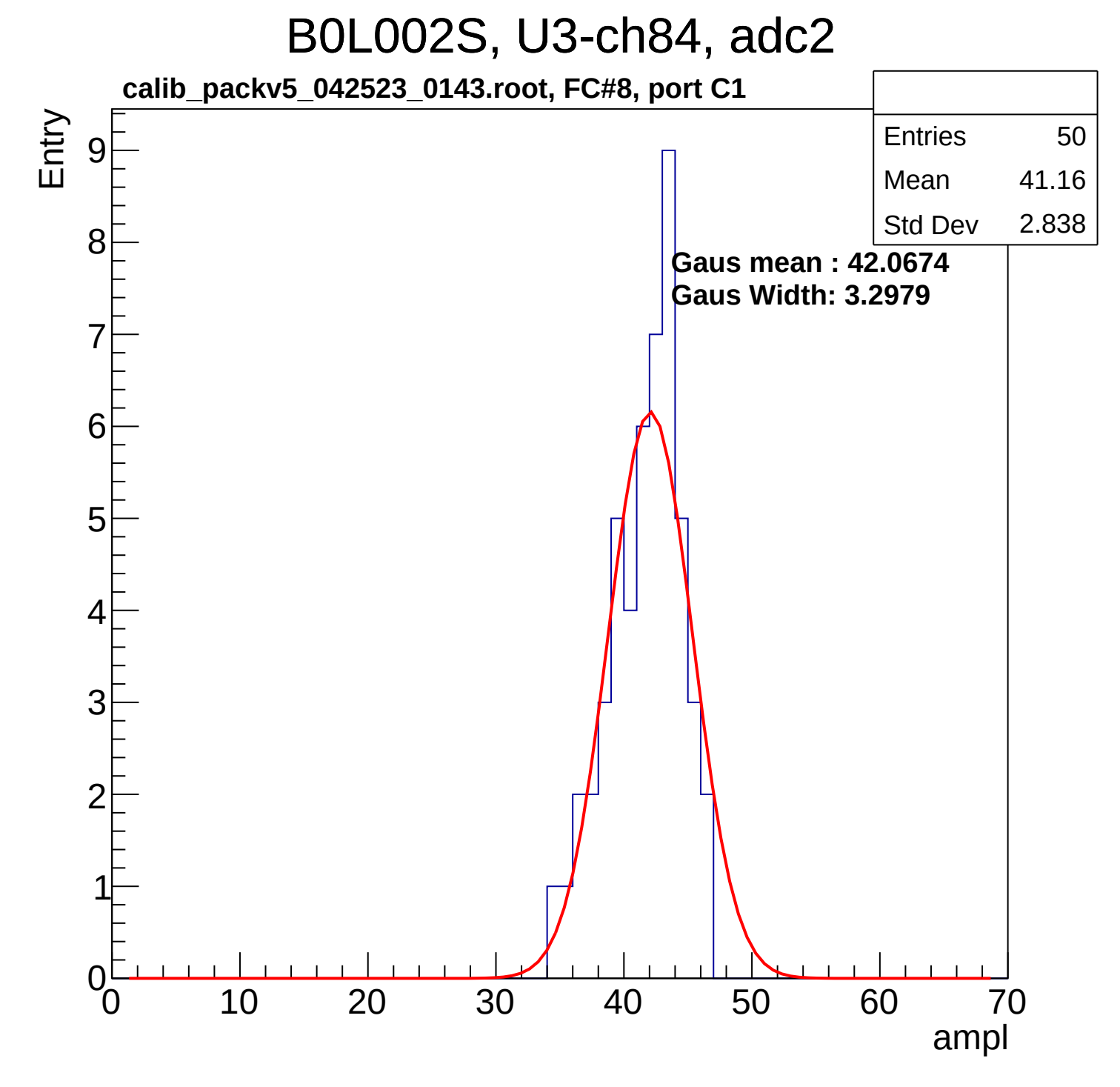
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch84, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 41.16
Std Dev 2.838
Gaus mean : 42.0674
Gaus Width: 3.2979
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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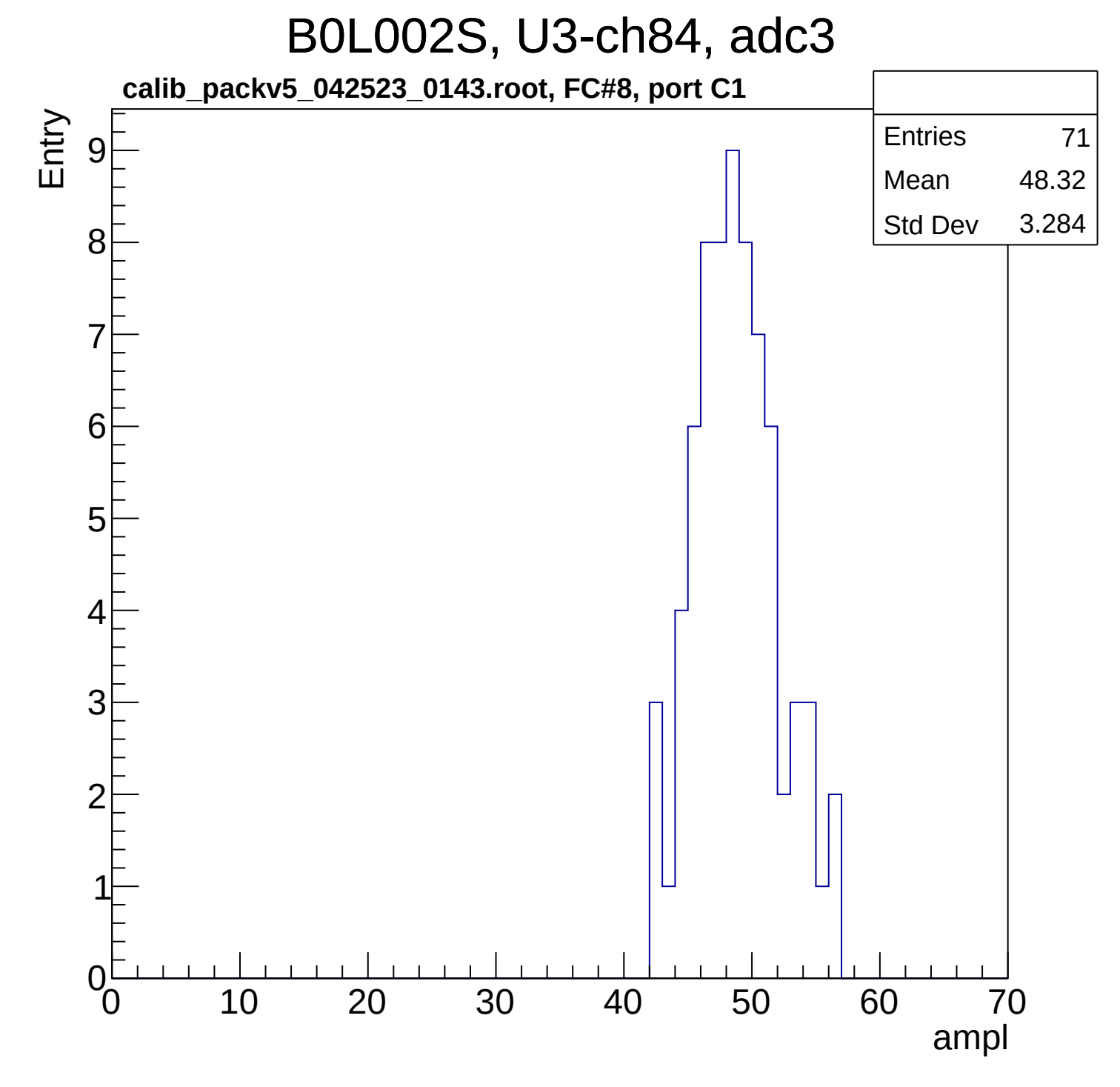
B0L002S, U3-ch84, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 48.32
Std Dev 3.284
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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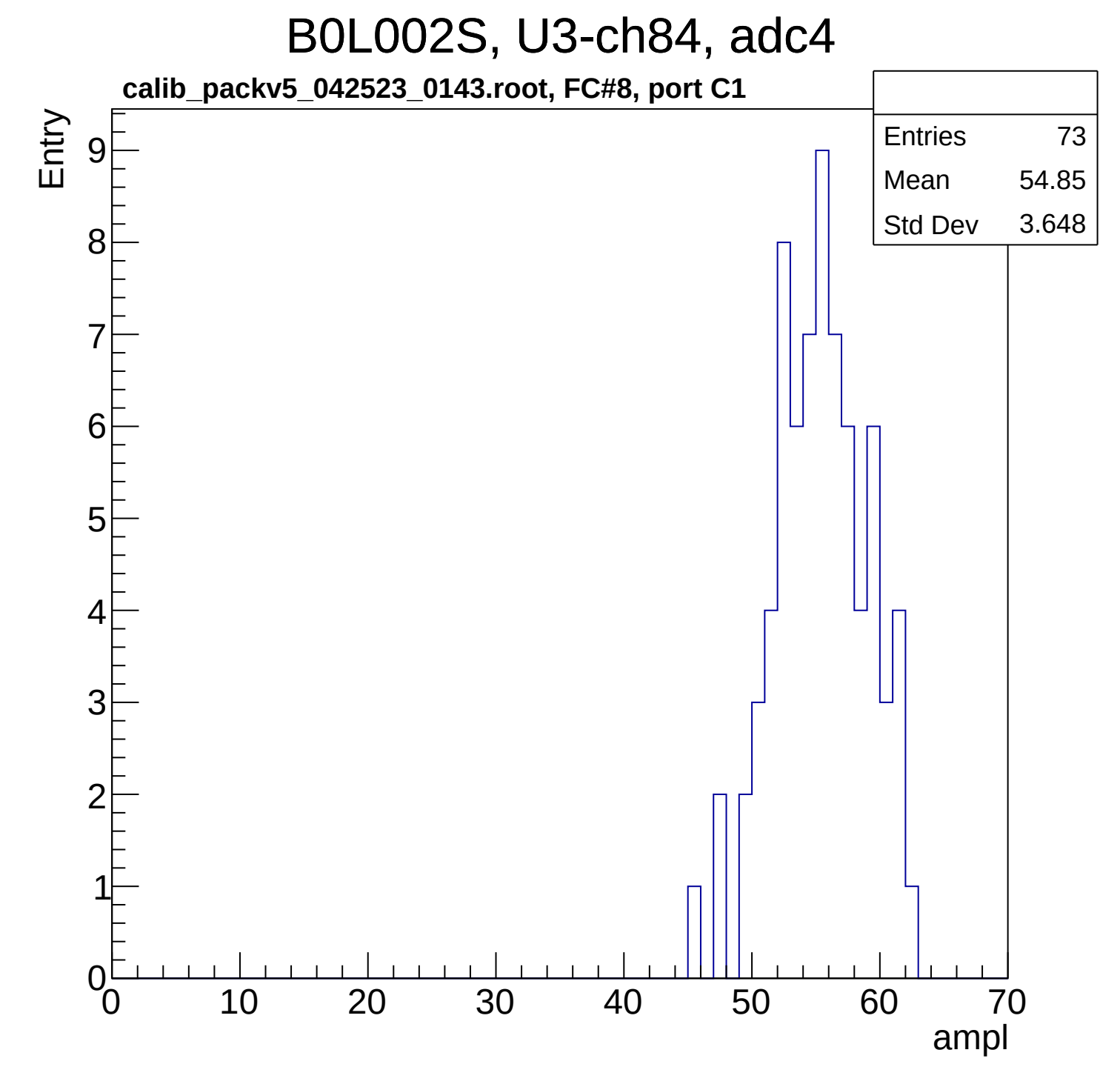
B0L002S, U3-ch84, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 54.85
Std Dev 3.648
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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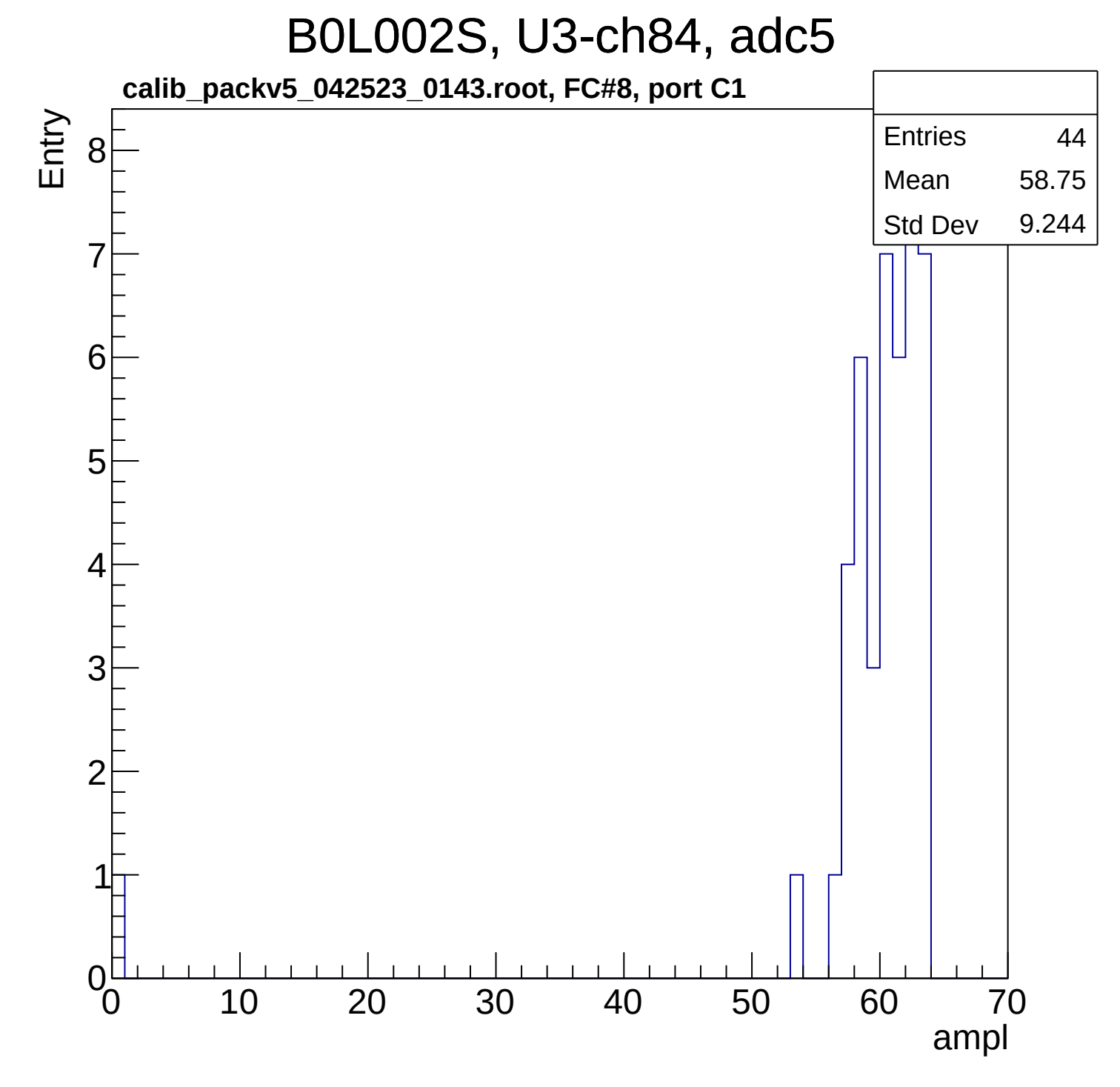
B0L002S, U3-ch84, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.75
Std Dev 9.244
Entry
ampl
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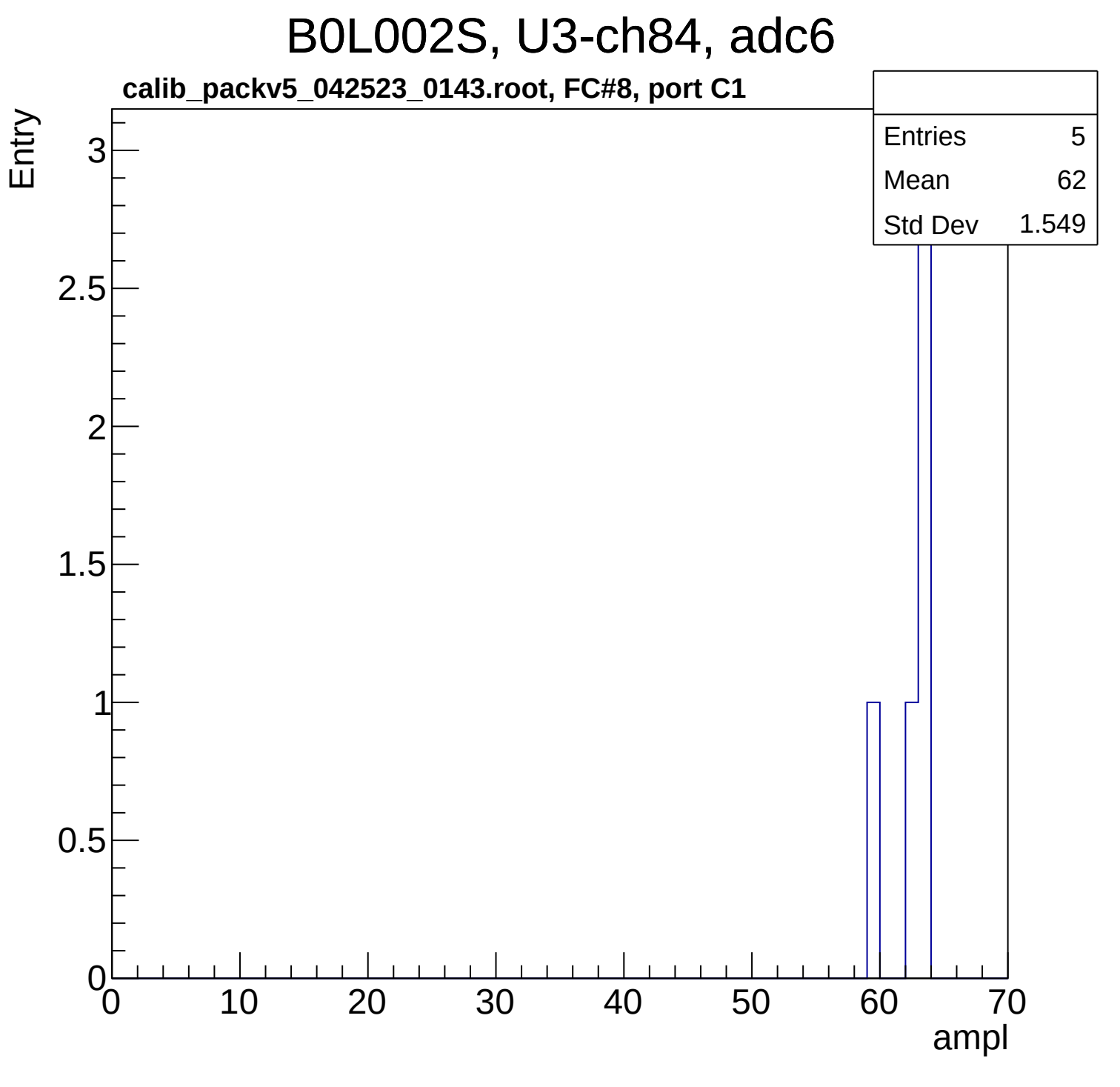
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch84, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62
Std Dev 1.549
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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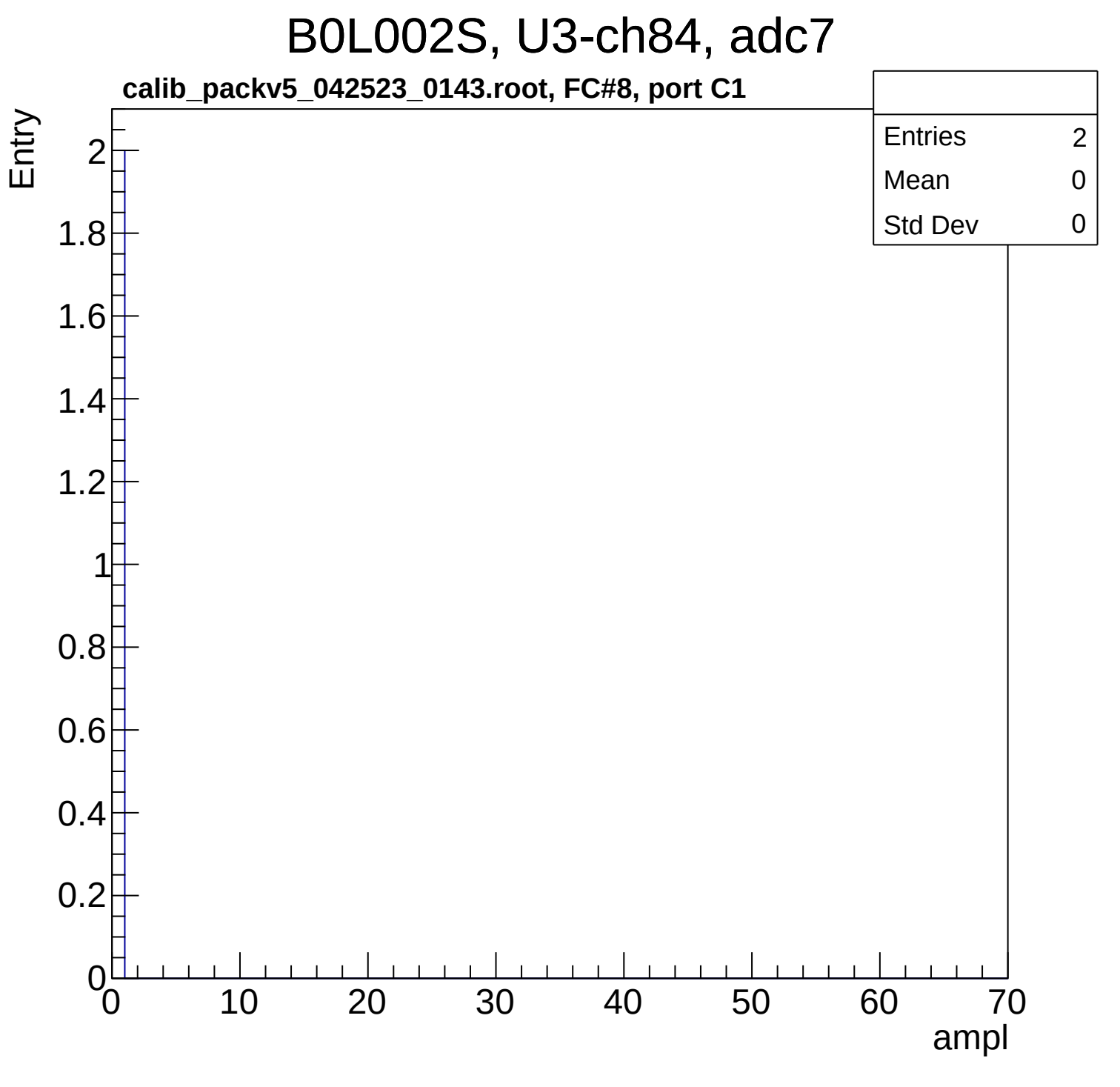
B0L002S, U3-ch84, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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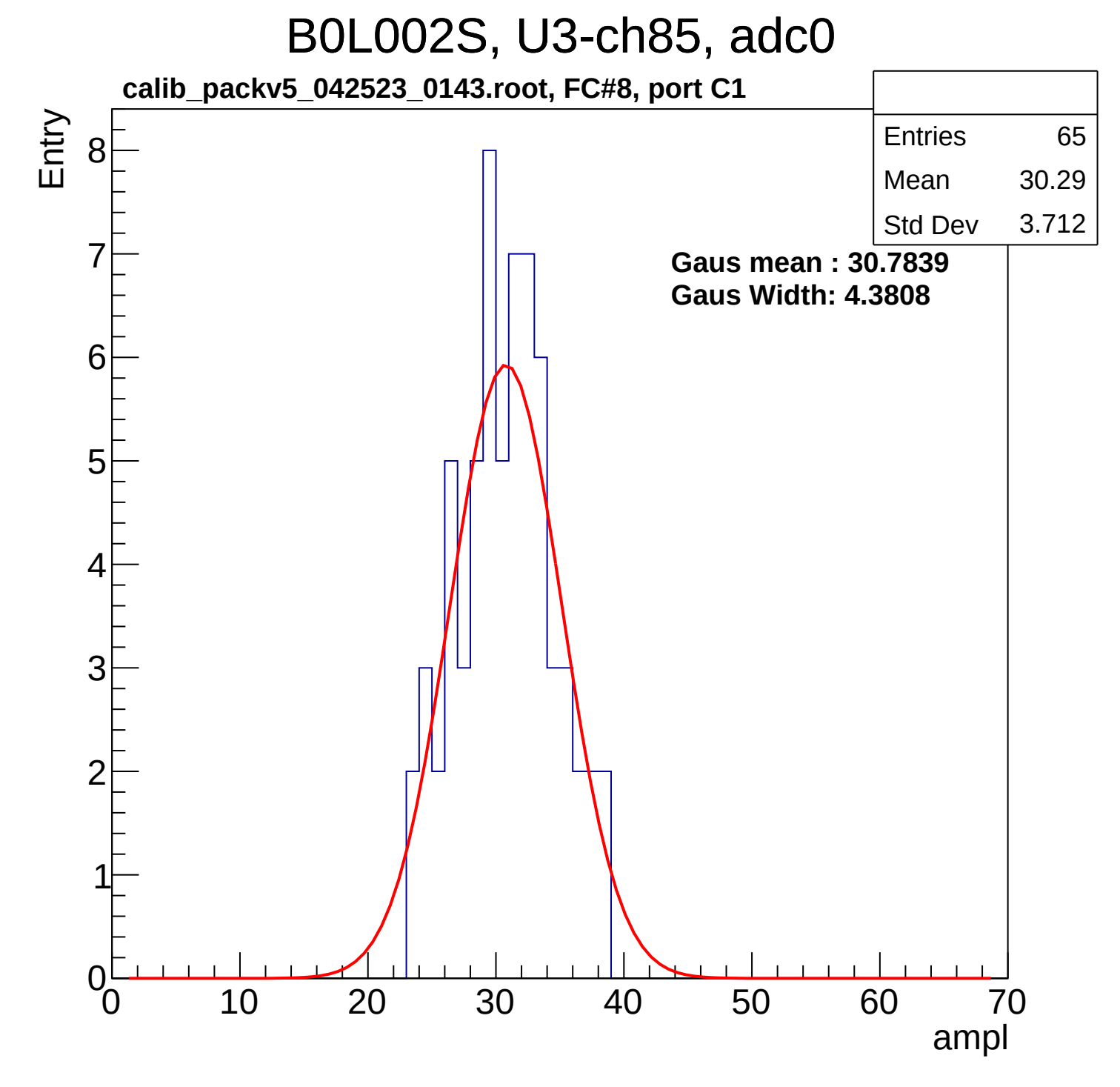
B0L002S, U3-ch85, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.29
Std Dev 3.712
Gaus mean : 30.7839
Gaus Width: 4.3808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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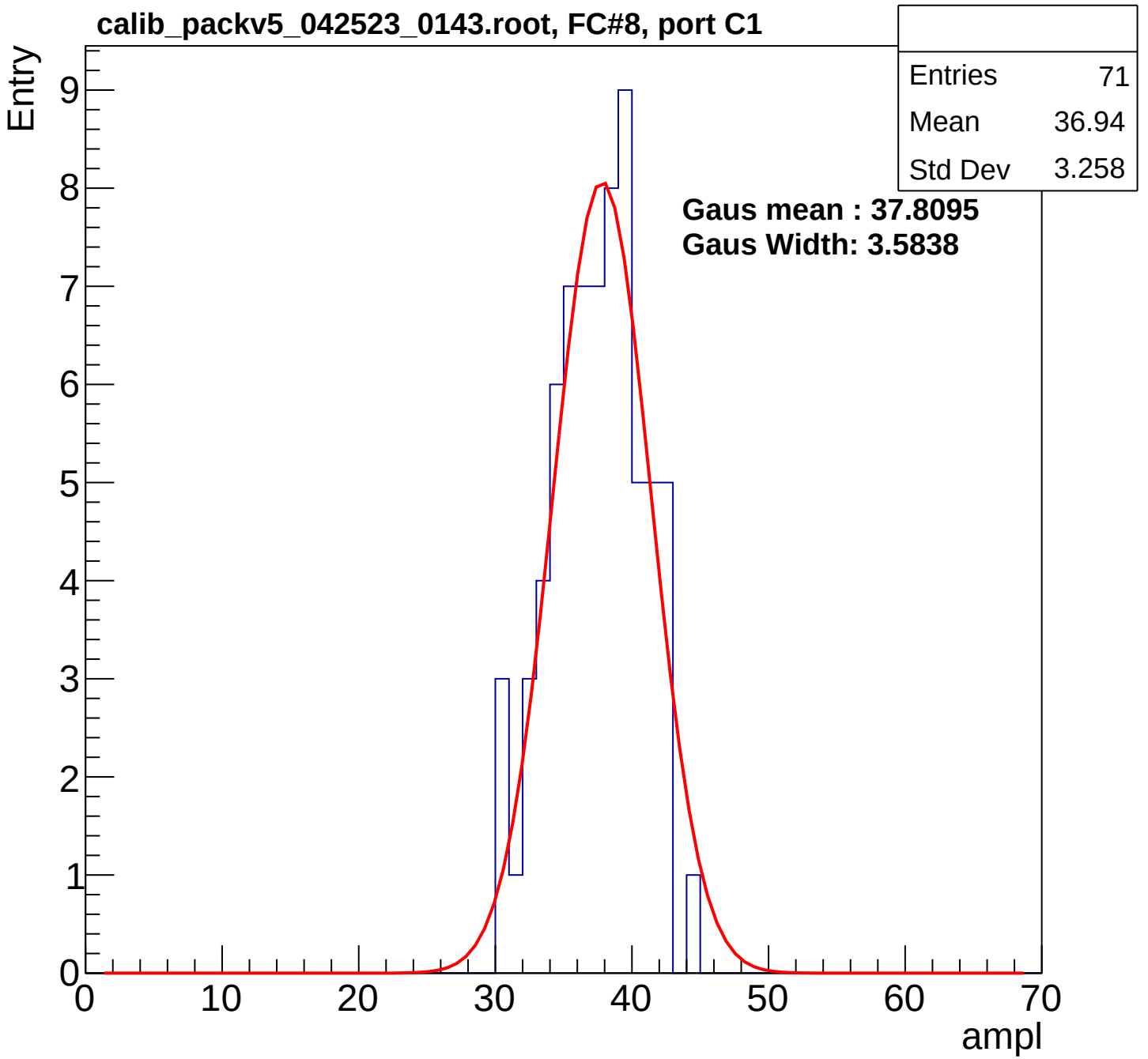
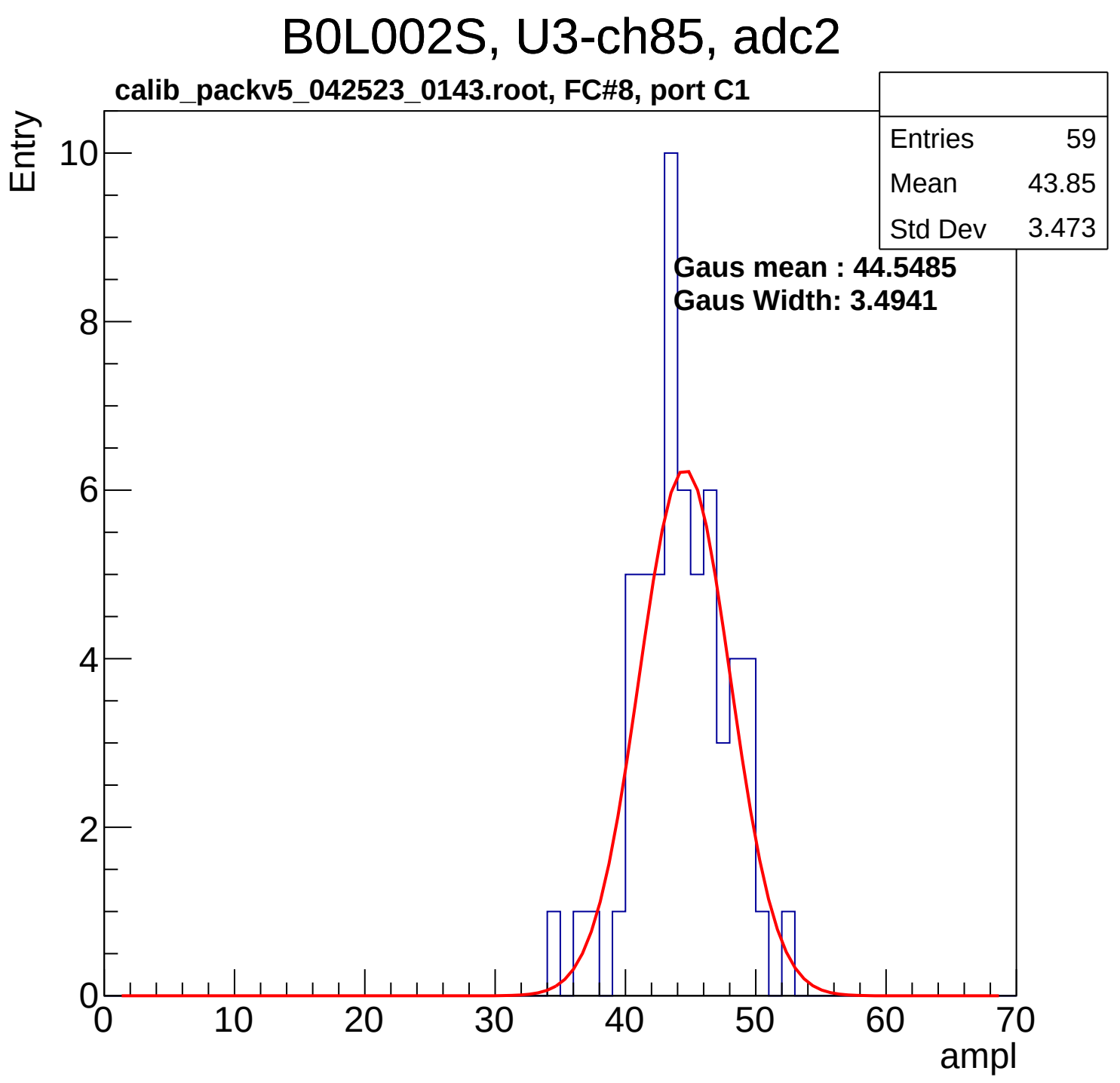
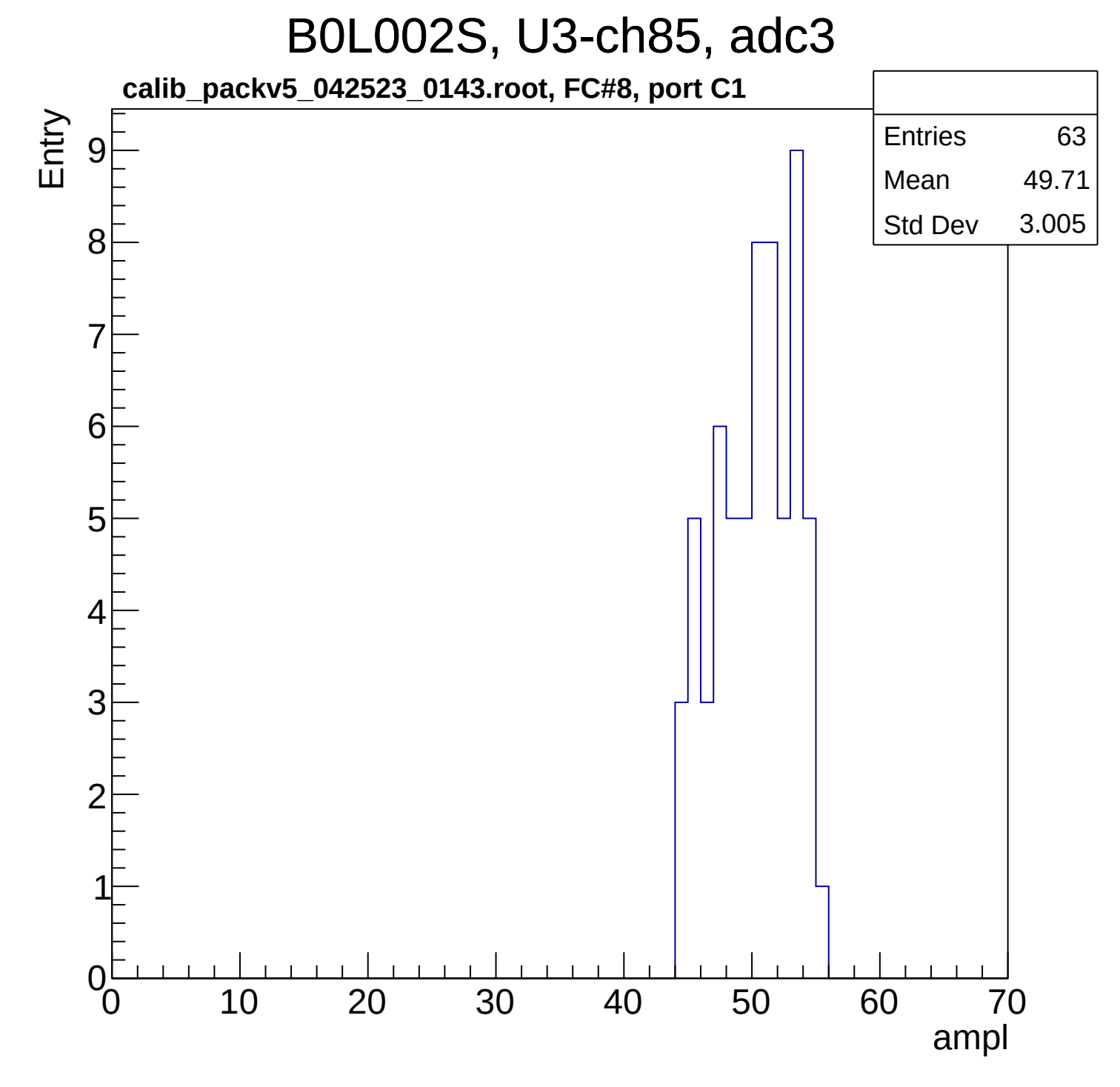
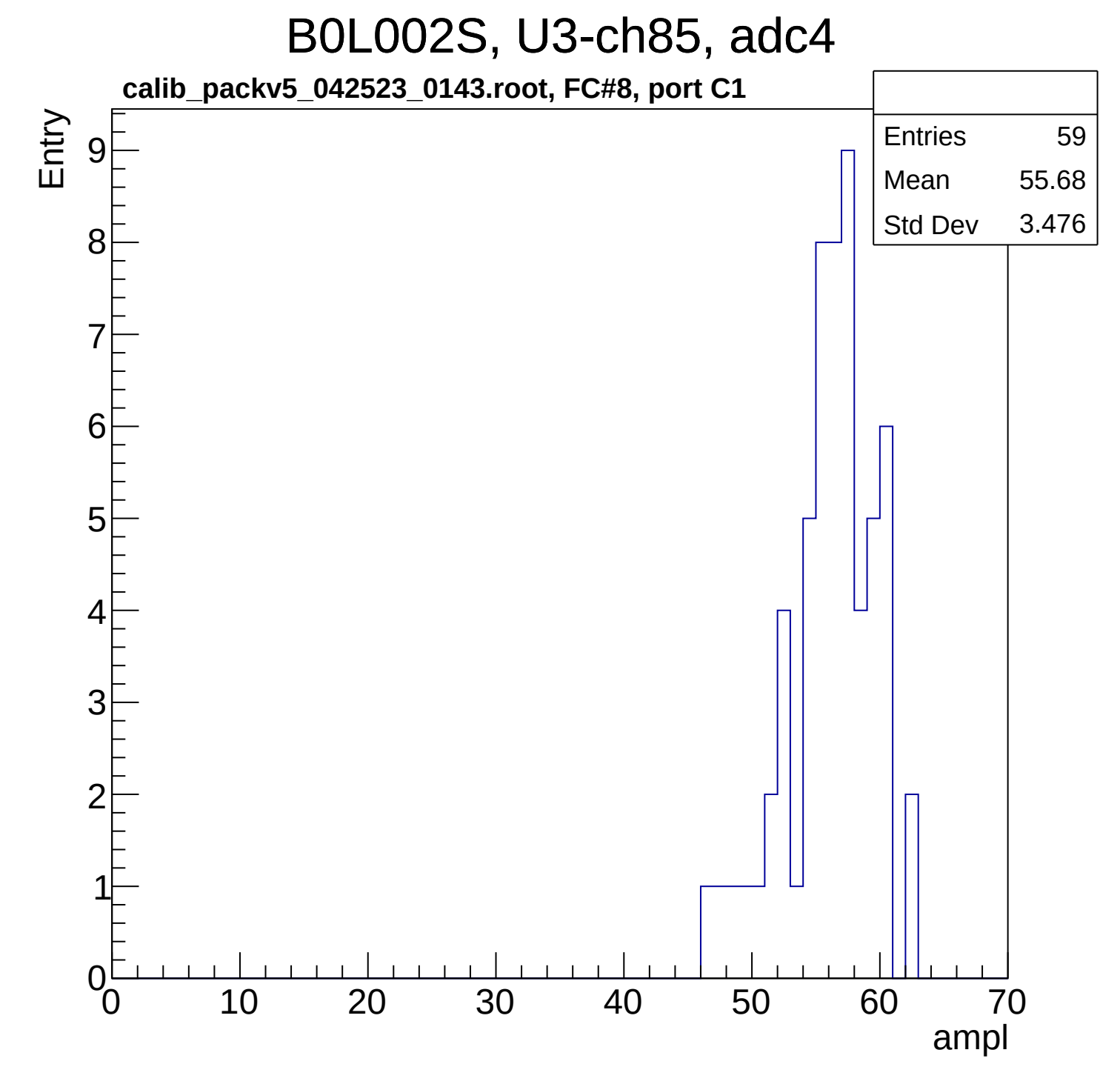
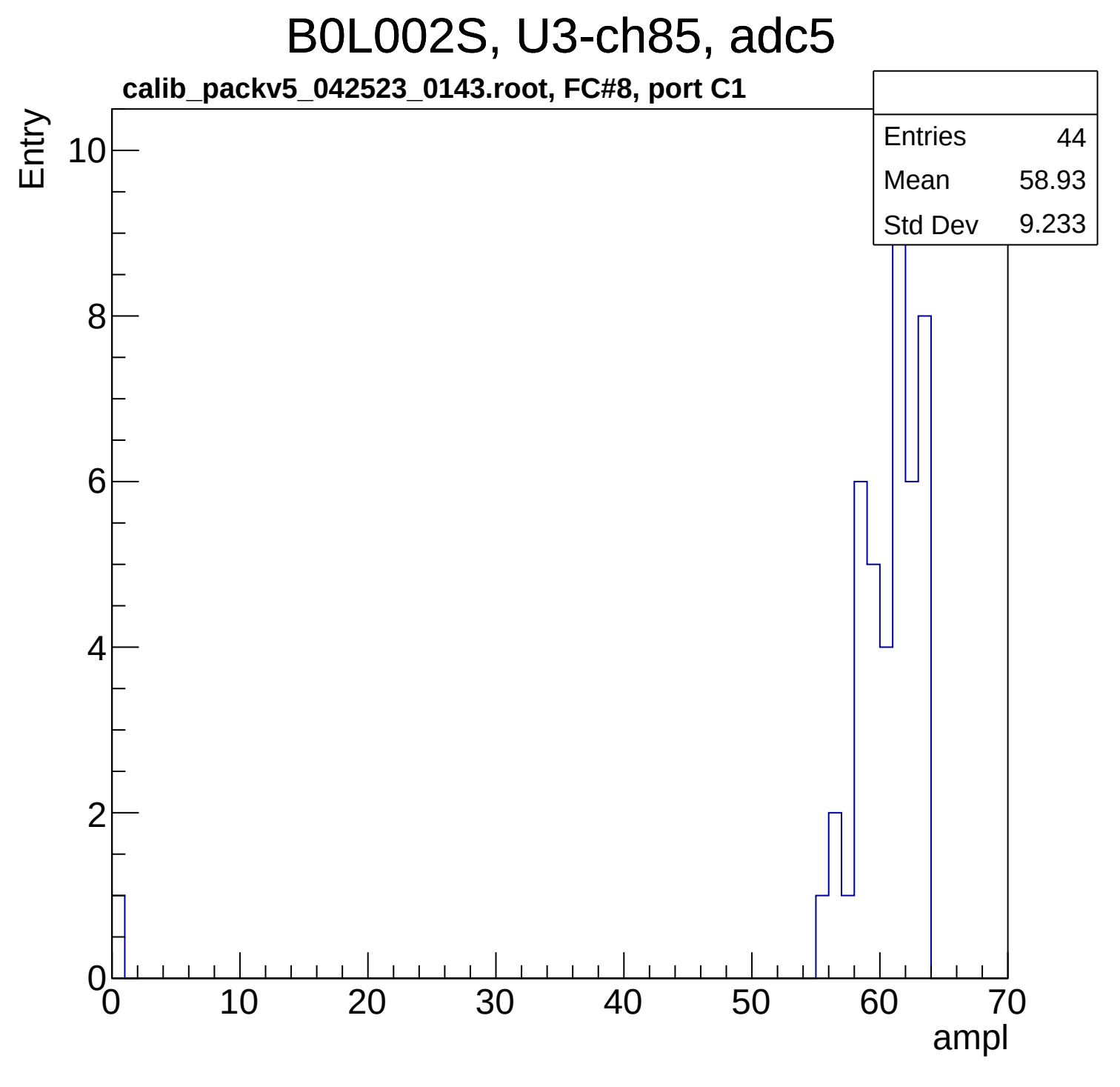
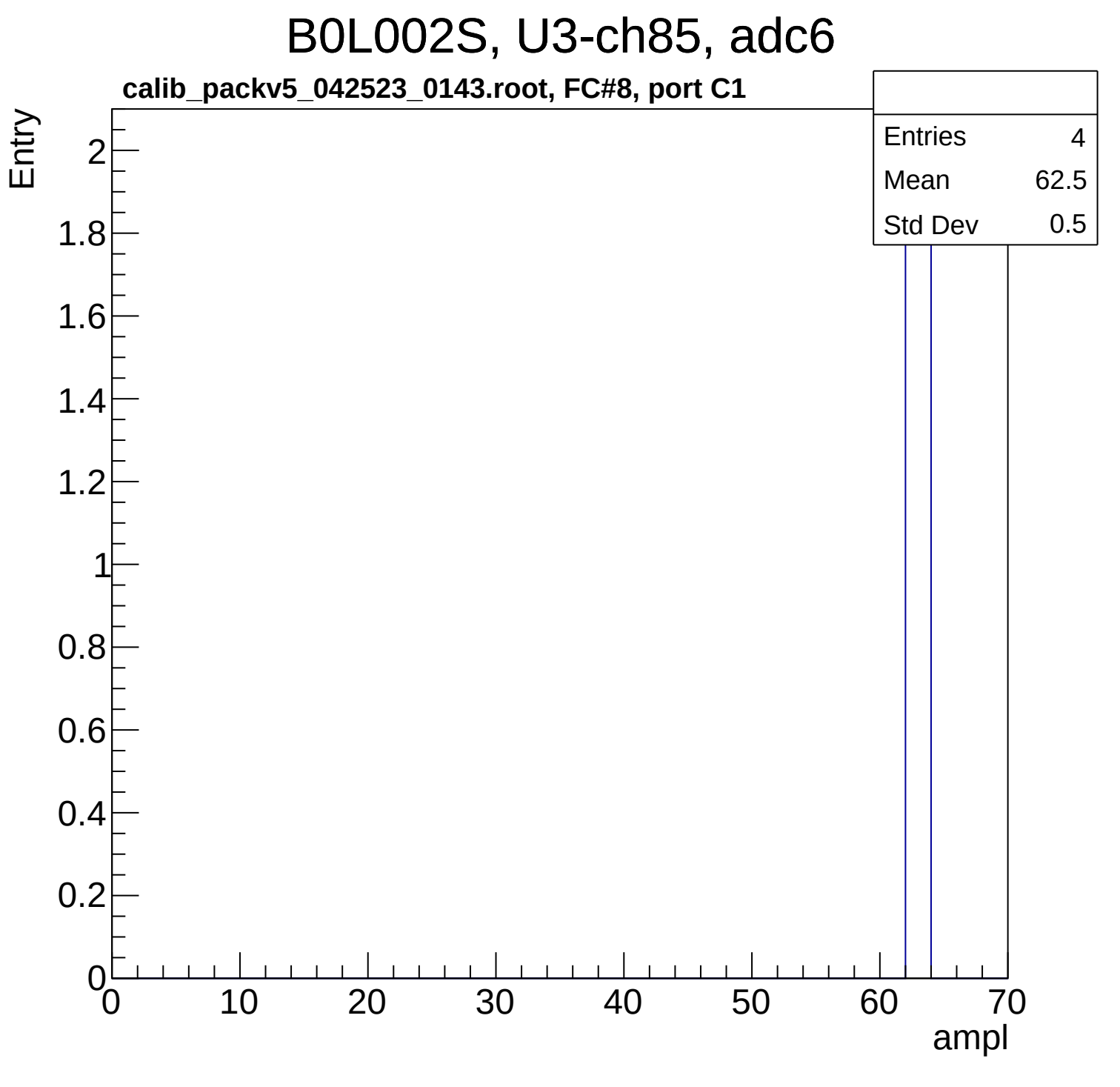

B0L002S, U3-ch85, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 36.94
Std Dev 3.258
Gaus mean : 37.8095
Gaus Width: 3.5838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

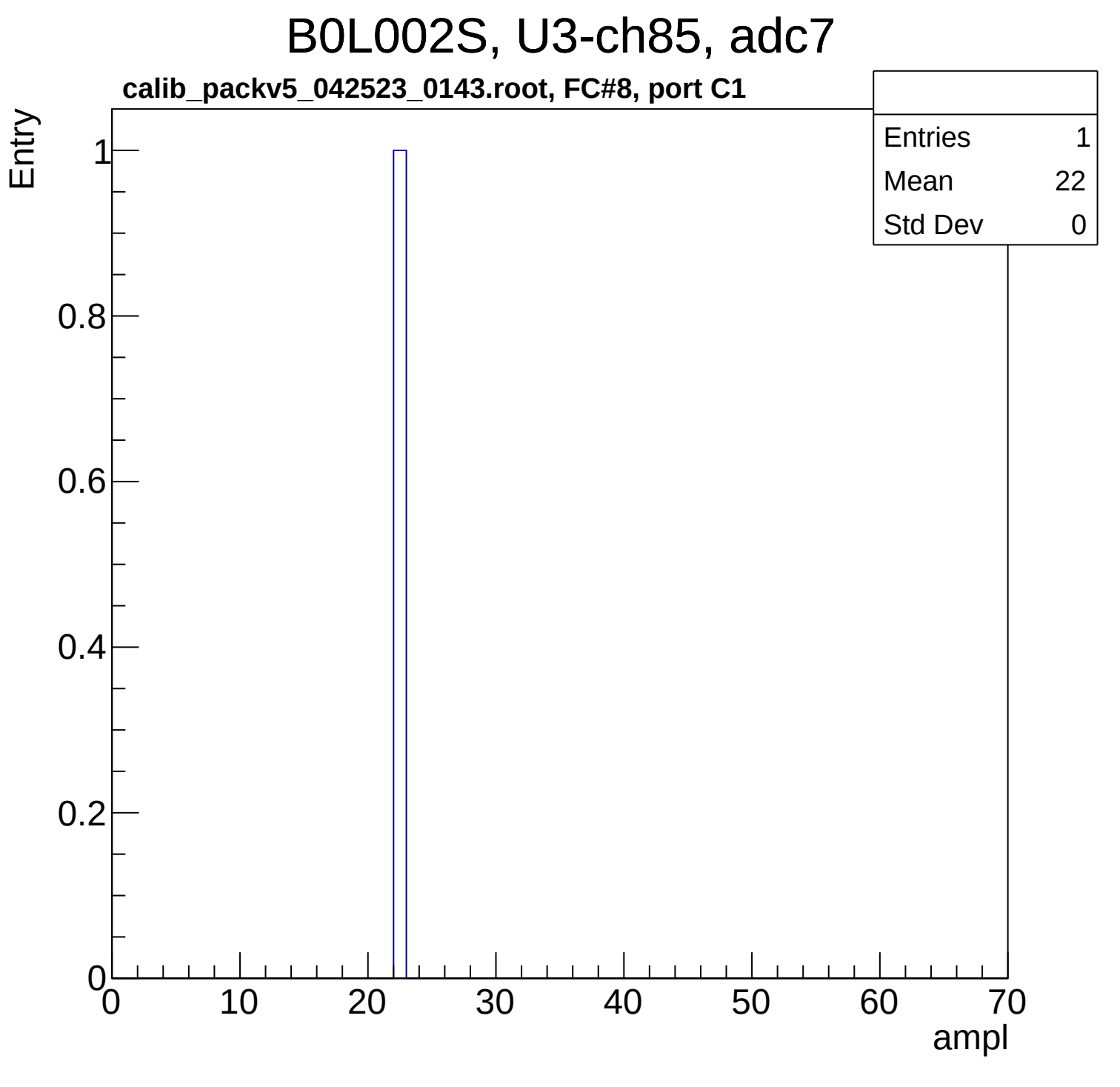

B0L002S, U3-ch85, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 43.85
Std Dev 3.473
Gaus mean : 44.5485
Gaus Width: 3.4941
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

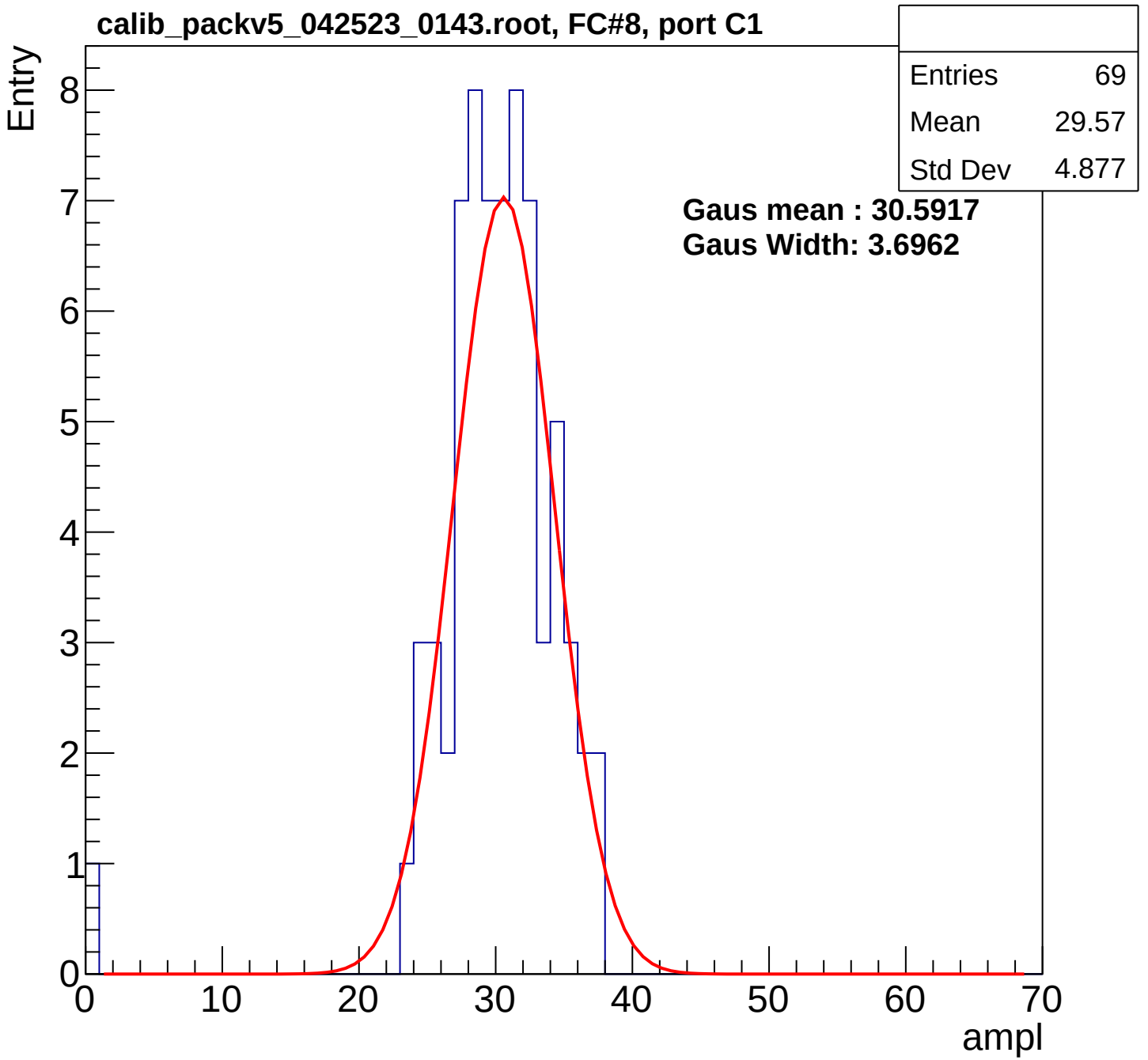

B0L002S, U3-ch85, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 49.71
Std Dev 3.005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

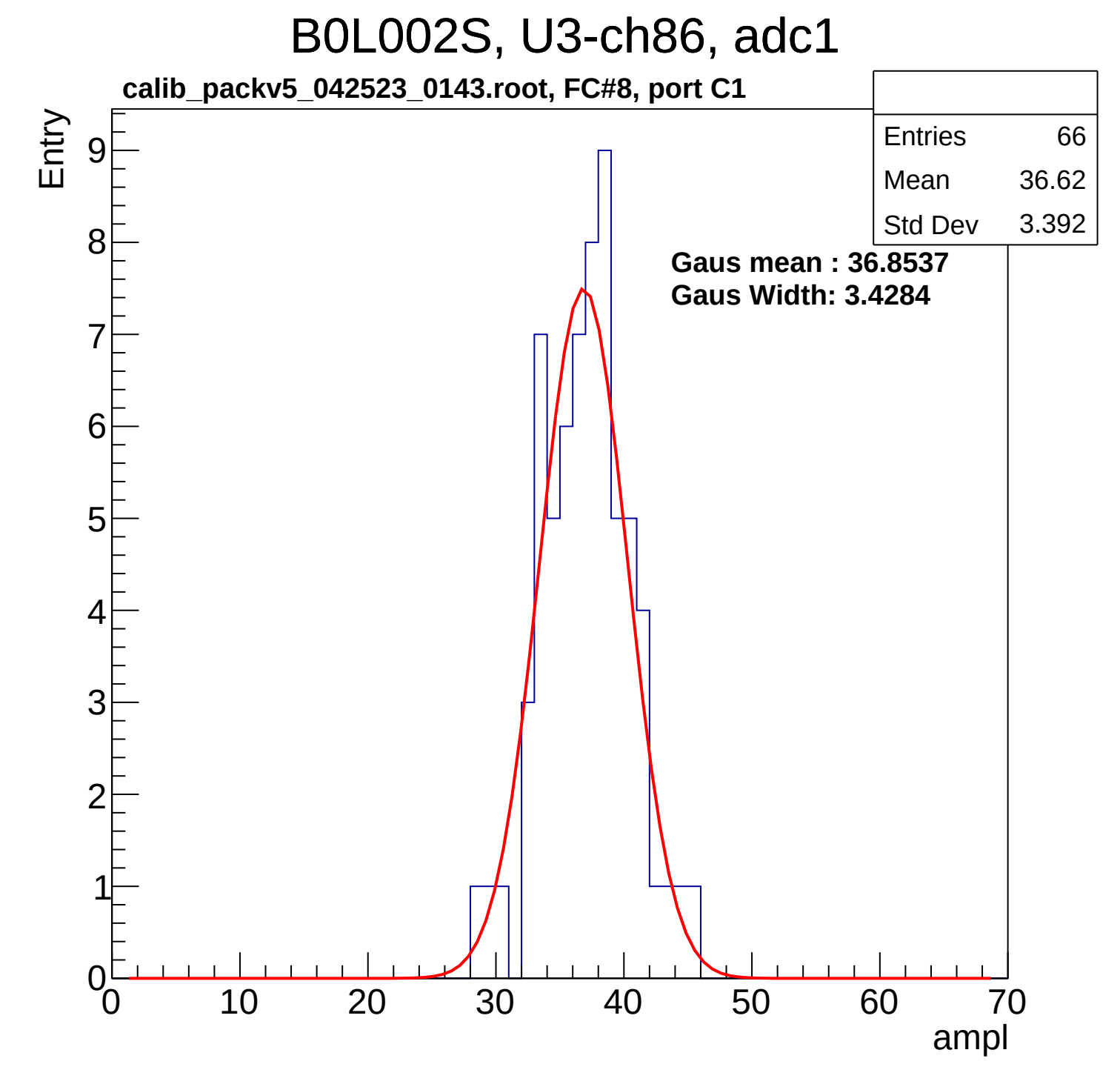

B0L002S, U3-ch85, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 55.68
Std Dev 3.476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

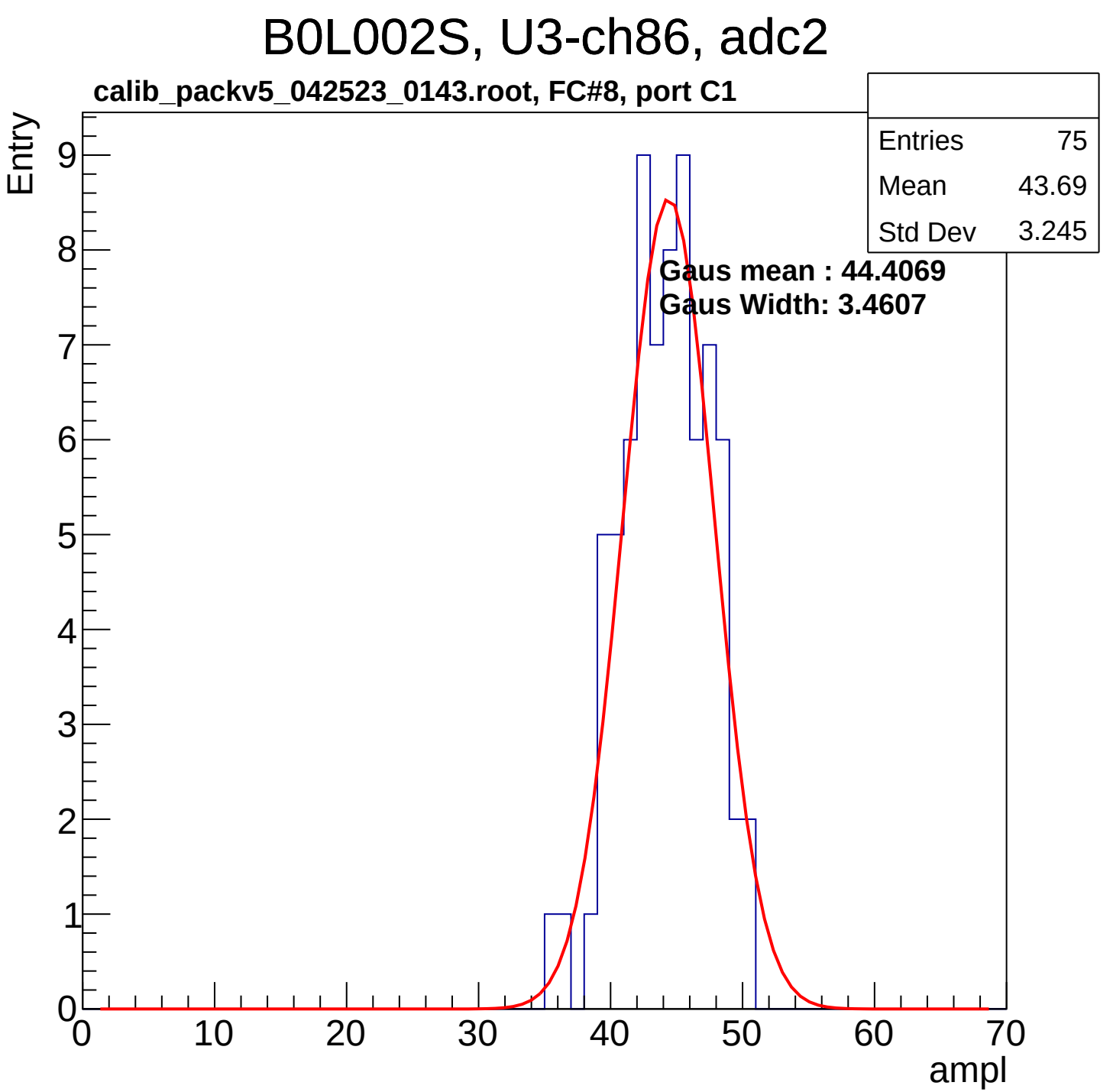

B0L002S, U3-ch85, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.93
Std Dev 9.233
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

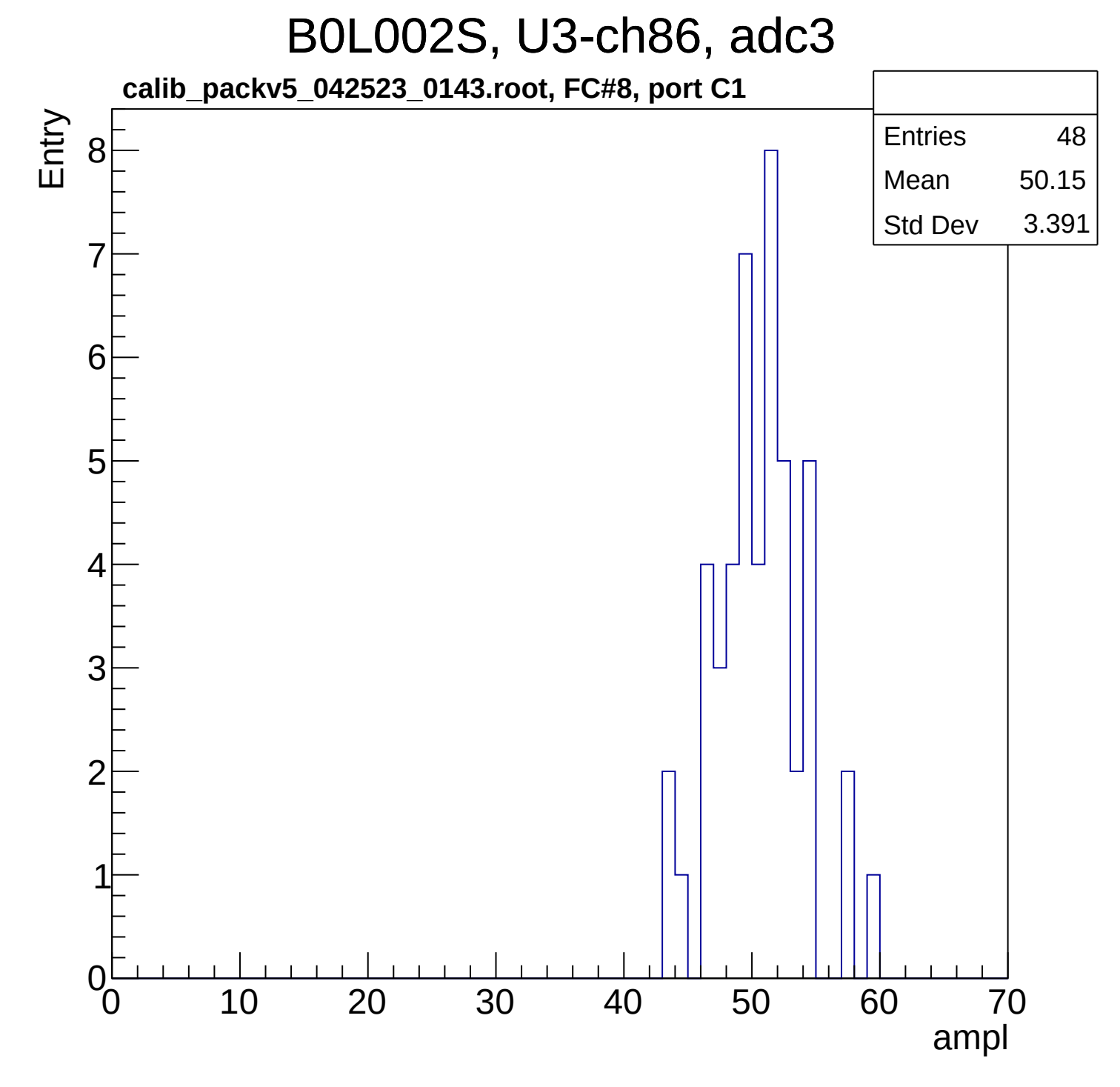

B0L002S, U3-ch85, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

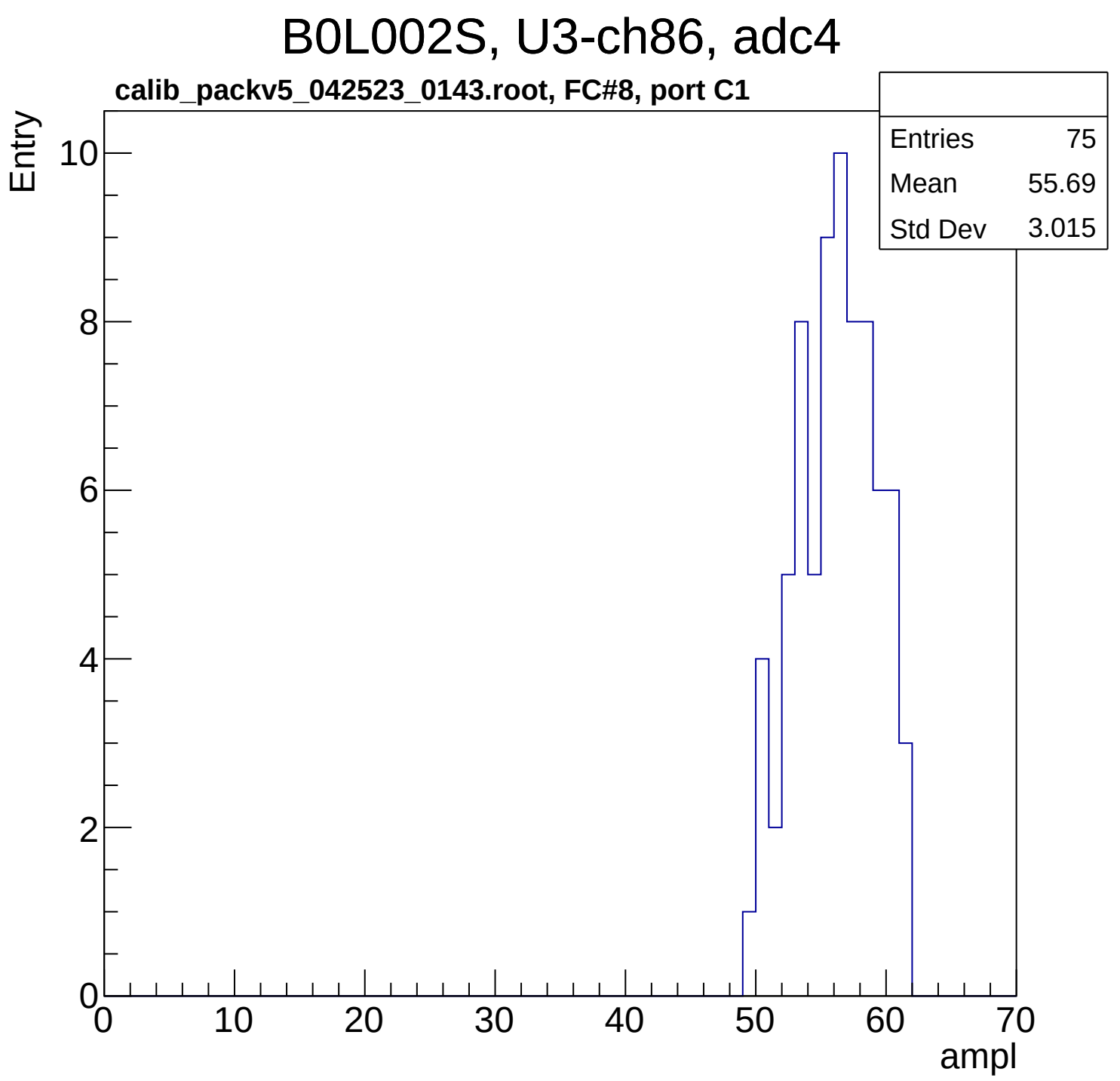

B0L002S, U3-ch85, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

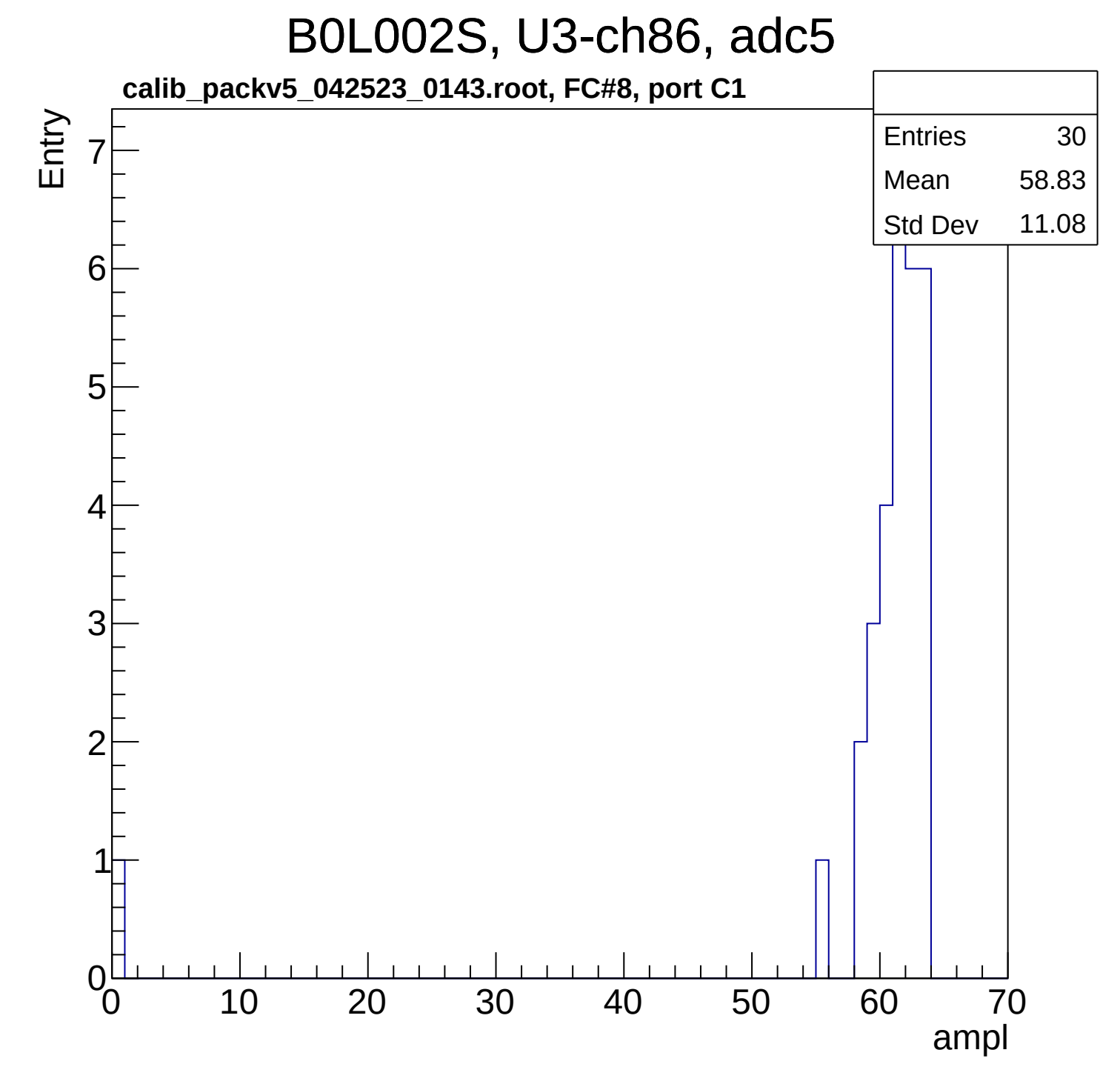

B0L002S, U3-ch86, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 29.57
Std Dev 4.877
Gaus mean : 30.5917
Gaus Width: 3.6962
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

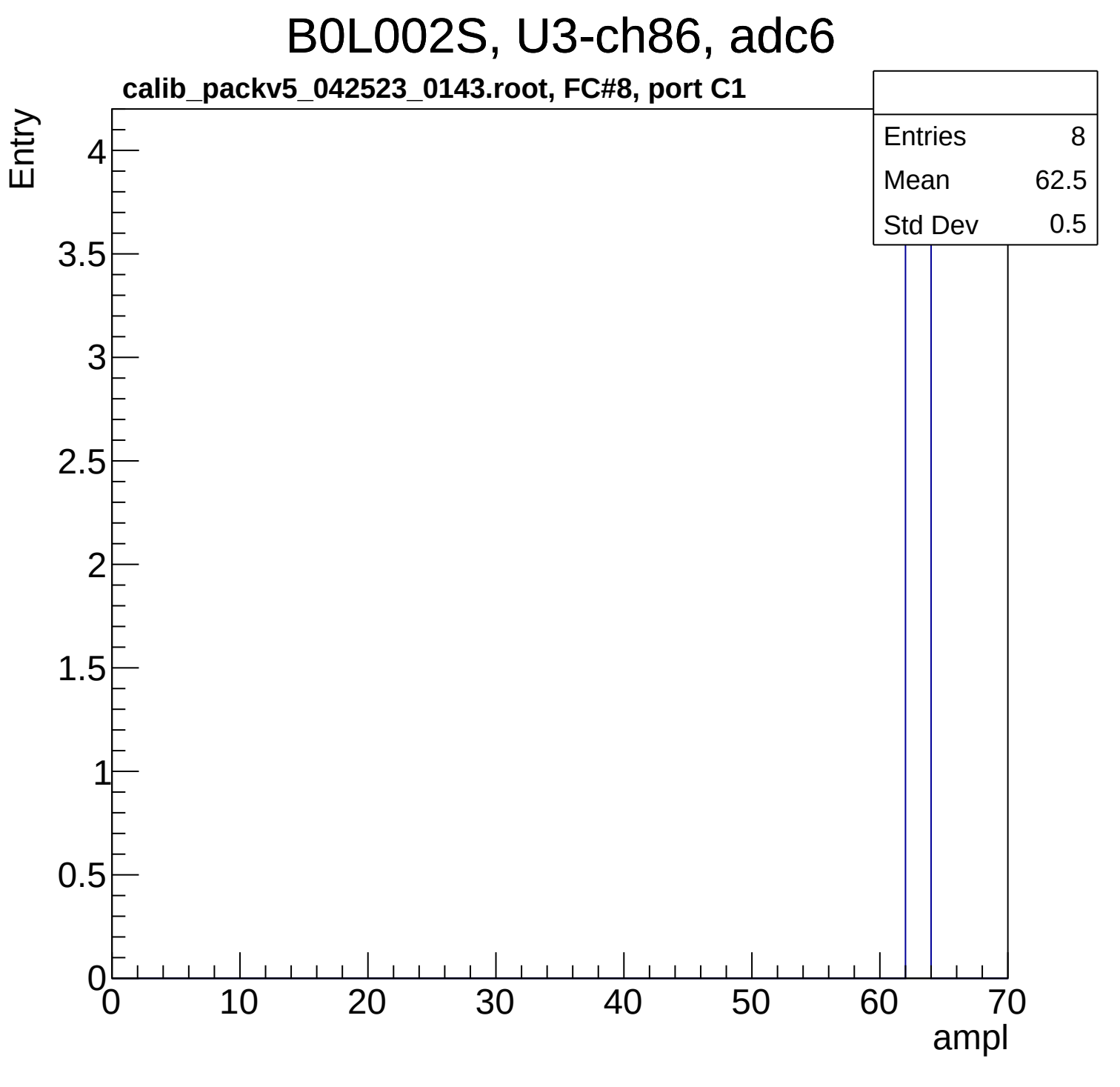

B0L002S, U3-ch86, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 36.62
Std Dev 3.392
Gaus mean : 36.8537
Gaus Width: 3.4284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

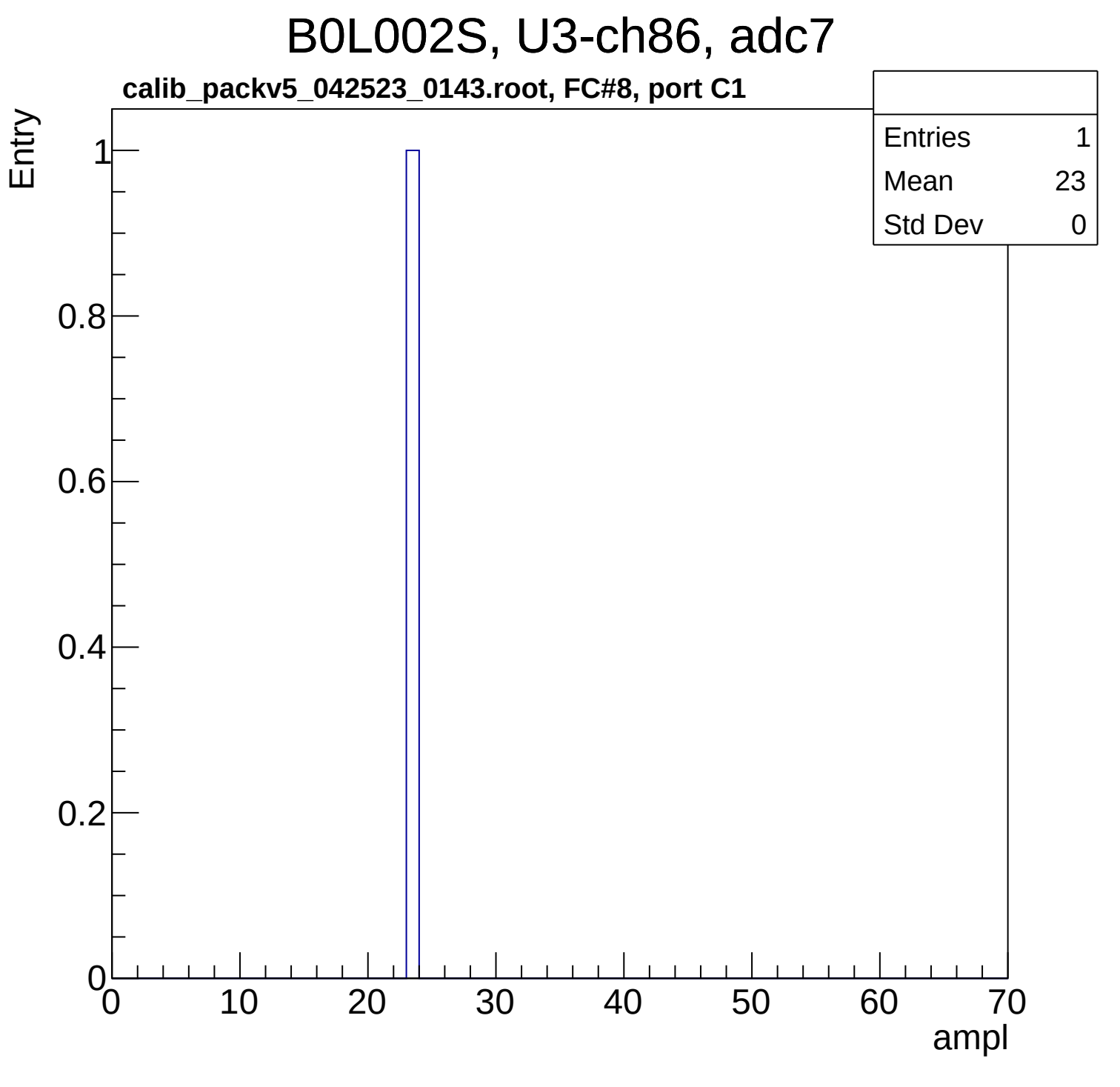

B0L002S, U3-ch86, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 43.69
Std Dev 3.245
Gaus mean : 44.4069
Gaus Width: 3.4607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

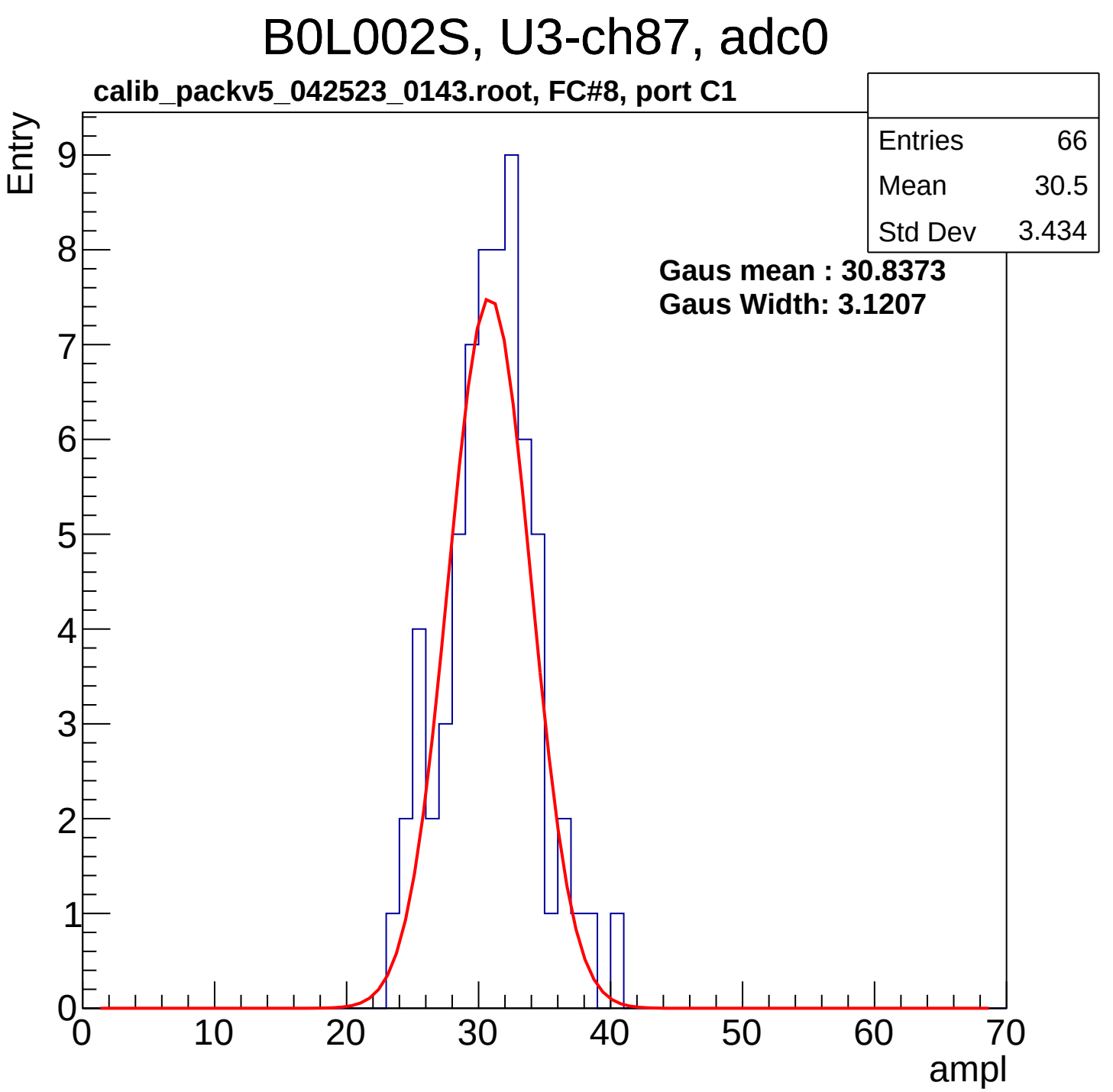

B0L002S, U3-ch86, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 50.15
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

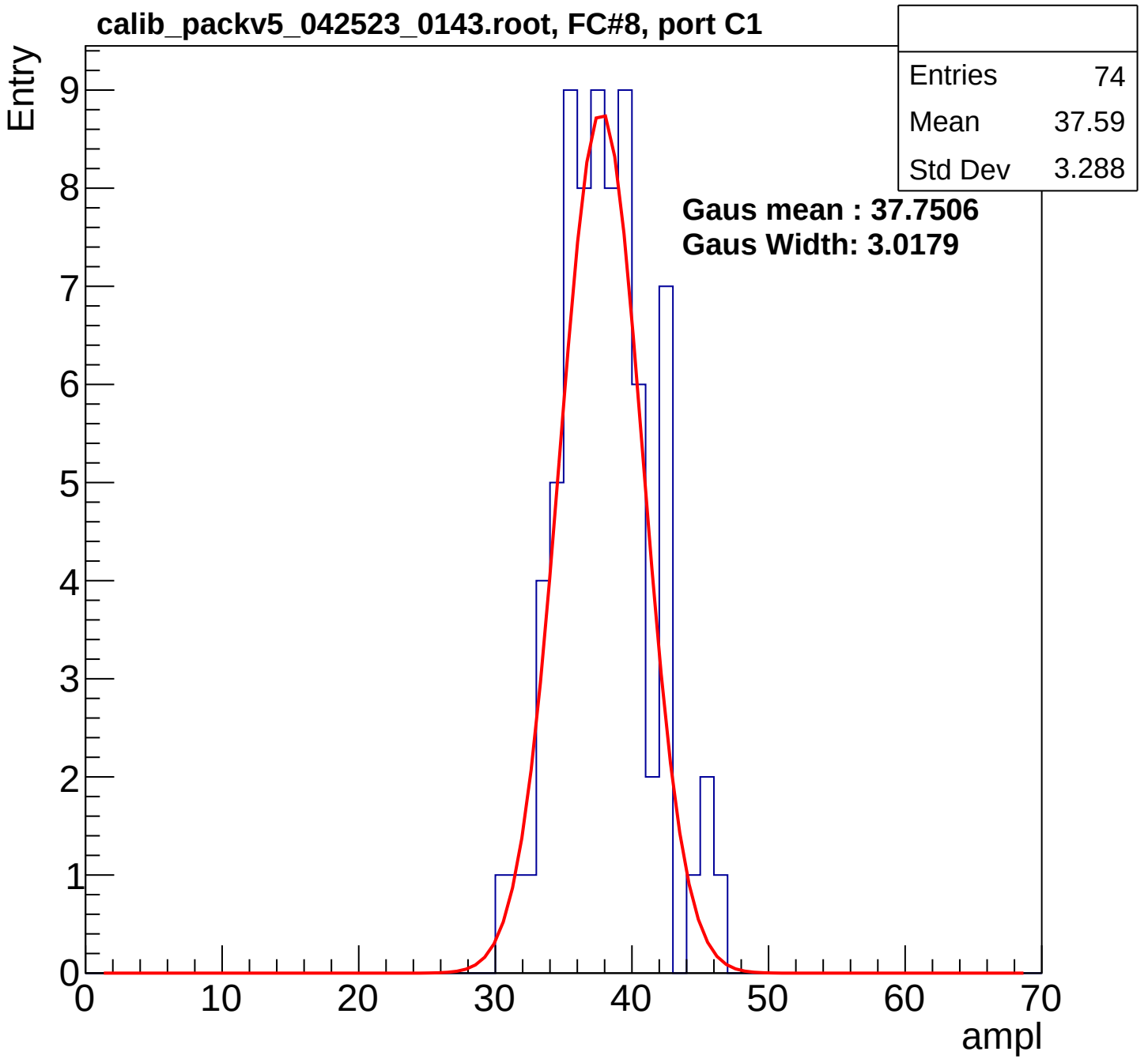

B0L002S, U3-ch86, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 55.69
Std Dev 3.015
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

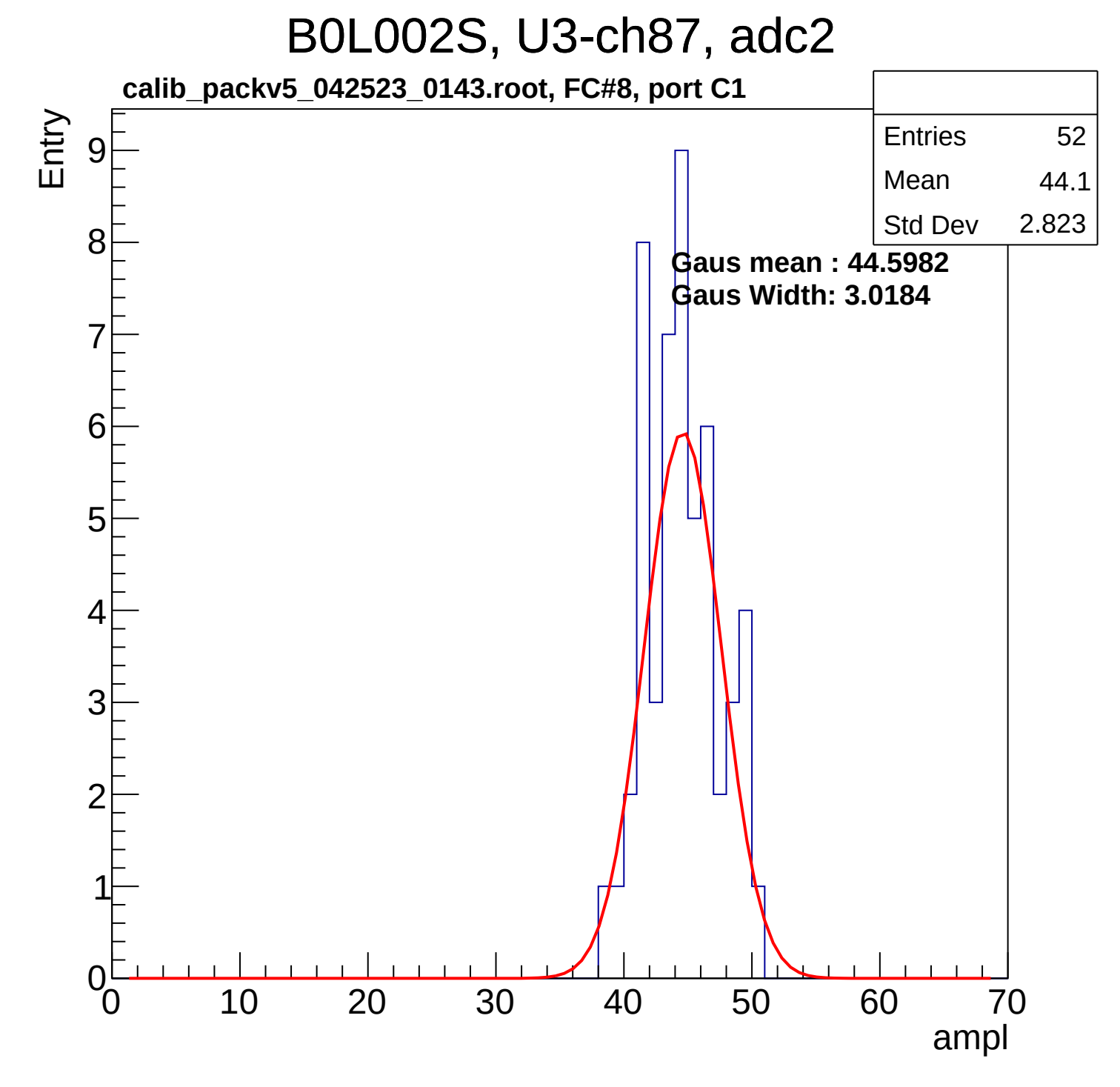

B0L002S, U3-ch86, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.83
Std Dev 11.08
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

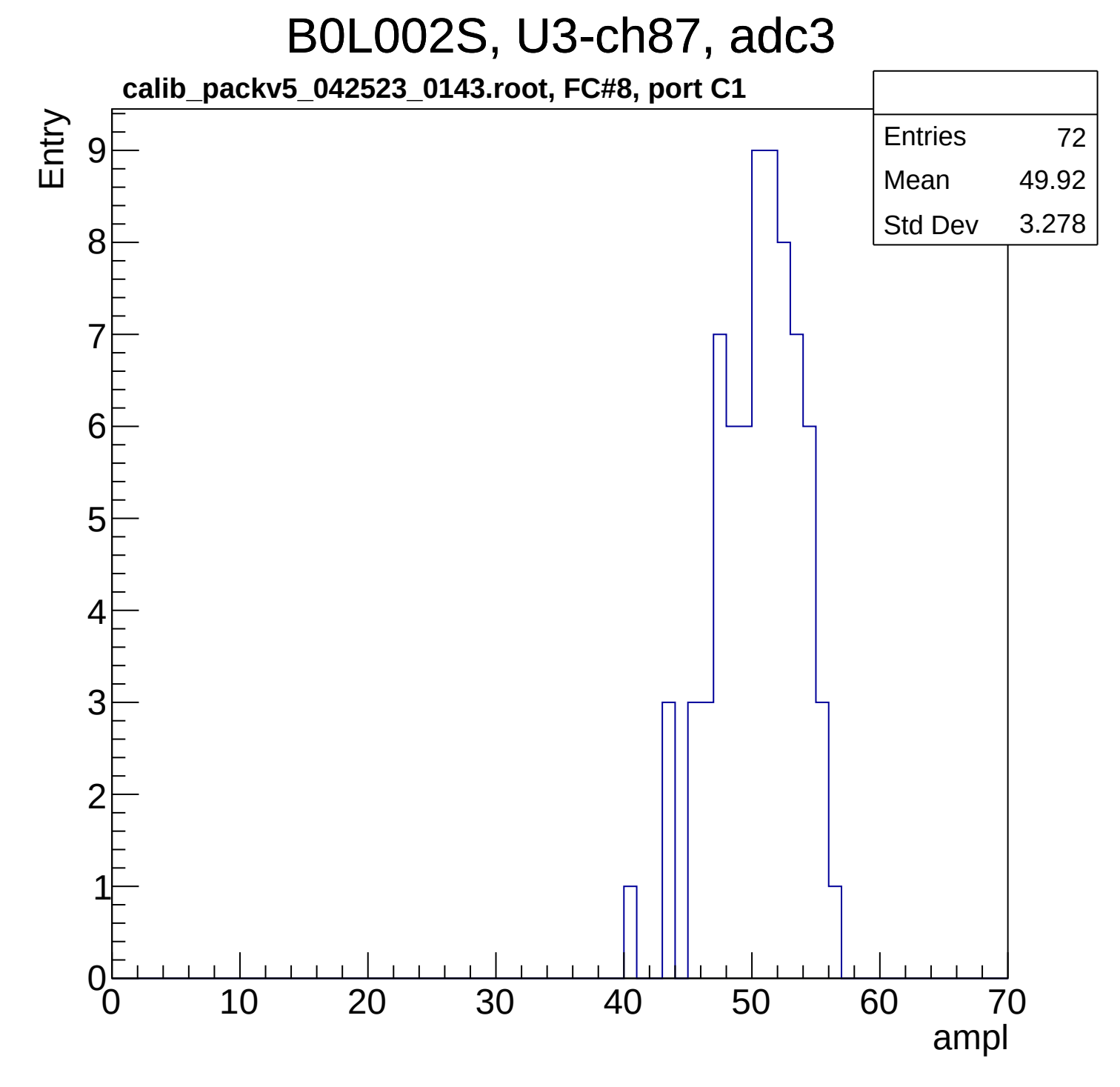

B0L002S, U3-ch86, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 62.5
Std Dev 0.5
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch86, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch87, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 30.5
Std Dev 3.434
Gaus mean : 30.8373
Gaus Width: 3.1207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch87, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 37.59
Std Dev 3.288
Gaus mean : 37.7506
Gaus Width: 3.0179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch87, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 44.1
Std Dev 2.823
Gaus mean : 44.5982
Gaus Width: 3.0184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch87, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 49.92
Std Dev 3.278
Entry
ampl
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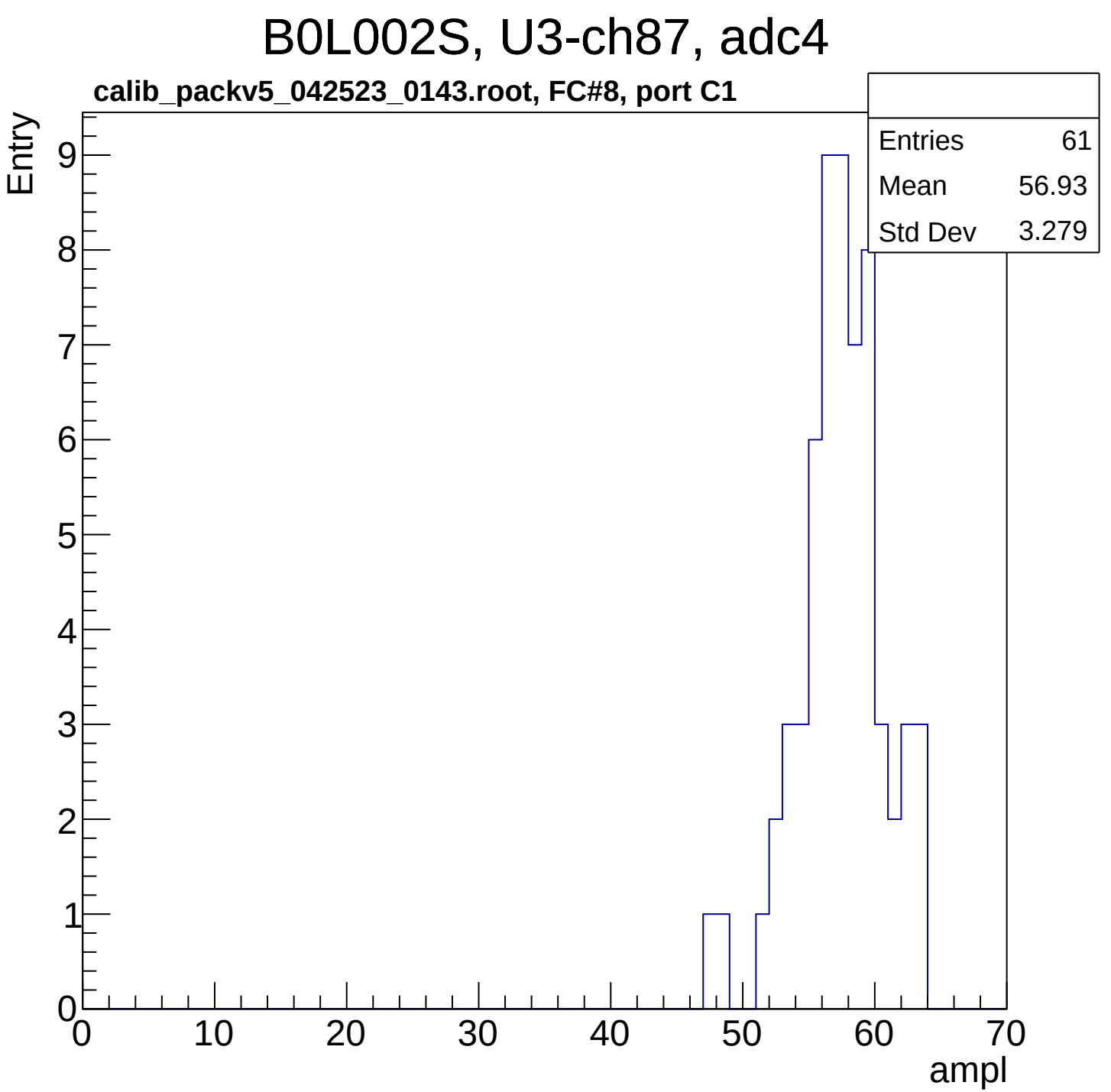
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch87, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.93
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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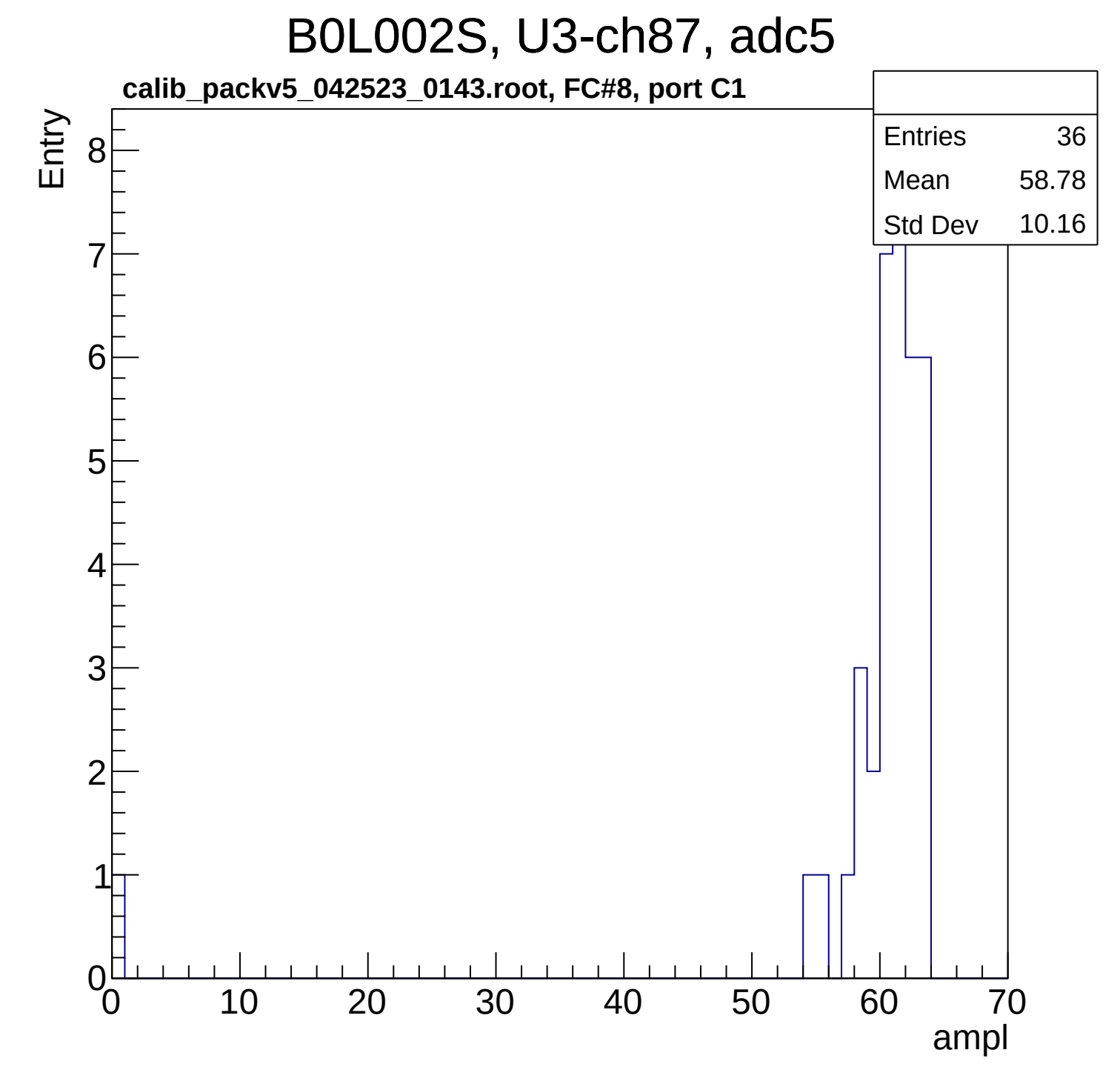
B0L002S, U3-ch87, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.78
Std Dev 10.16
Entry
ampl
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1
2
3
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5
6
7
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0
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20
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40
50
60
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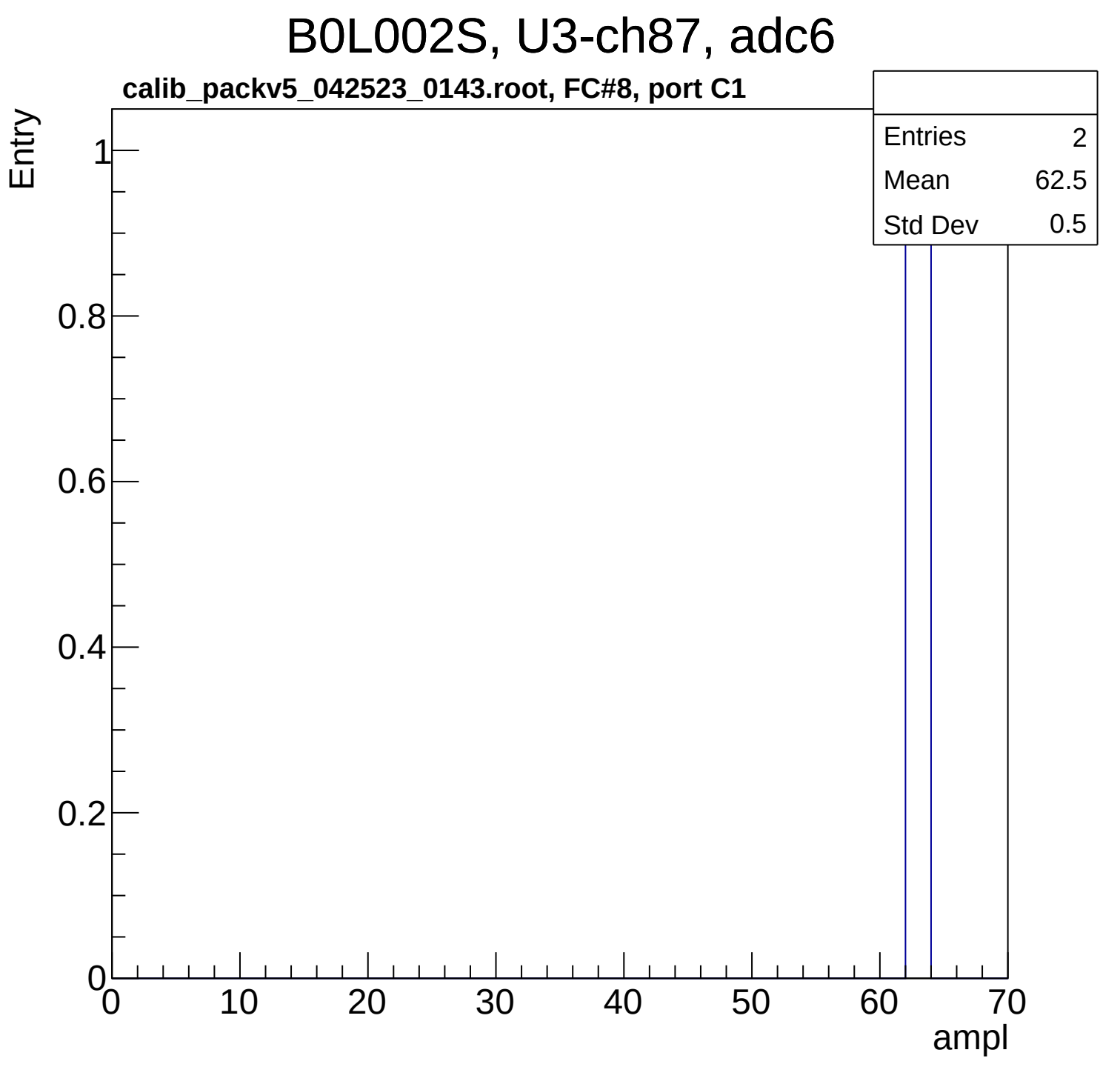
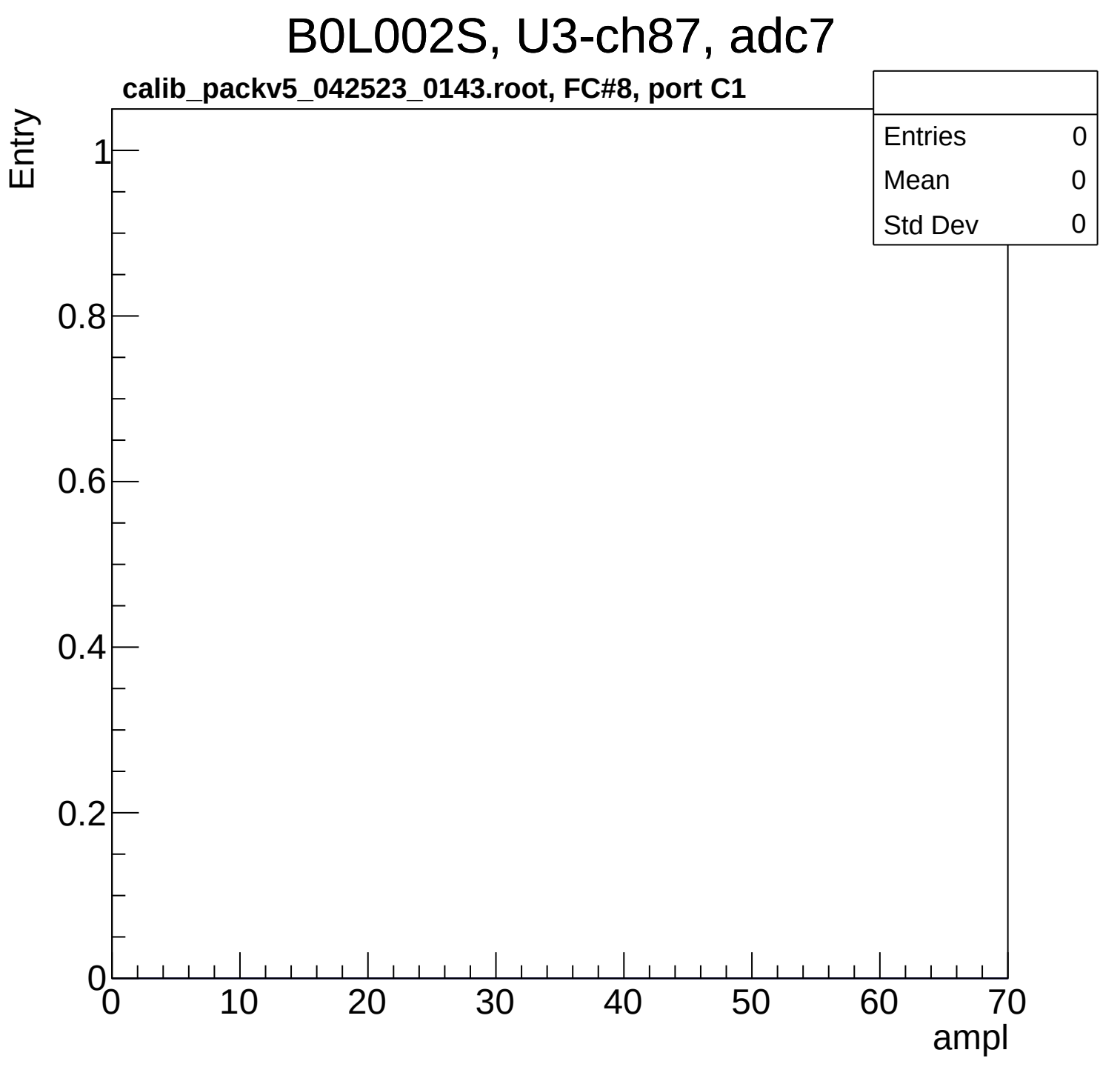

B0L002S, U3-ch87, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch87, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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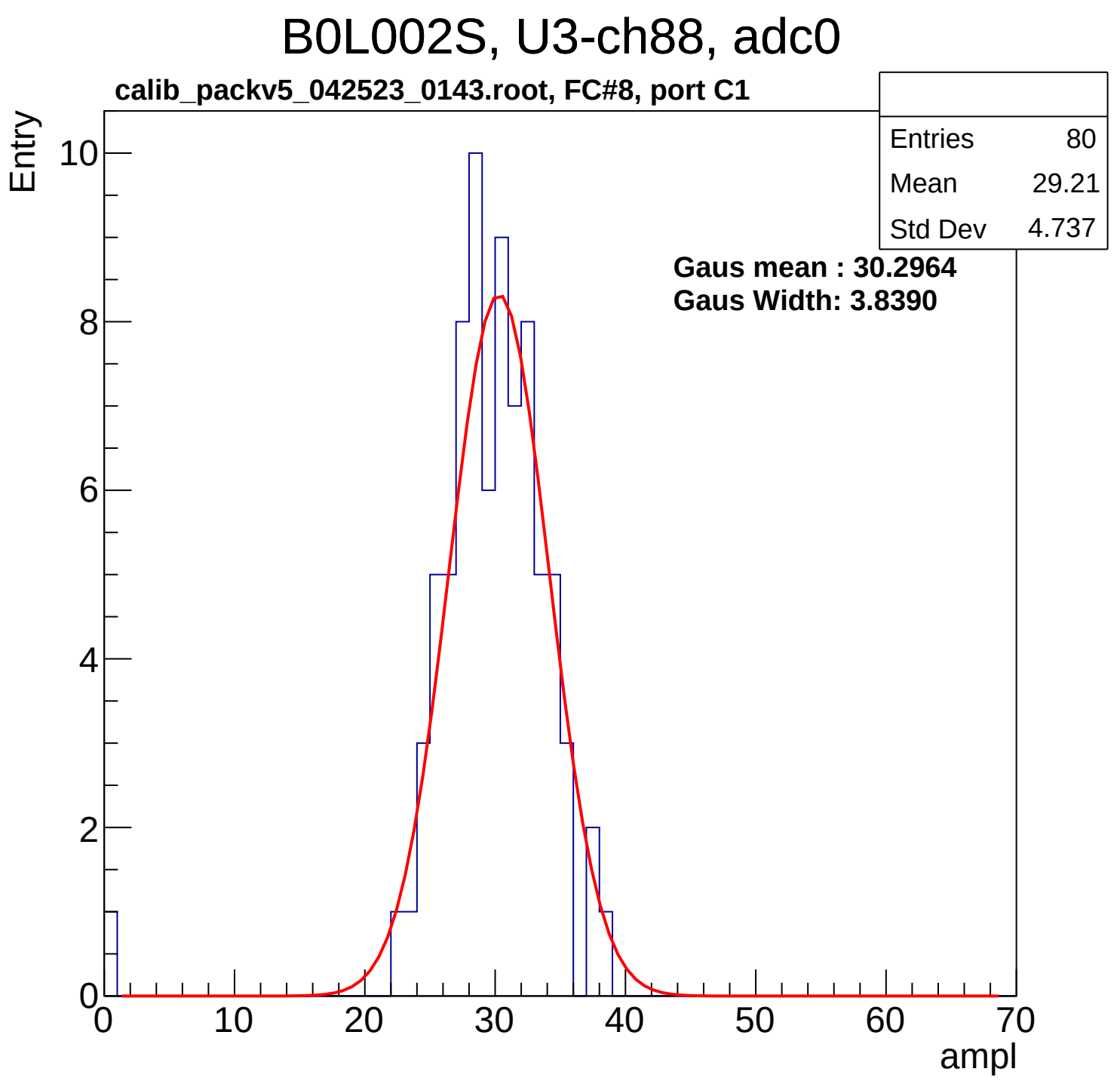
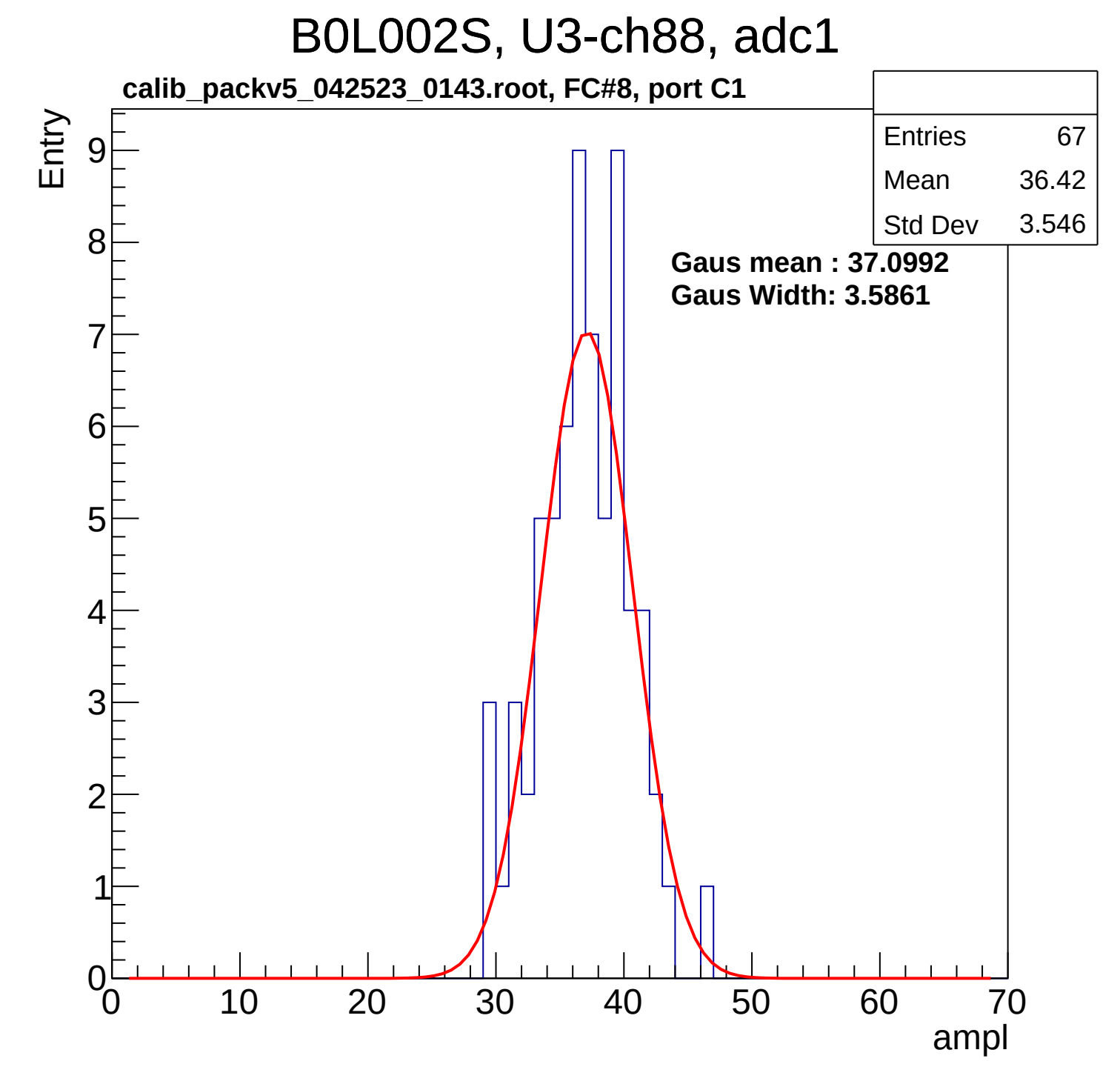
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch88, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 29.21
Std Dev 4.737
Gaus mean : 30.2964
Gaus Width: 3.8390
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch88, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.42
Std Dev 3.546
Gaus mean : 37.0992
Gaus Width: 3.5861
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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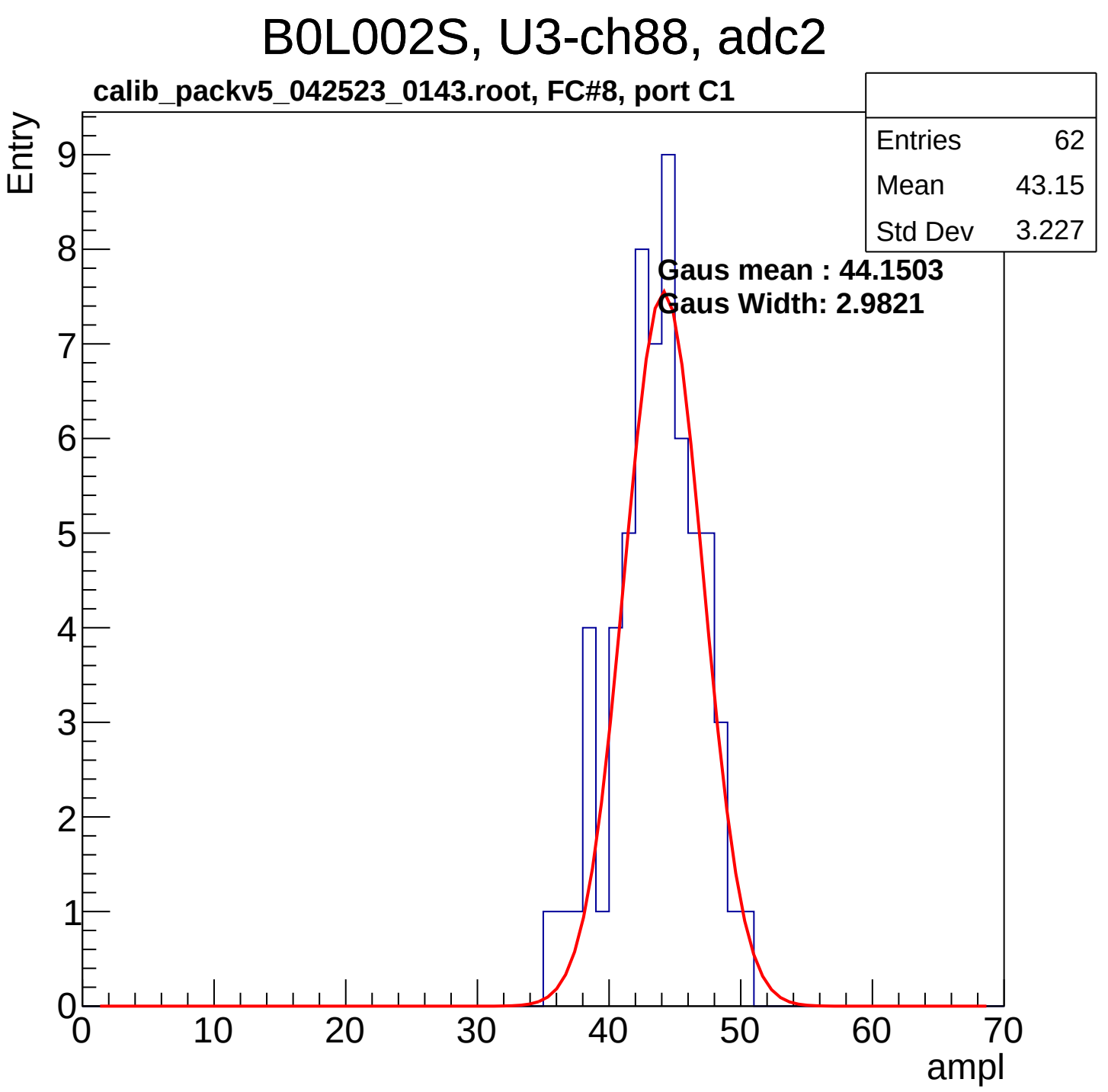
B0L002S, U3-ch88, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 43.15
Std Dev 3.227
Gaus mean : 44.1503
Gaus Width: 2.9821
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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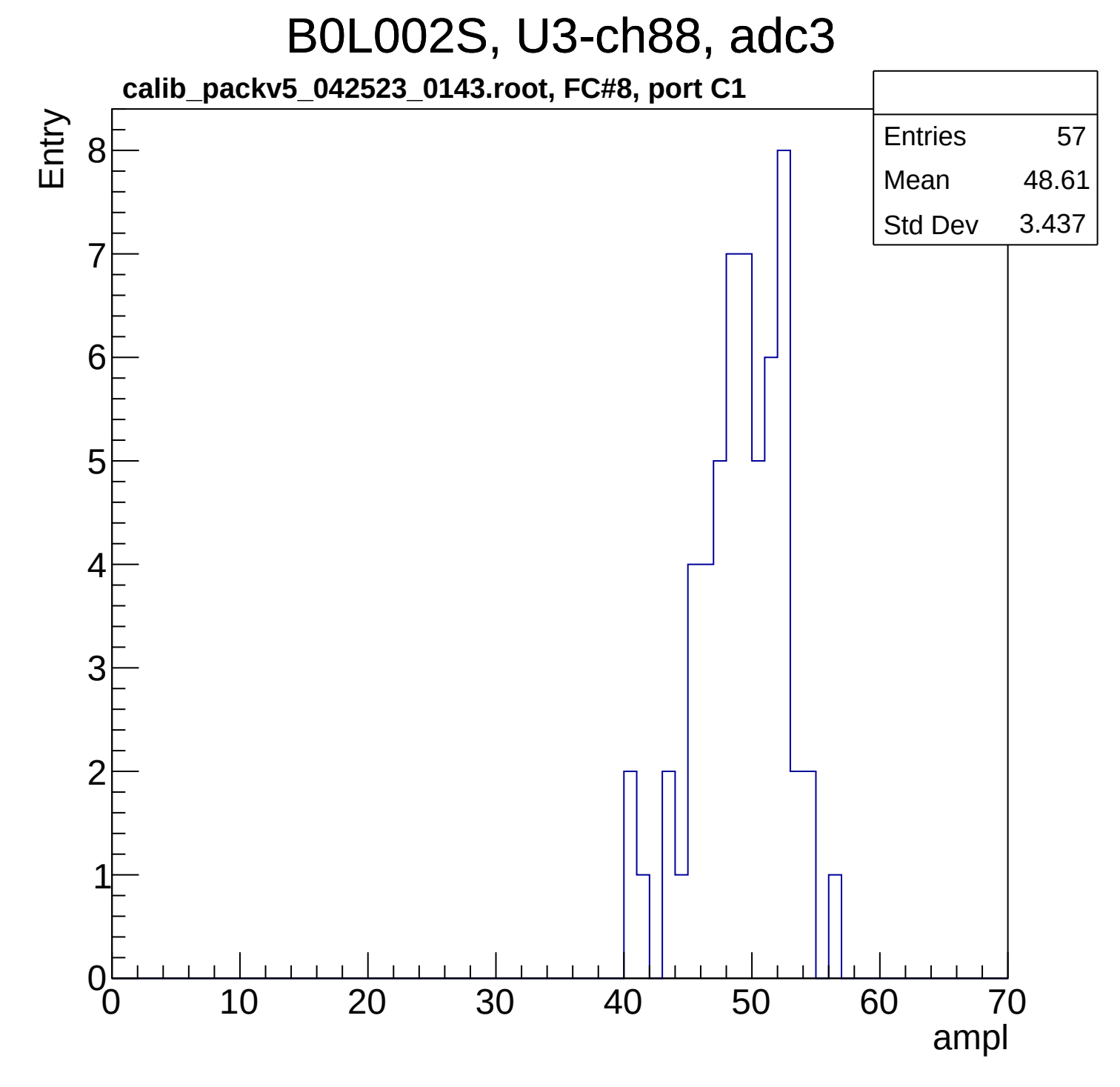
B0L002S, U3-ch88, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 48.61
Std Dev 3.437
Entry
ampl
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1
2
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4
5
6
7
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0
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60
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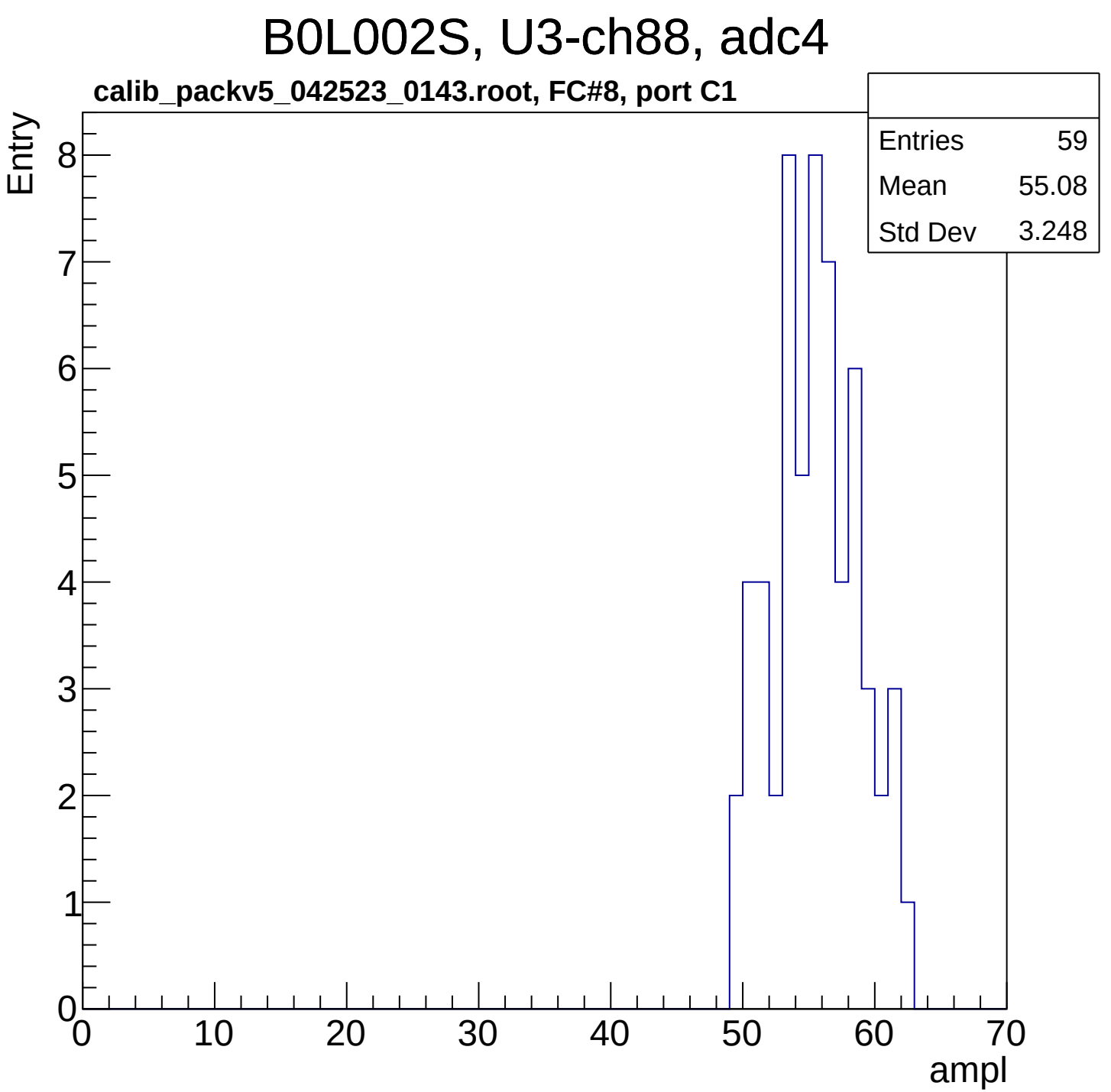
B0L002S, U3-ch88, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 55.08
Std Dev 3.248
Entry
ampl
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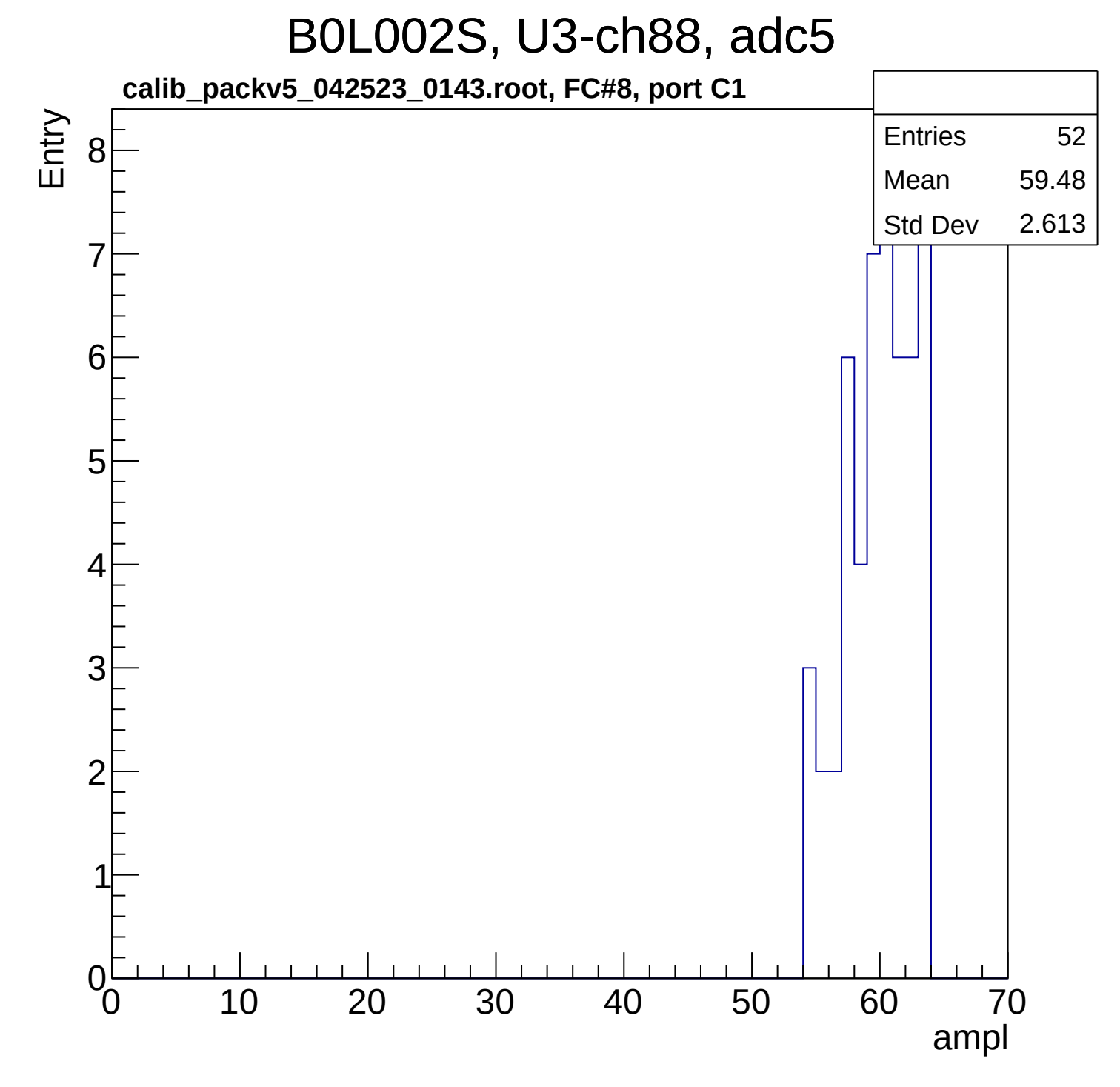
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch88, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 59.48
Std Dev 2.613
Entry
ampl
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1
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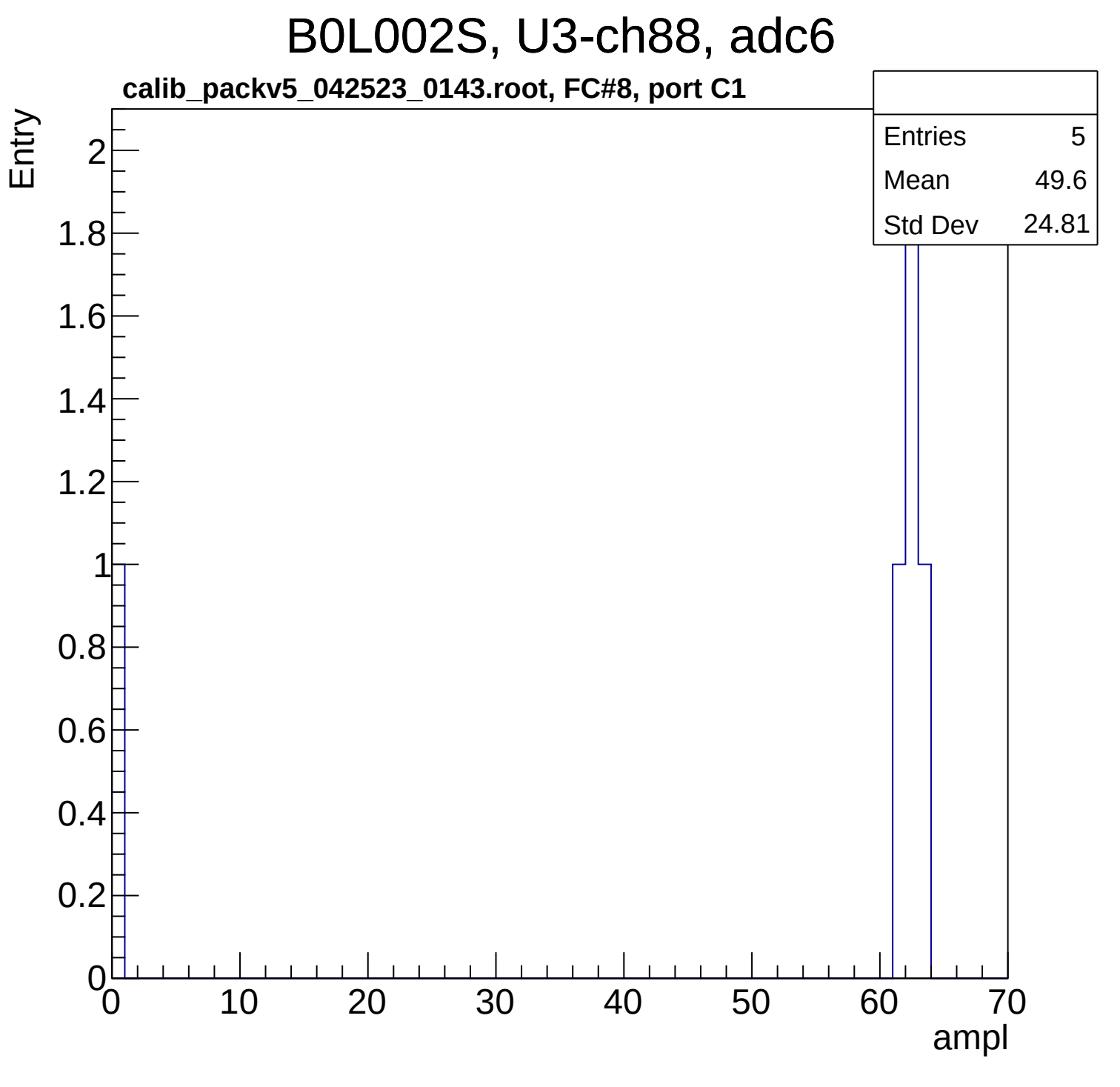
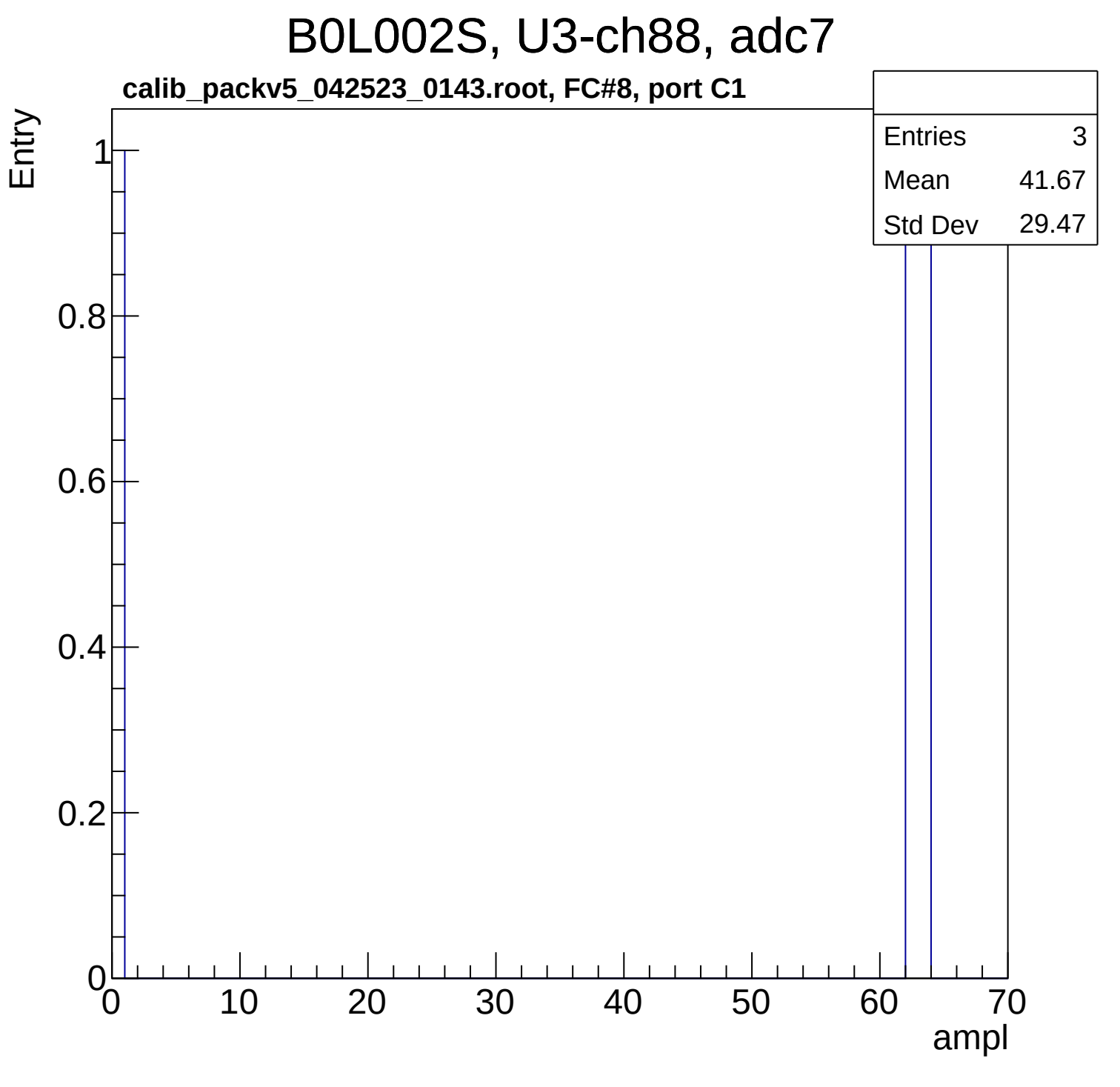
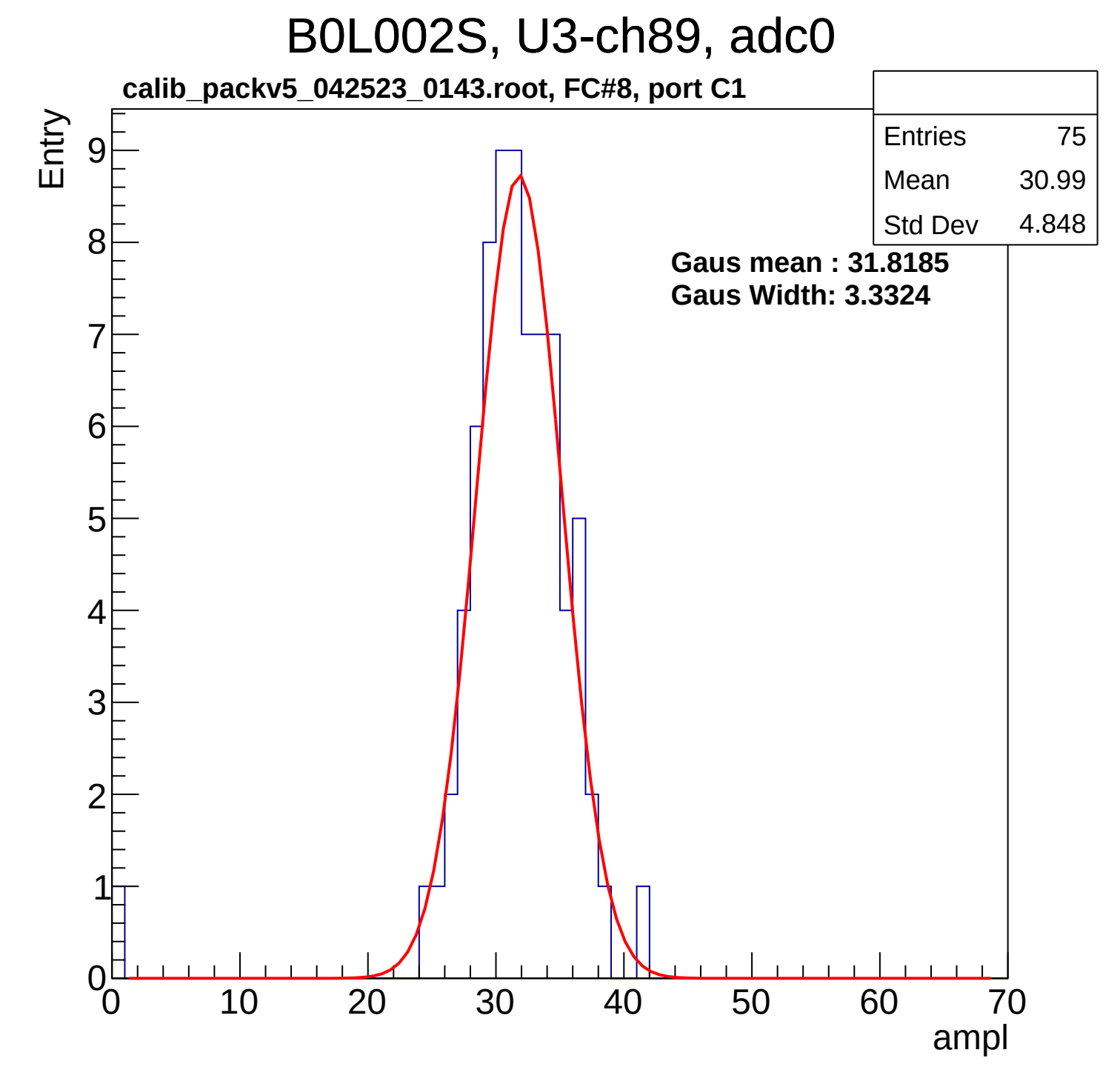

B0L002S, U3-ch88, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 49.6
Std Dev 24.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch88, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch89, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 30.99
Std Dev 4.848
Gaus mean : 31.8185
Gaus Width: 3.3324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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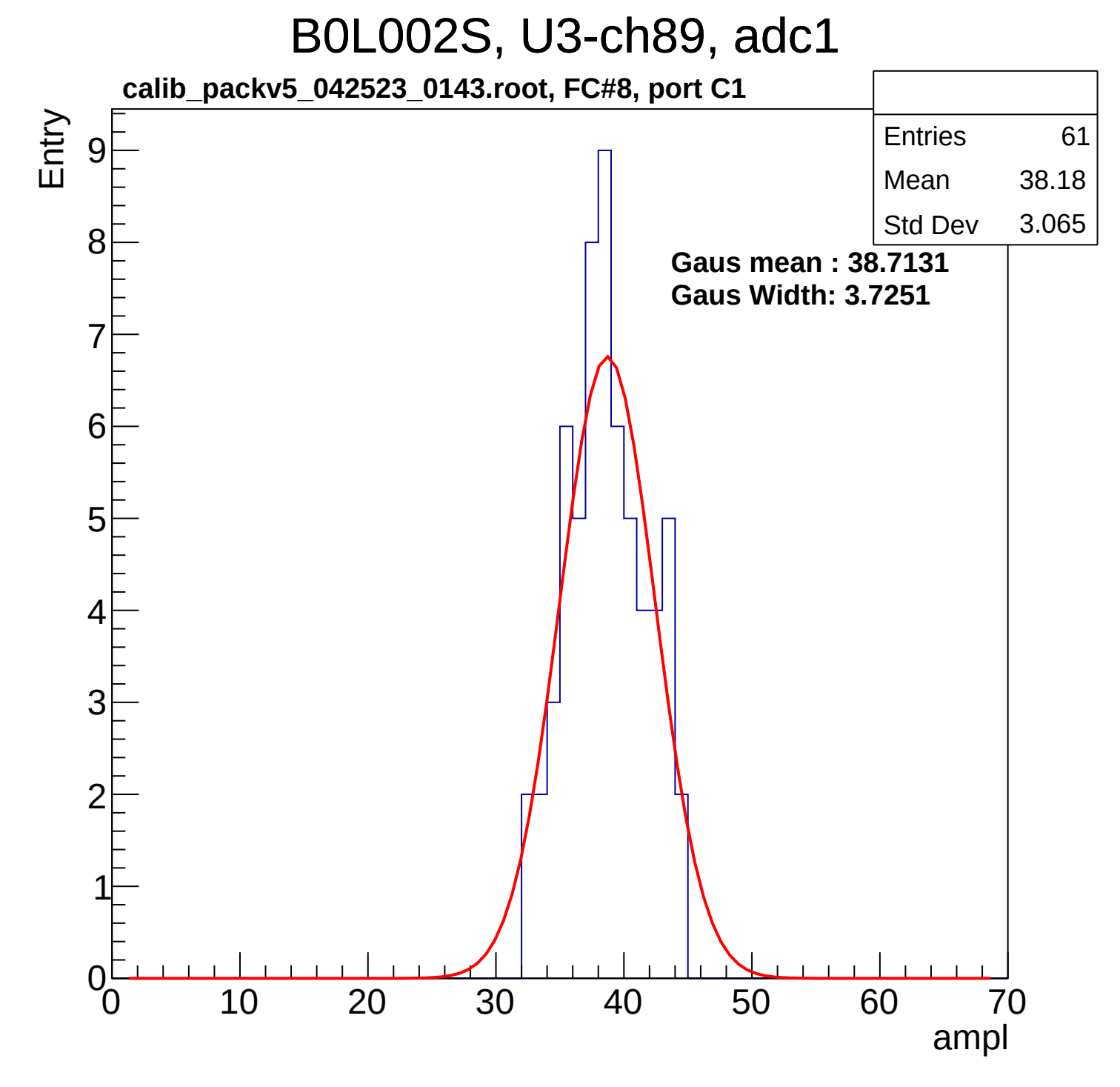
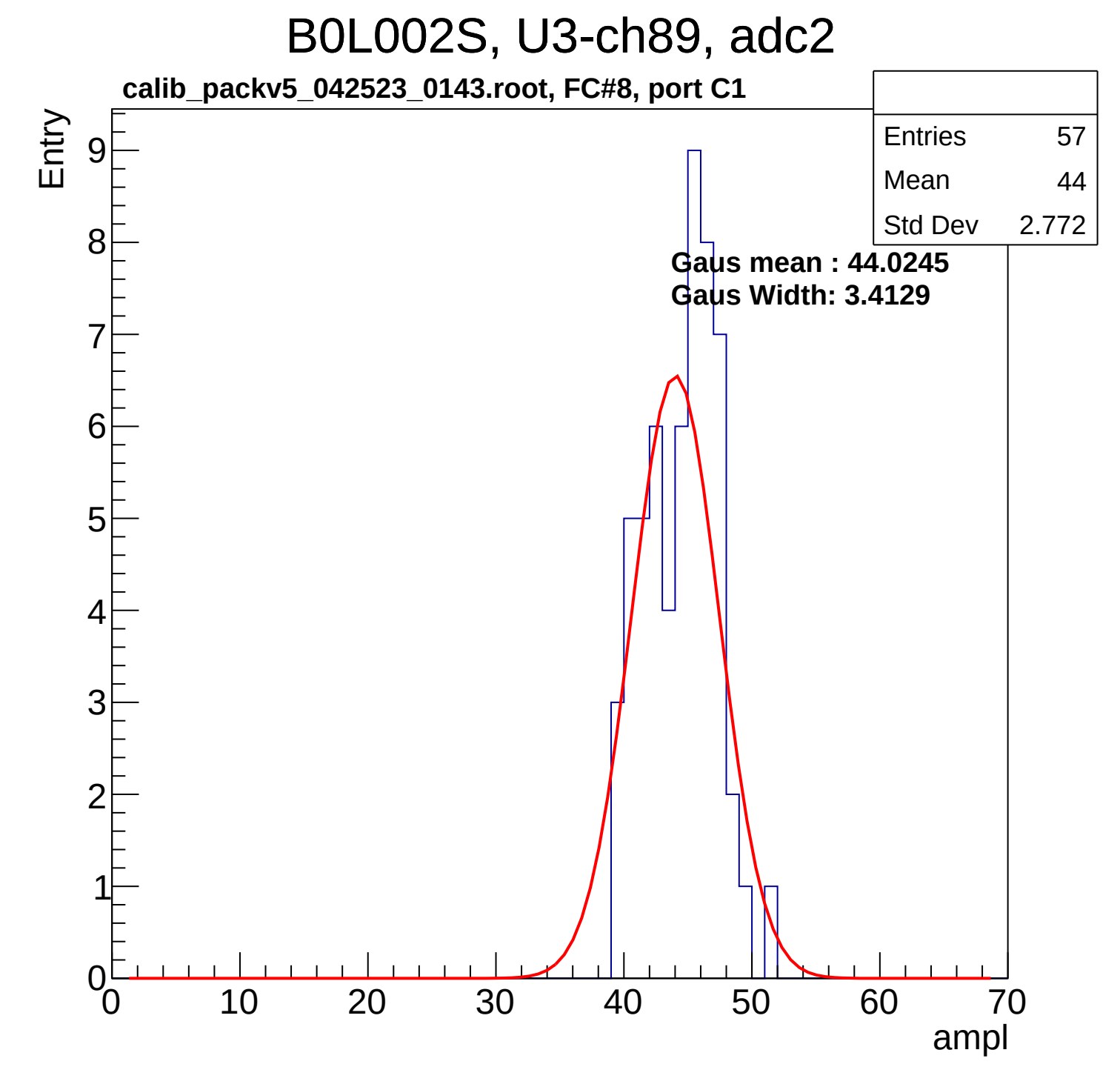

B0L002S, U3-ch89, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 38.18
Std Dev 3.065
Gaus mean : 38.7131
Gaus Width: 3.7251
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U3-ch89, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 44
Std Dev 2.772
Gaus mean : 44.0245
Gaus Width: 3.4129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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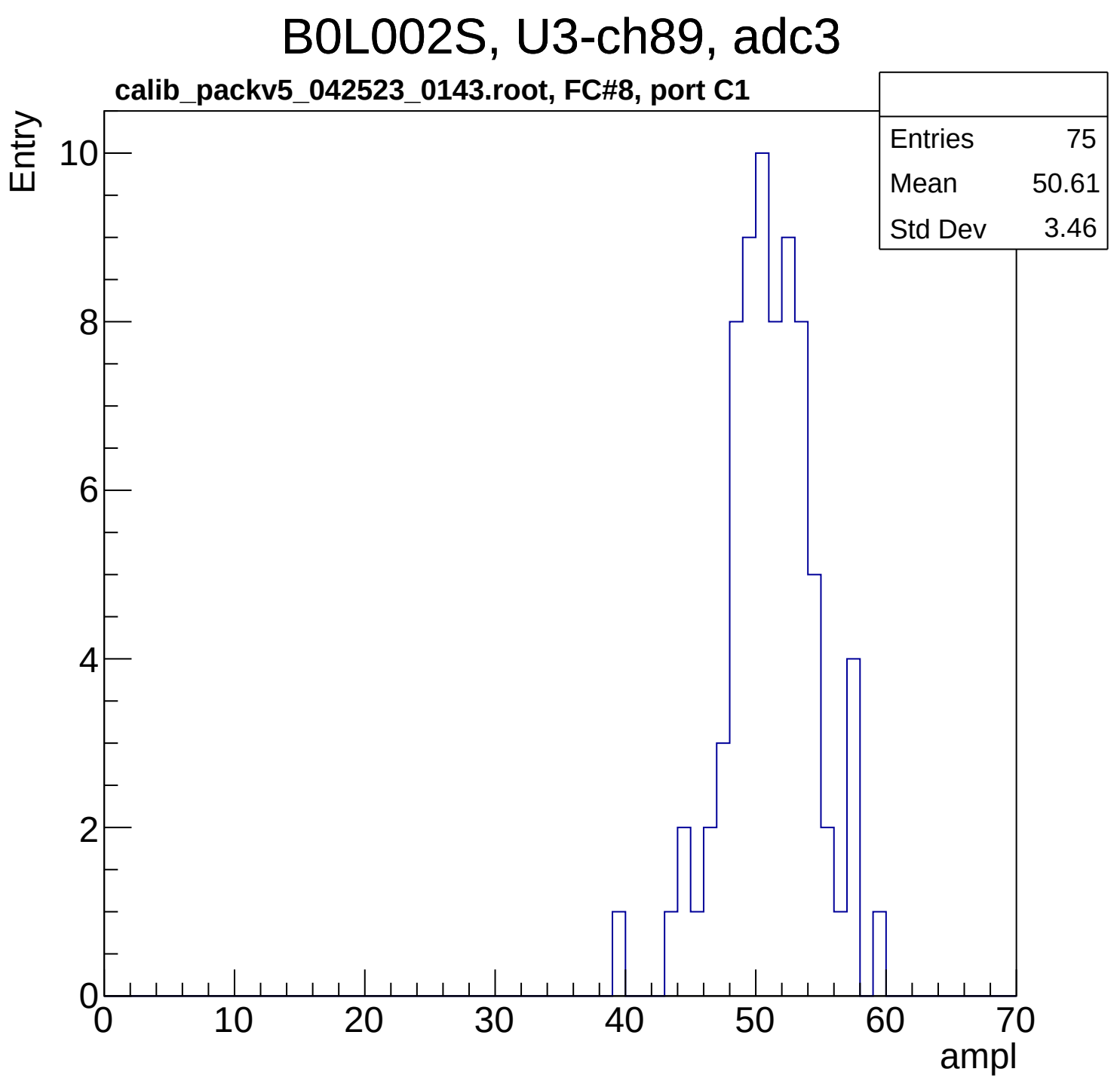
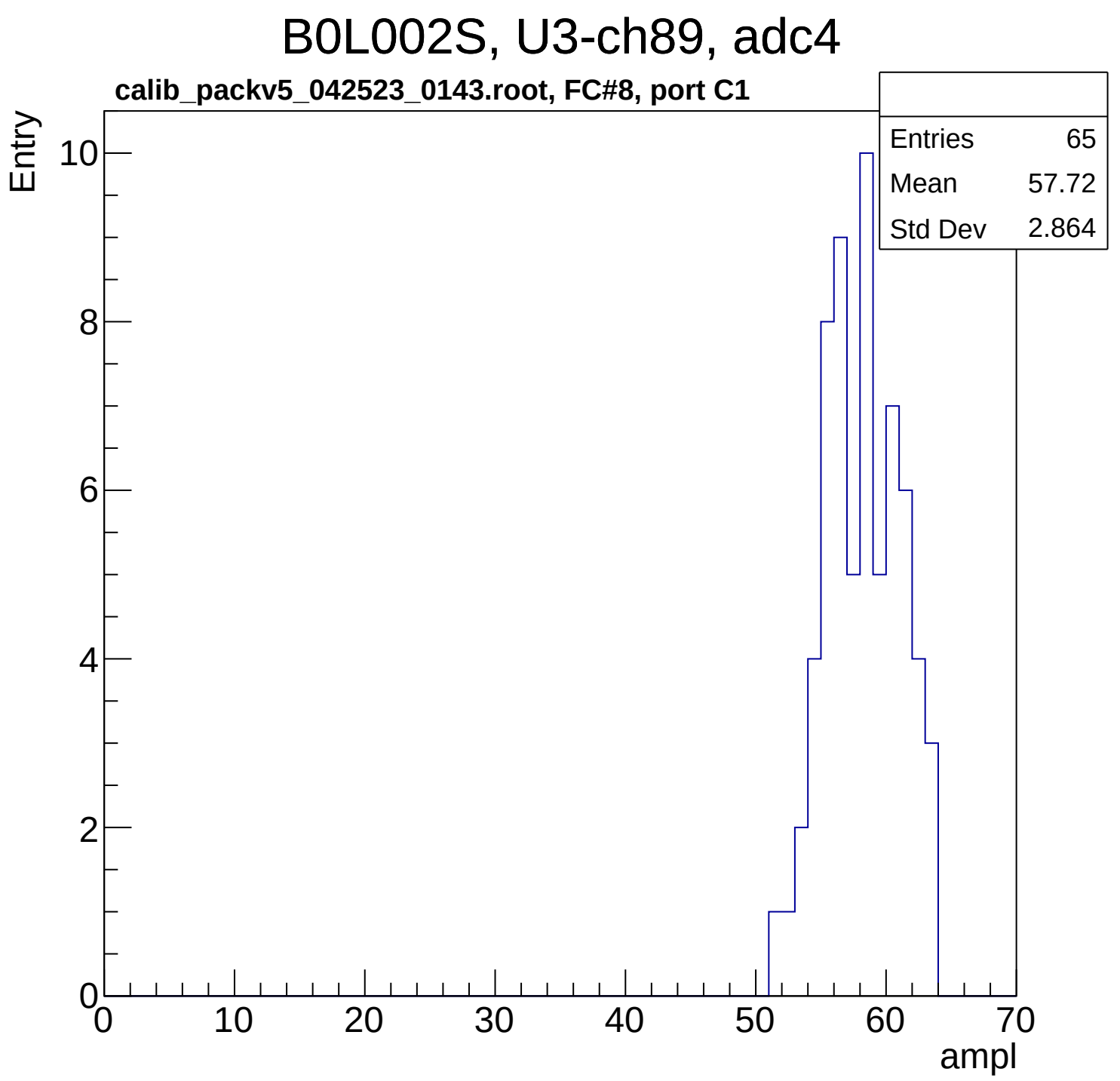
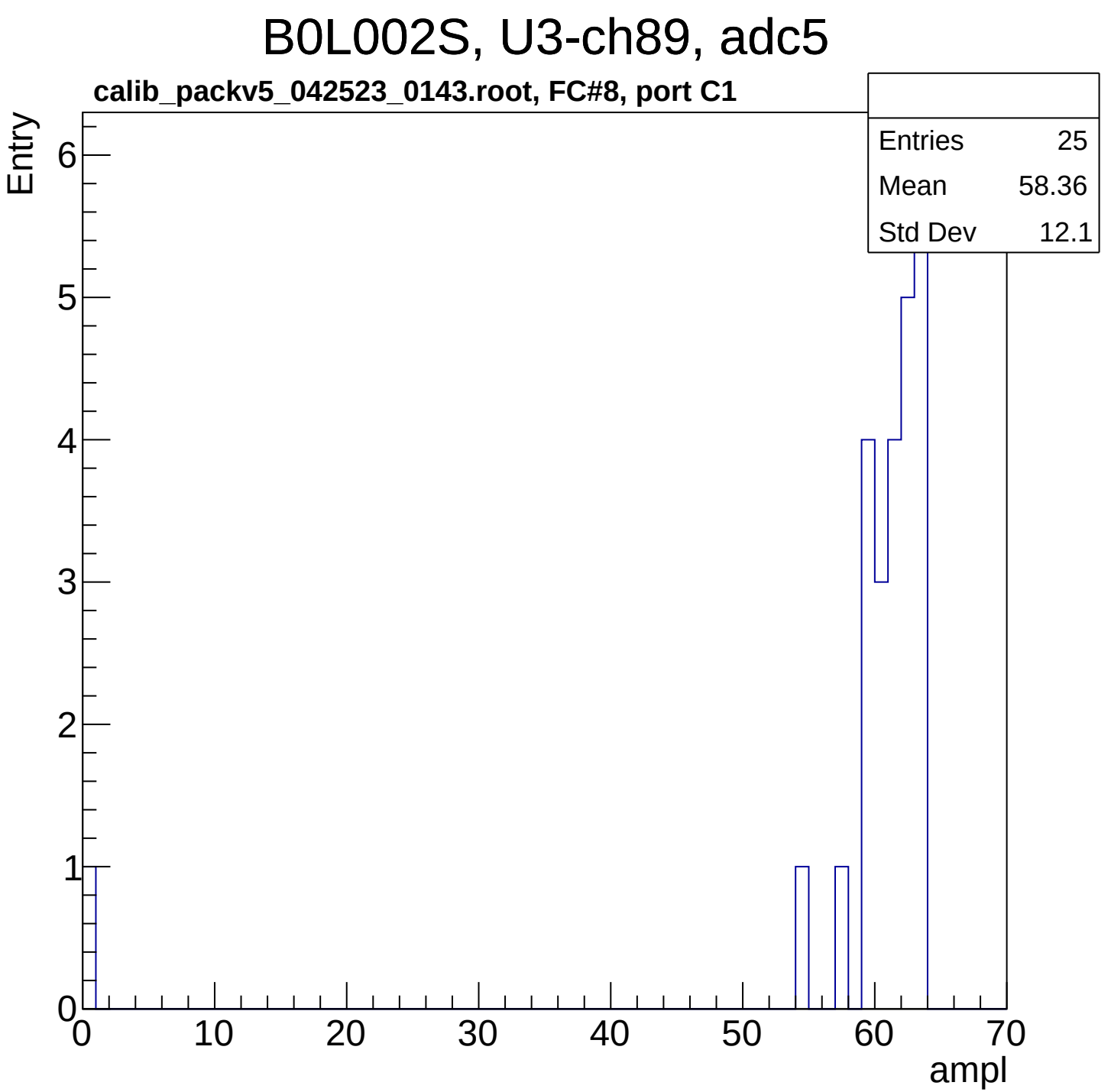
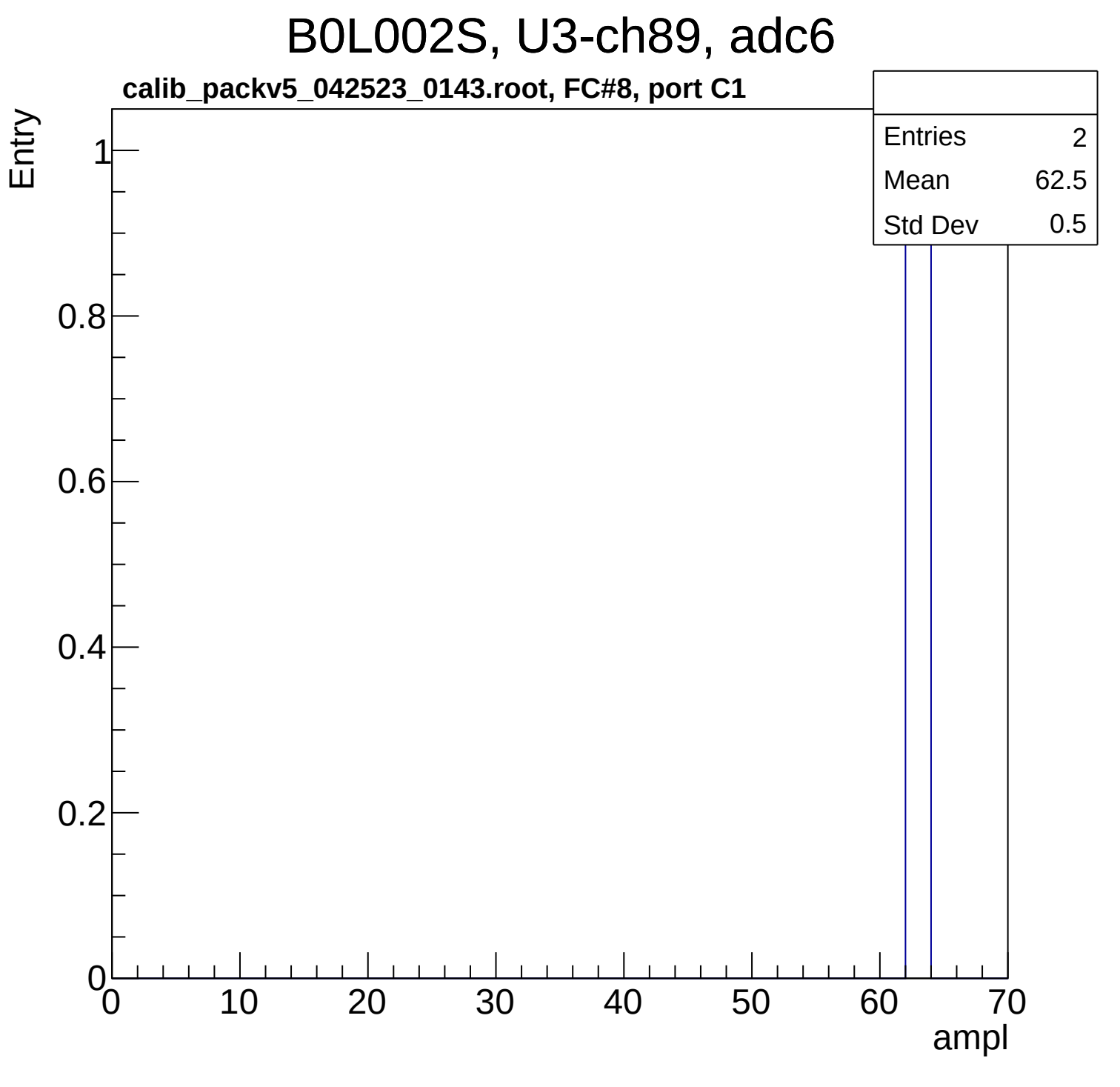
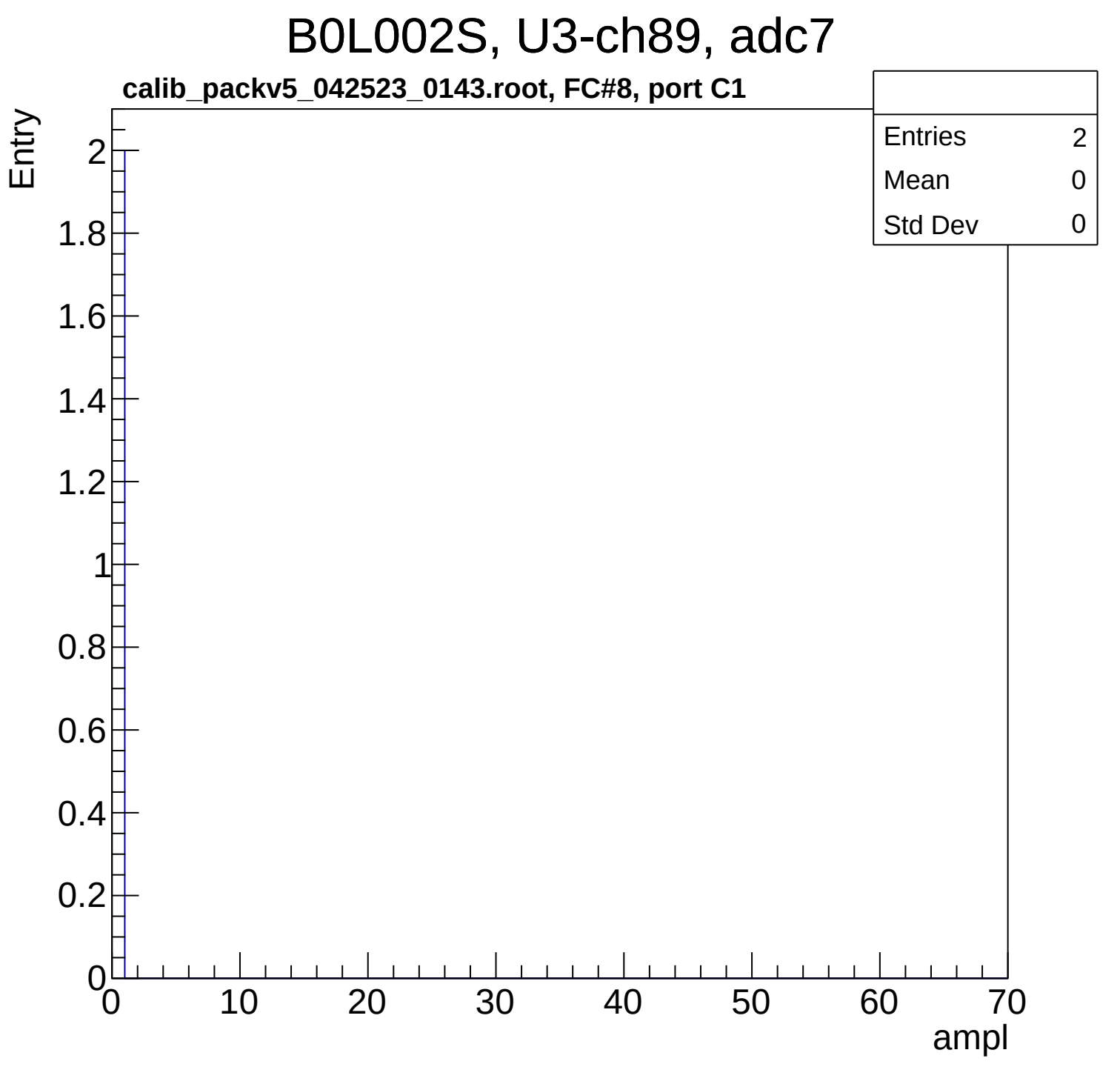
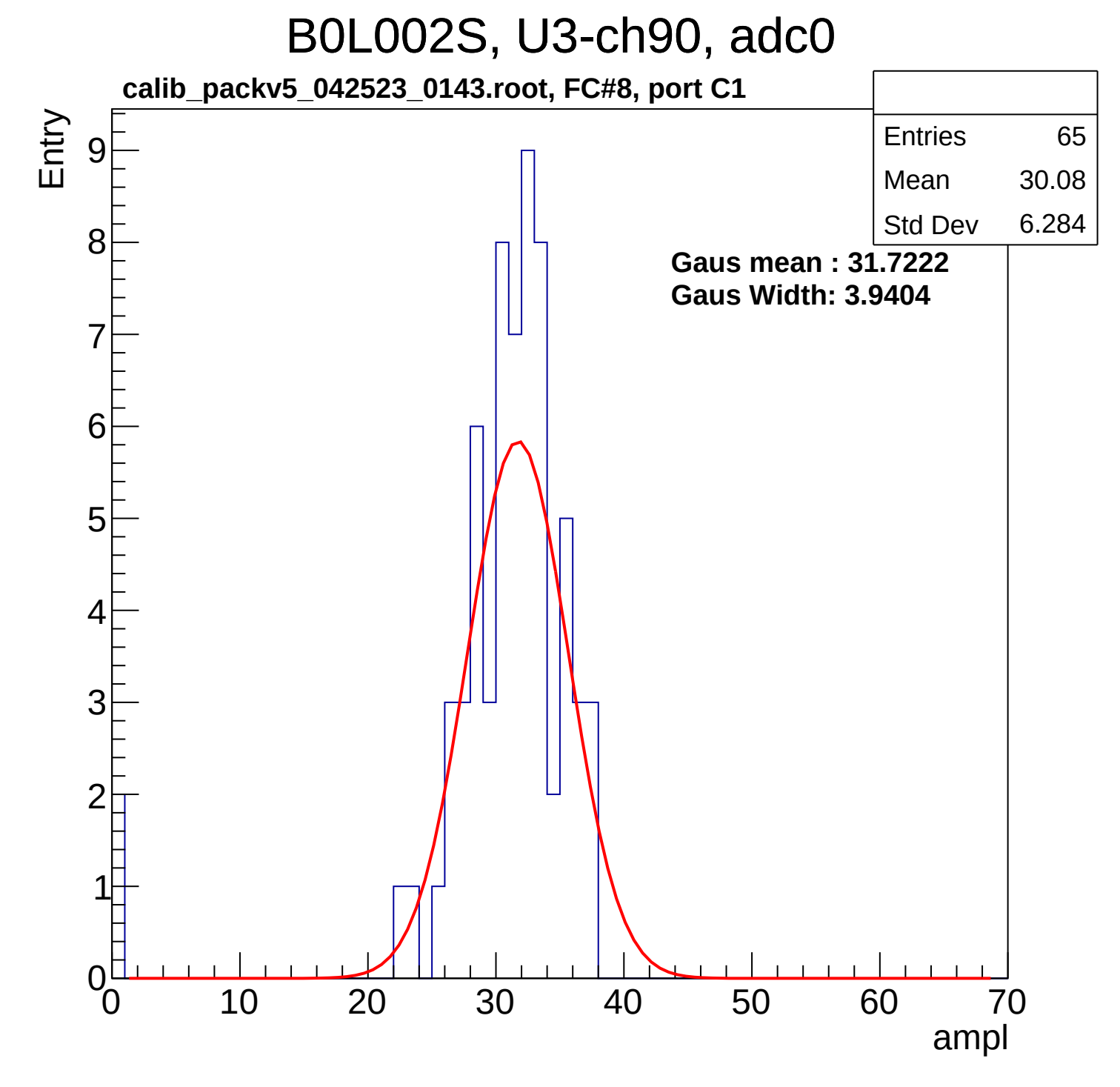

B0L002S, U3-ch89, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.61
Std Dev 3.46
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

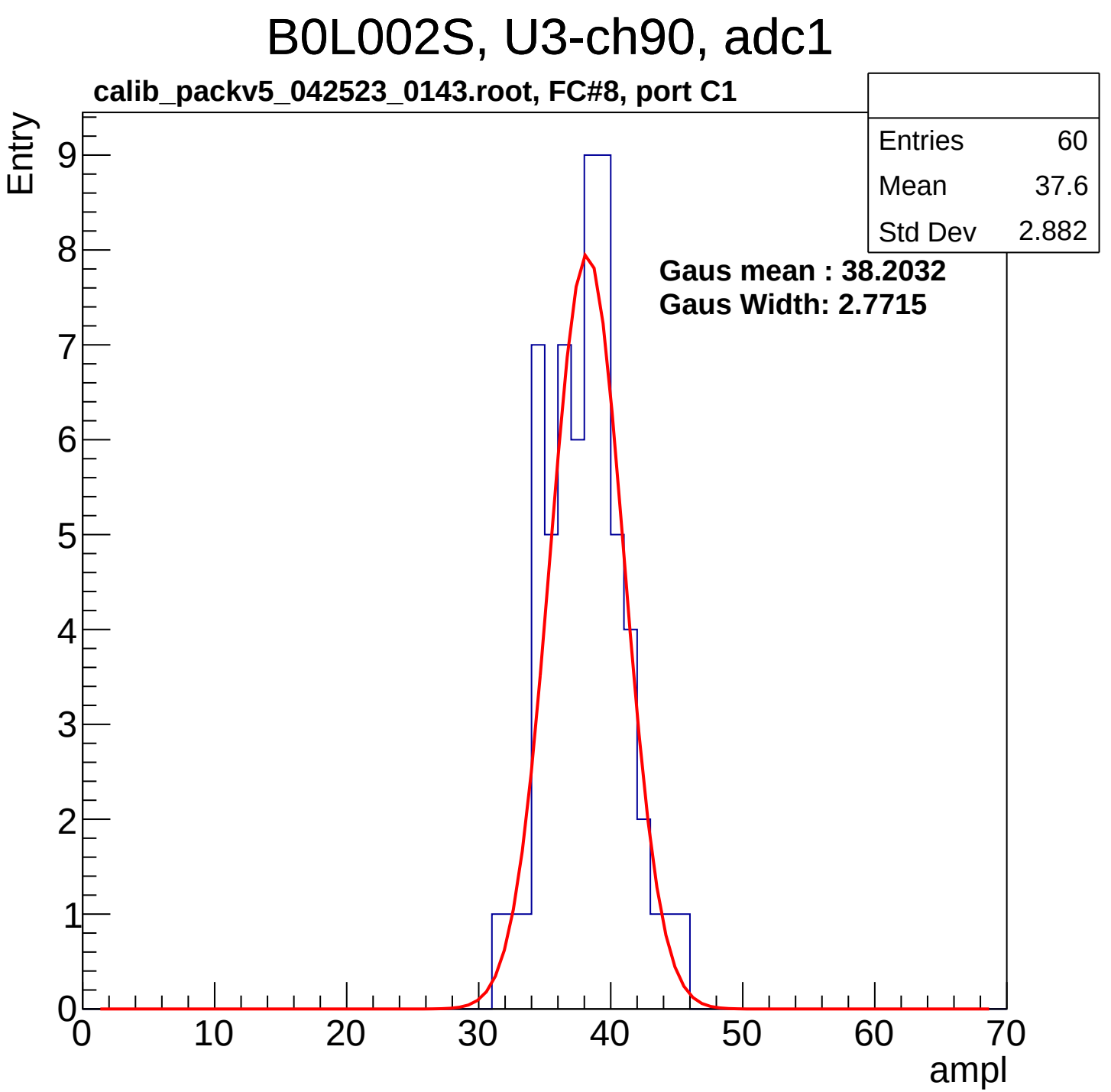

B0L002S, U3-ch89, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.72
Std Dev 2.864
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

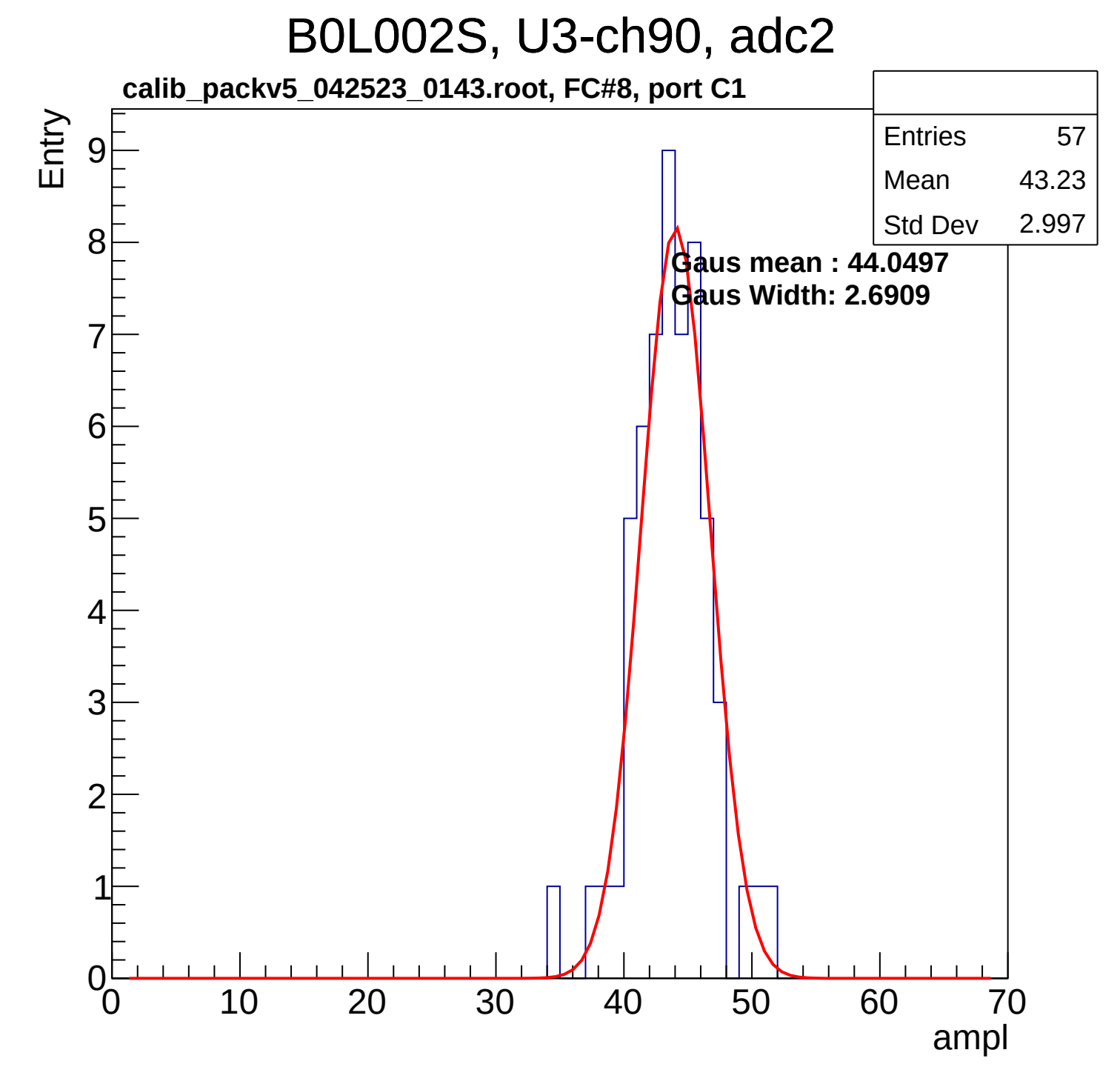

B0L002S, U3-ch89, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.36
Std Dev 12.1
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

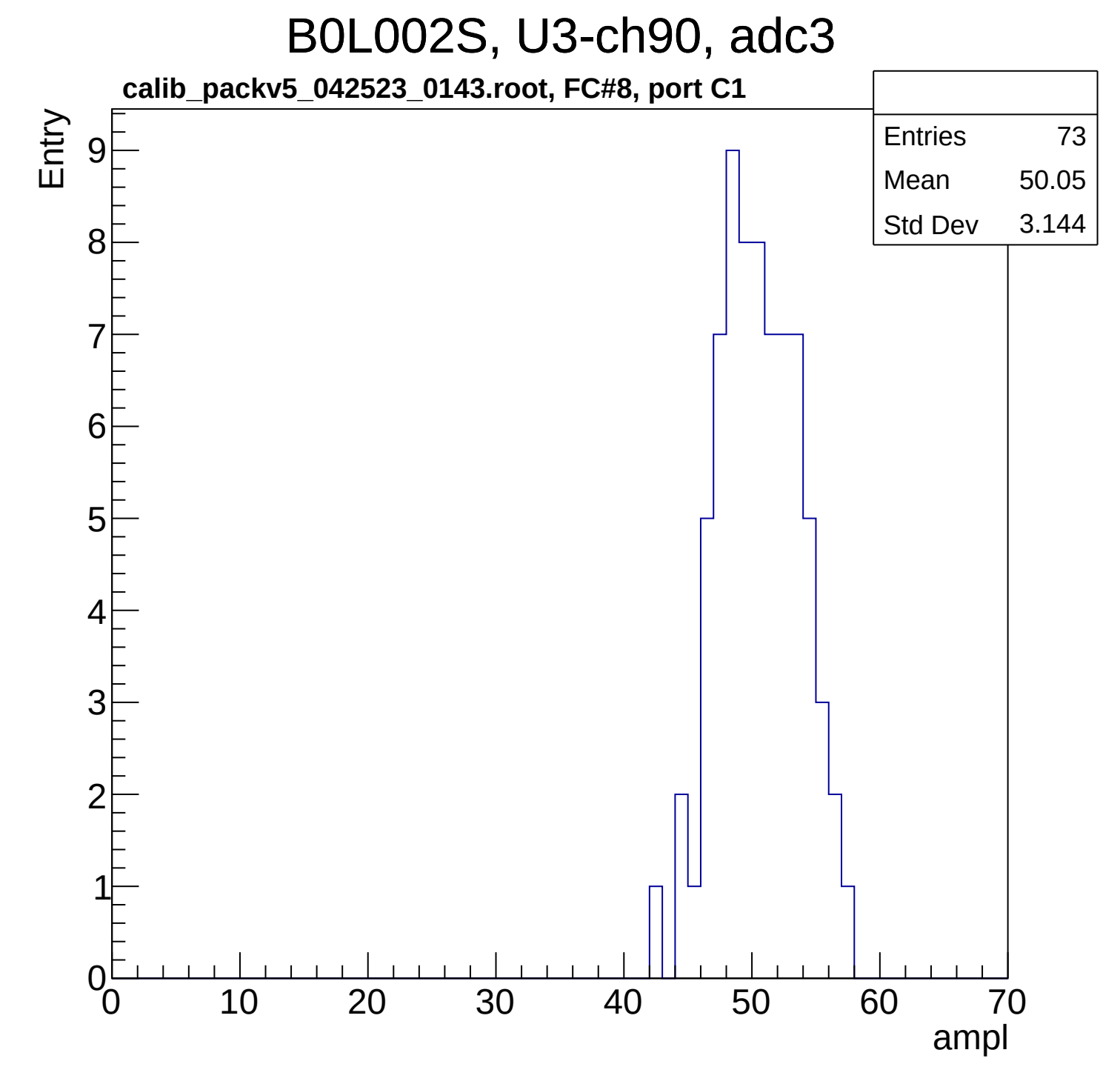

B0L002S, U3-ch89, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

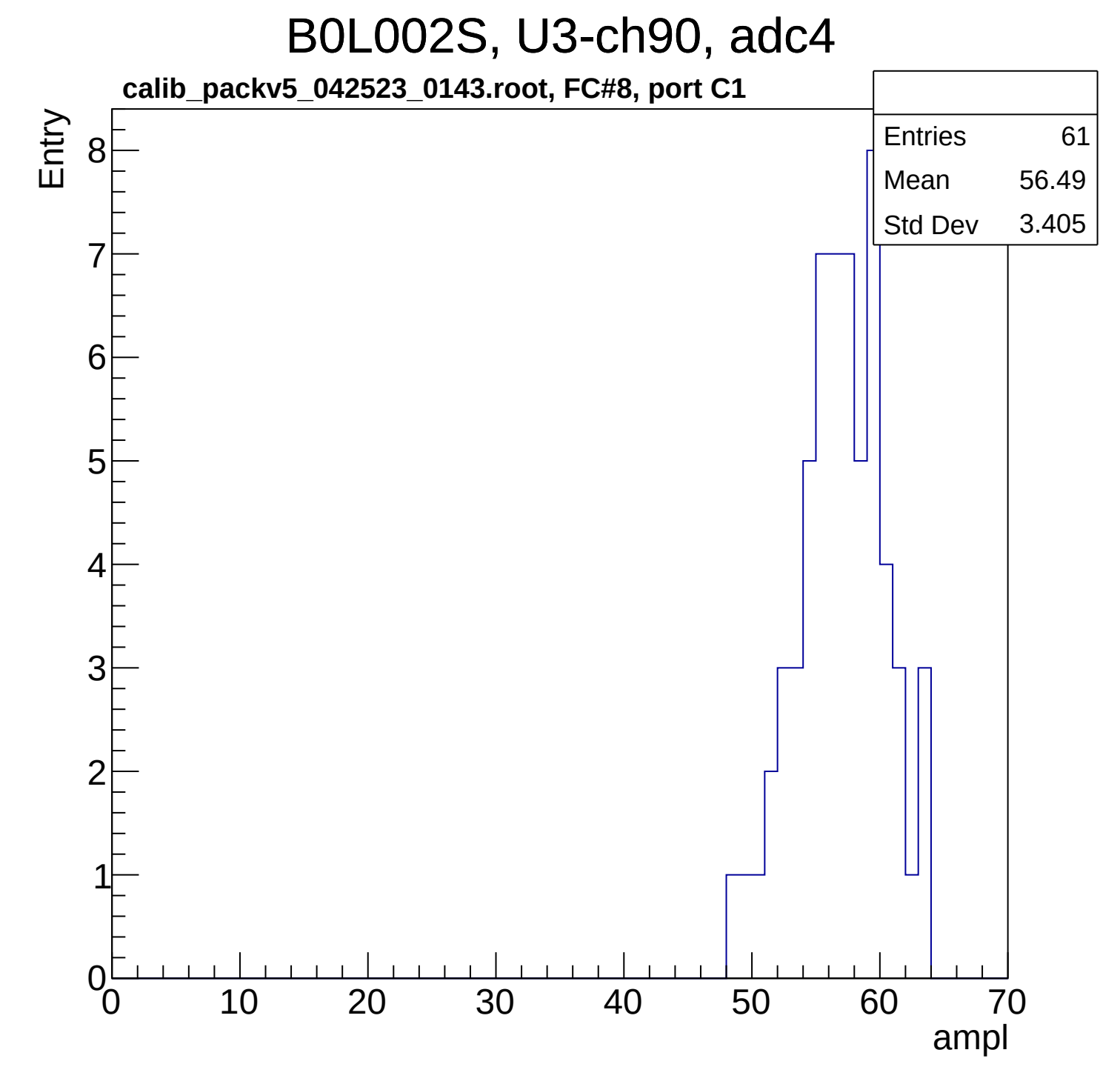

B0L002S, U3-ch89, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

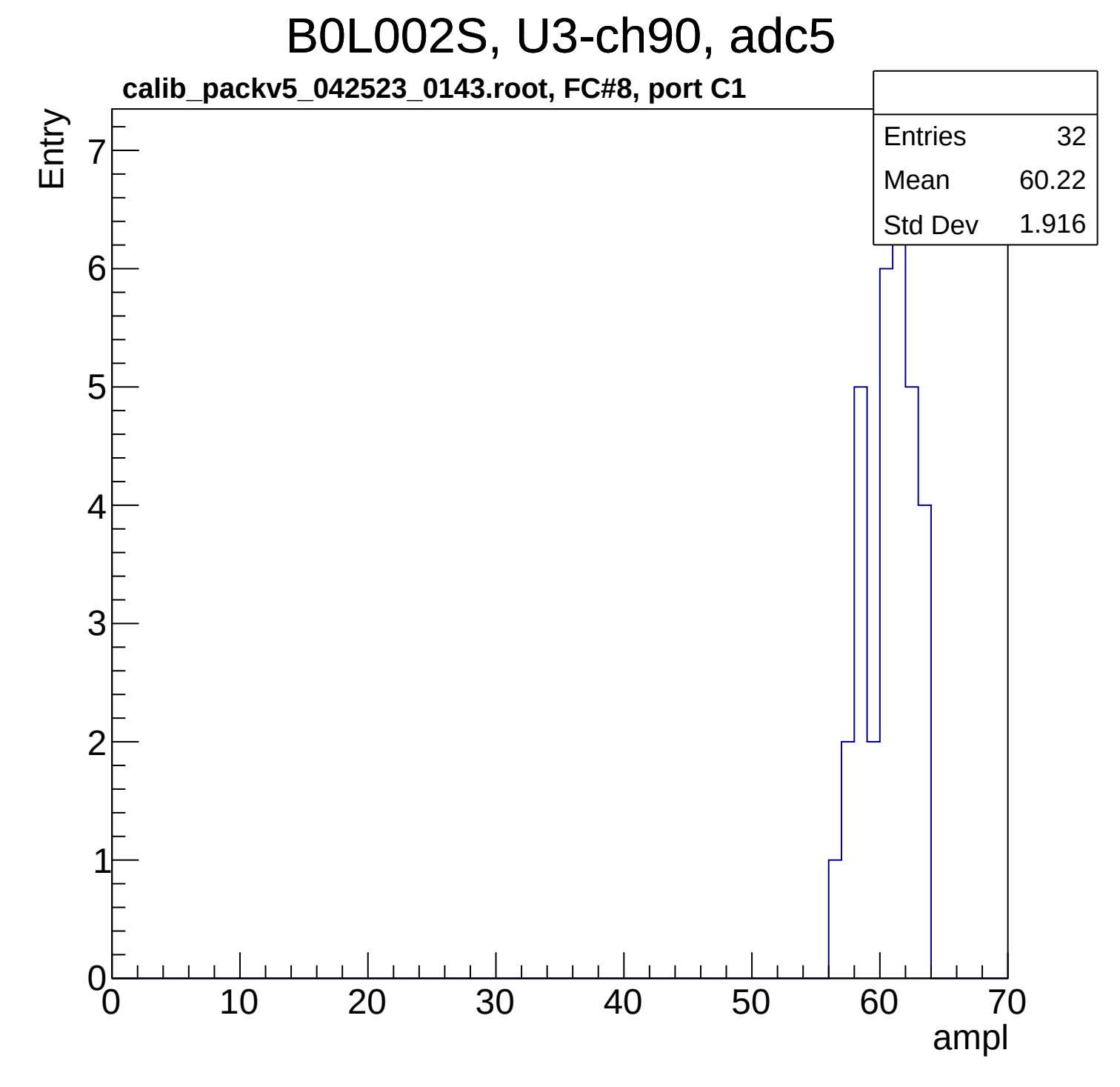

B0L002S, U3-ch90, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.08
Std Dev 6.284
Gaus mean : 31.7222
Gaus Width: 3.9404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

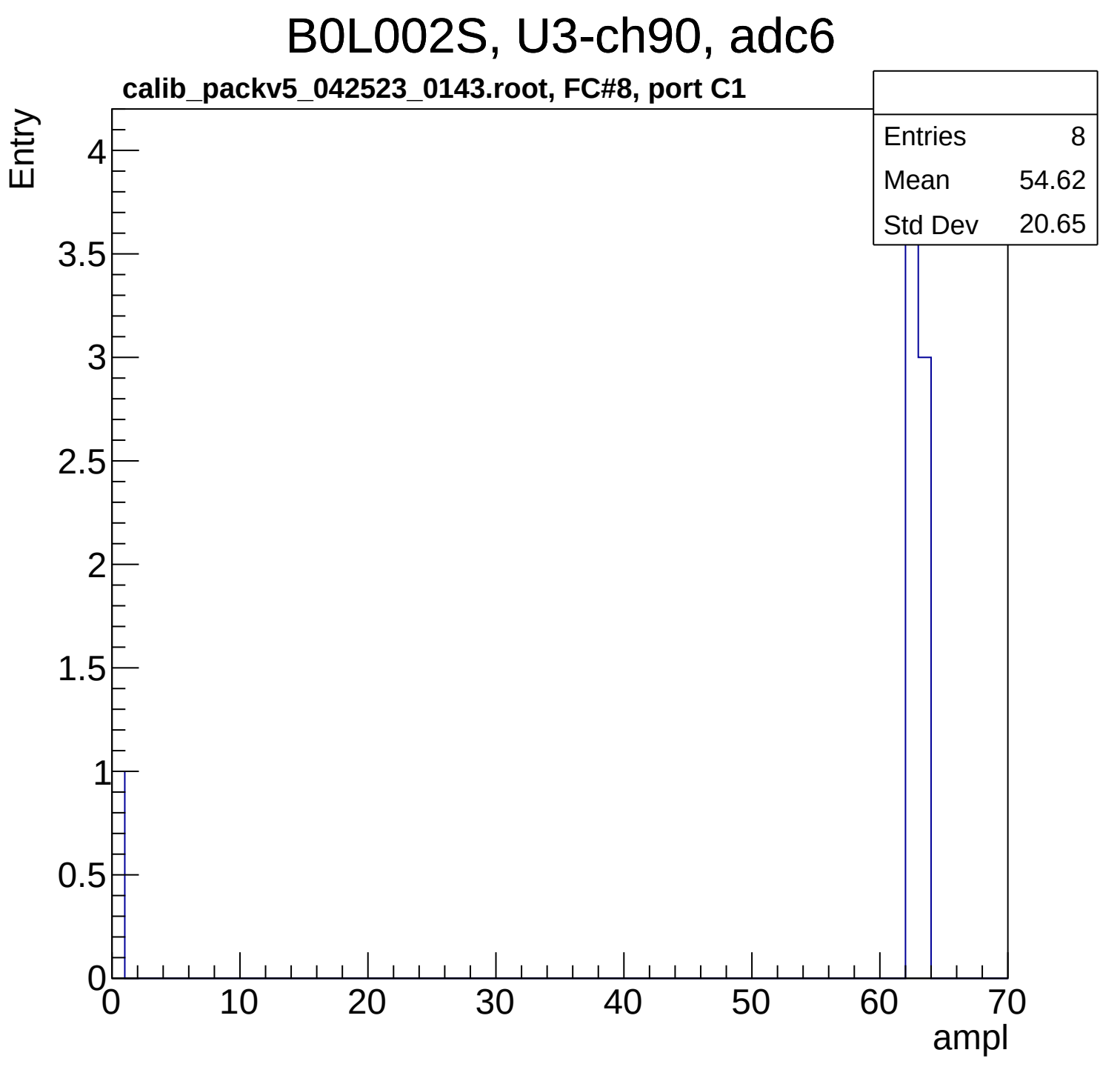

B0L002S, U3-ch90, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.6
Std Dev 2.882
Gaus mean : 38.2032
Gaus Width: 2.7715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

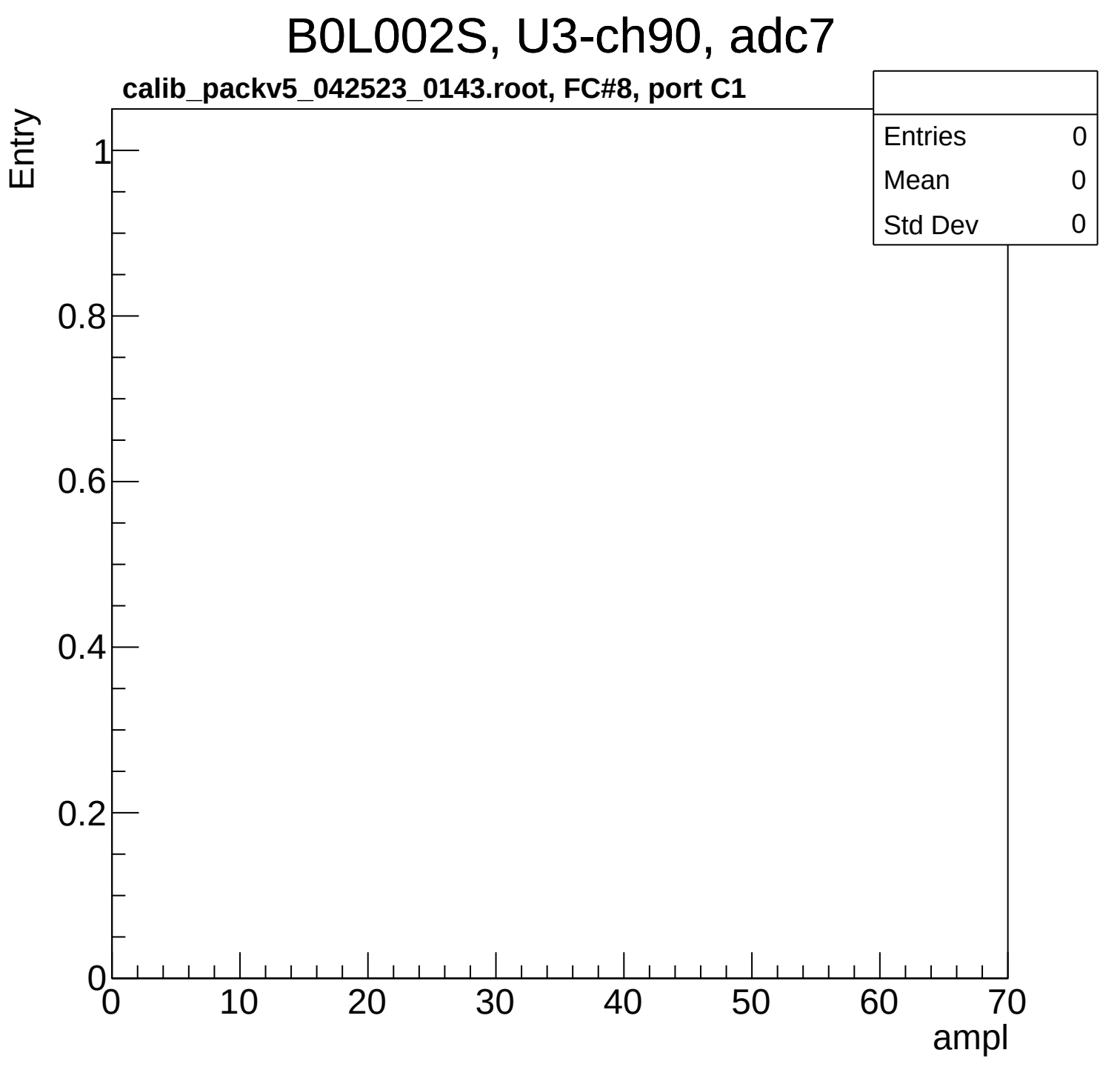

B0L002S, U3-ch90, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 43.23
Std Dev 2.997
Gaus mean : 44.0497
Gaus Width: 2.6909
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

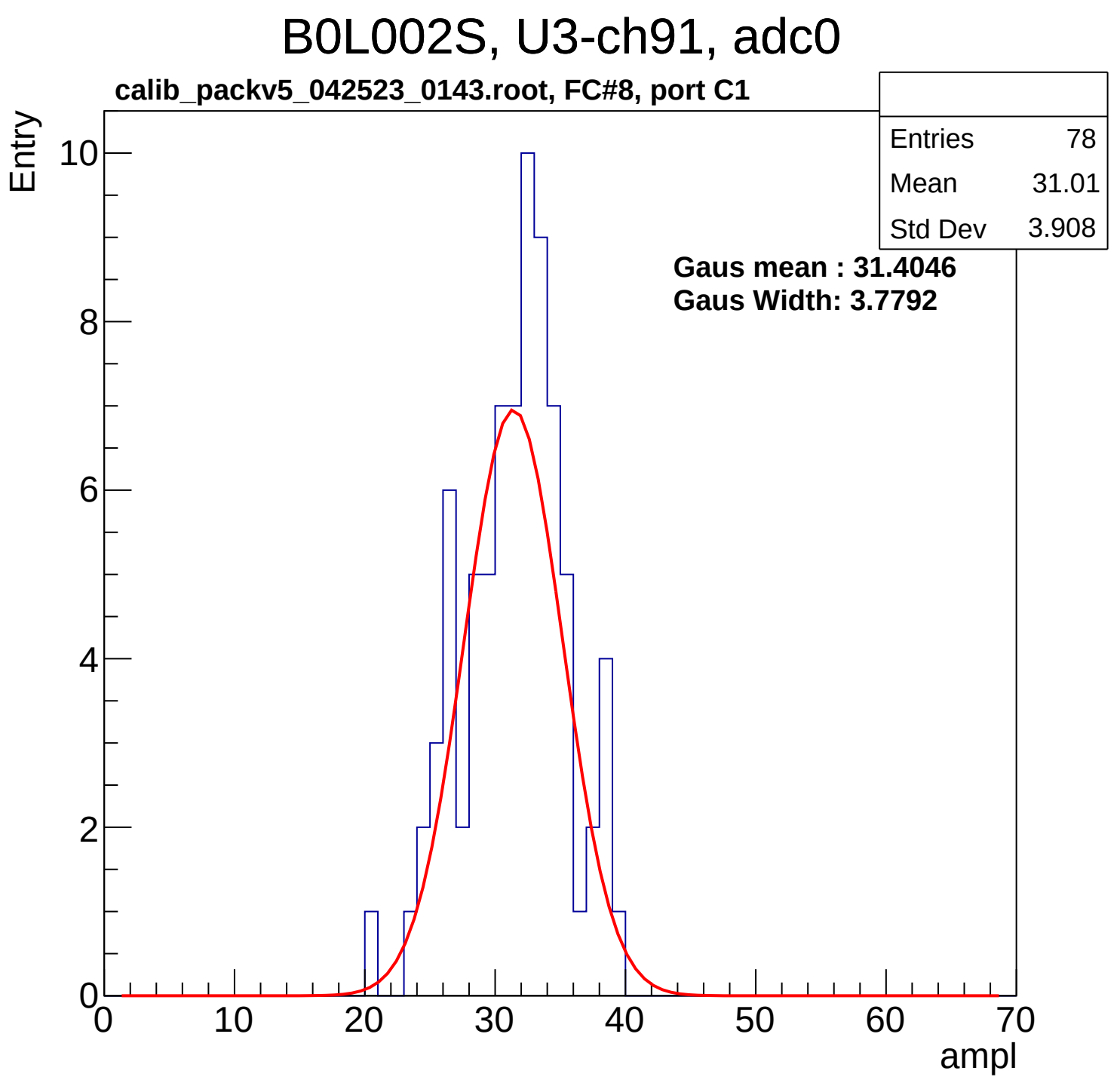

B0L002S, U3-ch90, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.05
Std Dev 3.144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

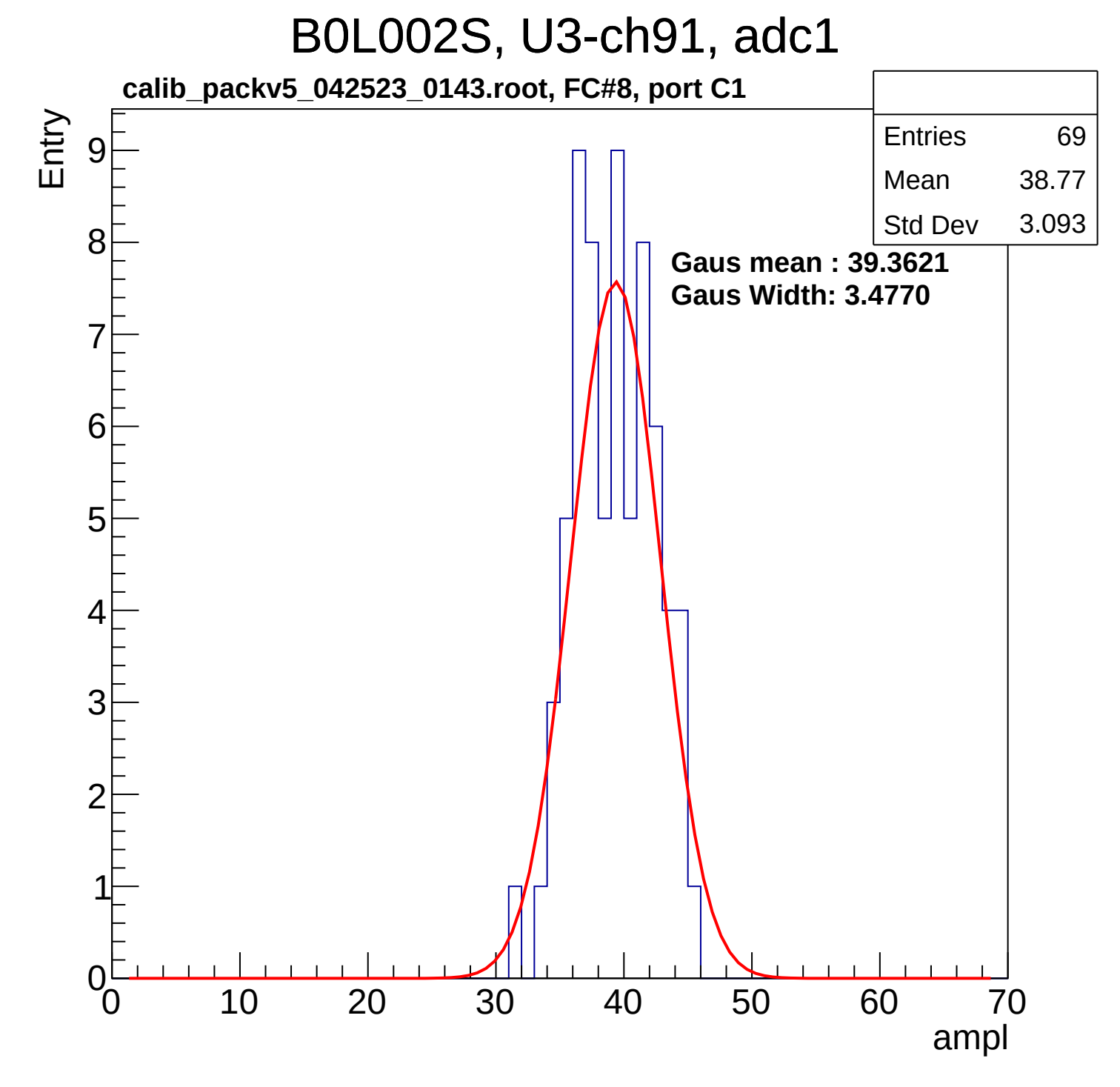

B0L002S, U3-ch90, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.49
Std Dev 3.405
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

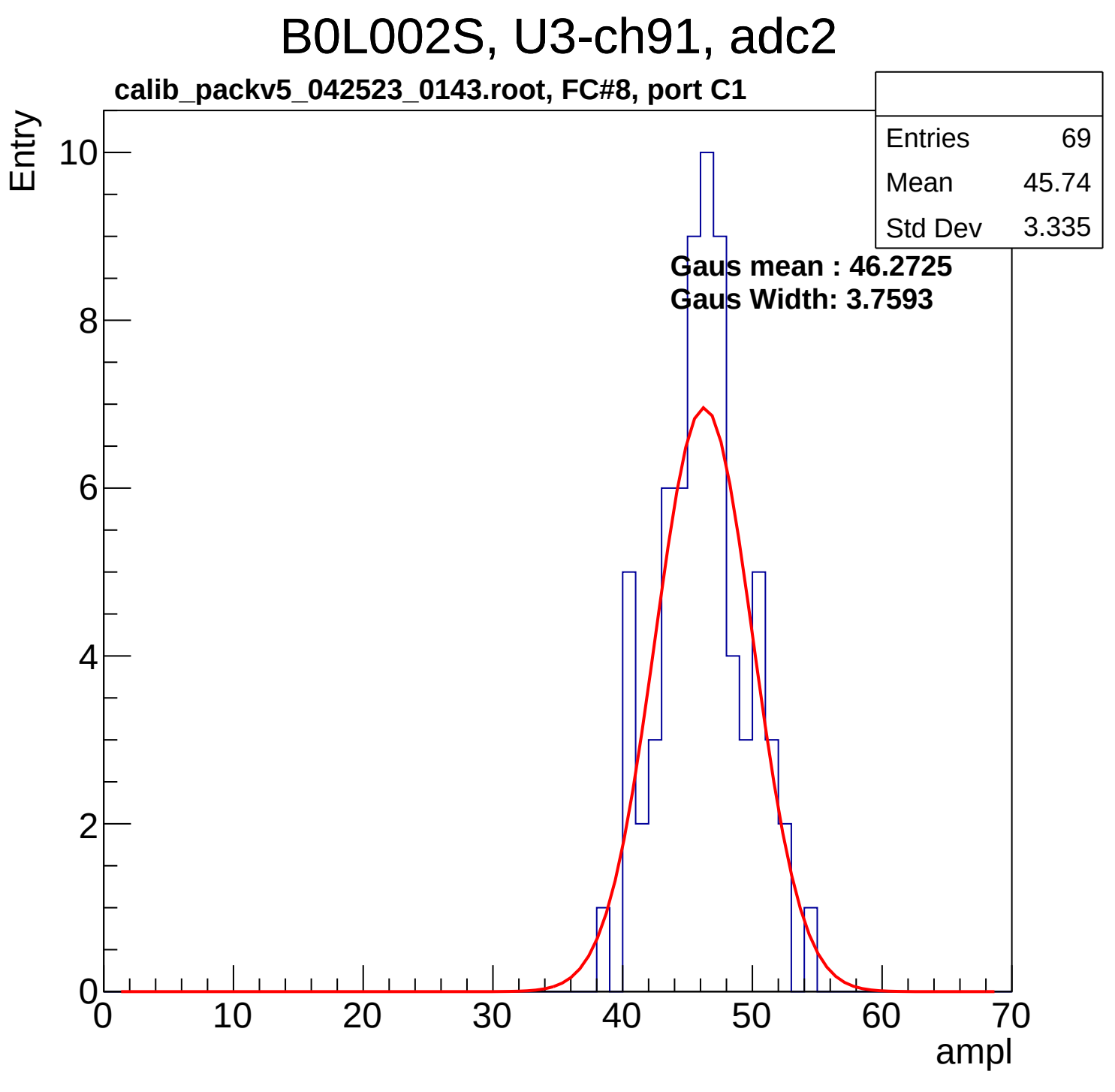

B0L002S, U3-ch90, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 60.22
Std Dev 1.916
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

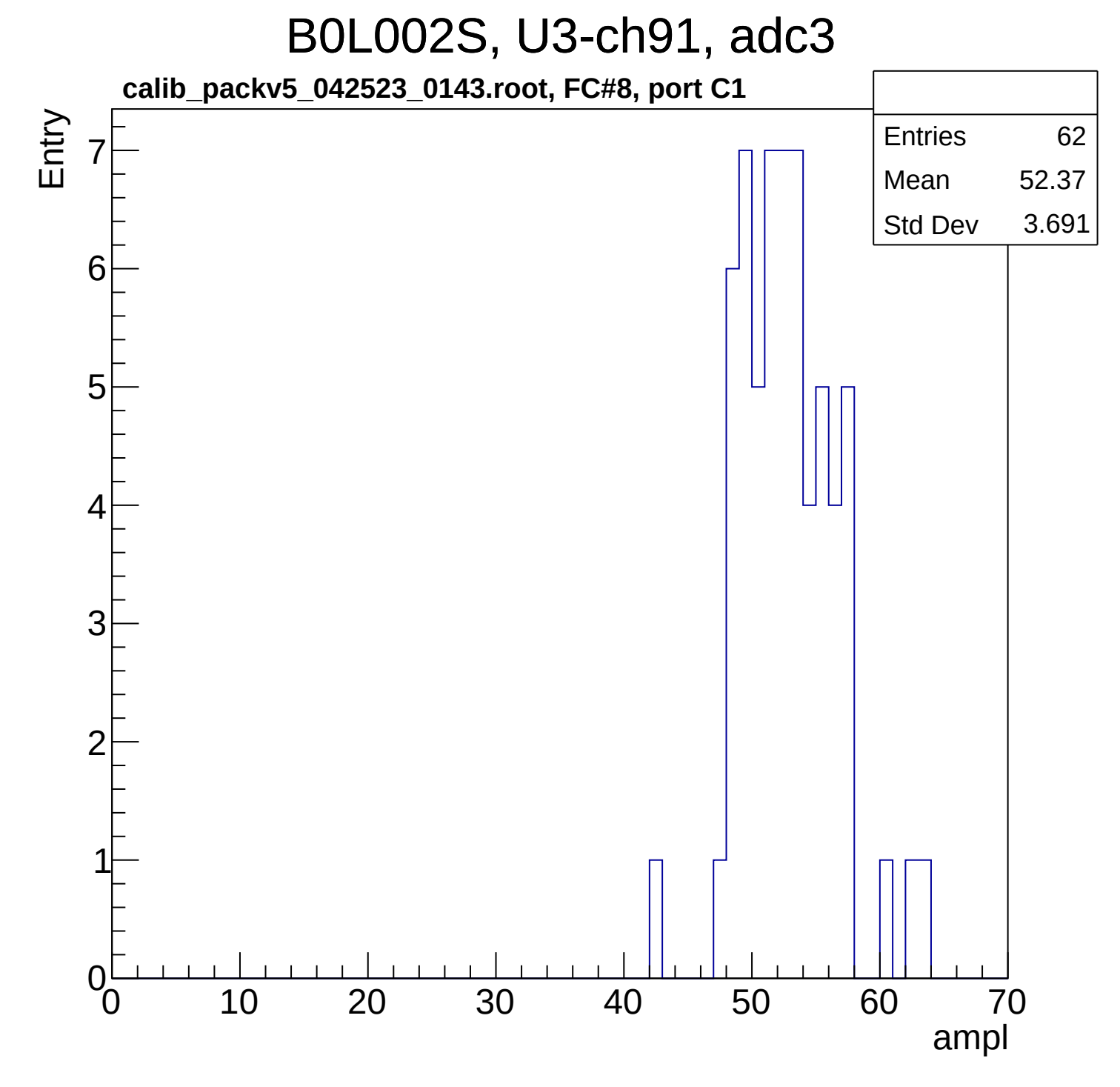

B0L002S, U3-ch90, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 54.62
Std Dev 20.65
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

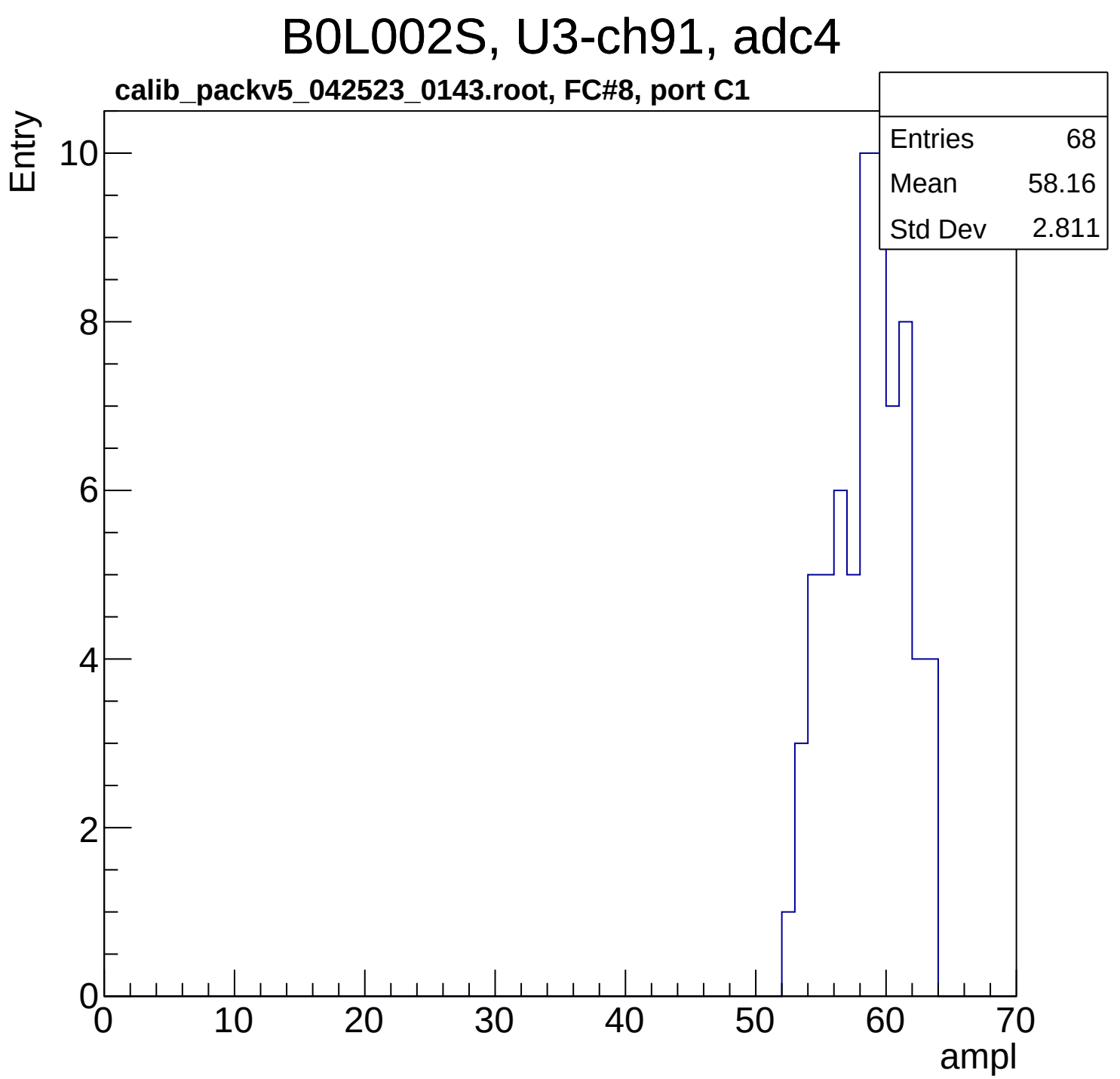

B0L002S, U3-ch90, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

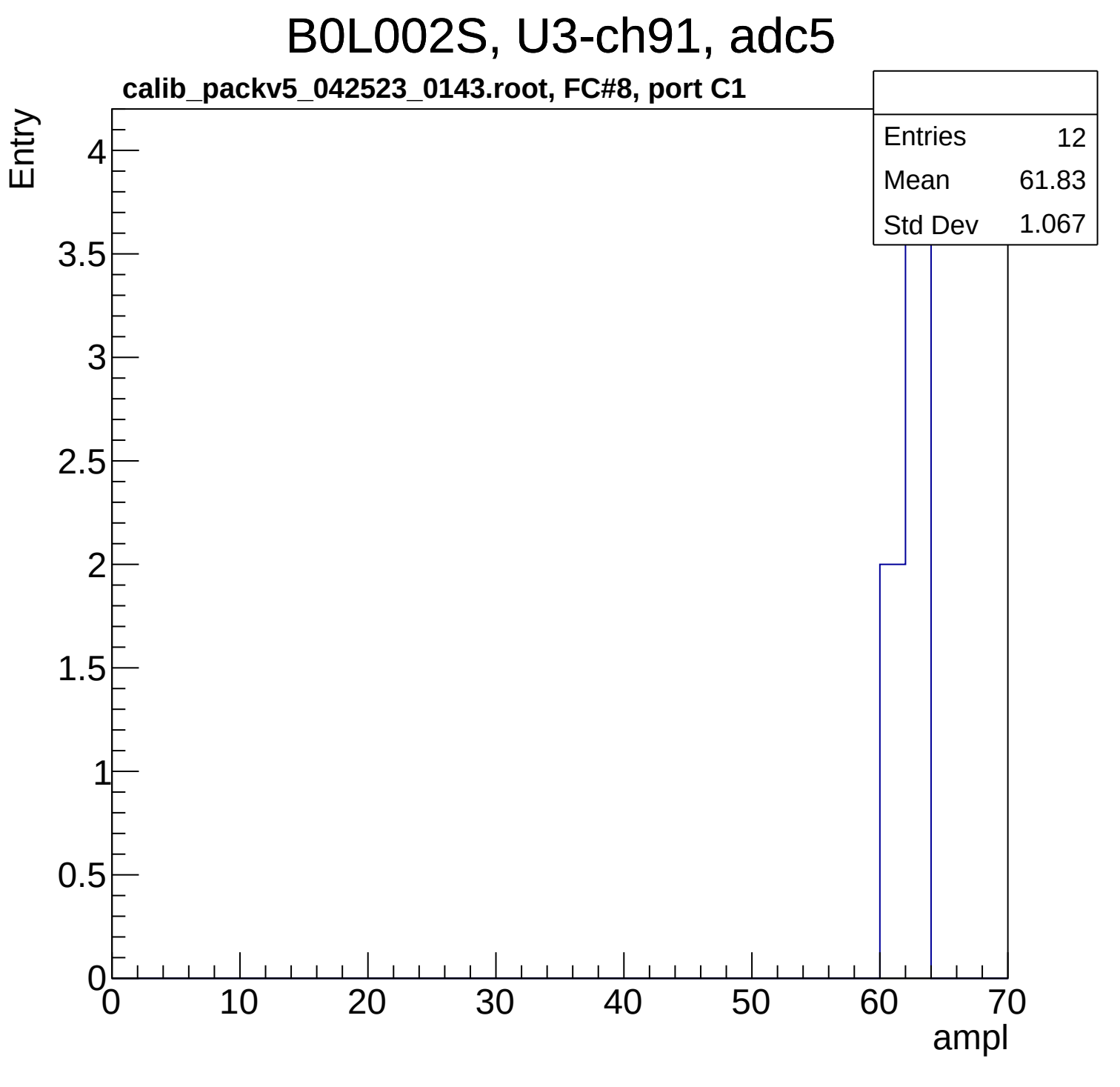

B0L002S, U3-ch91, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 31.01
Std Dev 3.908
Gaus mean : 31.4046
Gaus Width: 3.7792
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

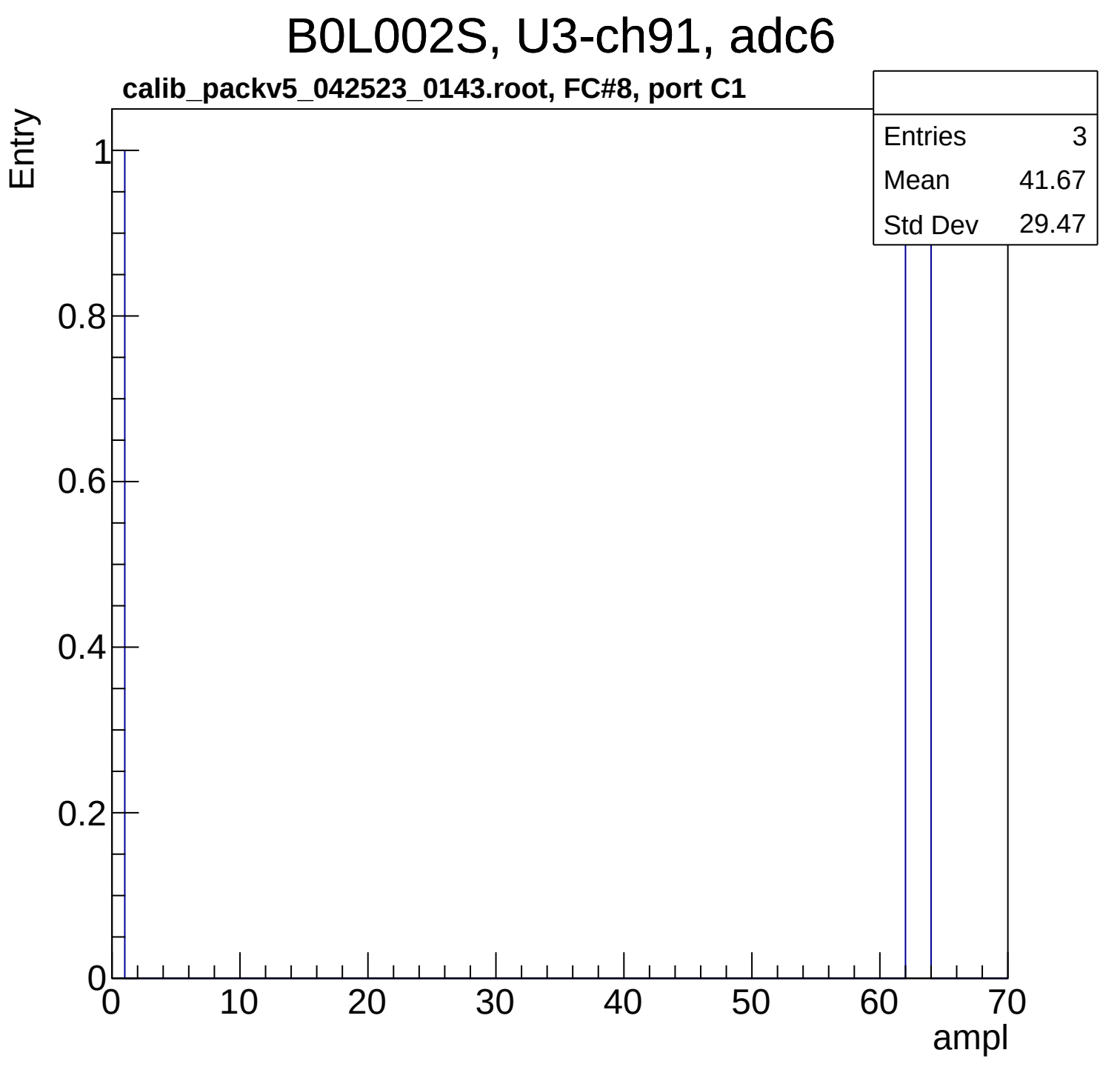

B0L002S, U3-ch91, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 38.77
Std Dev 3.093
Gaus mean : 39.3621
Gaus Width: 3.4770
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

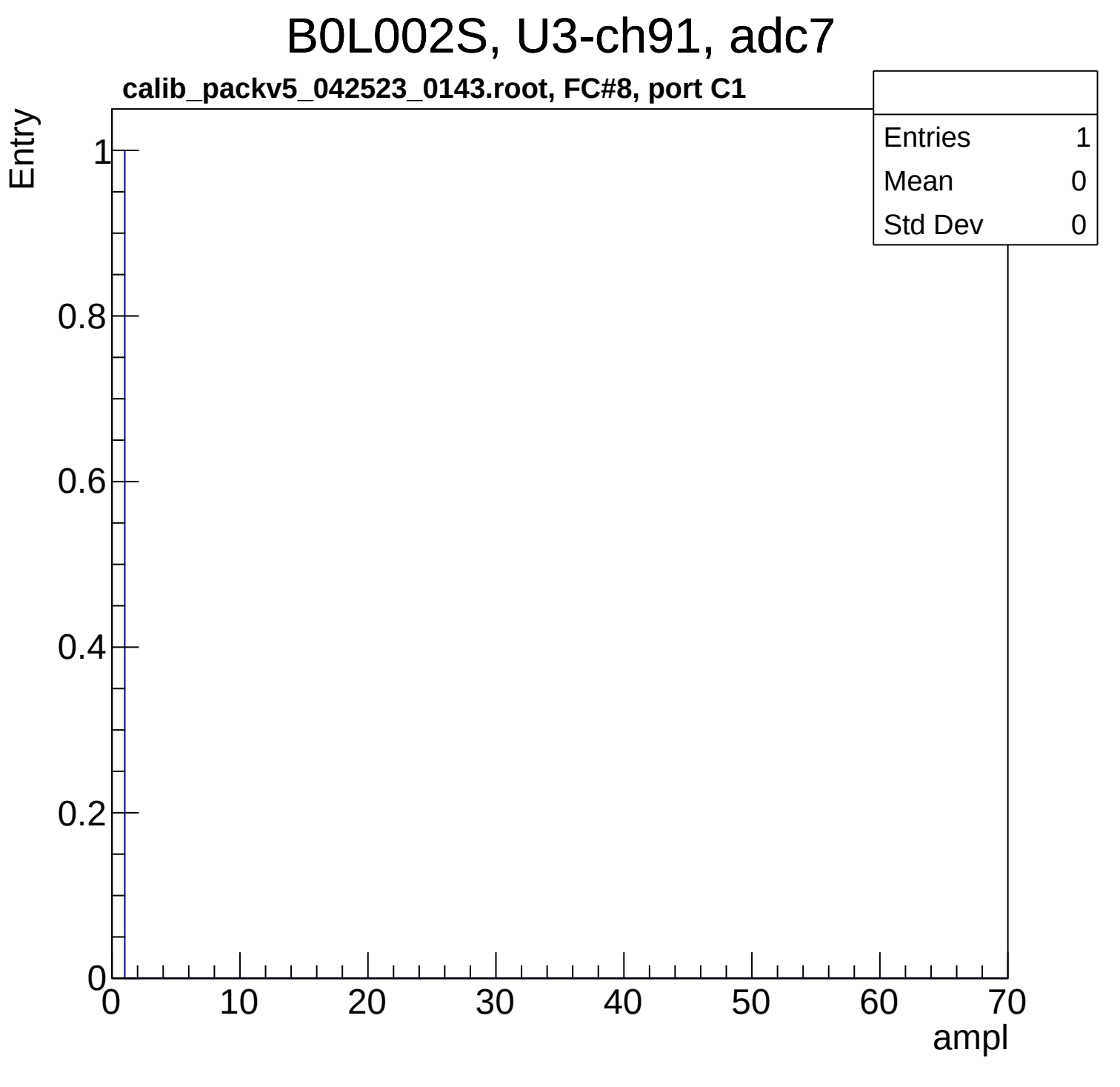

B0L002S, U3-ch91, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 45.74
Std Dev 3.335
Gaus mean : 46.2725
Gaus Width: 3.7593
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch91, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 52.37
Std Dev 3.691
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch91, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 58.16
Std Dev 2.811
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch91, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 12
Mean 61.83
Std Dev 1.067
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U3-ch91, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch91, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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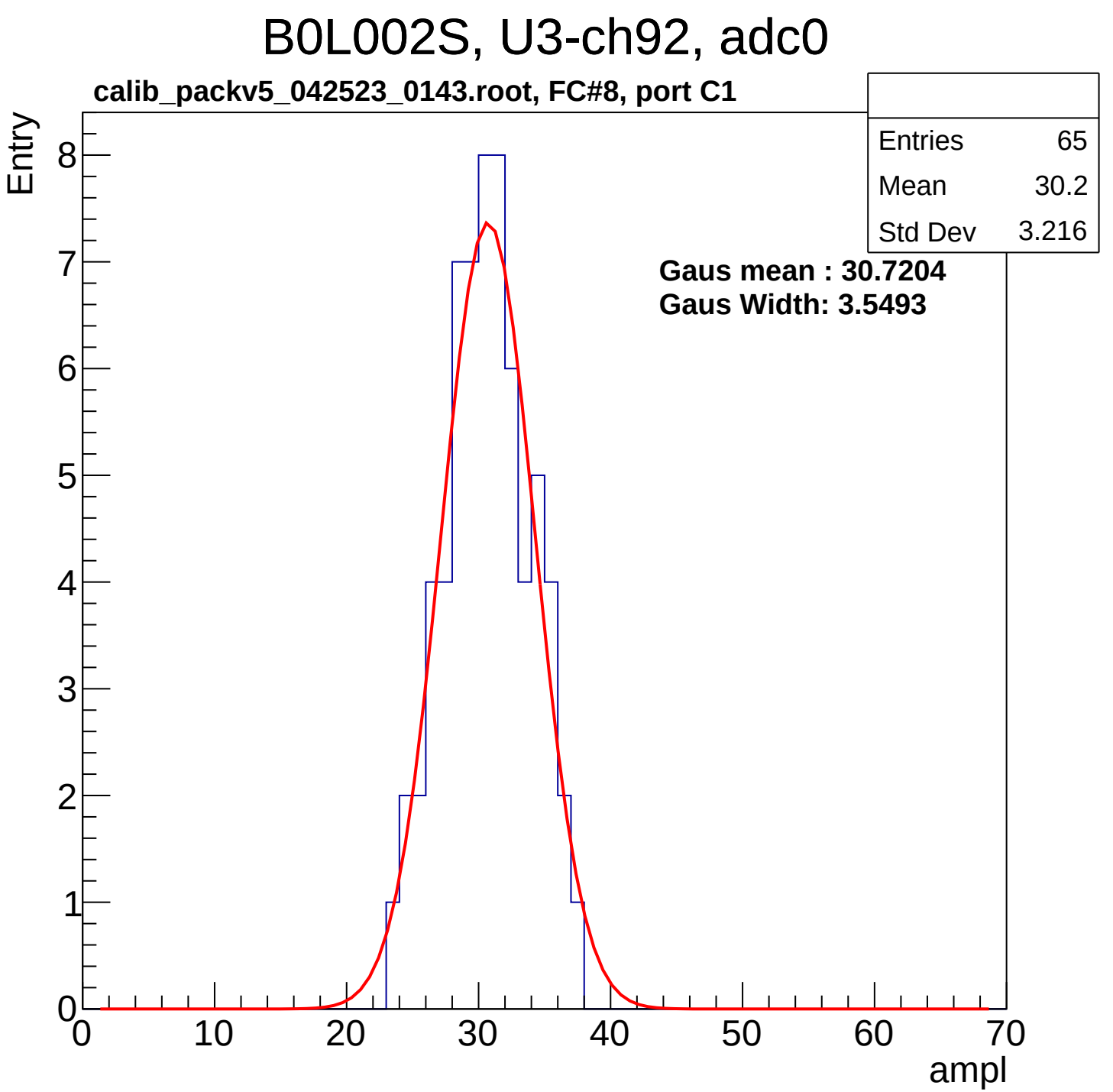
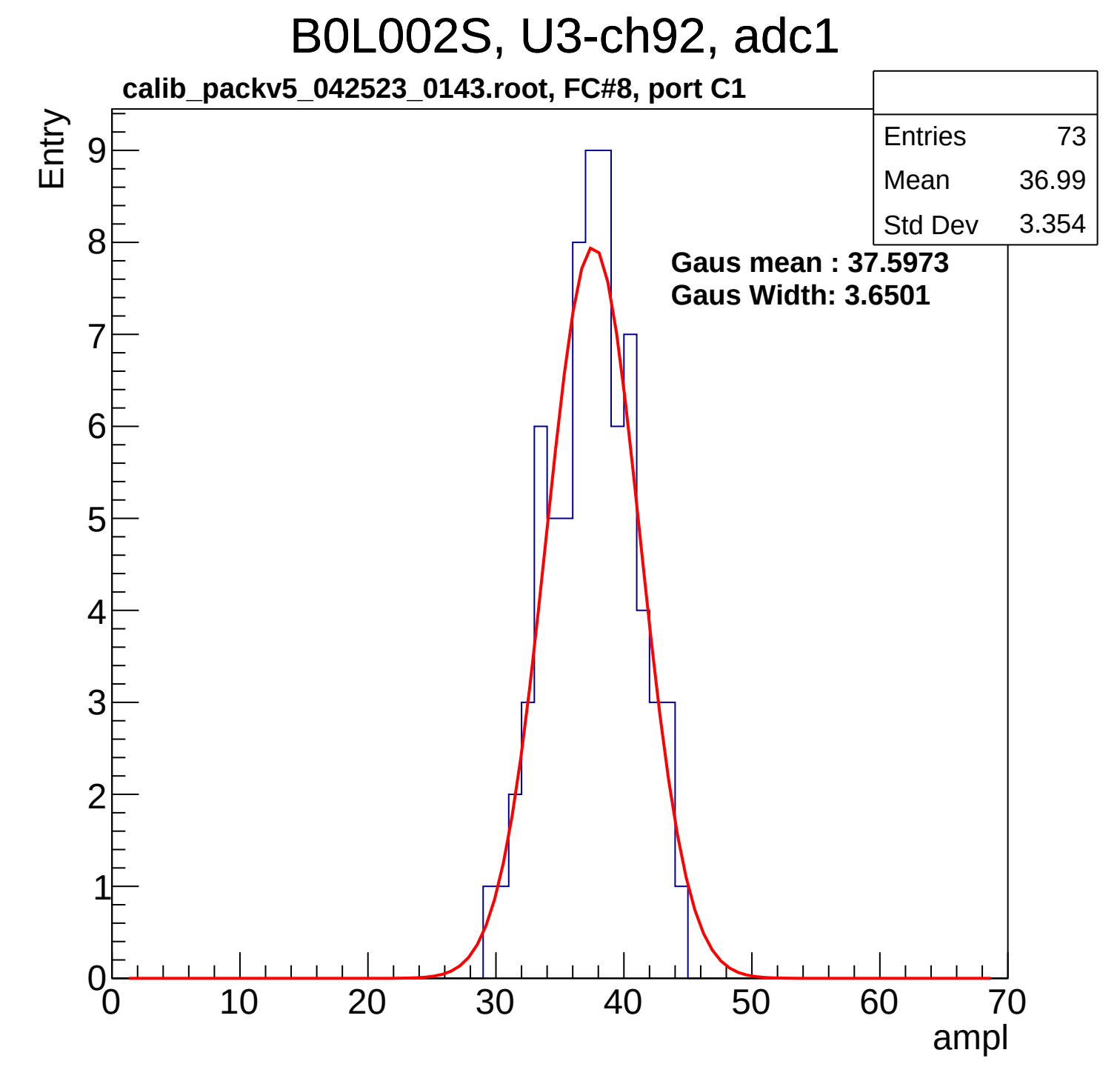
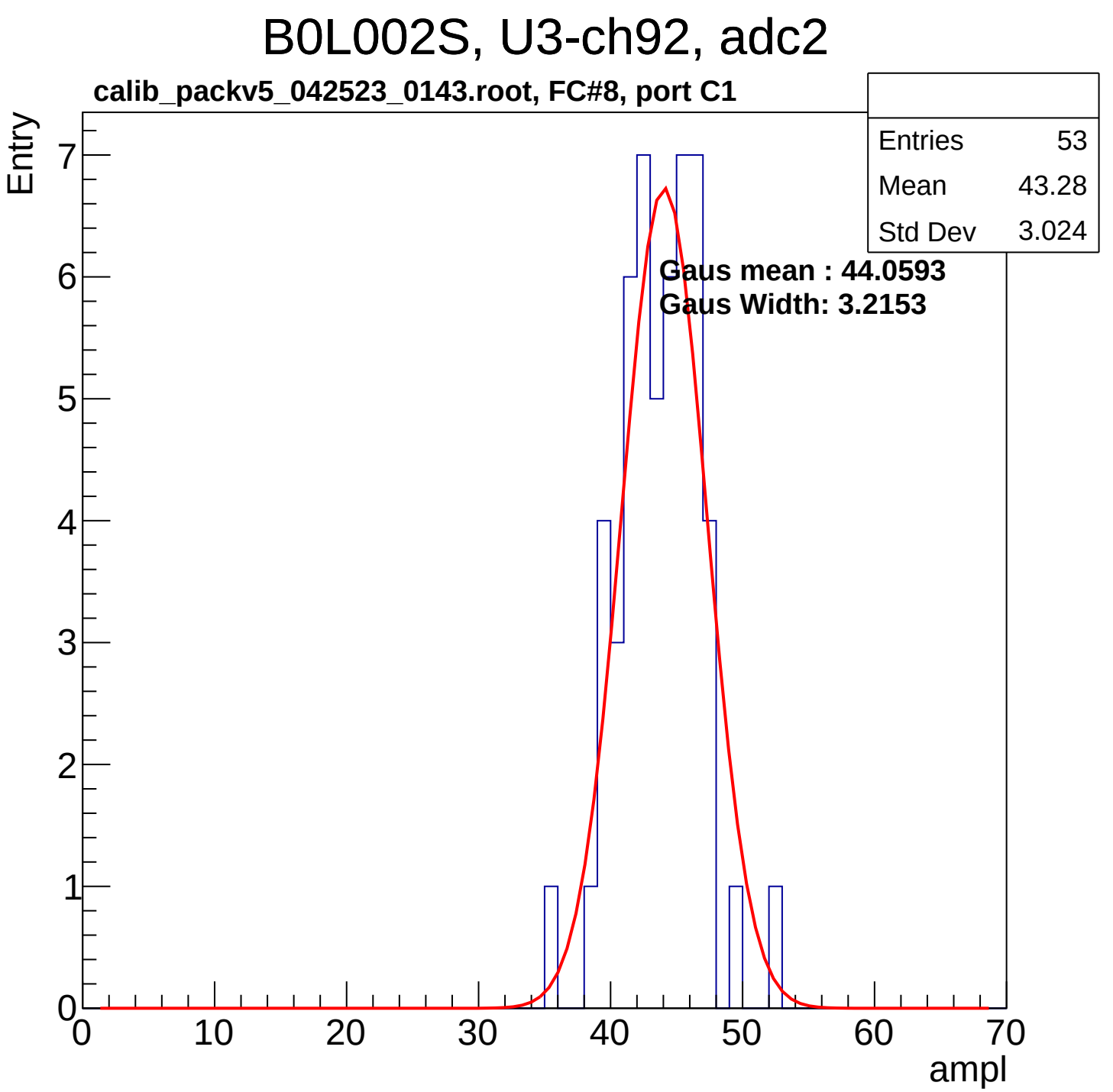
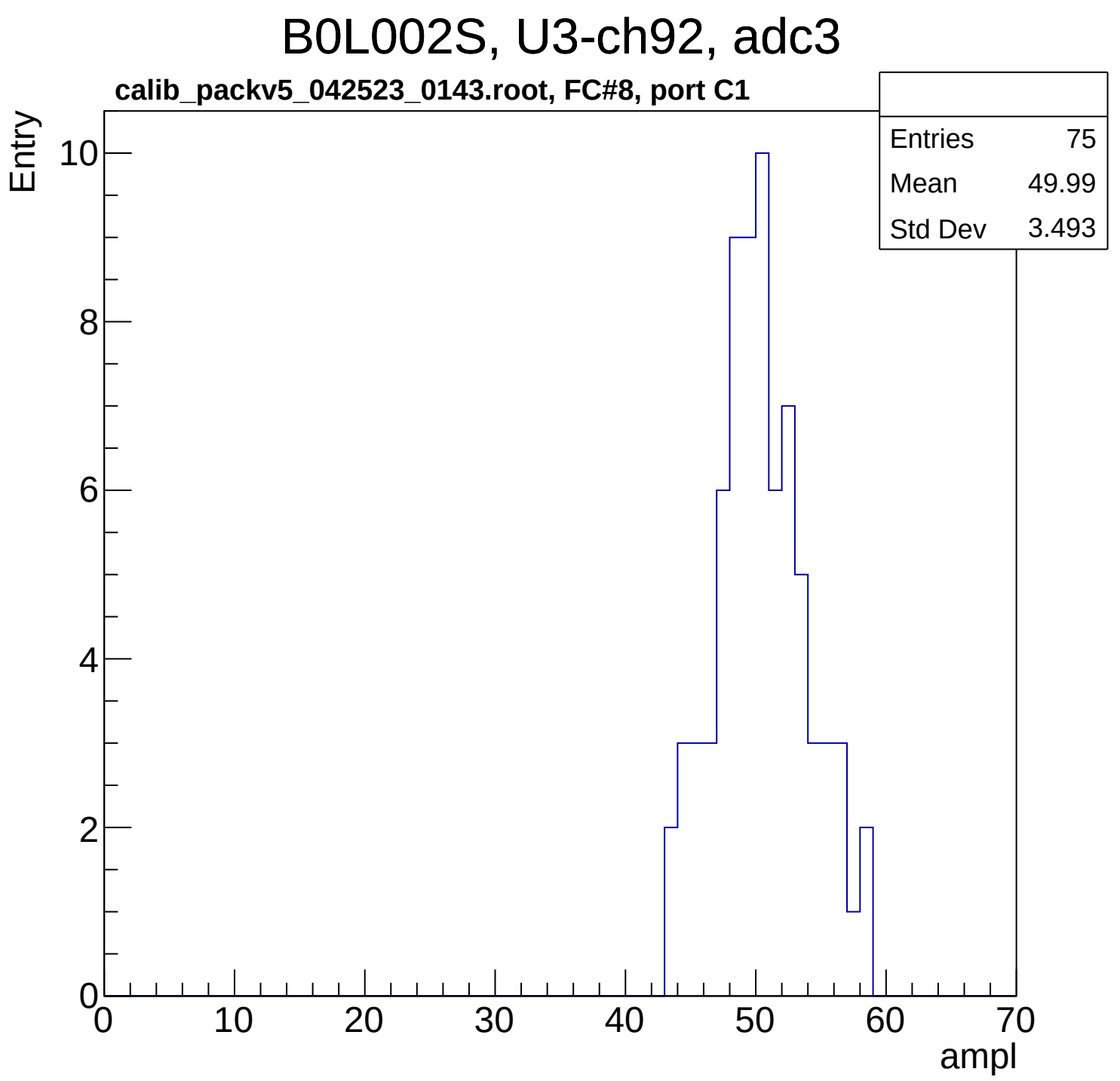
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch92, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.2
Std Dev 3.216
Gaus mean : 30.7204
Gaus Width: 3.5493
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch92, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 36.99
Std Dev 3.354
Gaus mean : 37.5973
Gaus Width: 3.6501
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch92, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 43.28
Std Dev 3.024
Gaus mean : 44.0593
Gaus Width: 3.2153
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch92, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 49.99
Std Dev 3.493
Entry
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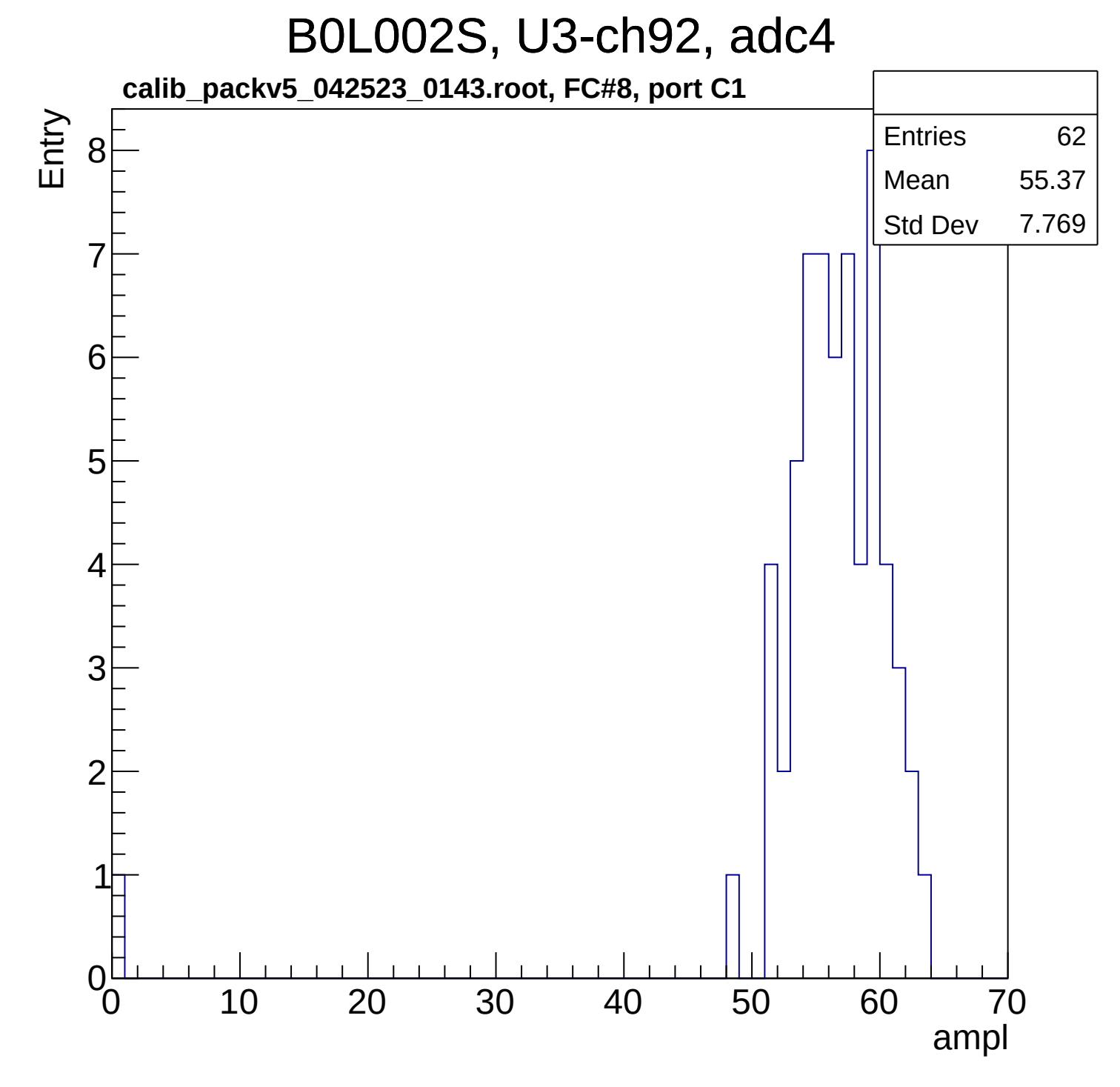
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch92, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 55.37
Std Dev 7.769
Entry
ampl
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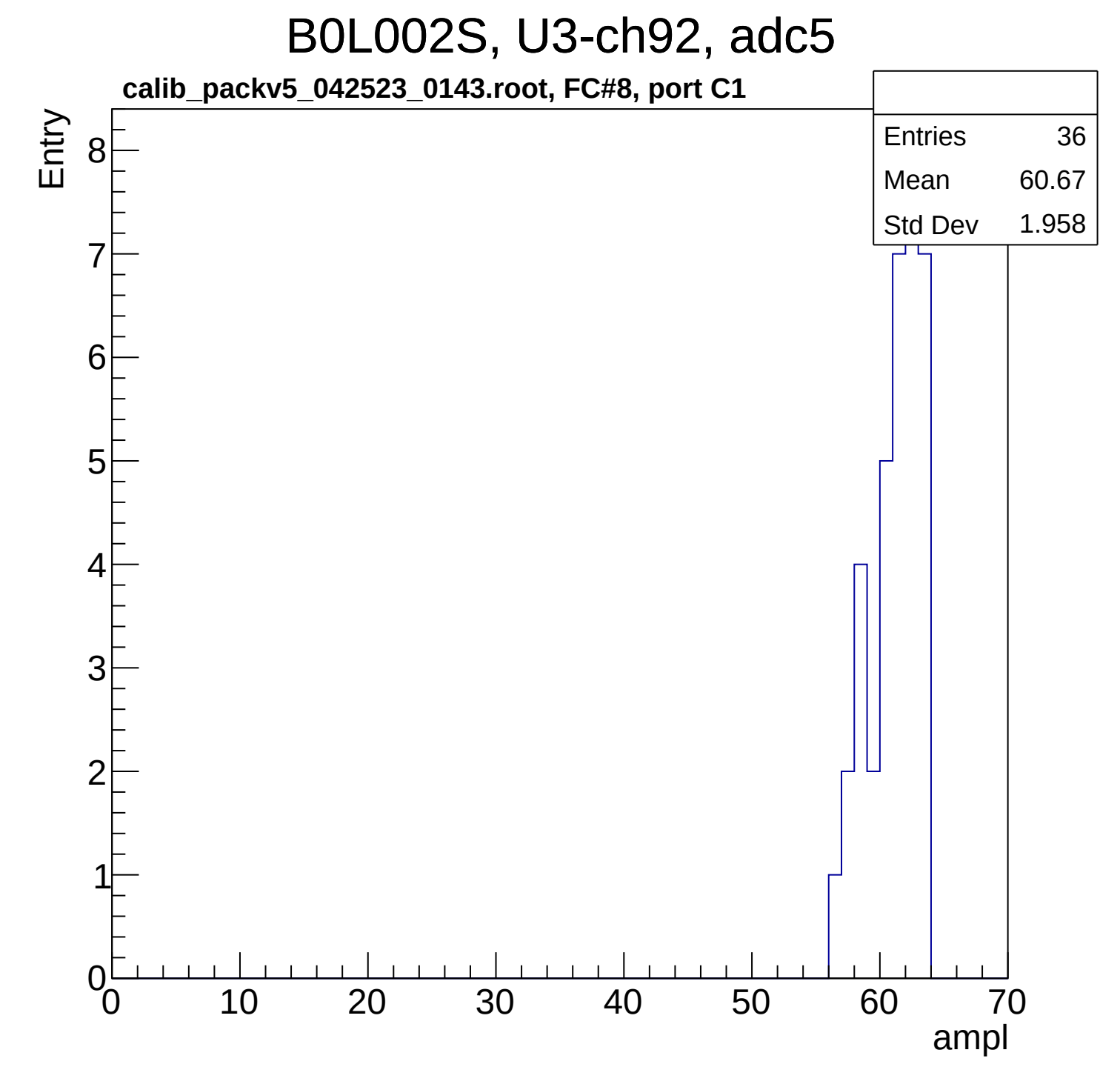
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch92, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 60.67
Std Dev 1.958
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
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60
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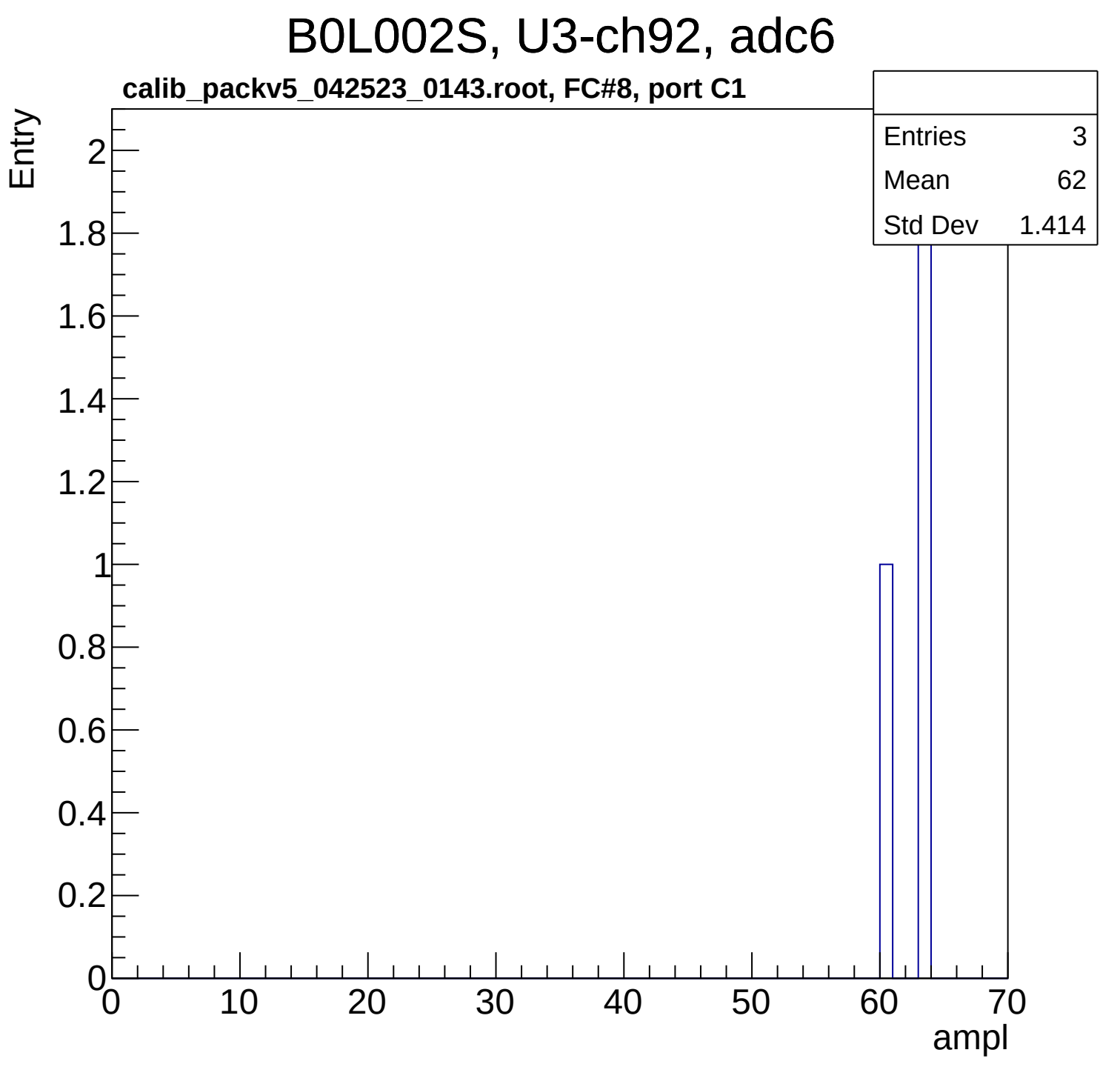
B0L002S, U3-ch92, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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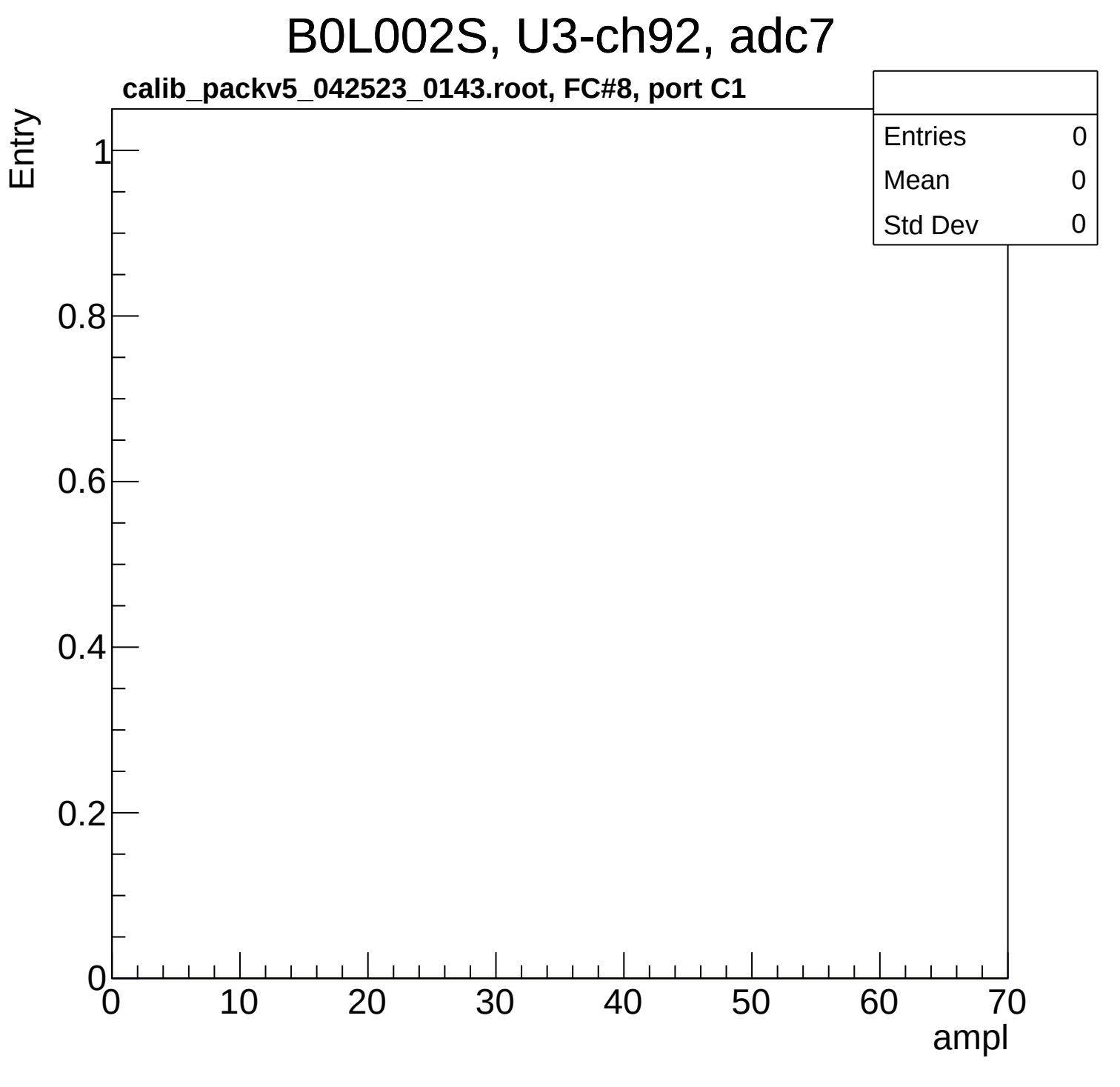
B0L002S, U3-ch92, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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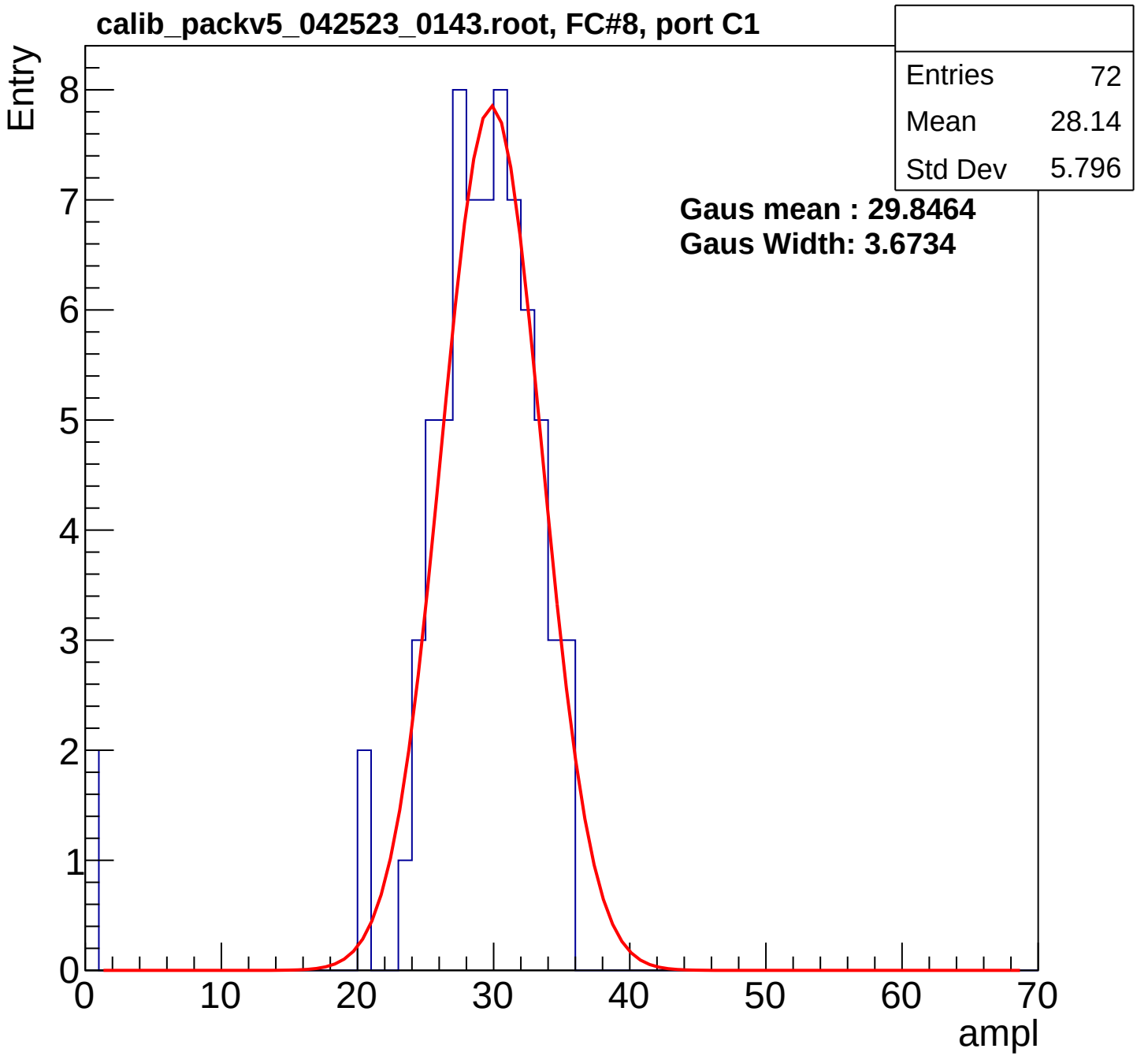
70
ampl


B0L002S, U3-ch93, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 28.14
Std Dev 5.796
Gaus mean : 29.8464
Gaus Width: 3.6734
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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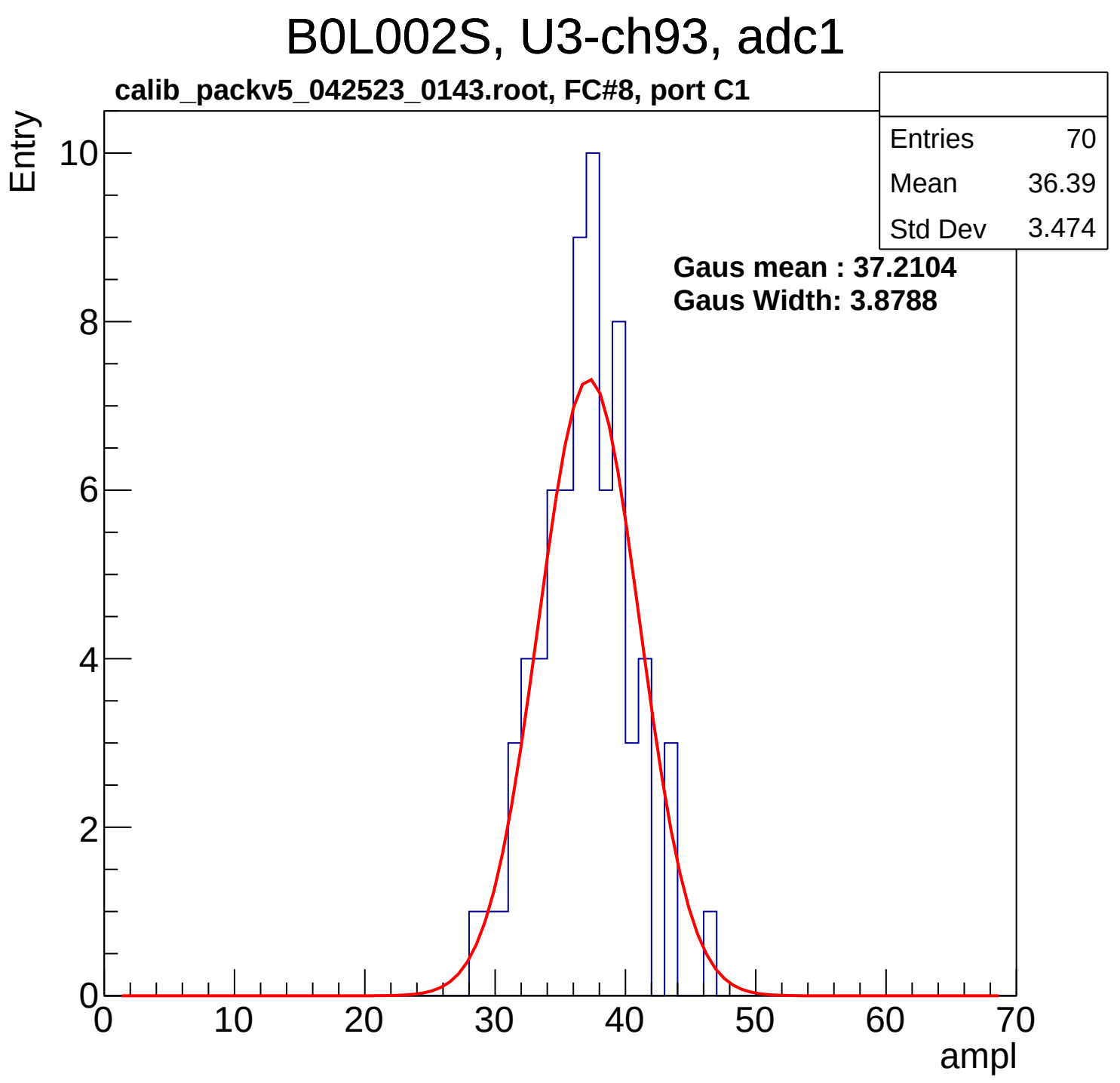
B0L002S, U3-ch93, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.39
Std Dev 3.474
Gaus mean : 37.2104
Gaus Width: 3.8788
Entry
ampl
0
2
4
6
8
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0
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40
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60
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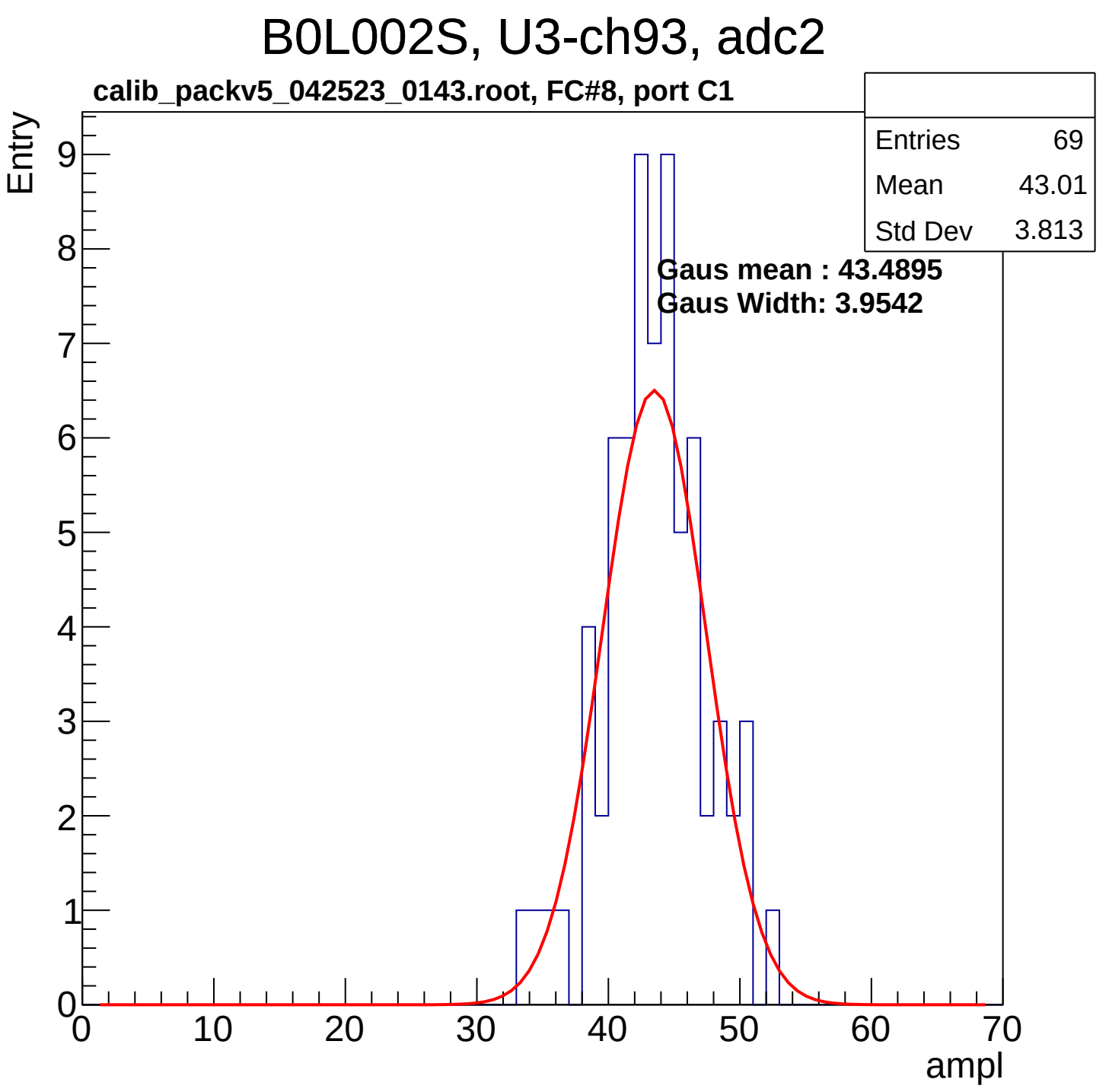
B0L002S, U3-ch93, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.01
Std Dev 3.813
Gaus mean : 43.4895
Gaus Width: 3.9542
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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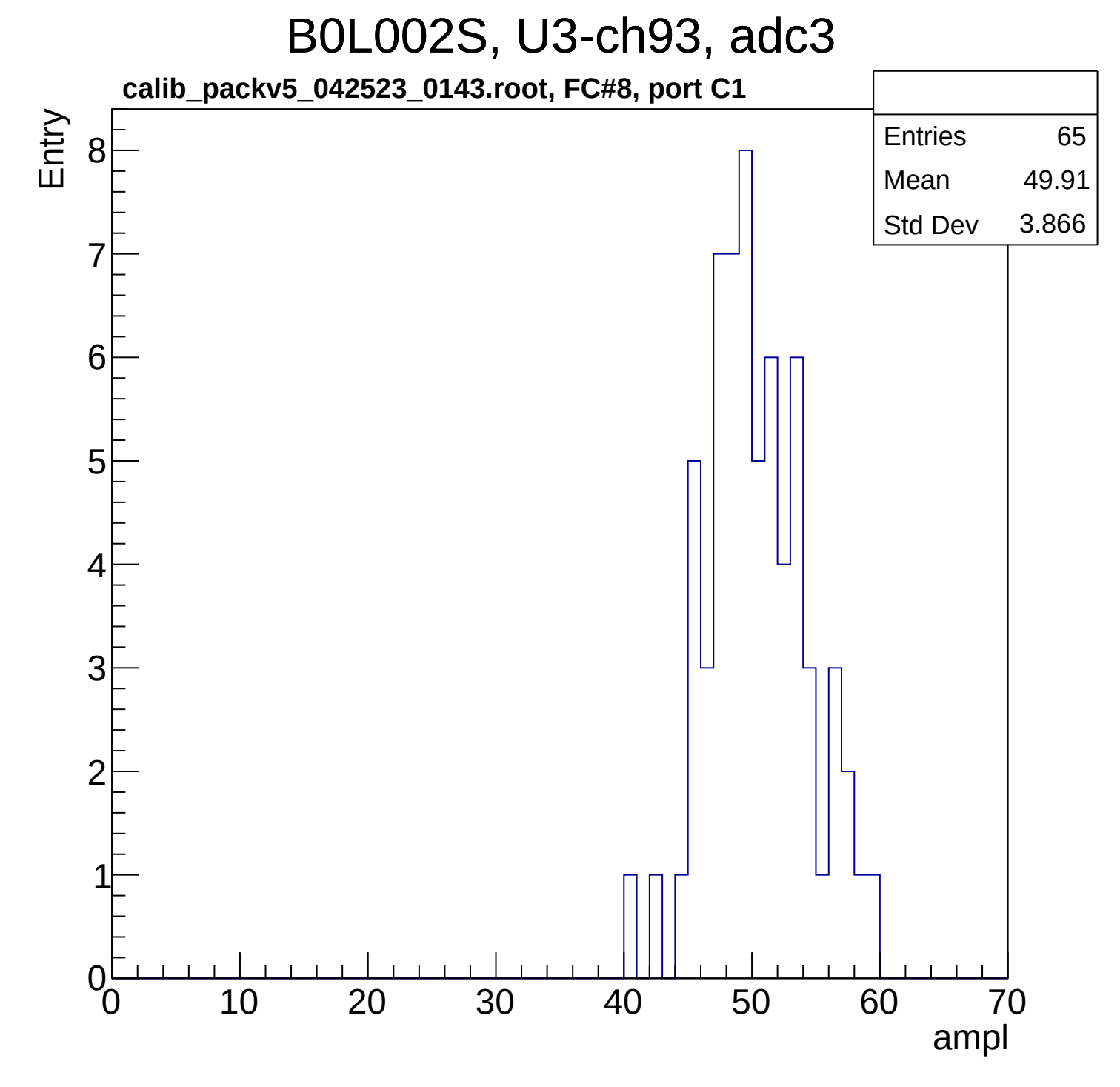
B0L002S, U3-ch93, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 49.91
Std Dev 3.866
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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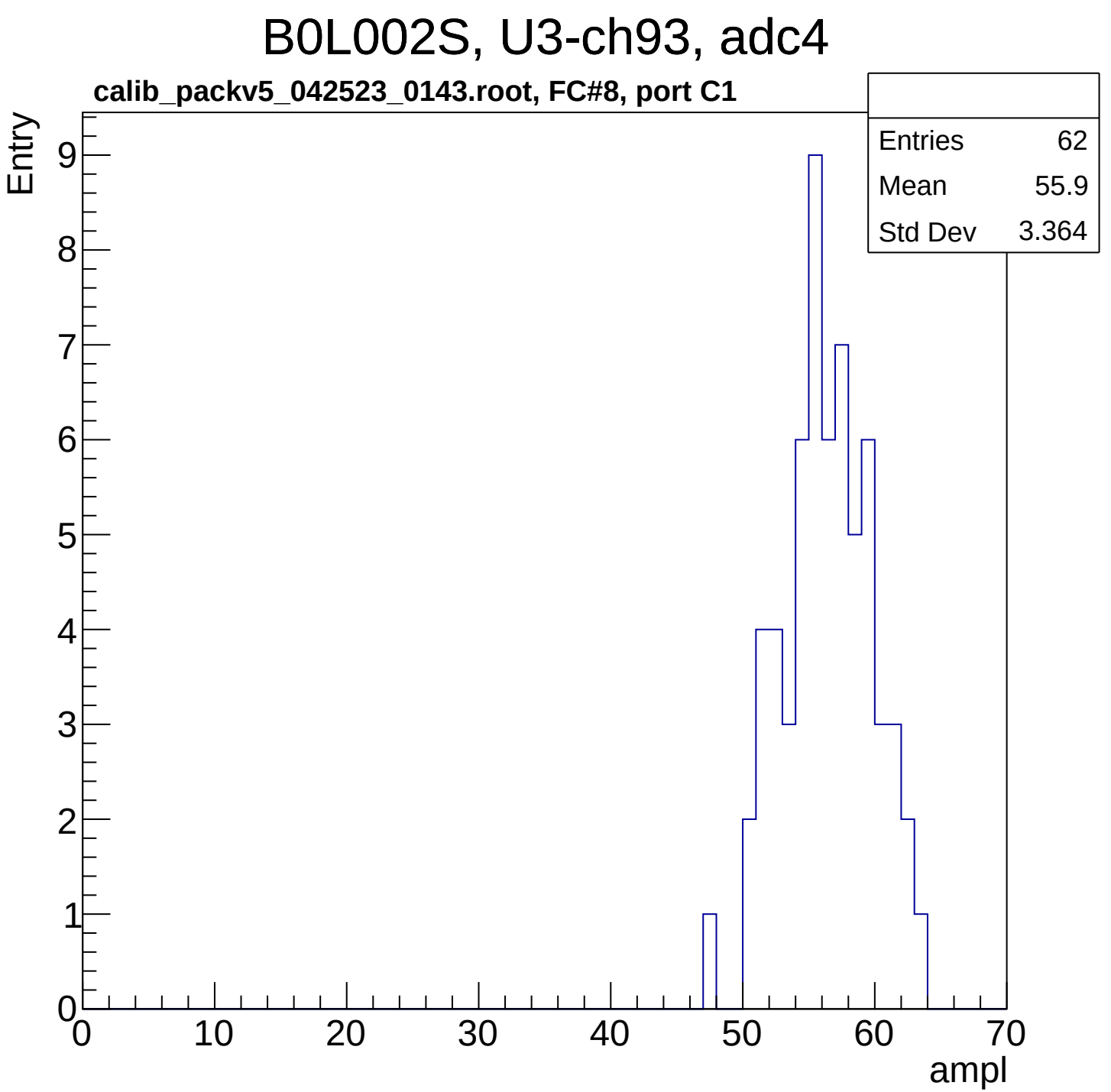
B0L002S, U3-ch93, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 55.9
Std Dev 3.364
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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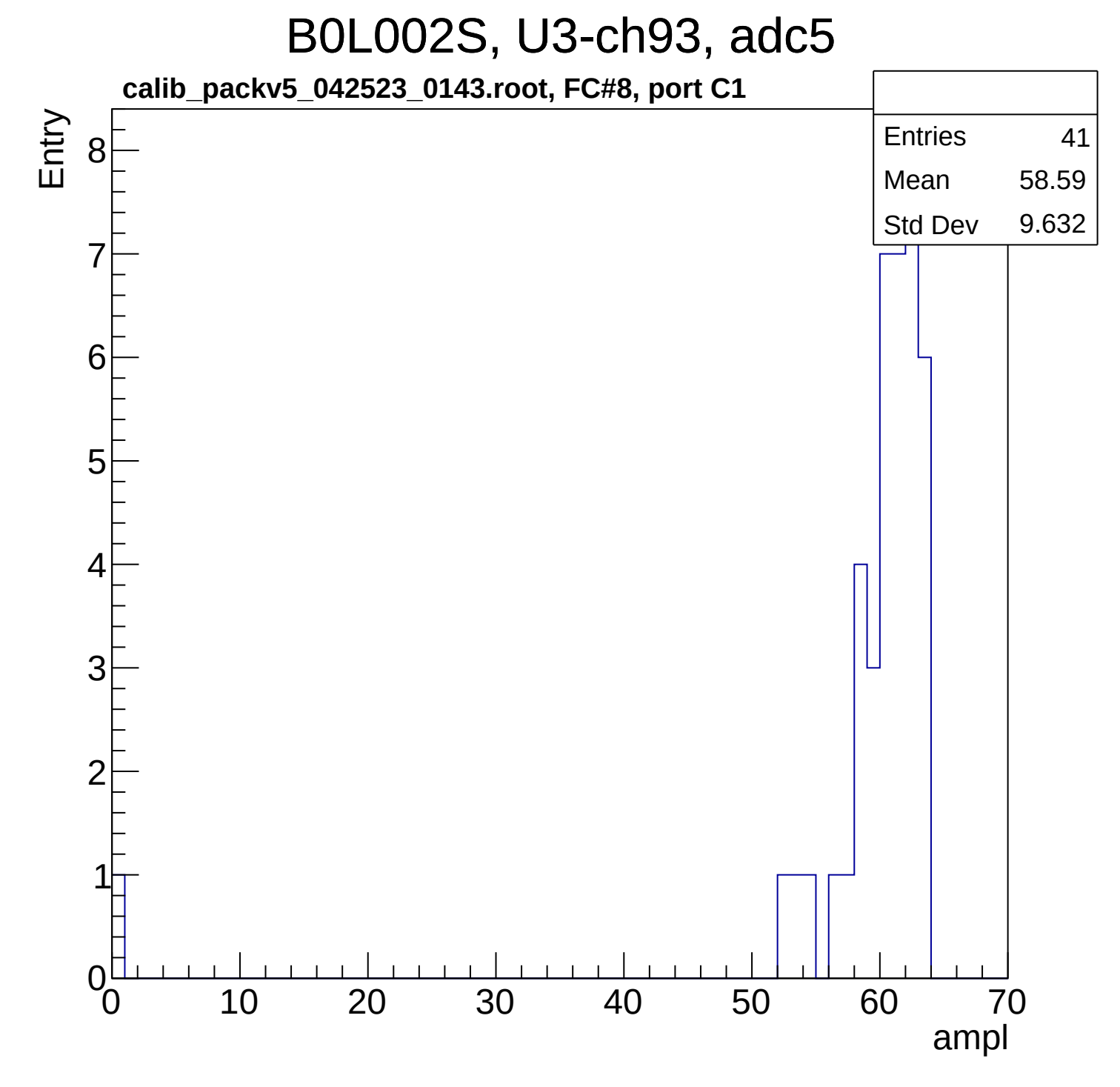
B0L002S, U3-ch93, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.59
Std Dev 9.632
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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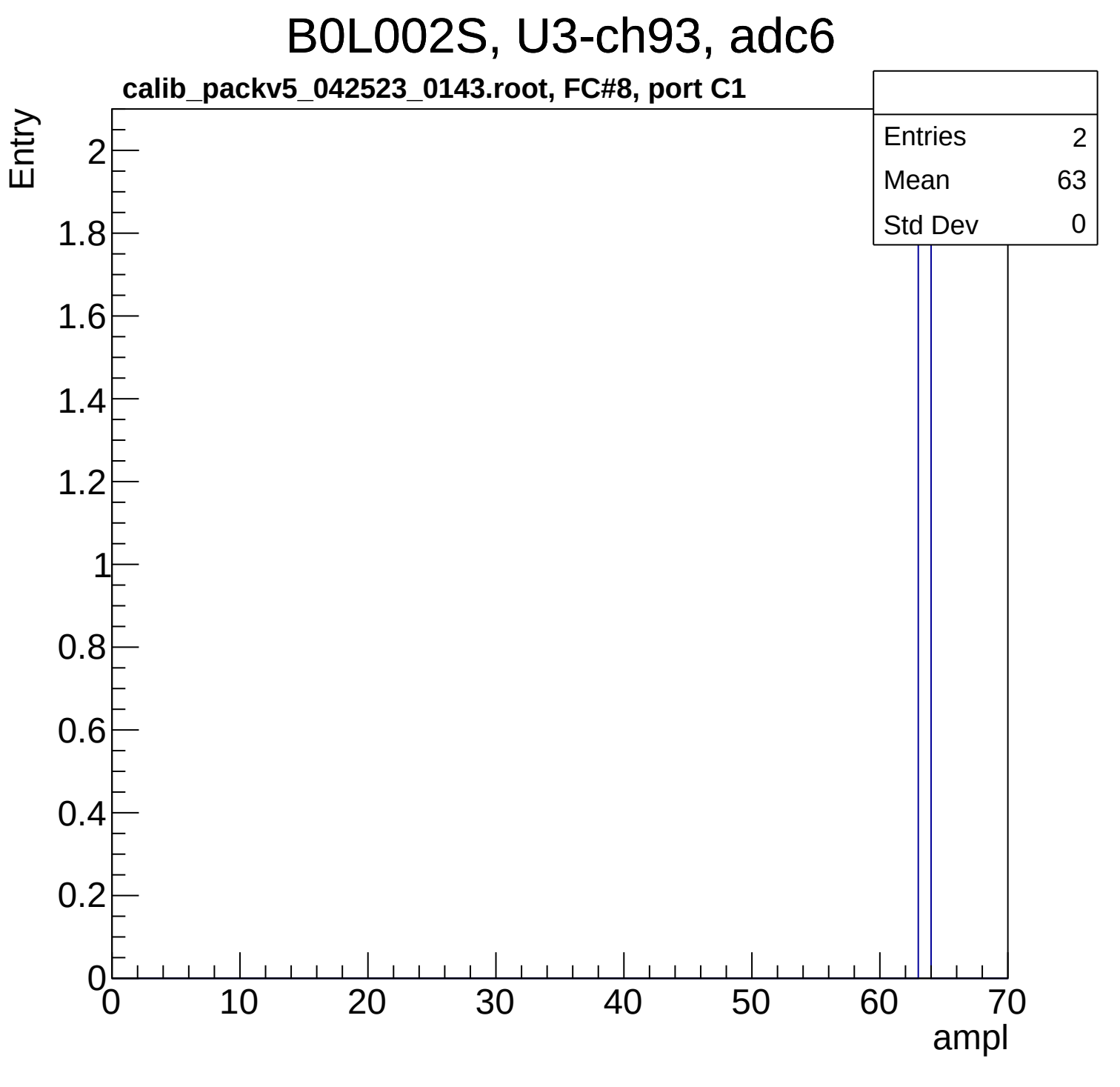
B0L002S, U3-ch93, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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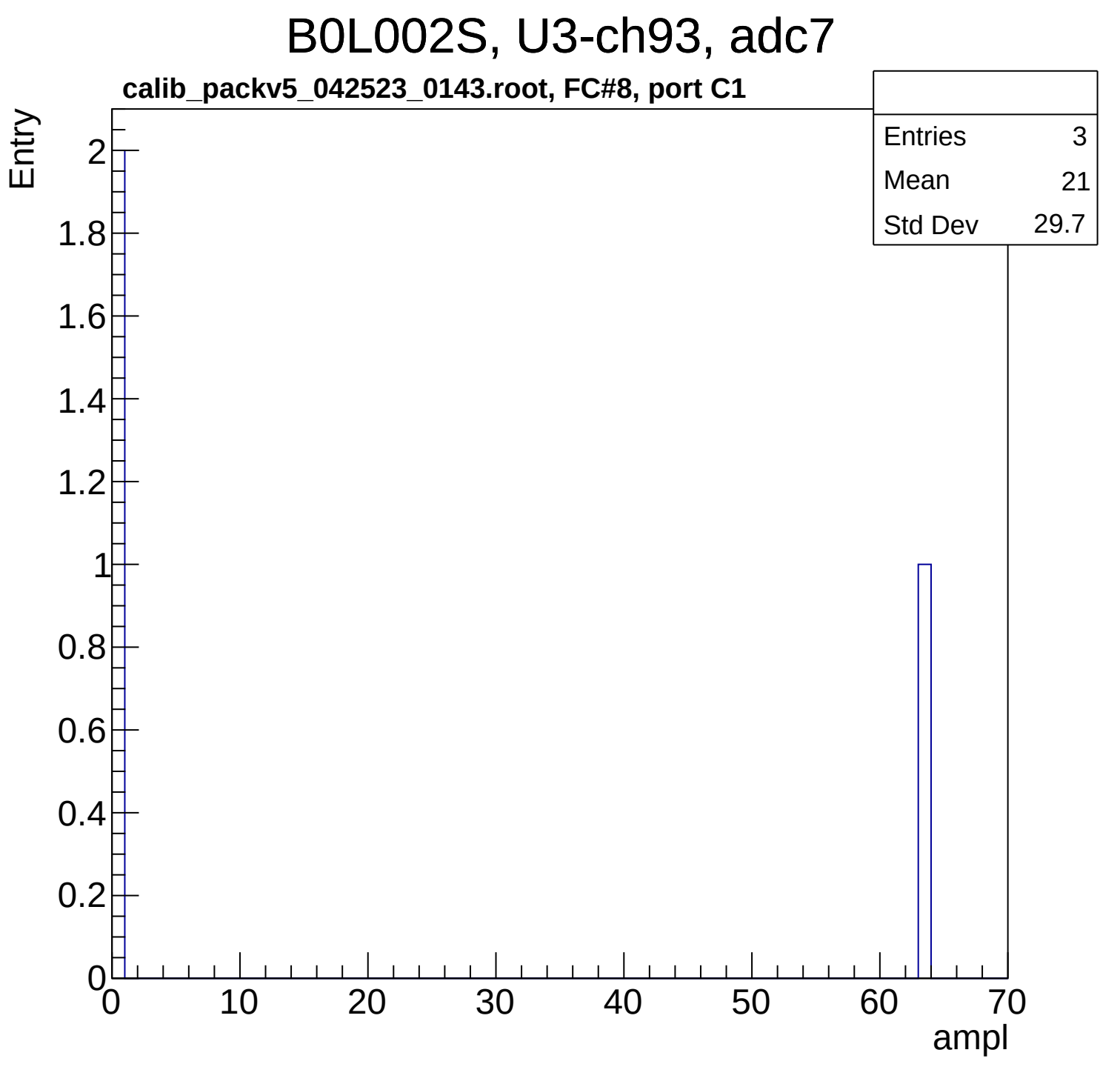
B0L002S, U3-ch93, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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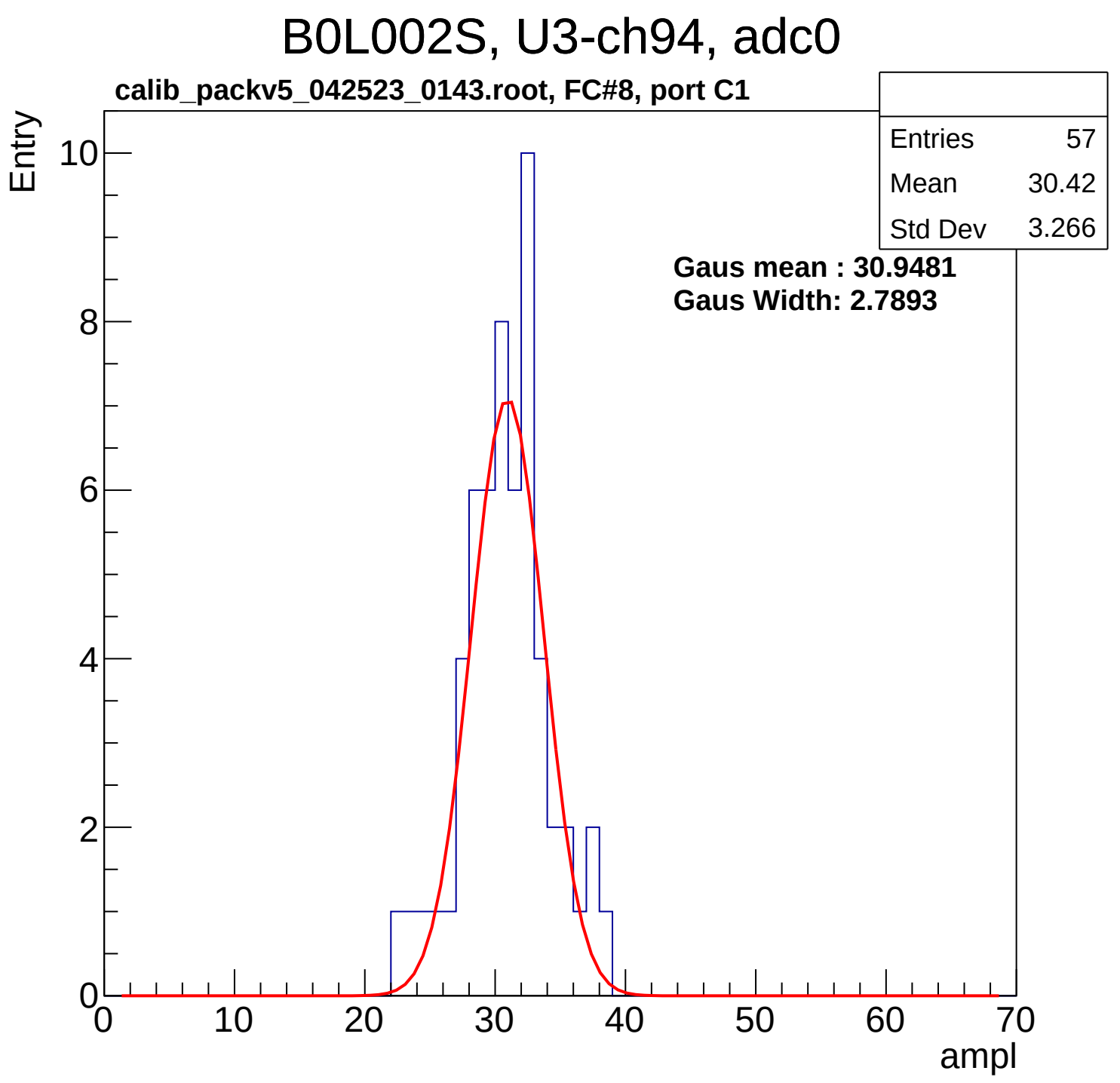
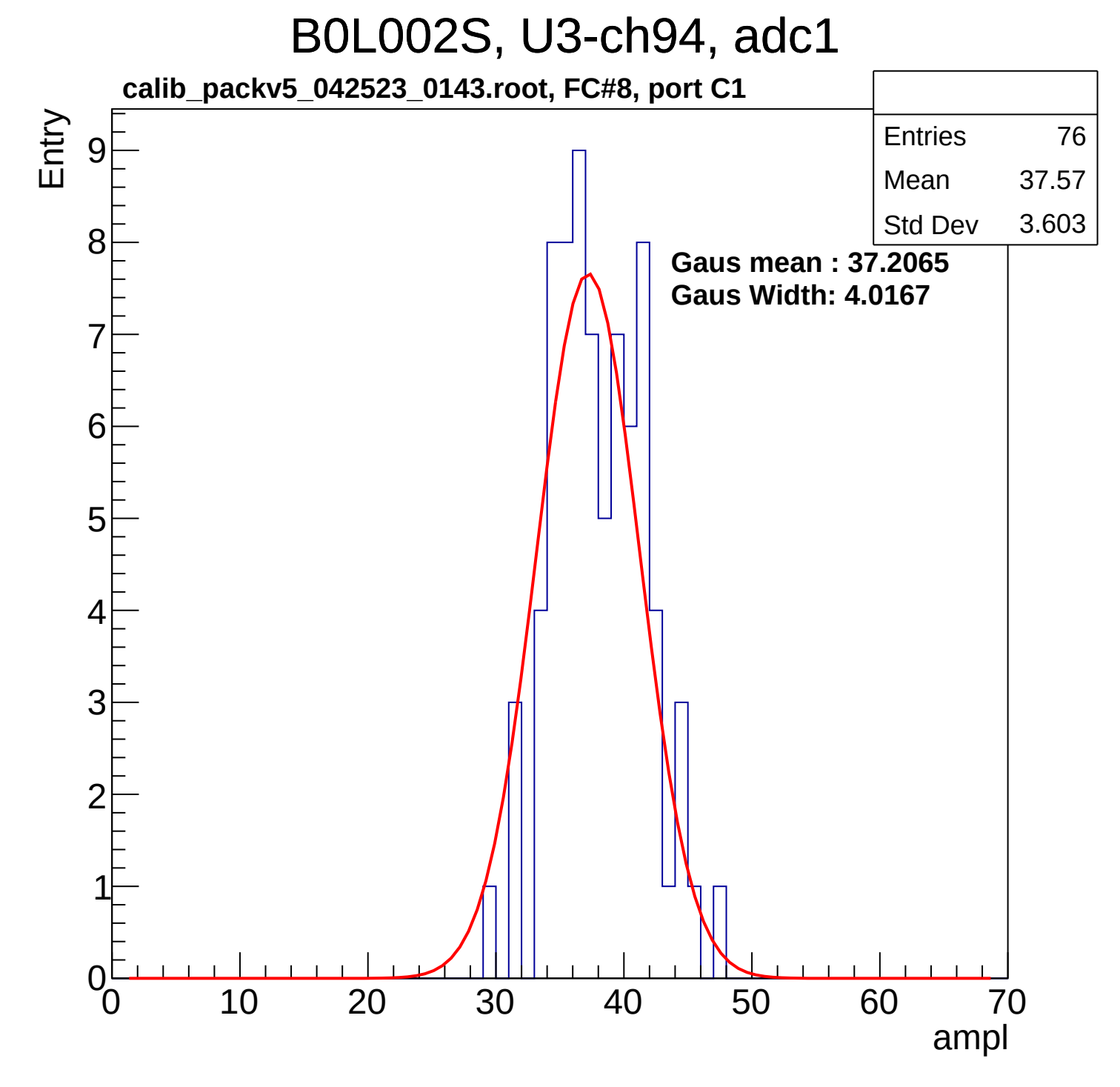

B0L002S, U3-ch94, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 30.42
Std Dev 3.266
Gaus mean : 30.9481
Gaus Width: 2.7893
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch94, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 37.57
Std Dev 3.603
Gaus mean : 37.2065
Gaus Width: 4.0167
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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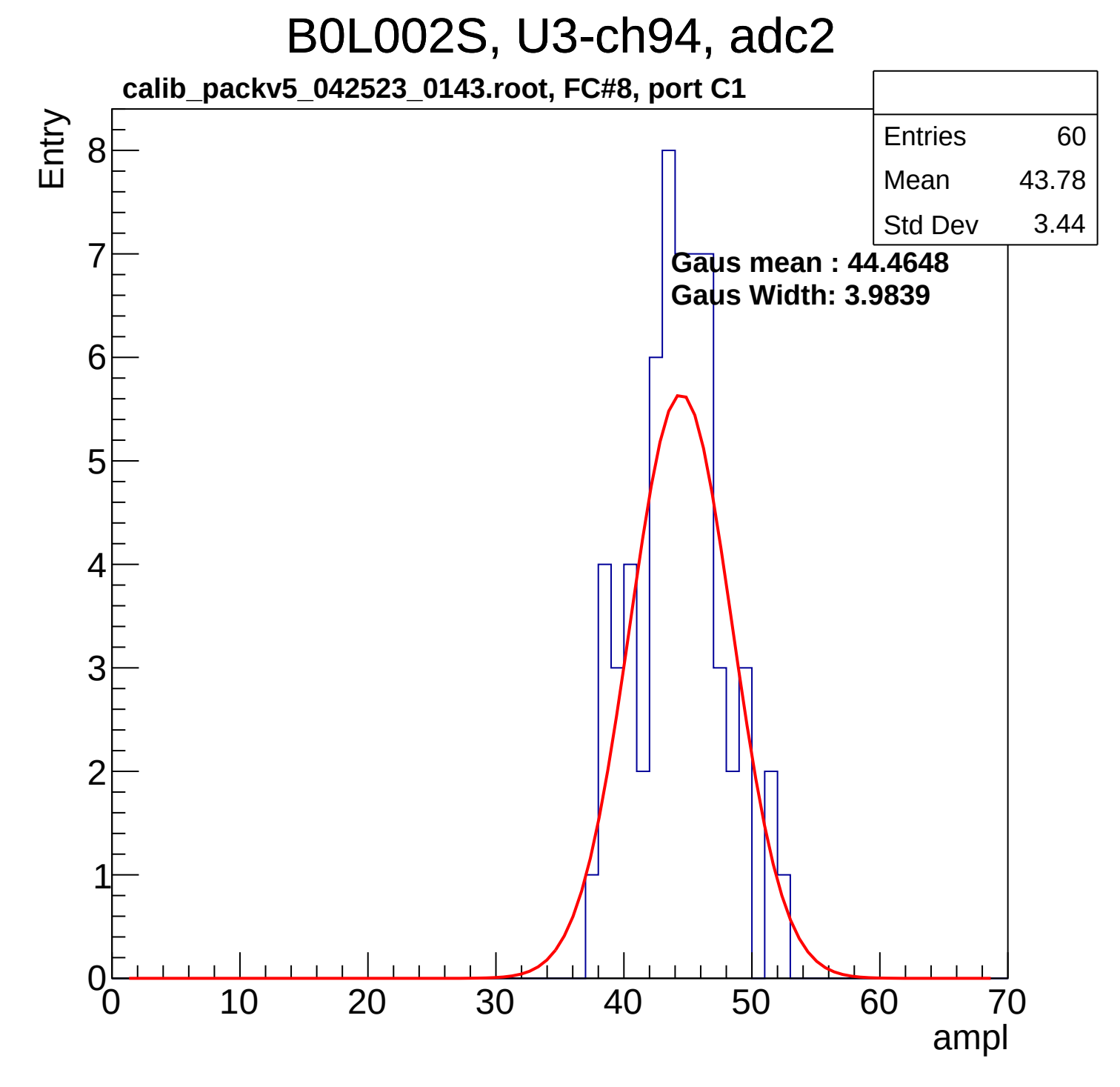
B0L002S, U3-ch94, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 43.78
Std Dev 3.44
Gaus mean : 44.4648
Gaus Width: 3.9839
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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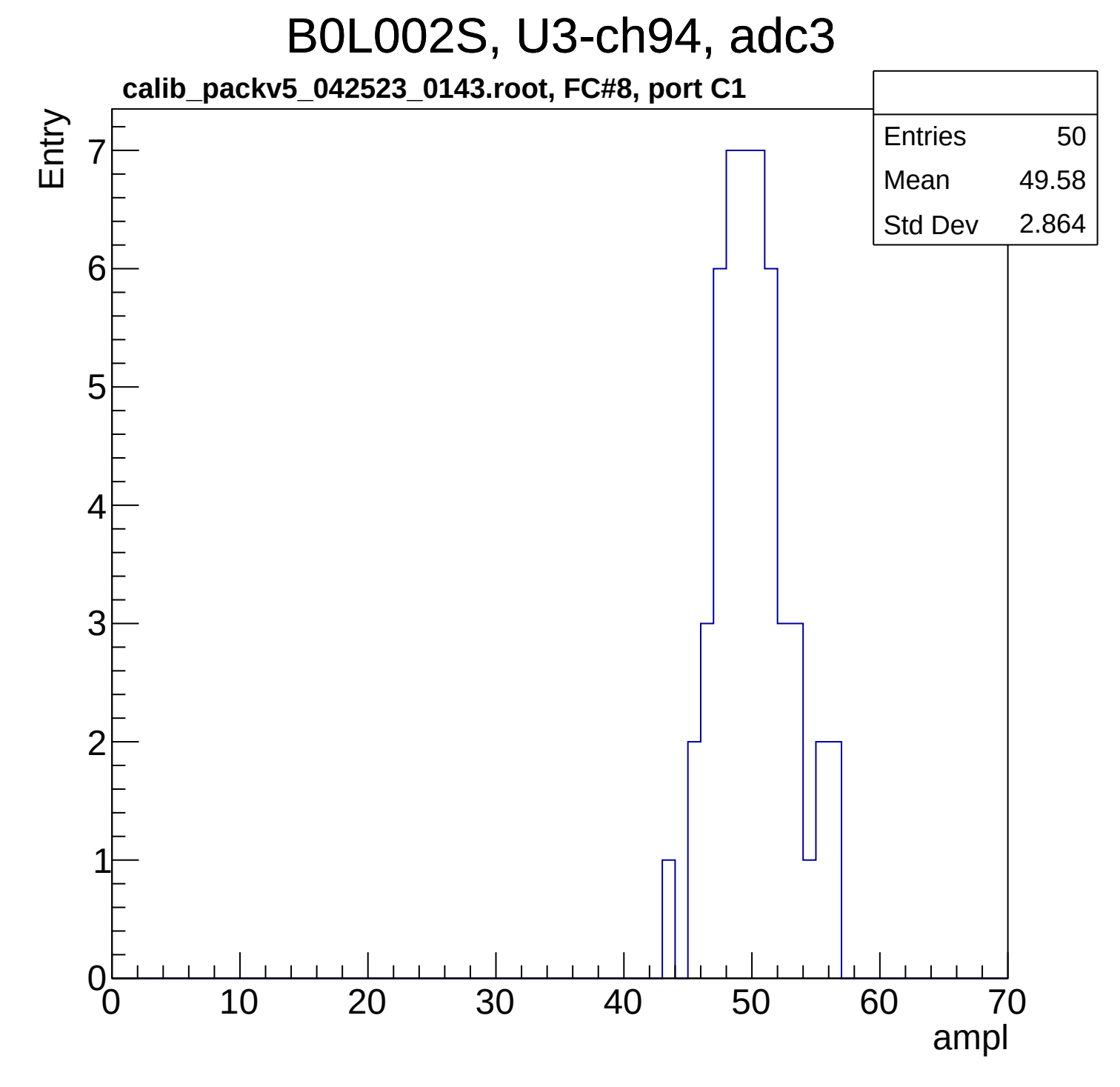
B0L002S, U3-ch94, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 49.58
Std Dev 2.864
Entry
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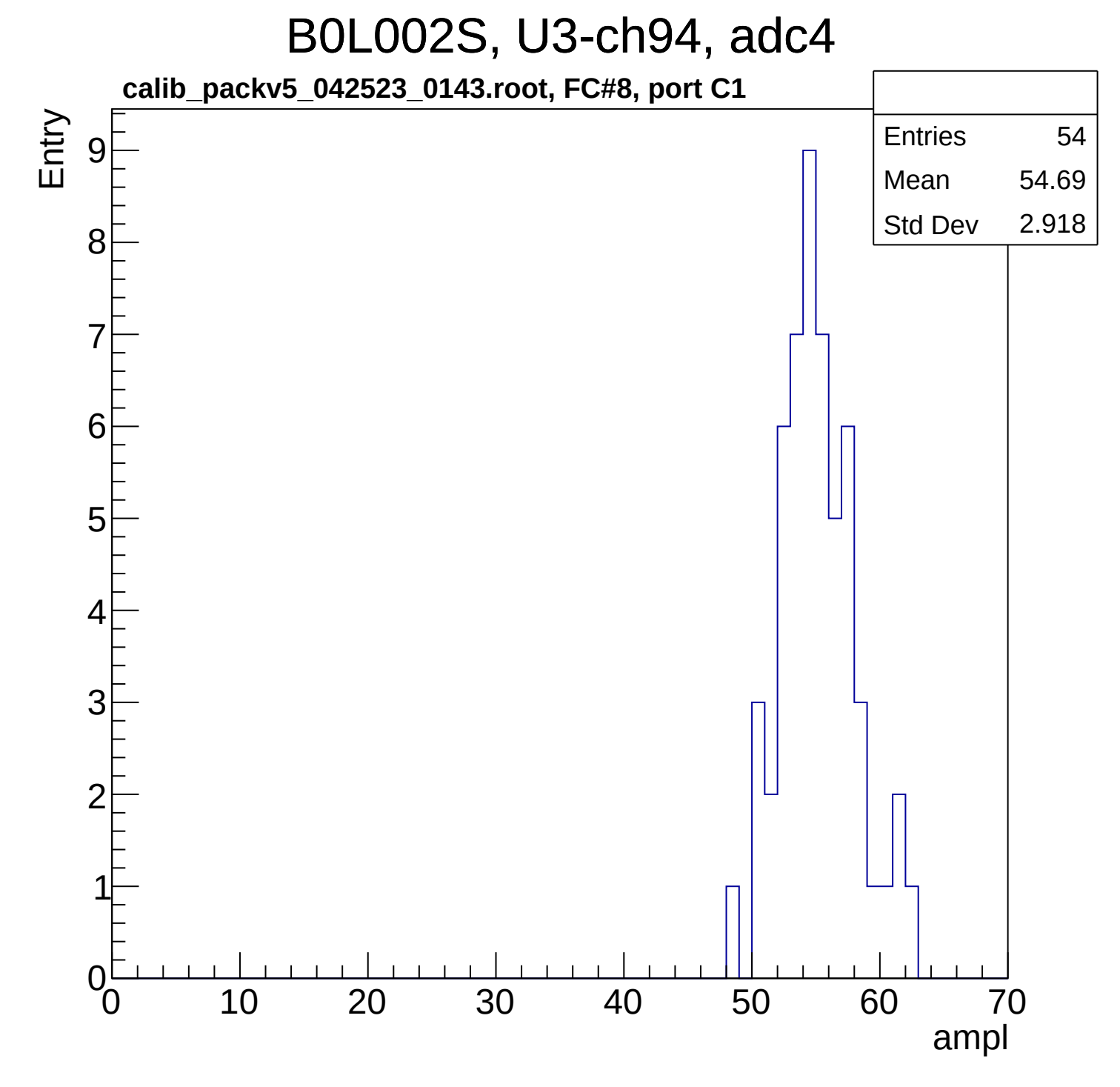
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch94, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 54.69
Std Dev 2.918
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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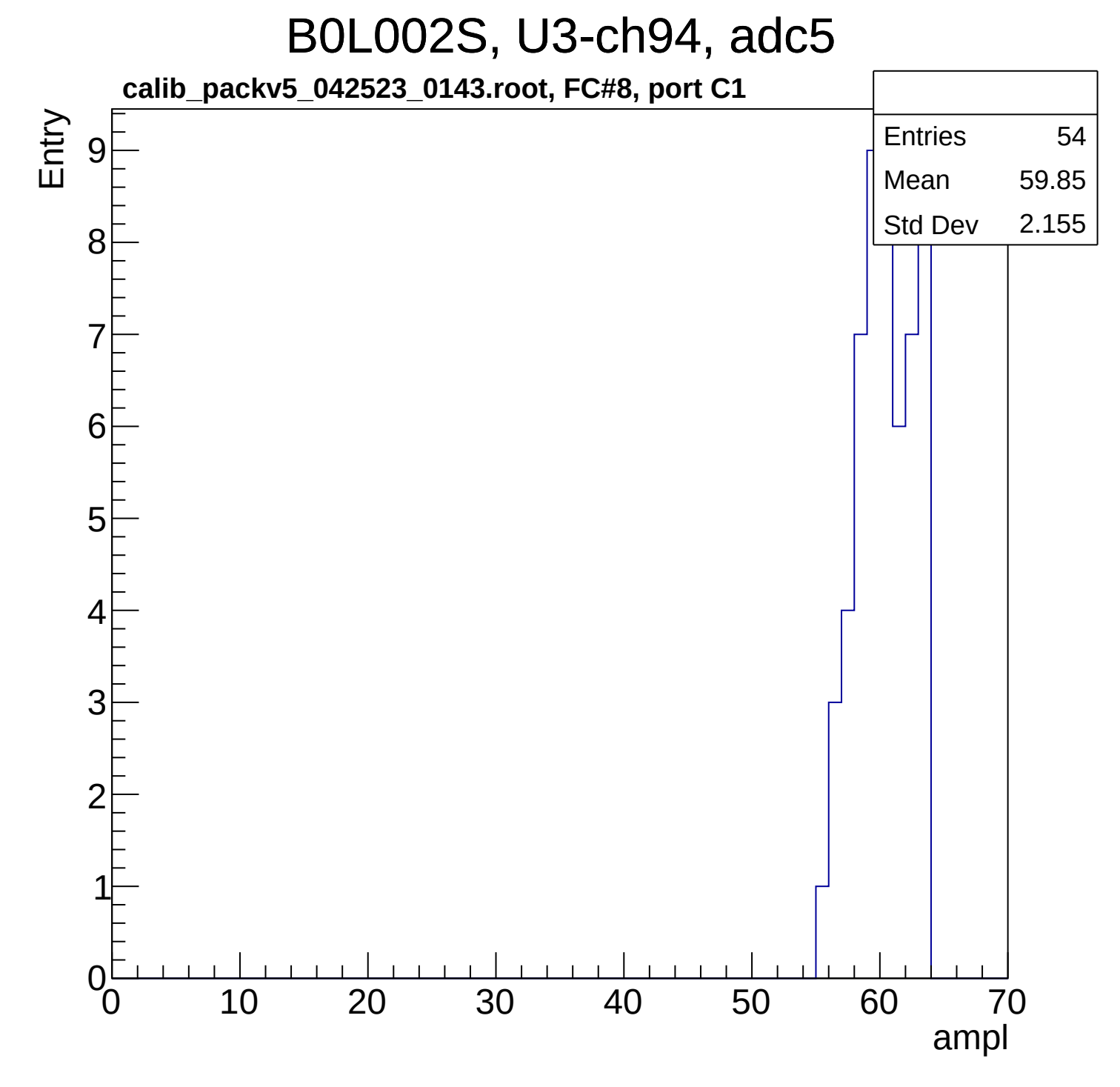
B0L002S, U3-ch94, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 59.85
Std Dev 2.155
Entry
ampl
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2
3
4
5
6
7
8
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0
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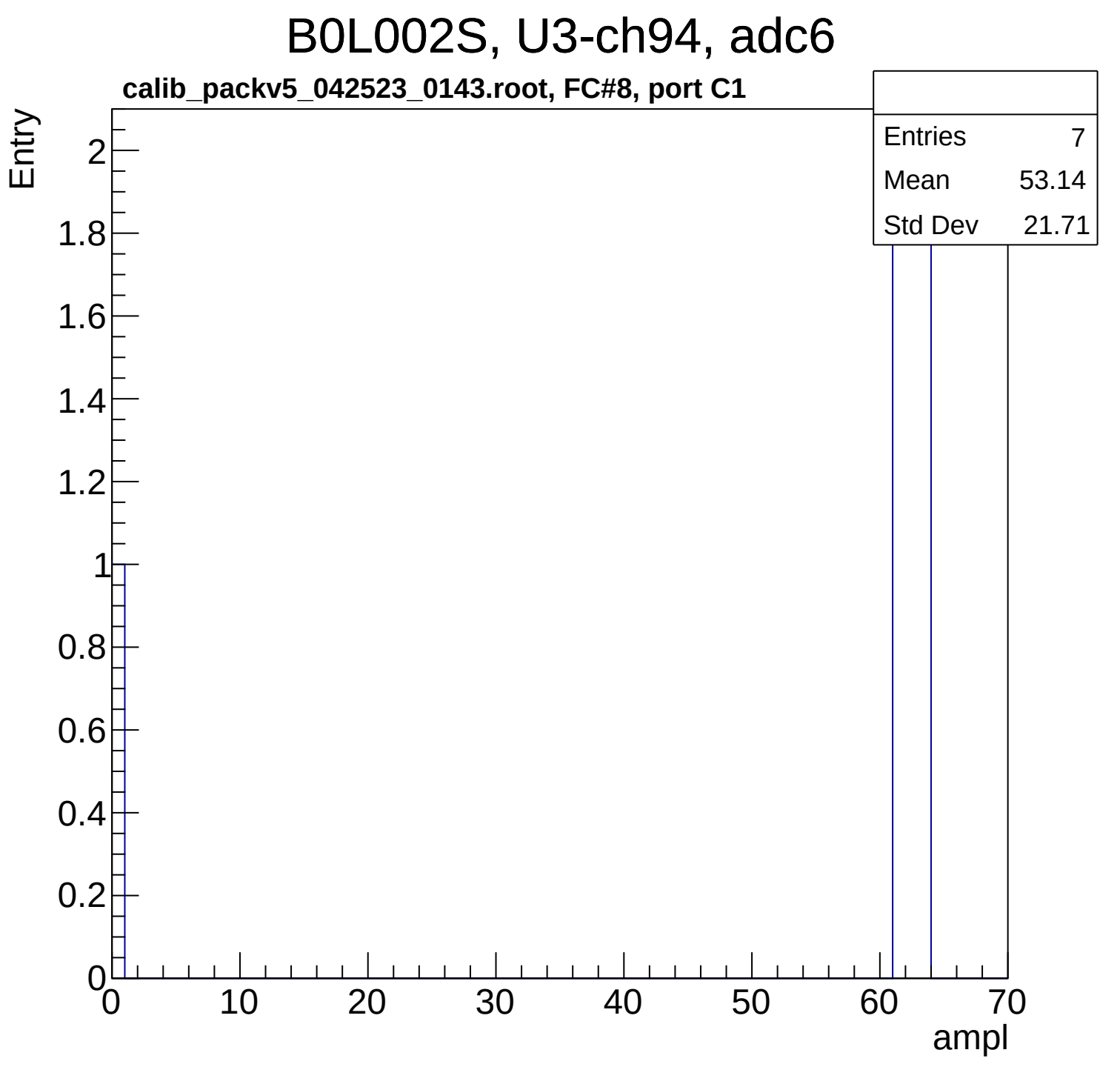
B0L002S, U3-ch94, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 53.14
Std Dev 21.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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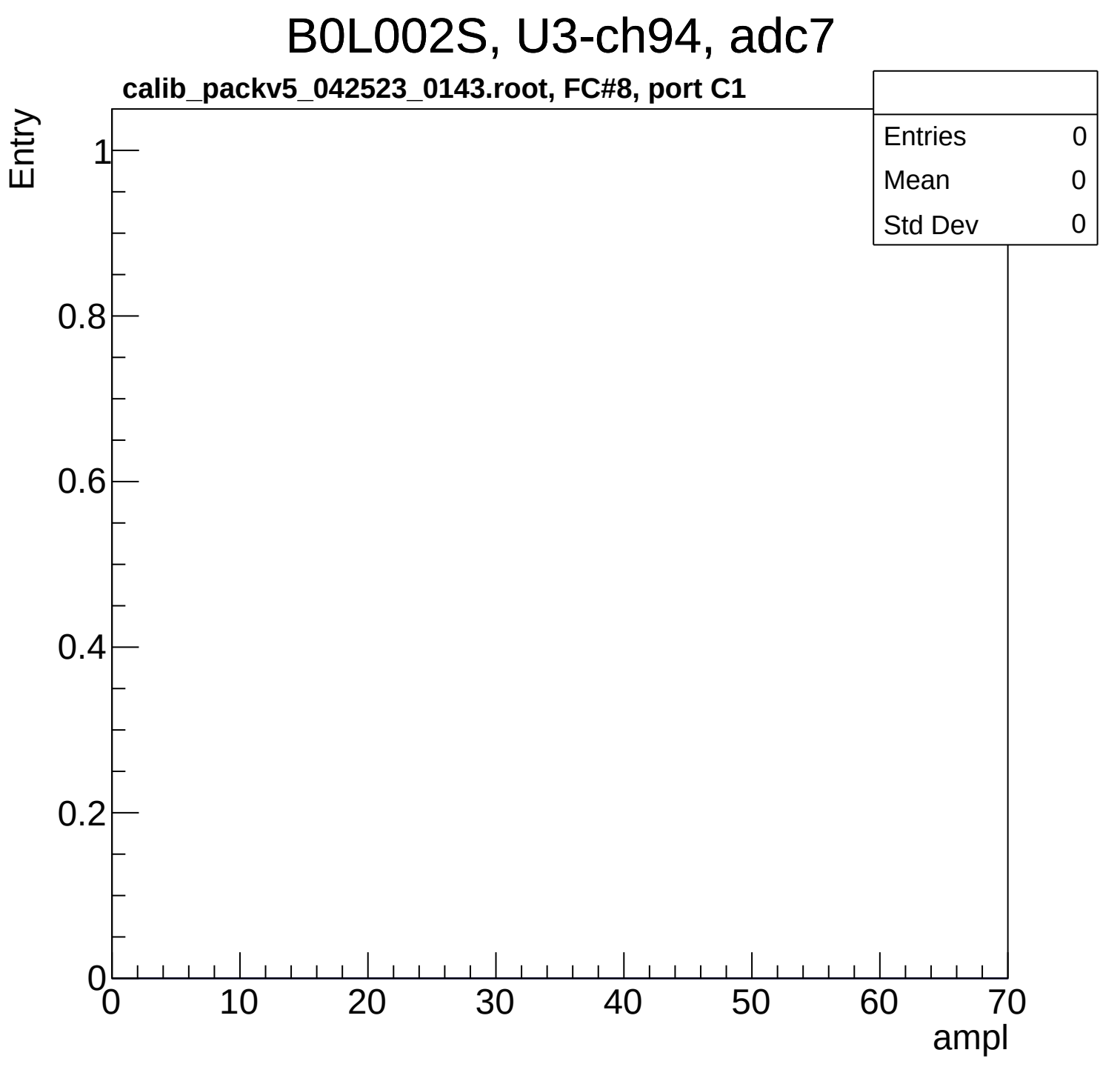
B0L002S, U3-ch94, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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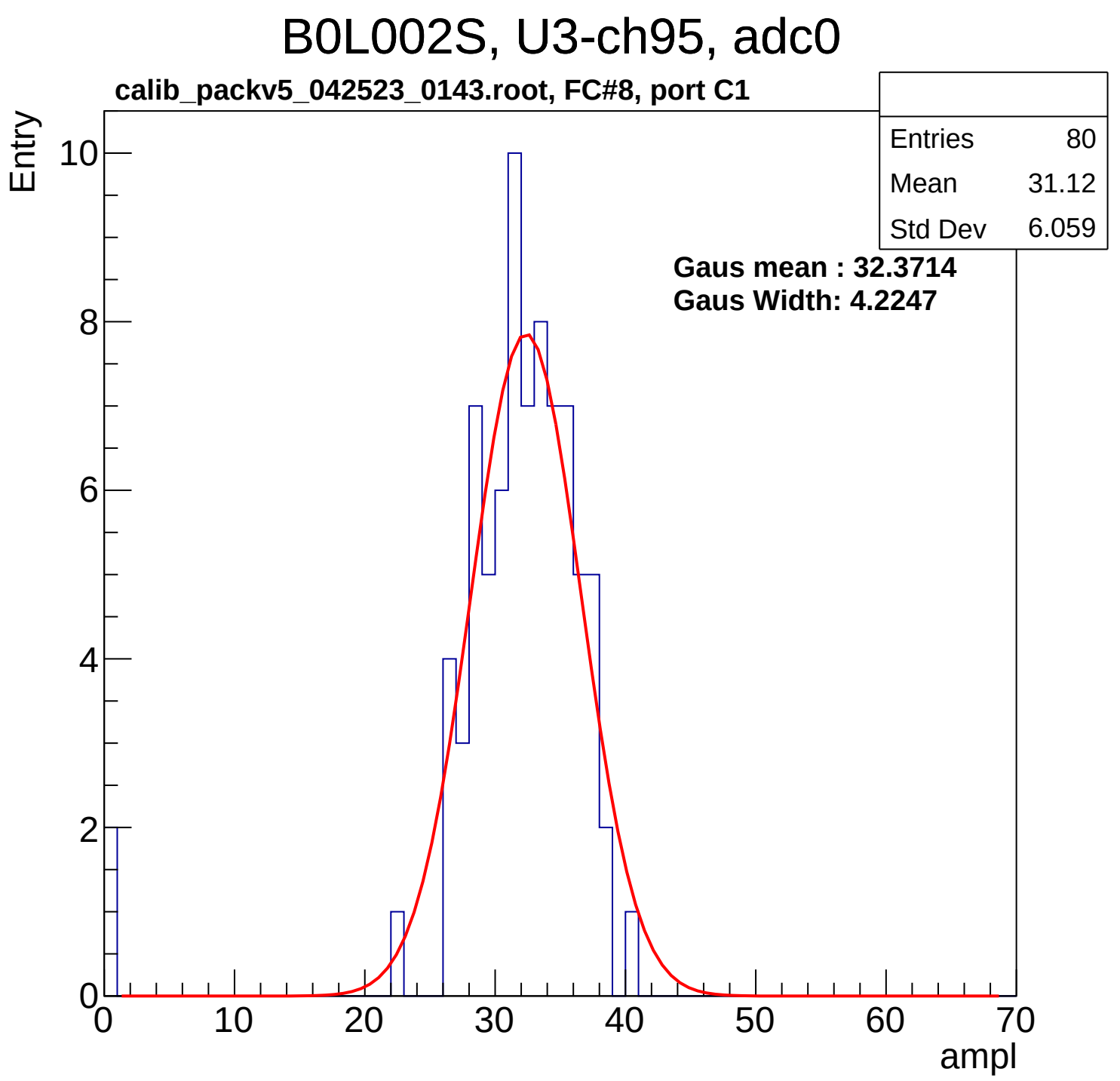
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch95, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 31.12
Std Dev 6.059
Gaus mean : 32.3714
Gaus Width: 4.2247
Entry
ampl
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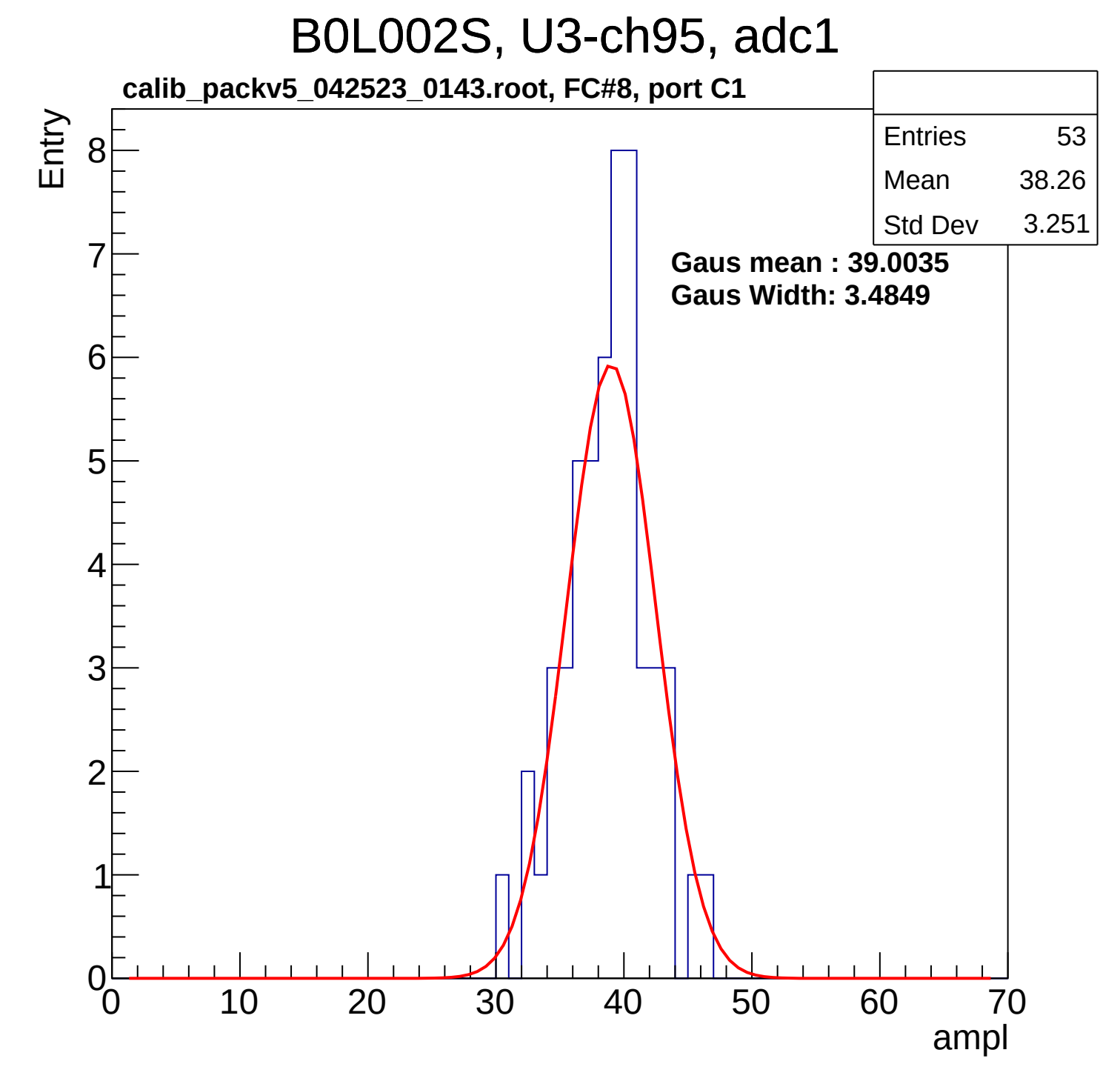
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch95, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 38.26
Std Dev 3.251
Gaus mean : 39.0035
Gaus Width: 3.4849
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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40
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60
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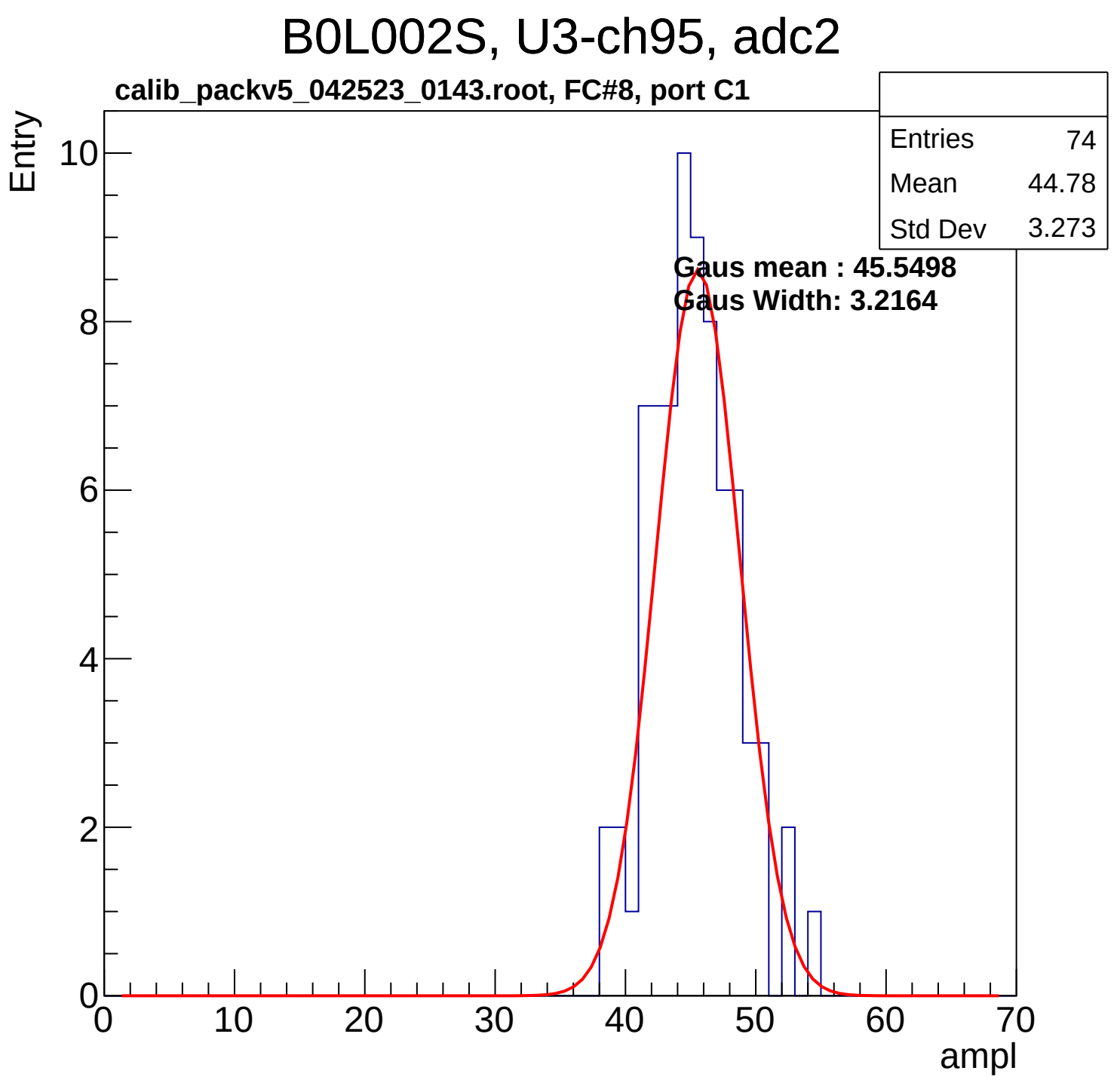
B0L002S, U3-ch95, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 44.78
Std Dev 3.273
Gaus mean : 45.5498
Gaus Width: 3.2164
Entry
ampl
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8
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0
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60
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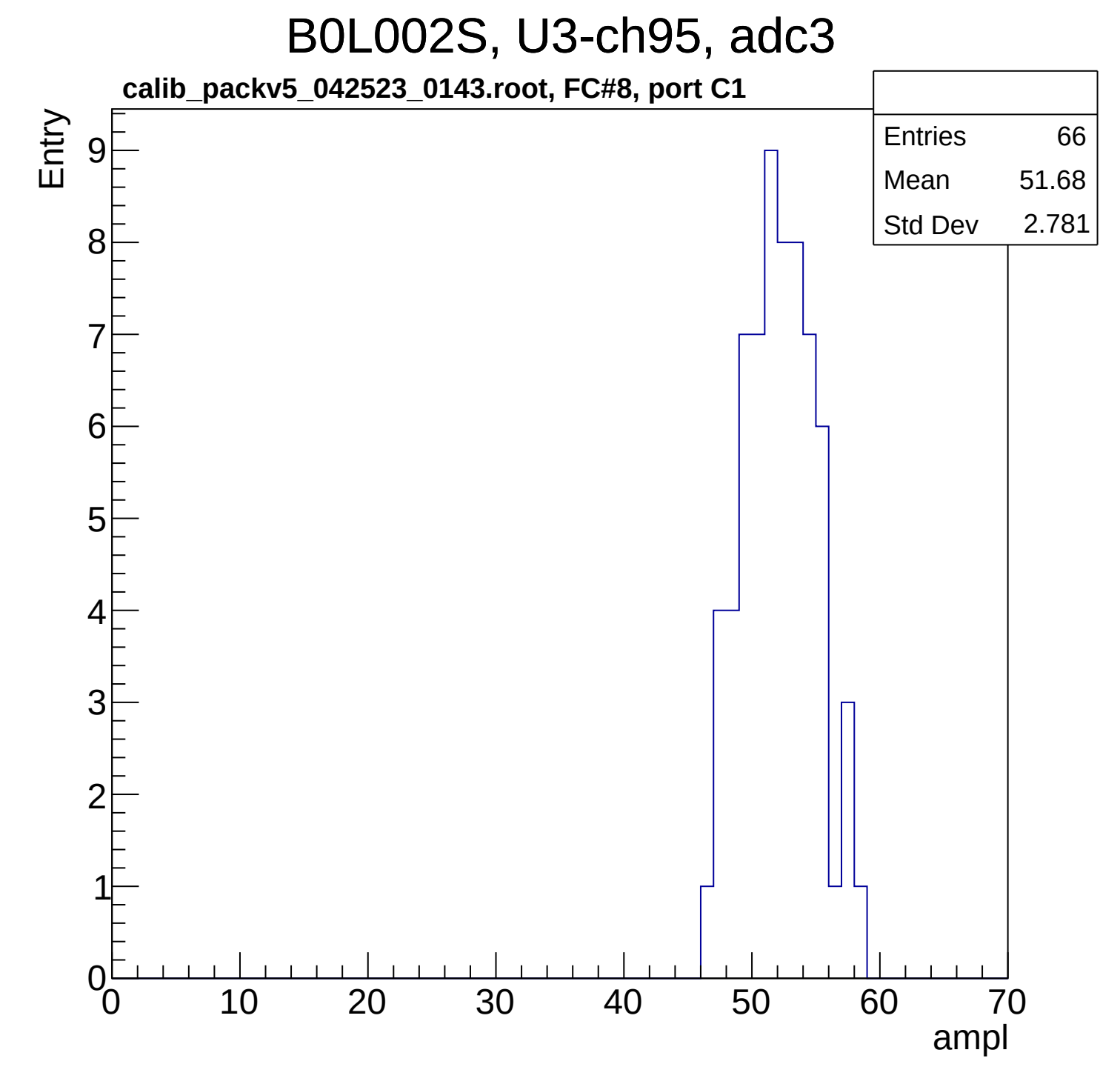
B0L002S, U3-ch95, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.68
Std Dev 2.781
Entry
ampl
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3
4
5
6
7
8
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0
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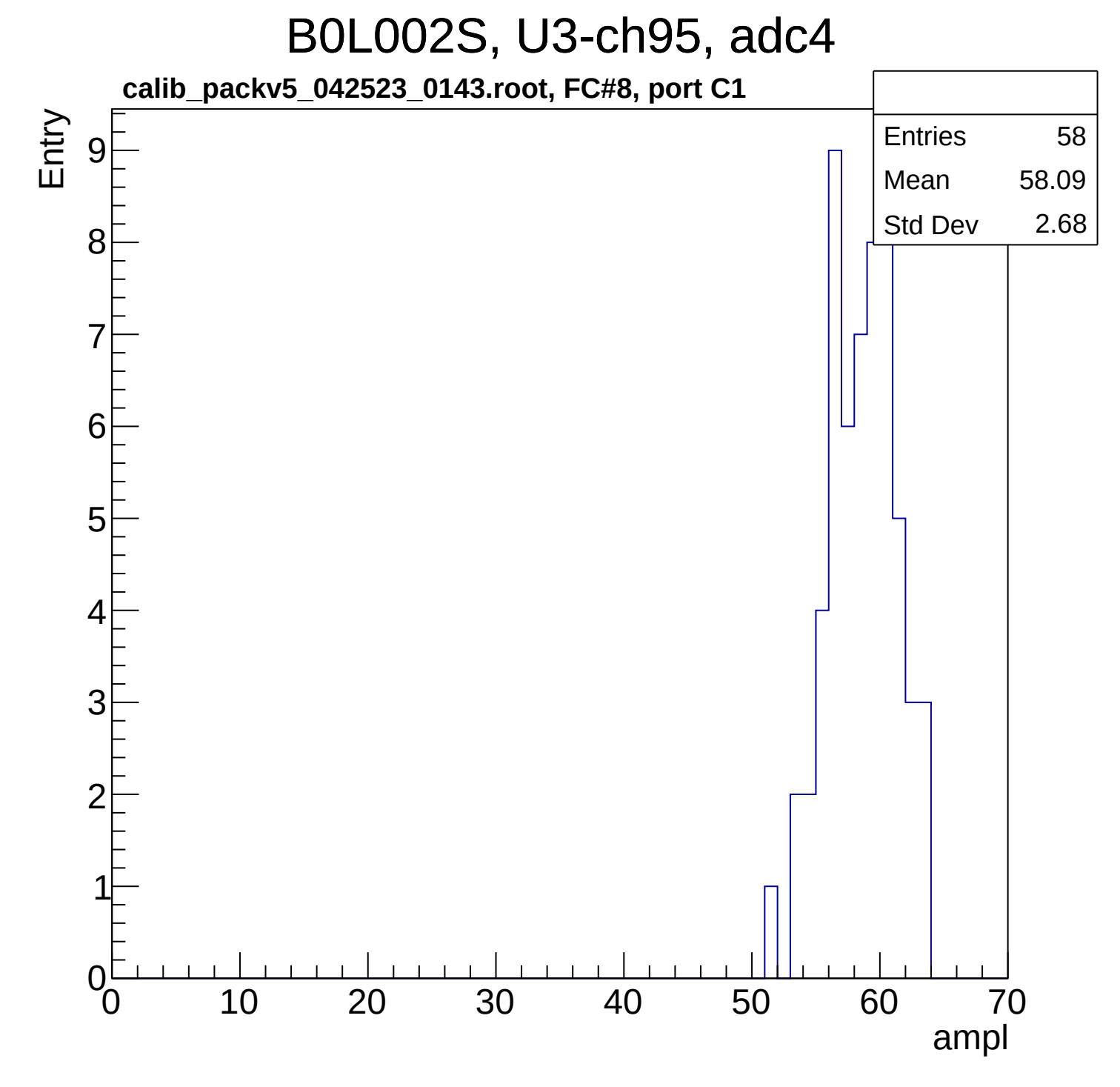
B0L002S, U3-ch95, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 58.09
Std Dev 2.68
Entry
ampl
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0
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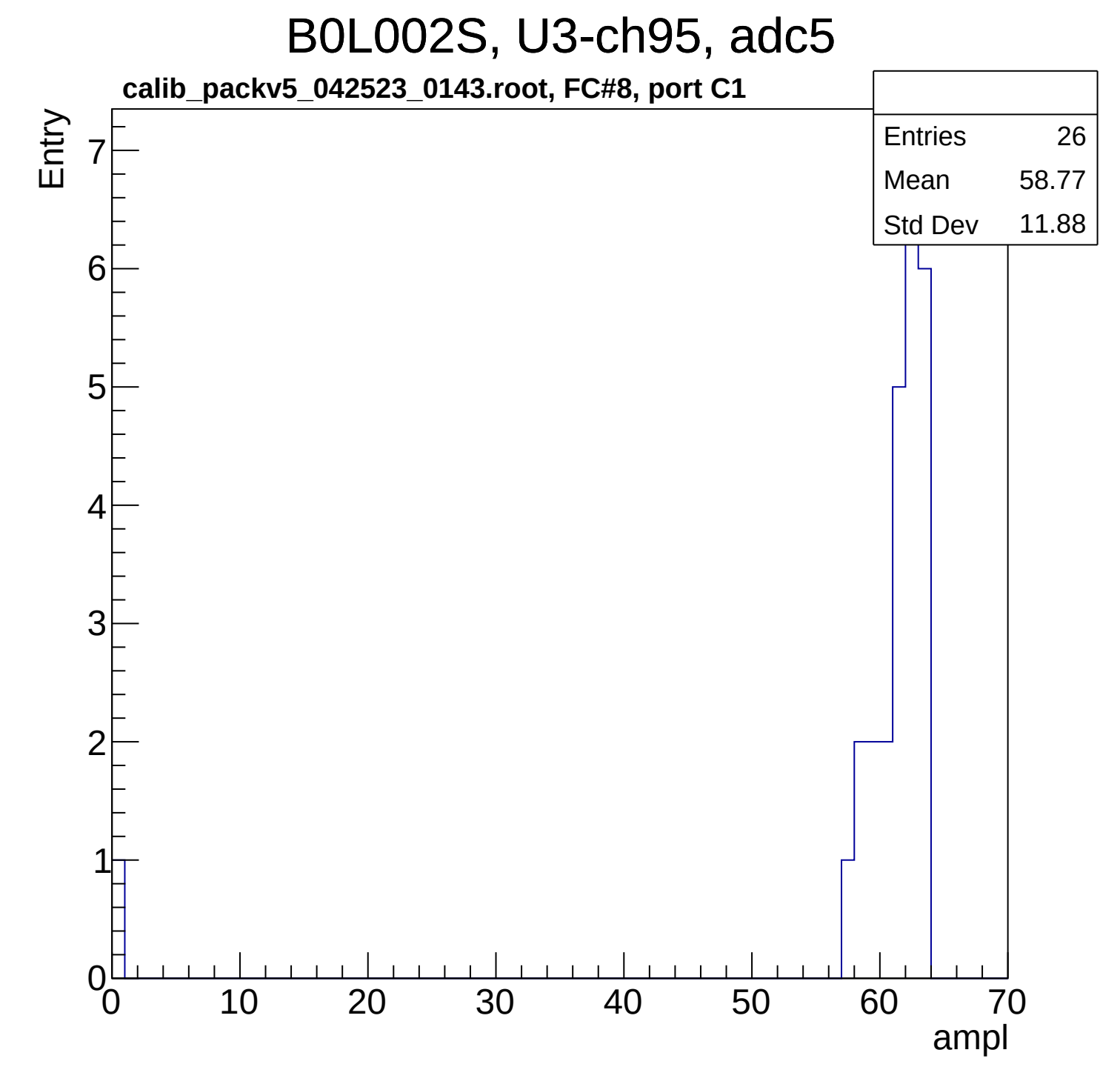
B0L002S, U3-ch95, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.77
Std Dev 11.88
Entry
ampl
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0
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60
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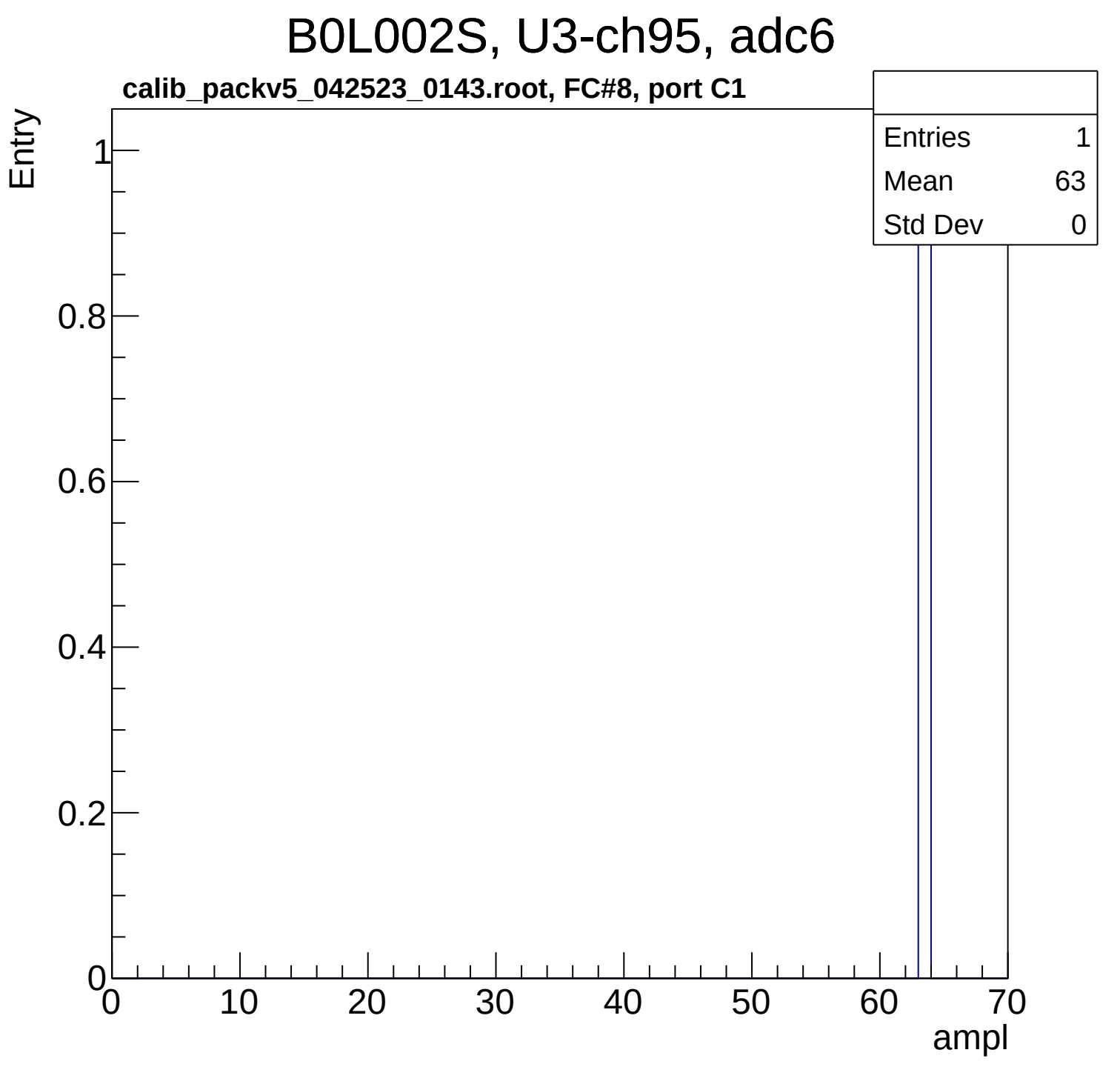
B0L002S, U3-ch95, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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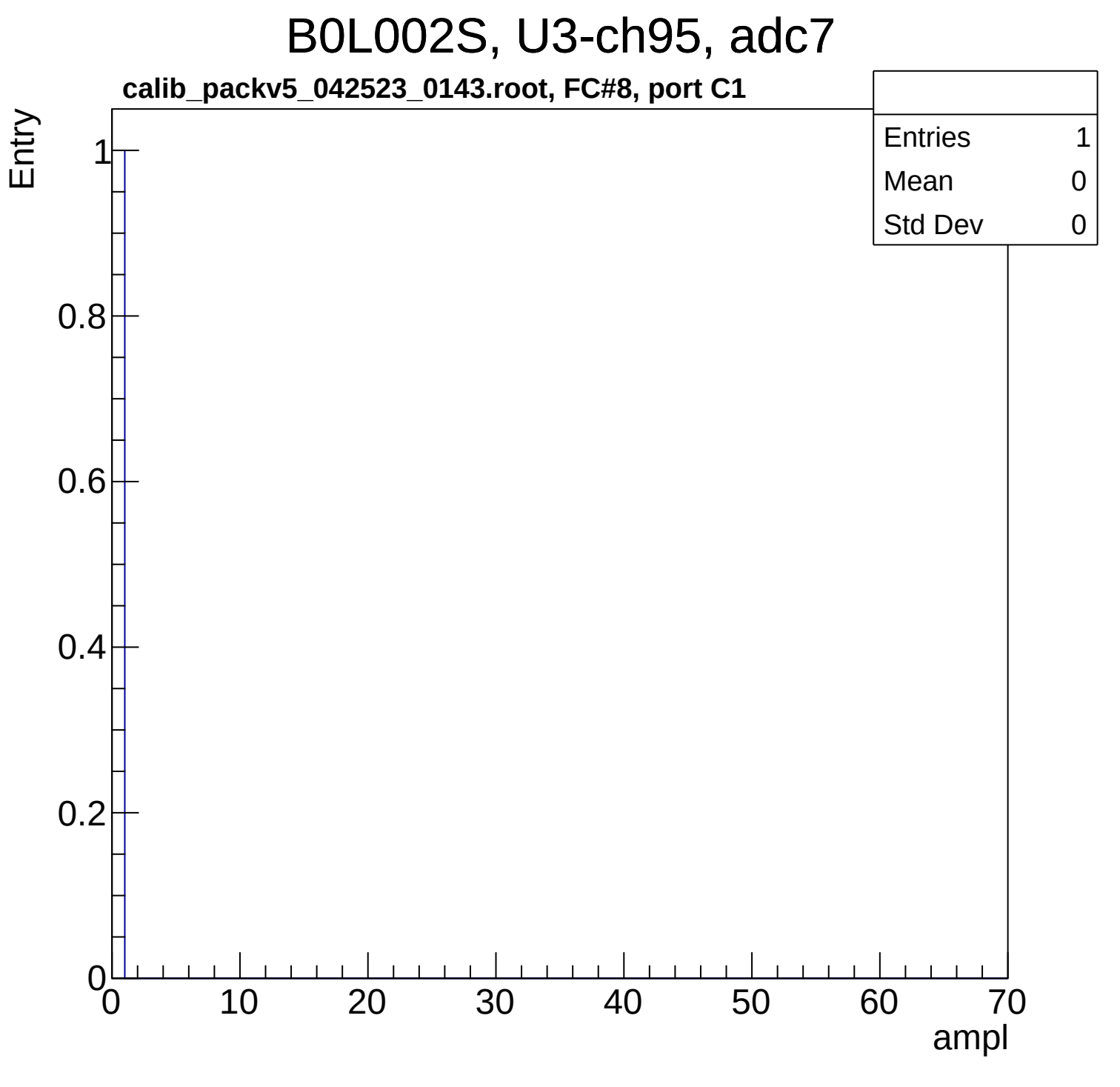
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch95, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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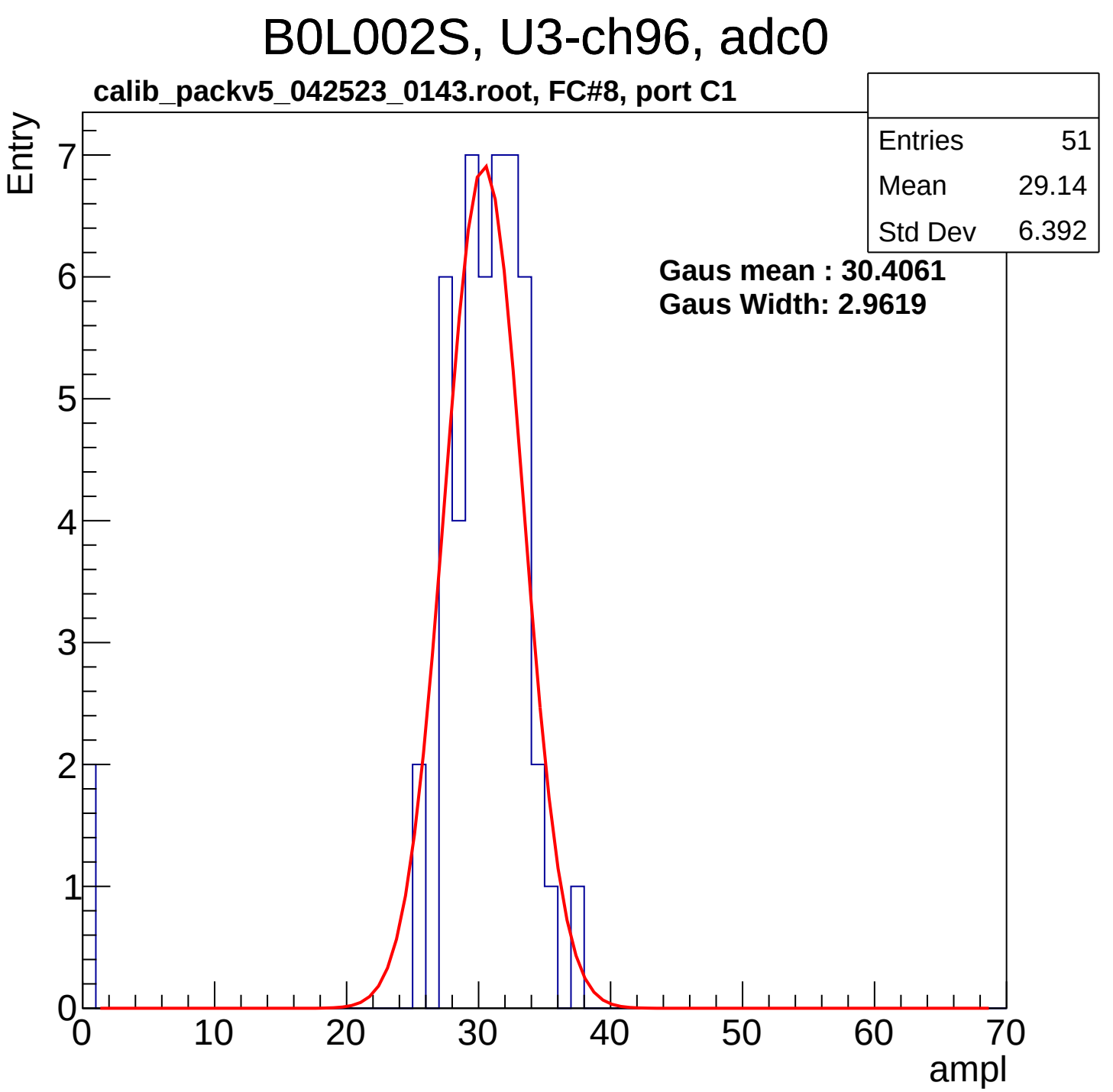
50
60
70
ampl


B0L002S, U3-ch96, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 29.14
Std Dev 6.392
Gaus mean : 30.4061
Gaus Width: 2.9619
Entry
ampl
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1
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3
4
5
6
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0
10
20
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40
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60
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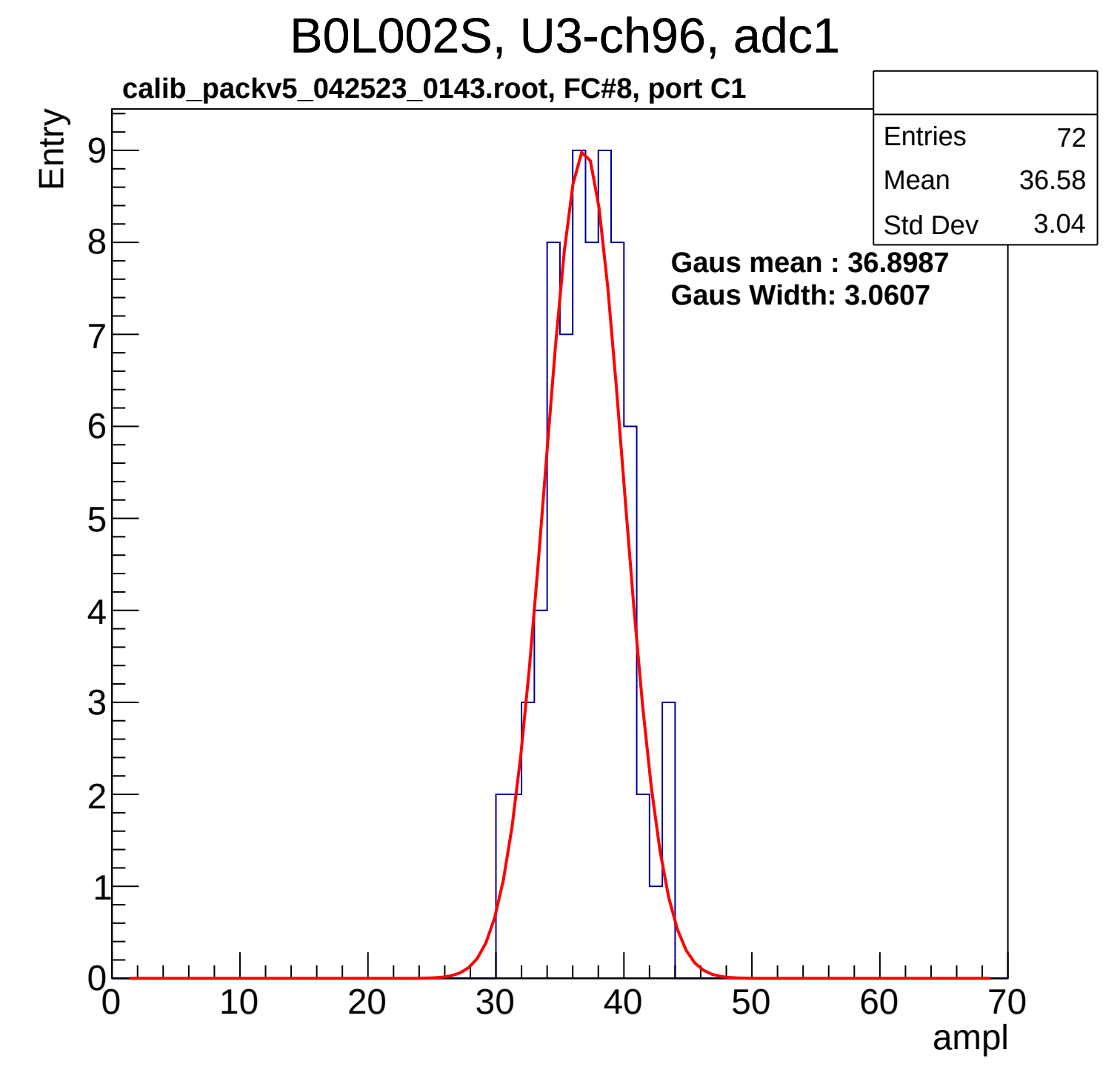
B0L002S, U3-ch96, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 36.58
Std Dev 3.04
Gaus mean : 36.8987
Gaus Width: 3.0607
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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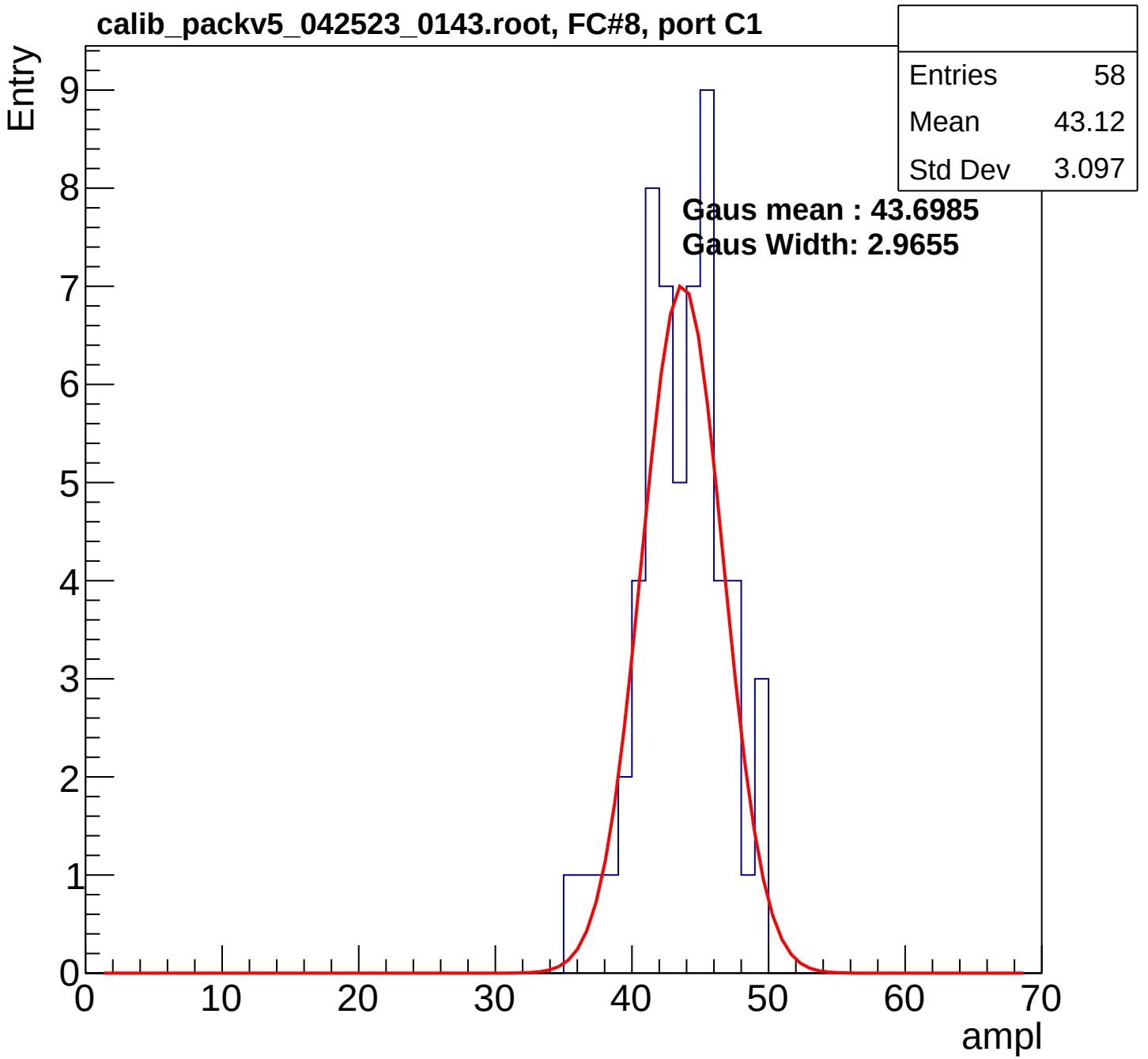
B0L002S, U3-ch96, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.12
Std Dev 3.097
Gaus mean : 43.6985
Gaus Width: 2.9655
Entry
ampl
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3
4
5
6
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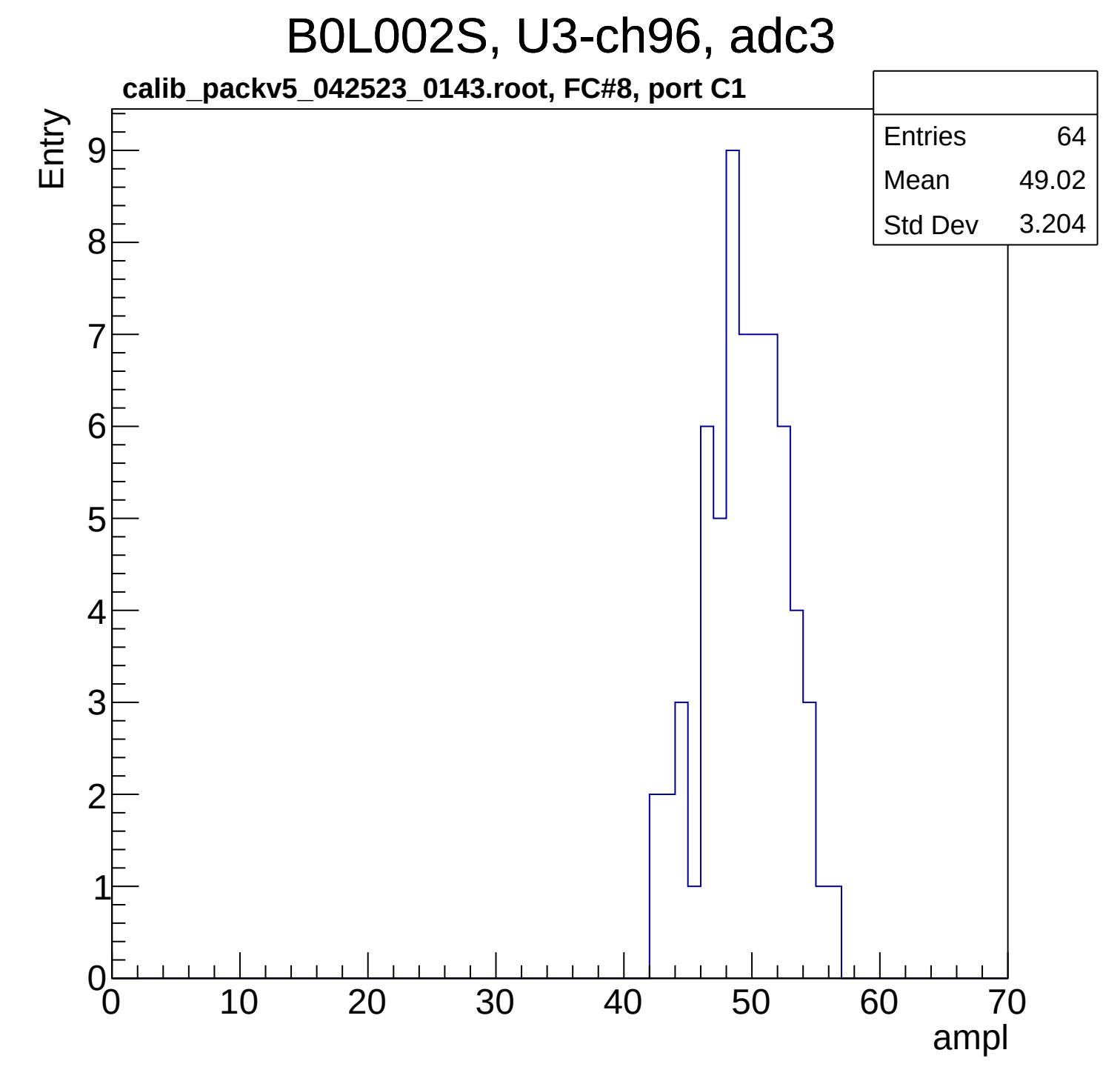
B0L002S, U3-ch96, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 49.02
Std Dev 3.204
Entry
ampl
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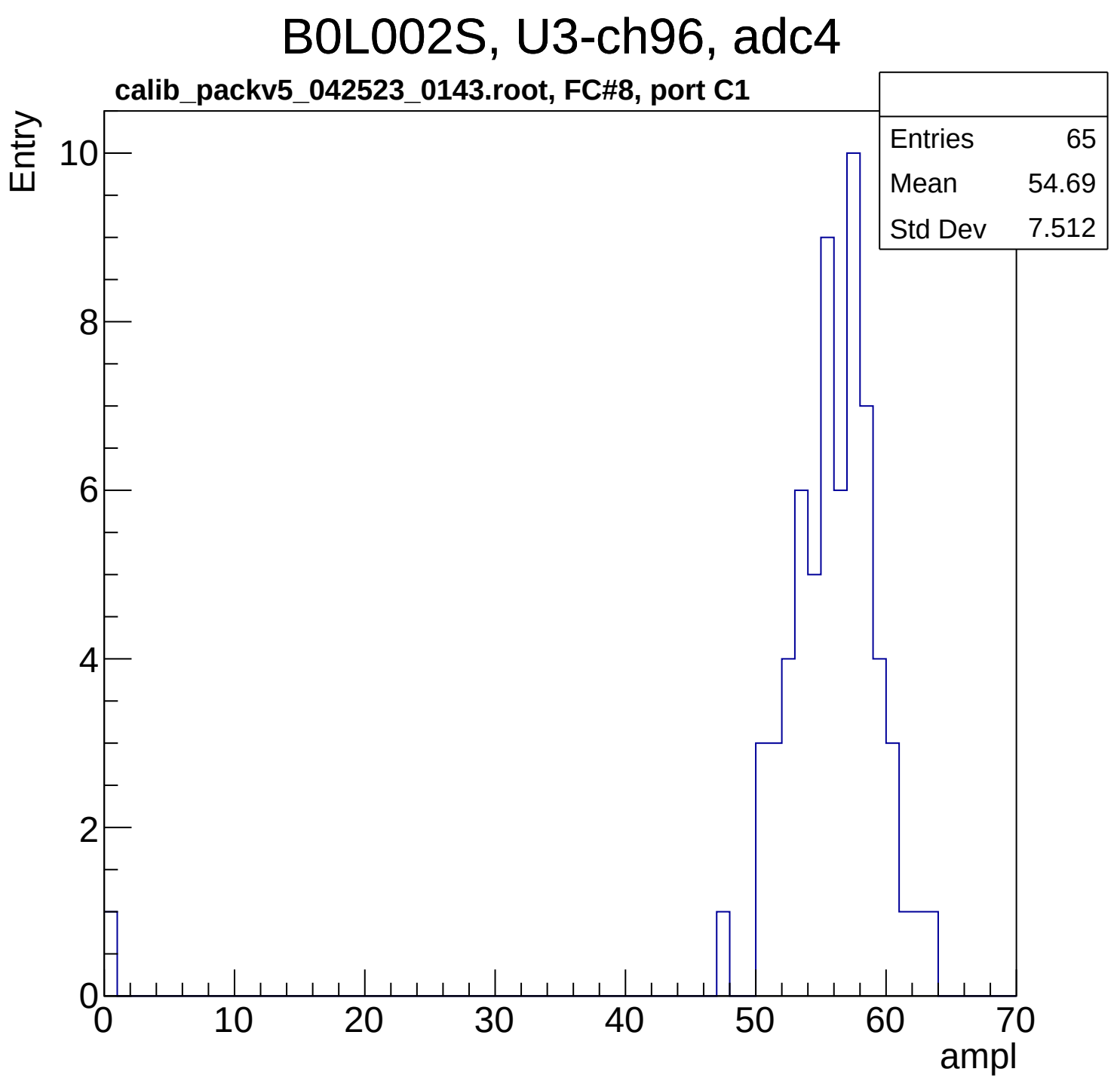
B0L002S, U3-ch96, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 54.69
Std Dev 7.512
Entry
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10
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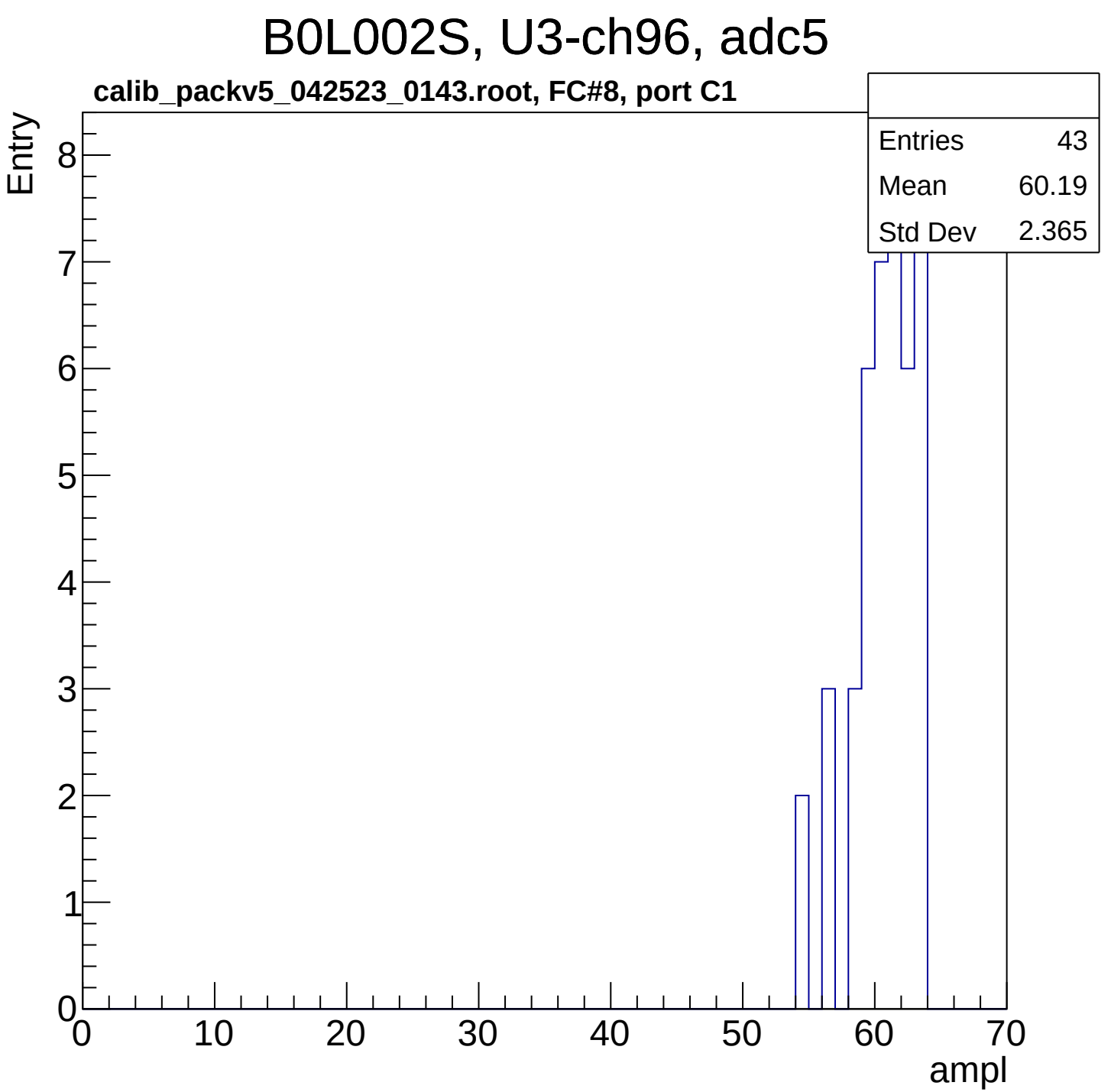
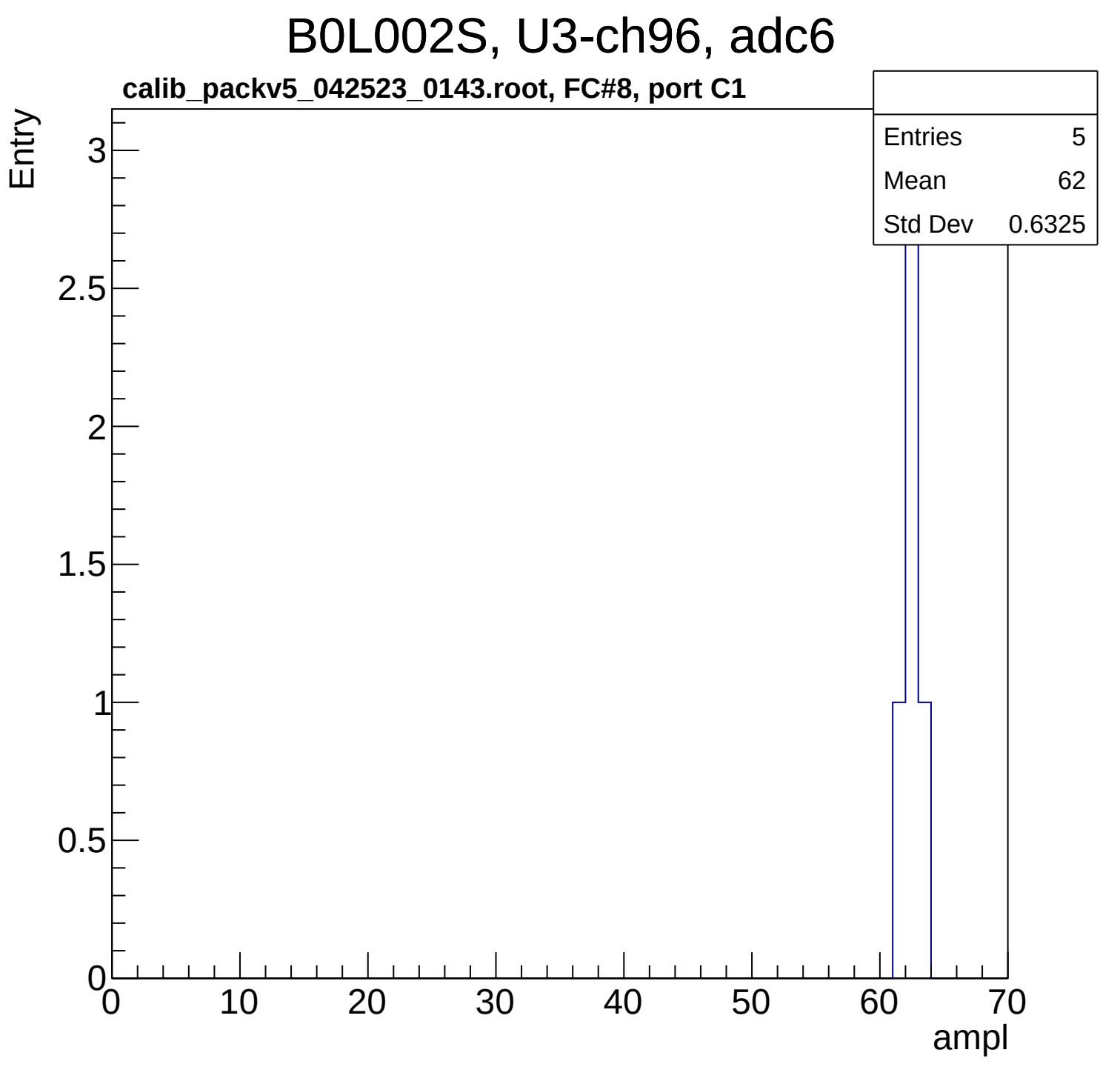
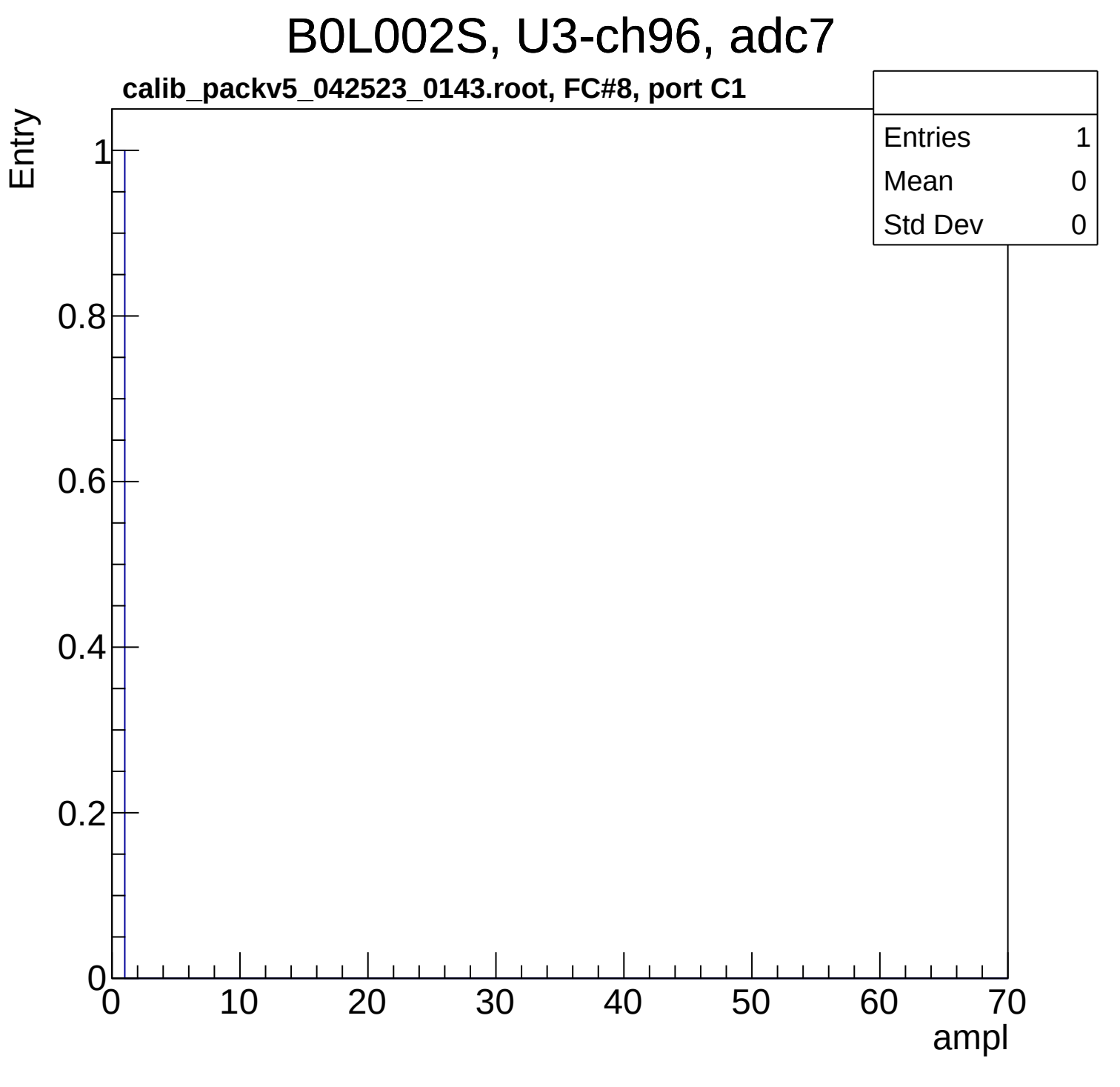
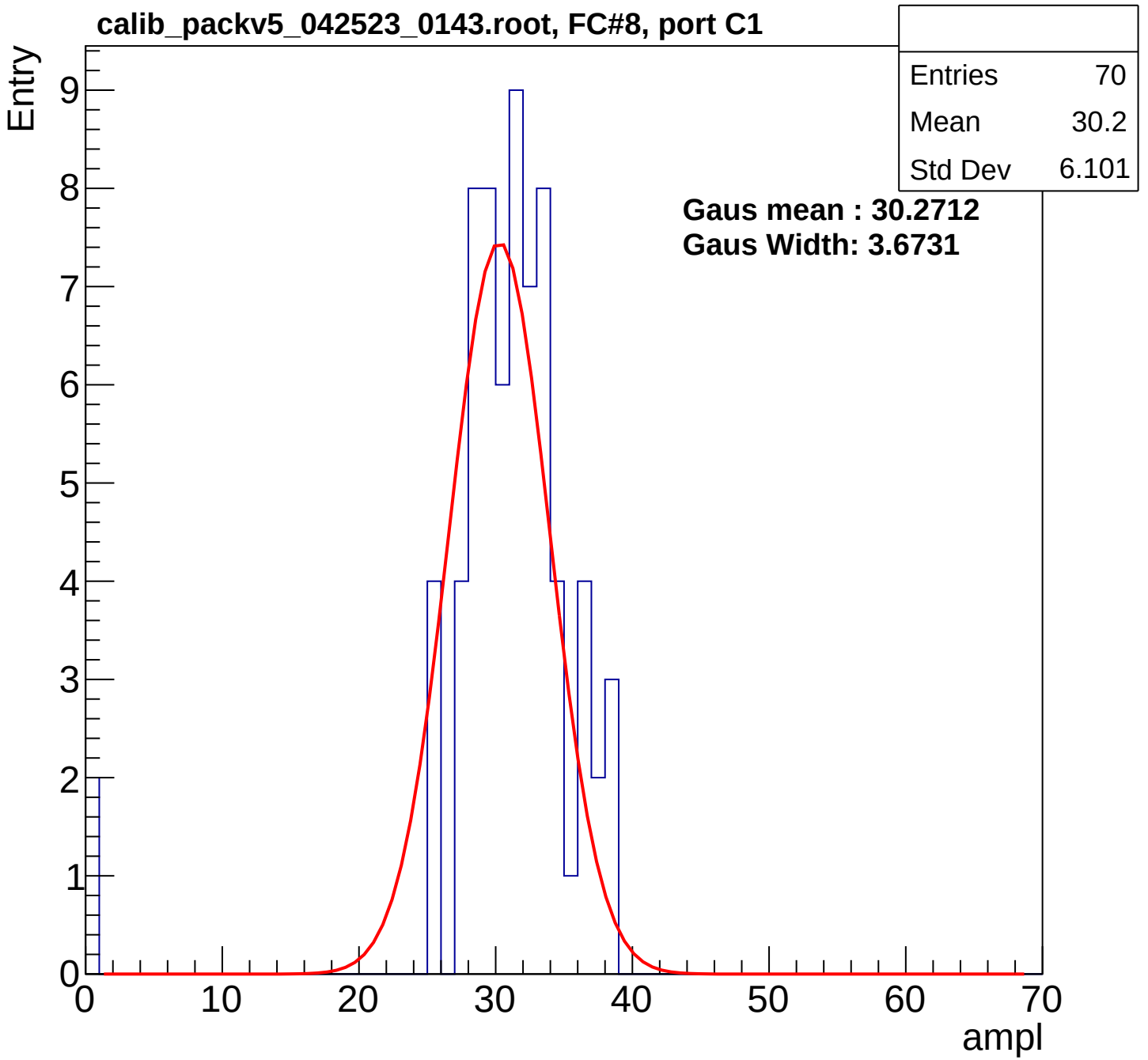
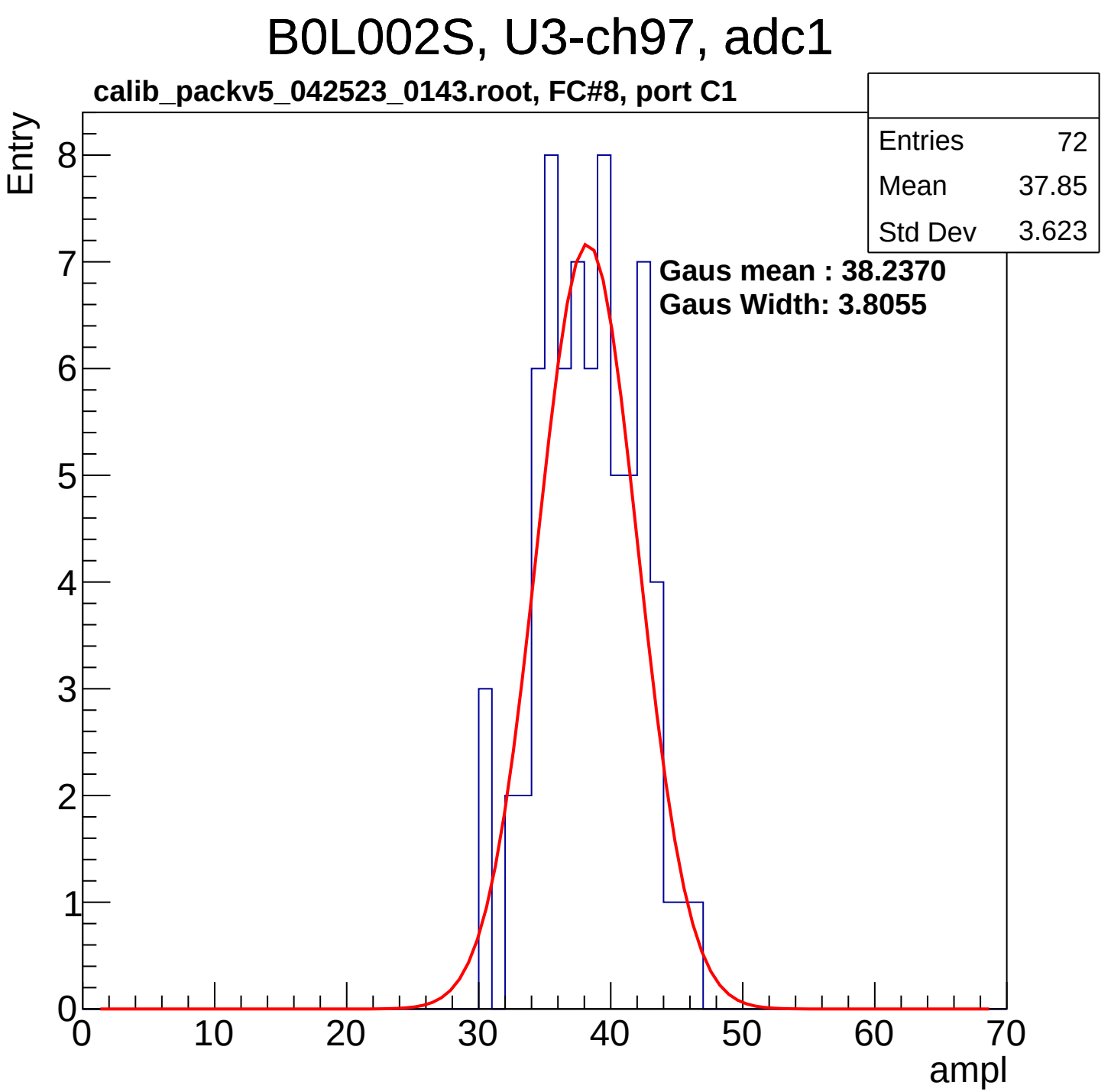
50
60
70
ampl

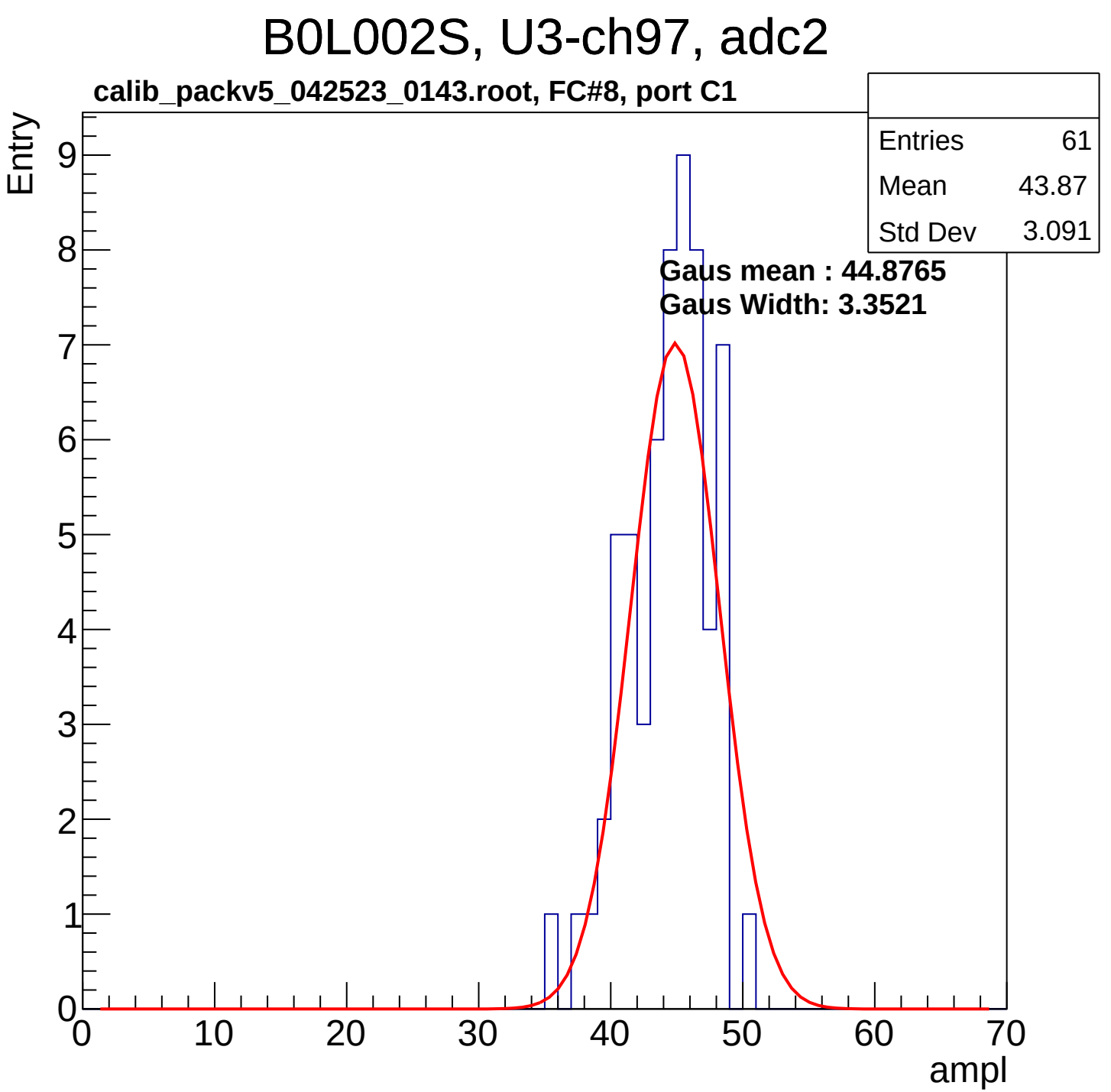

B0L002S, U3-ch96, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 60.19
Std Dev 2.365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

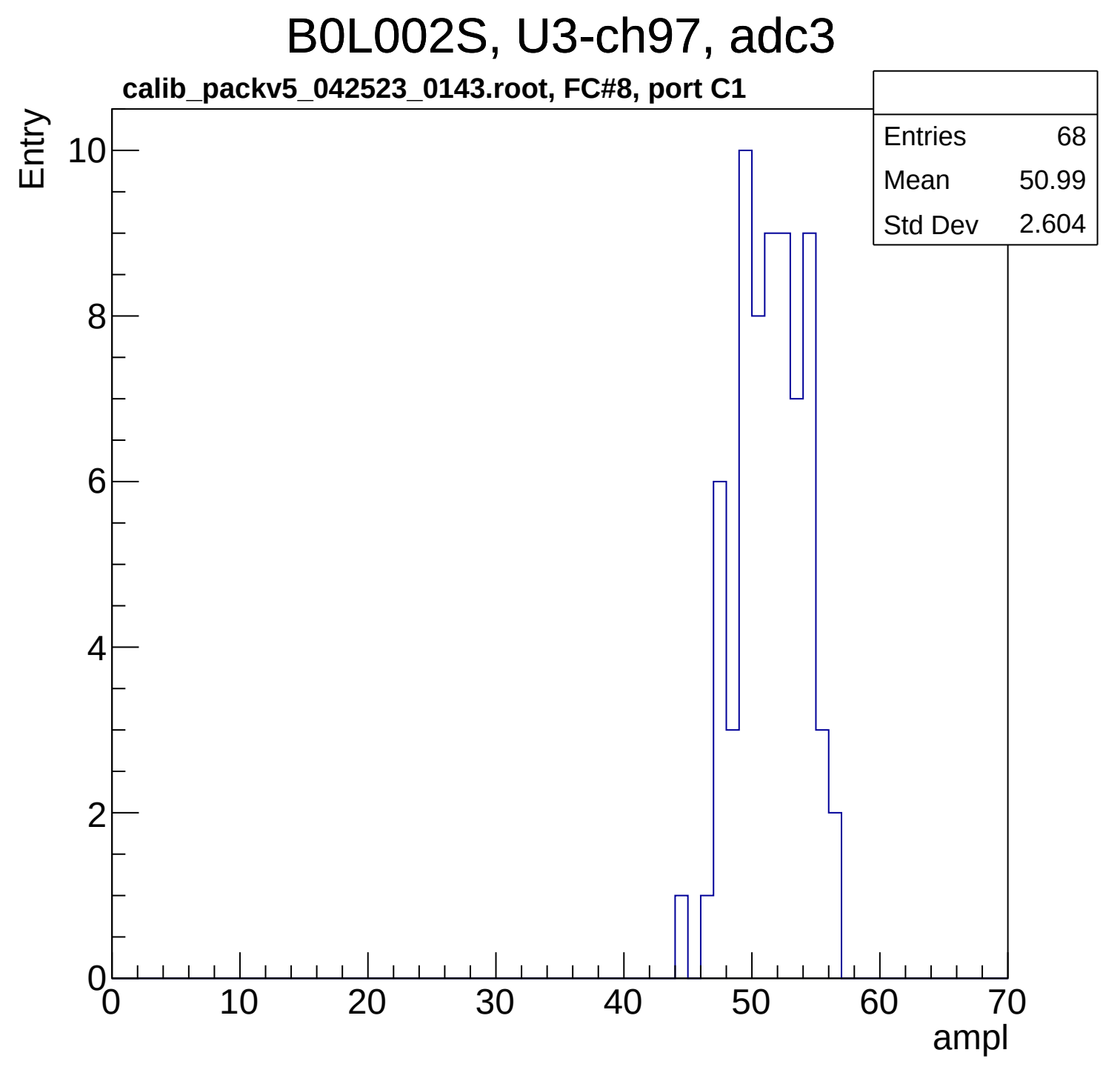

B0L002S, U3-ch96, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62
Std Dev 0.6325
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

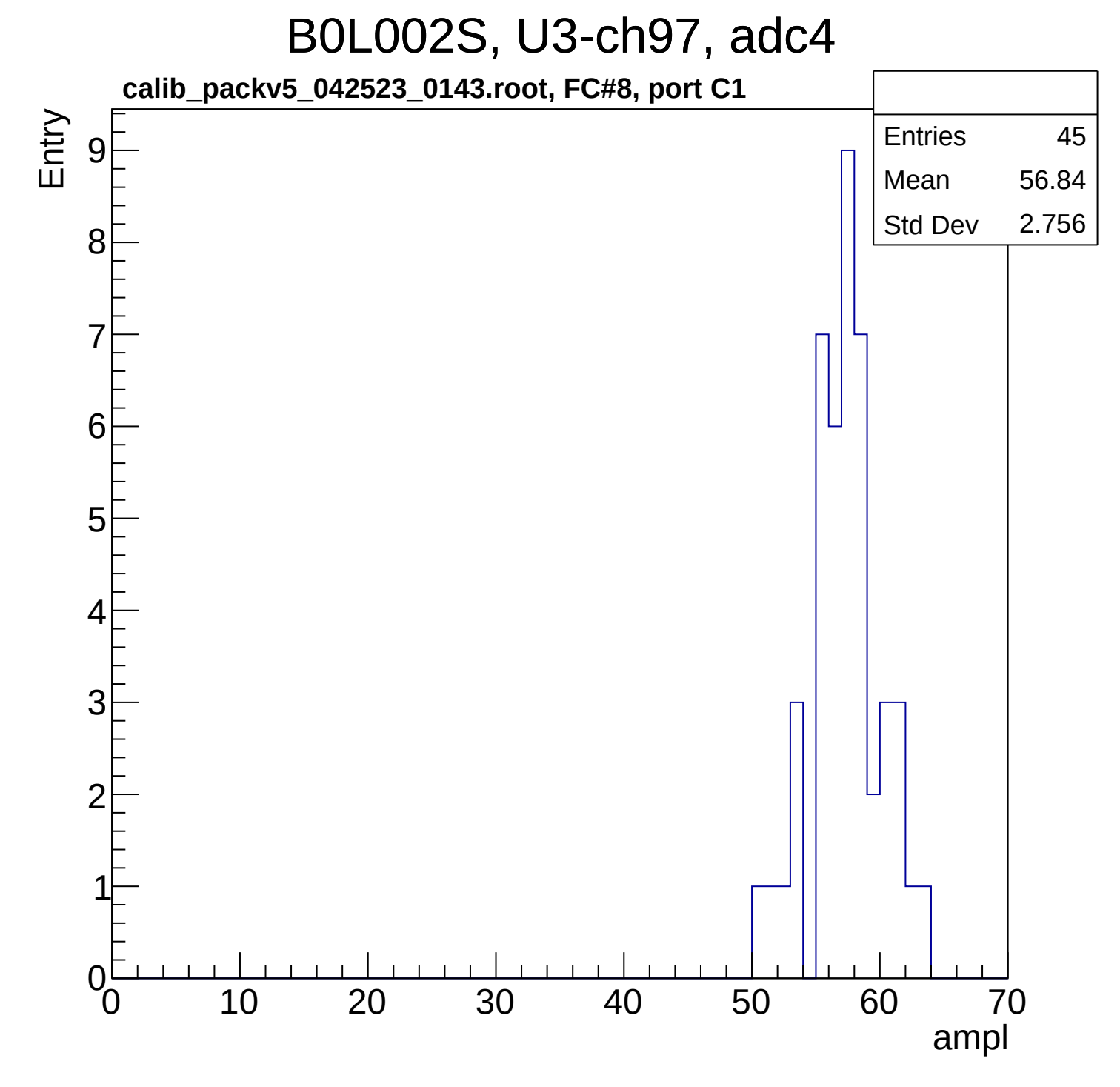

B0L002S, U3-ch96, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

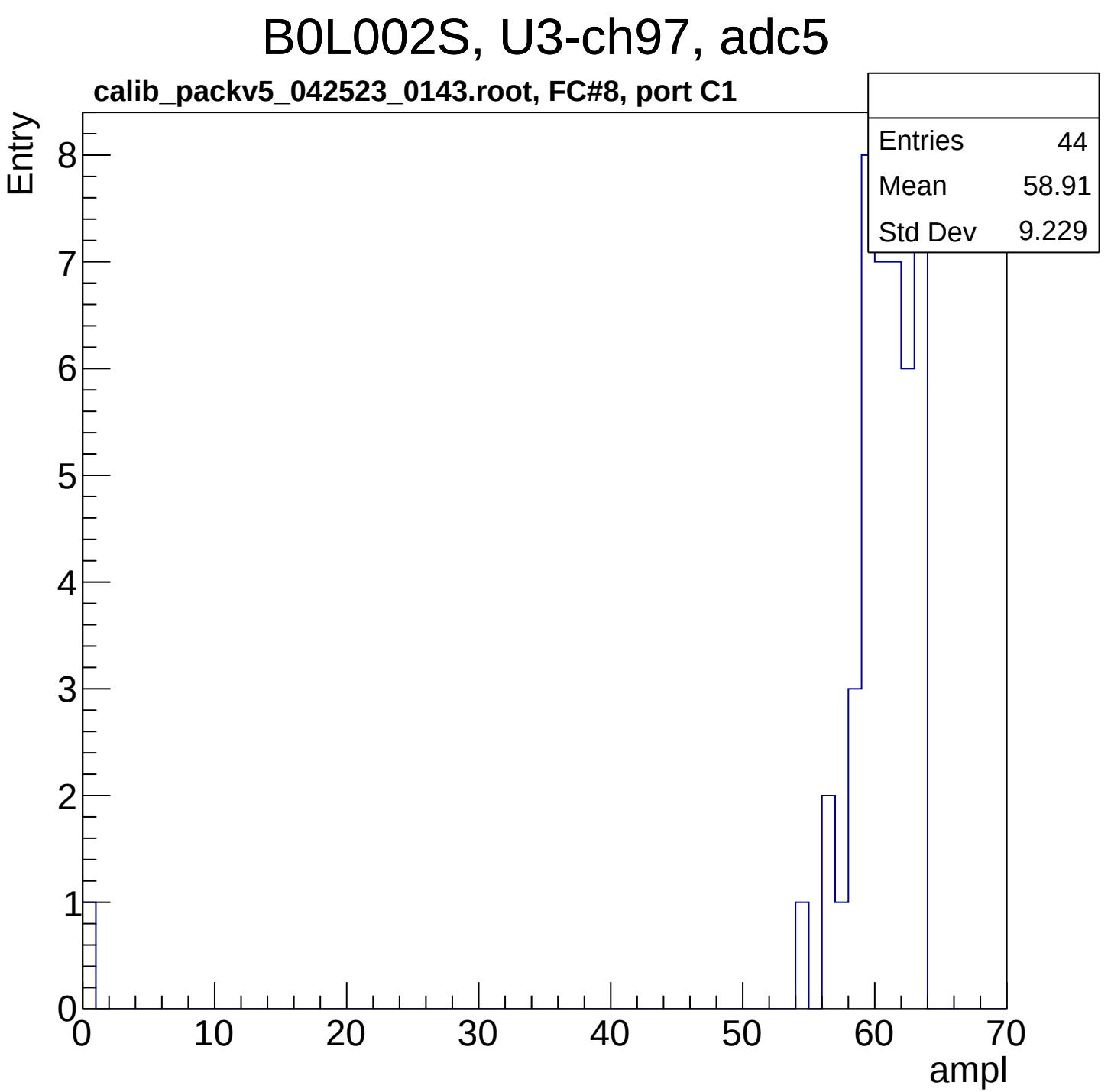

B0L002S, U3-ch97, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 30.2
Std Dev 6.101
Gaus mean : 30.2712
Gaus Width: 3.6731
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

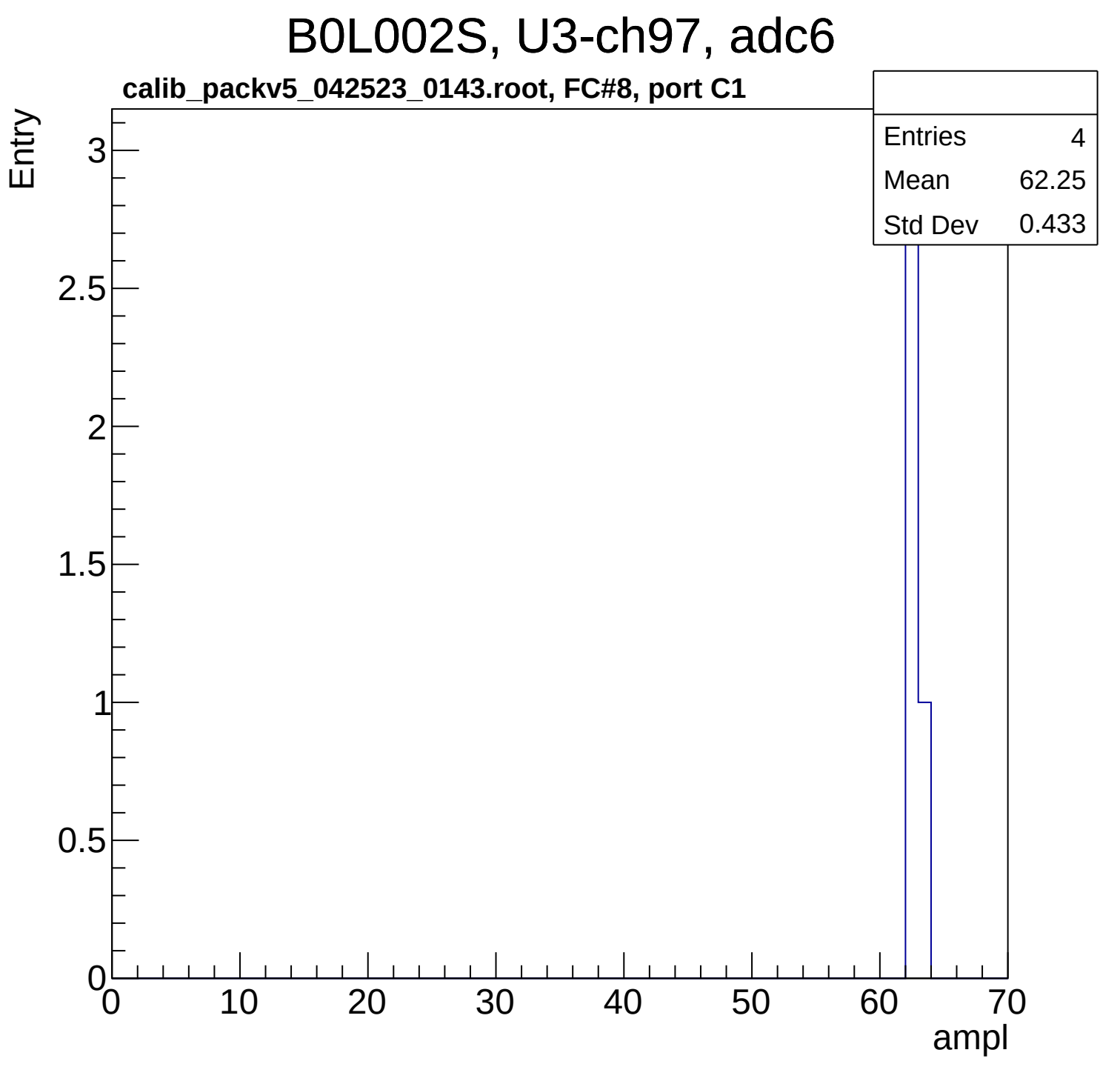

B0L002S, U3-ch97, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.85
Std Dev 3.623
Gaus mean : 38.2370
Gaus Width: 3.8055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

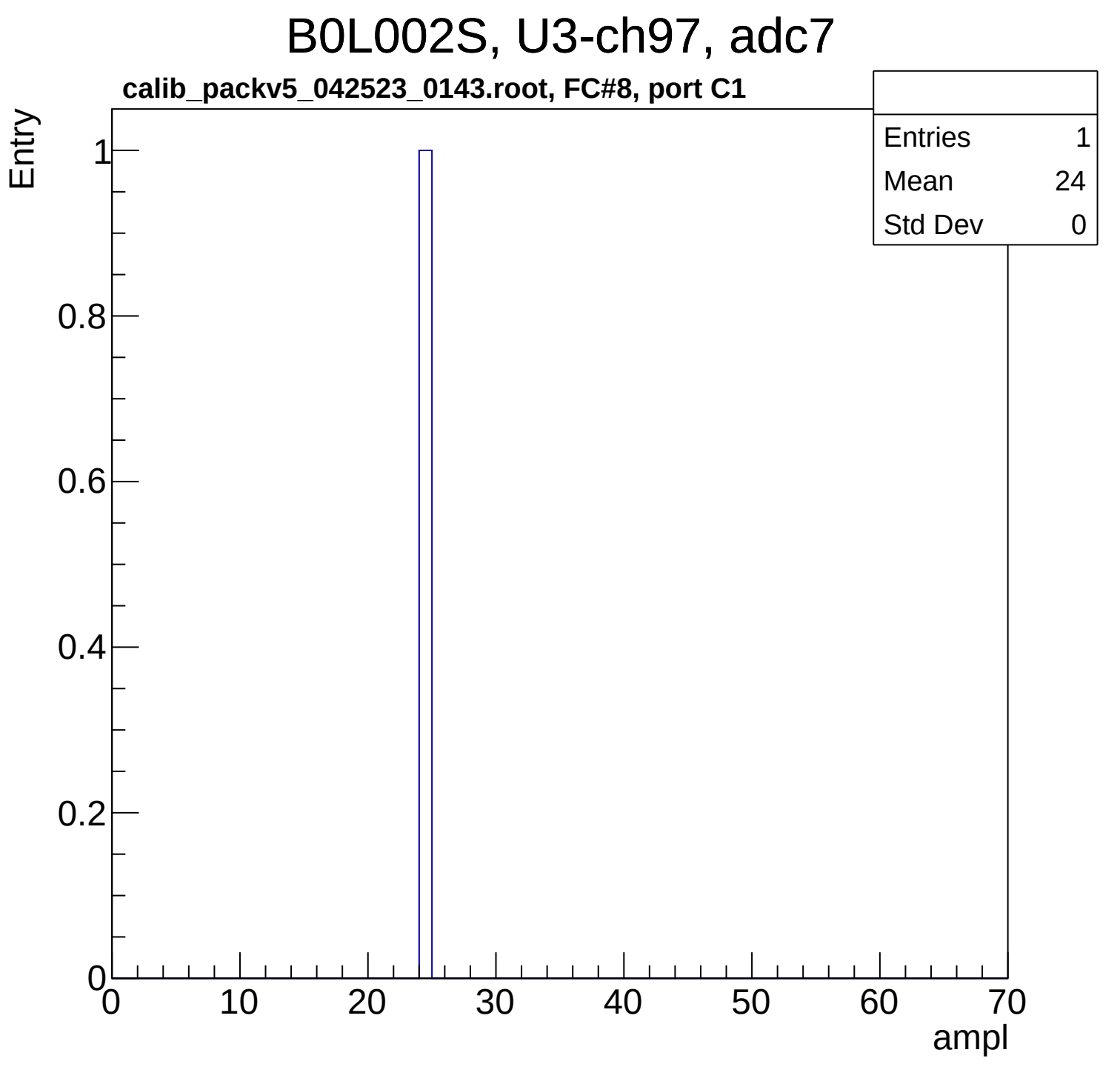

B0L002S, U3-ch97, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.87
Std Dev 3.091
Gaus mean : 44.8765
Gaus Width: 3.3521
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

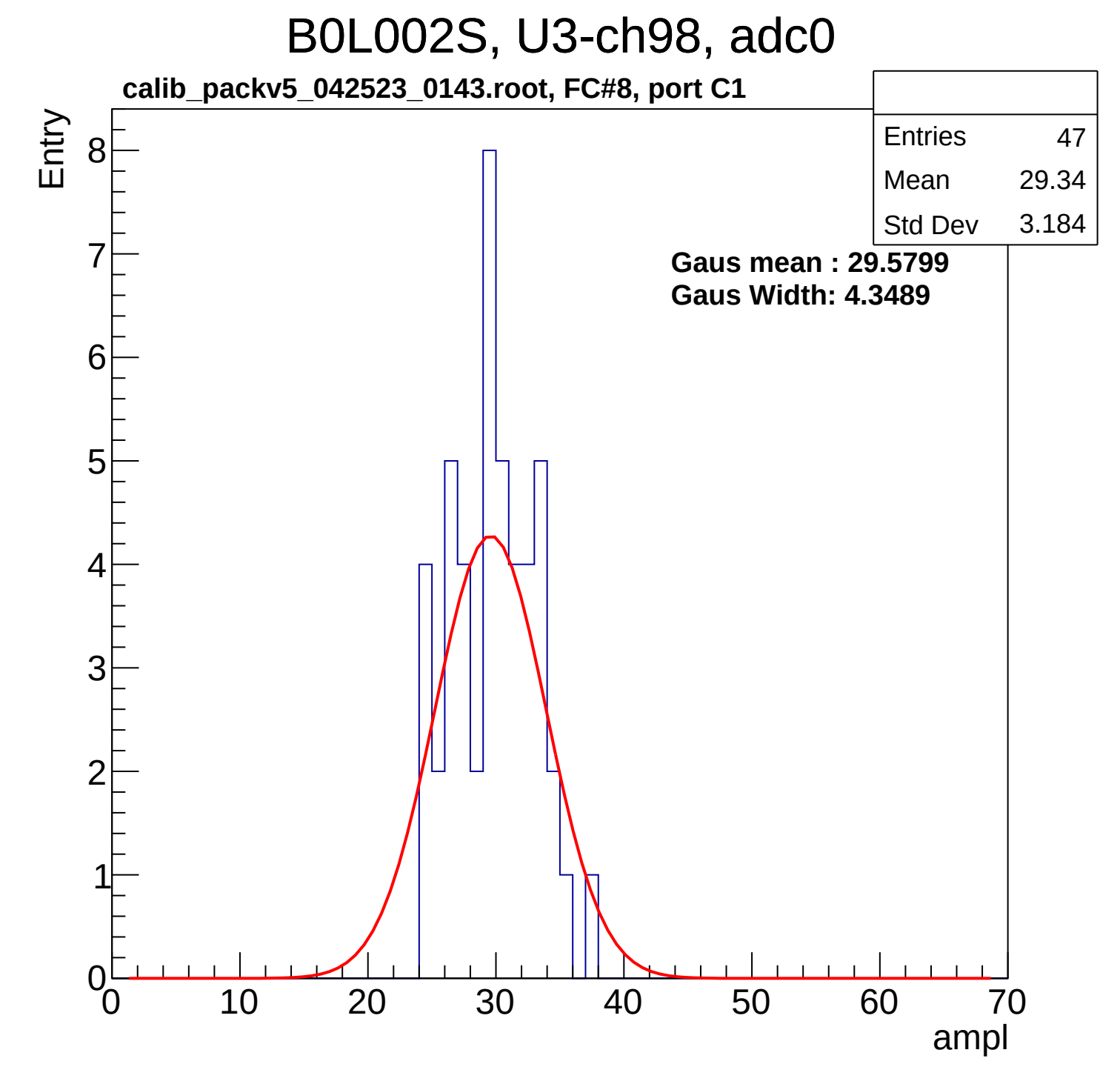

B0L002S, U3-ch97, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.99
Std Dev 2.604
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

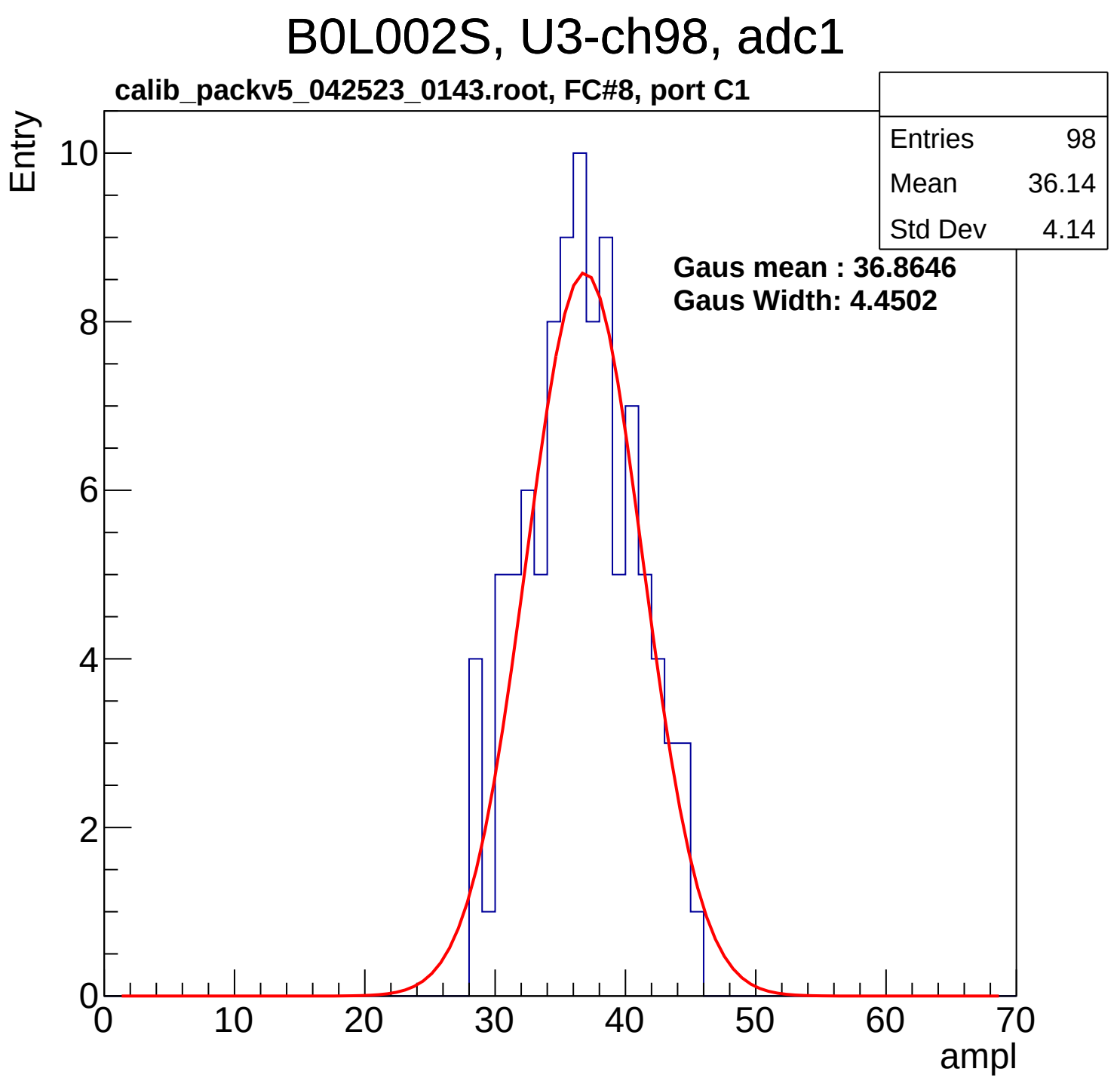

B0L002S, U3-ch97, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 56.84
Std Dev 2.756
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

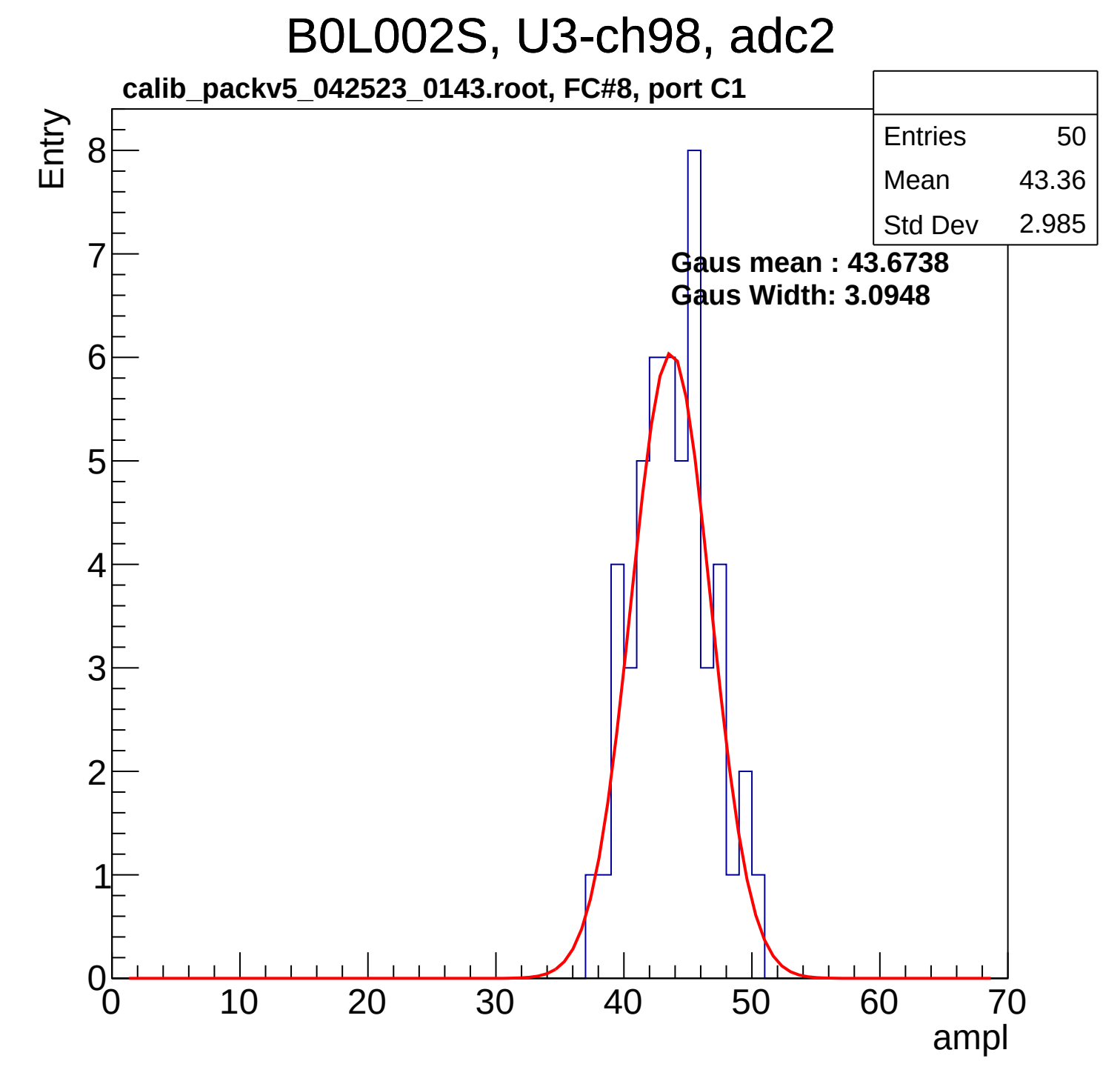

B0L002S, U3-ch97, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.91
Std Dev 9.229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

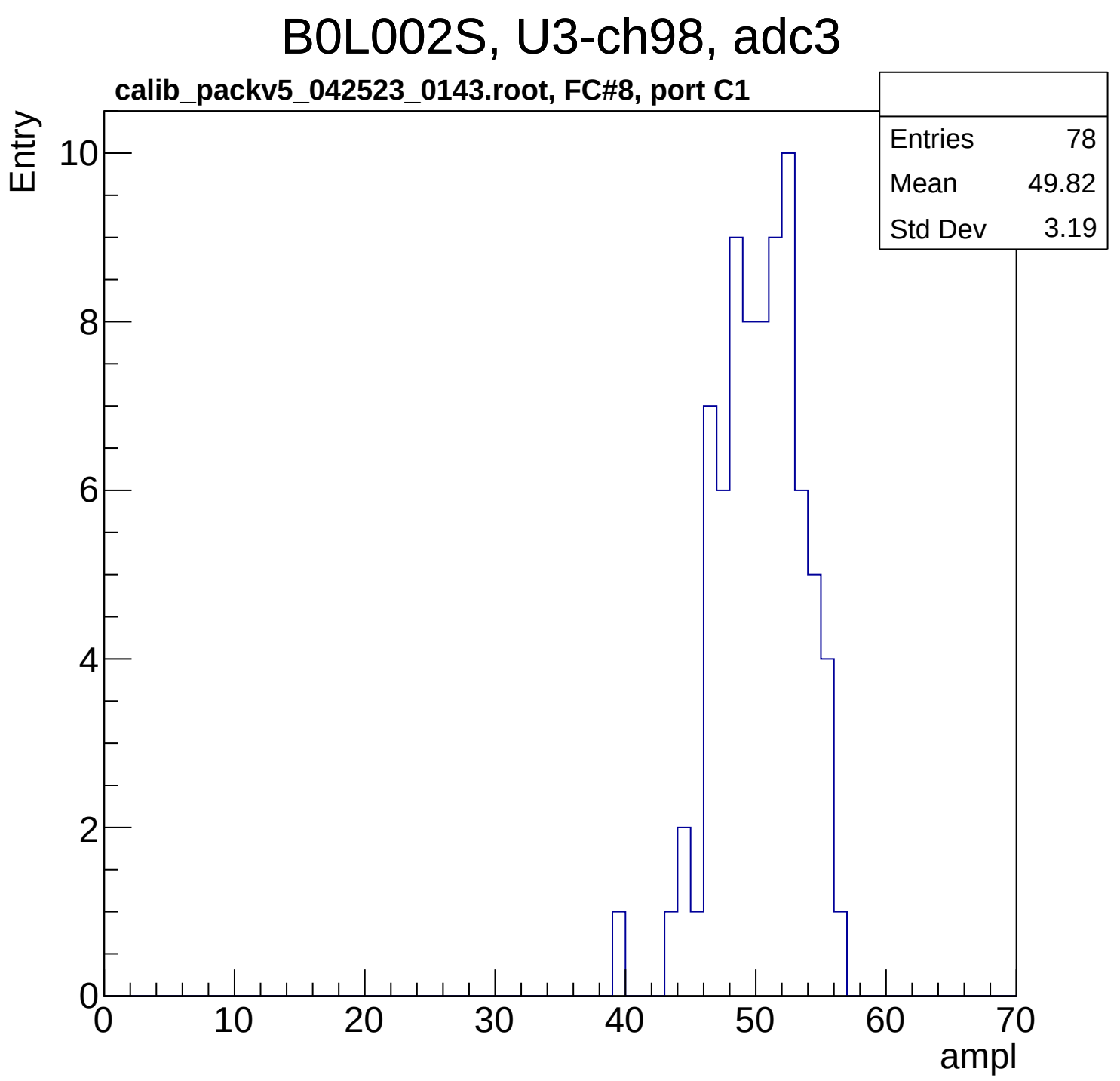

B0L002S, U3-ch97, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

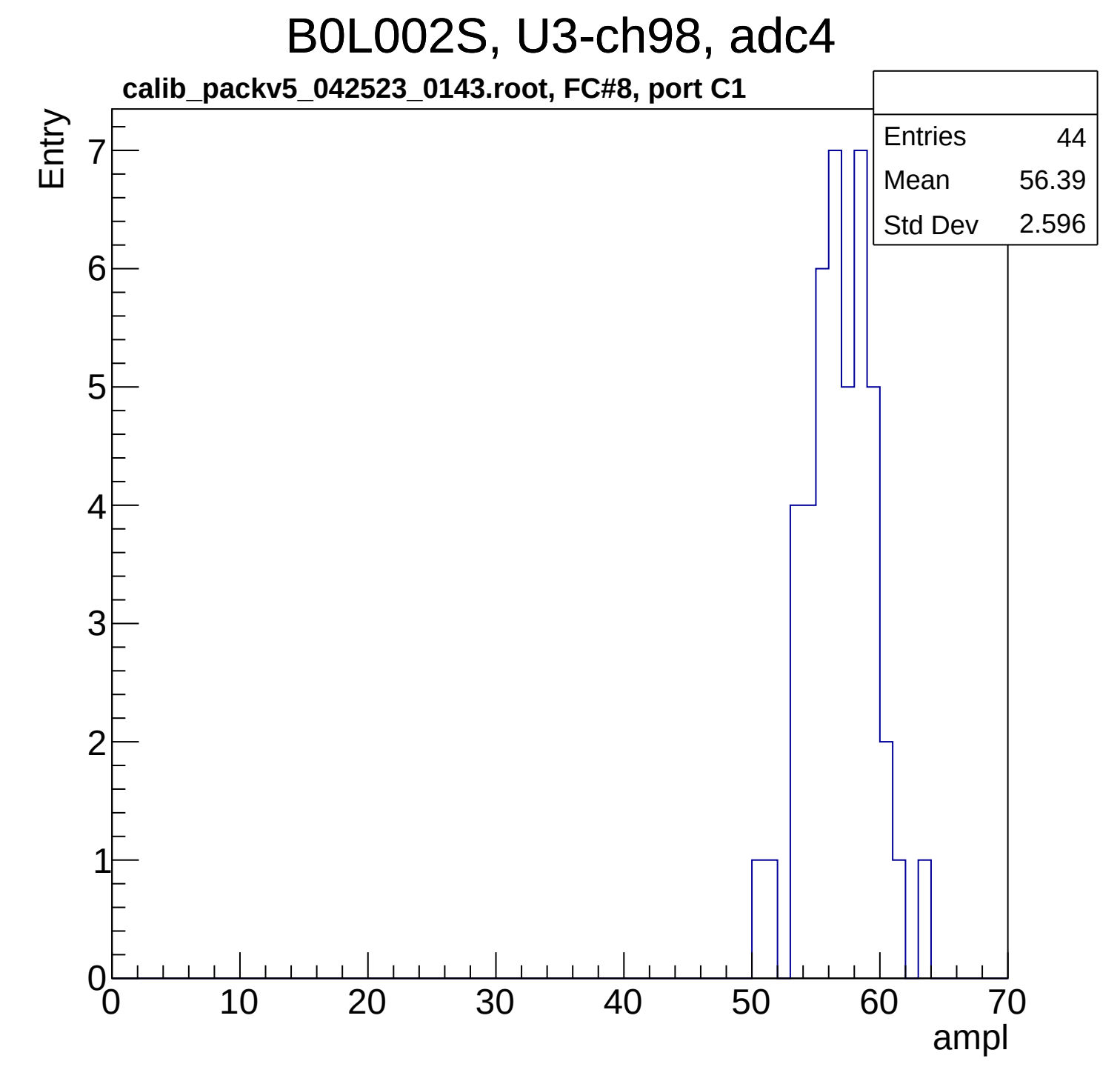

B0L002S, U3-ch97, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

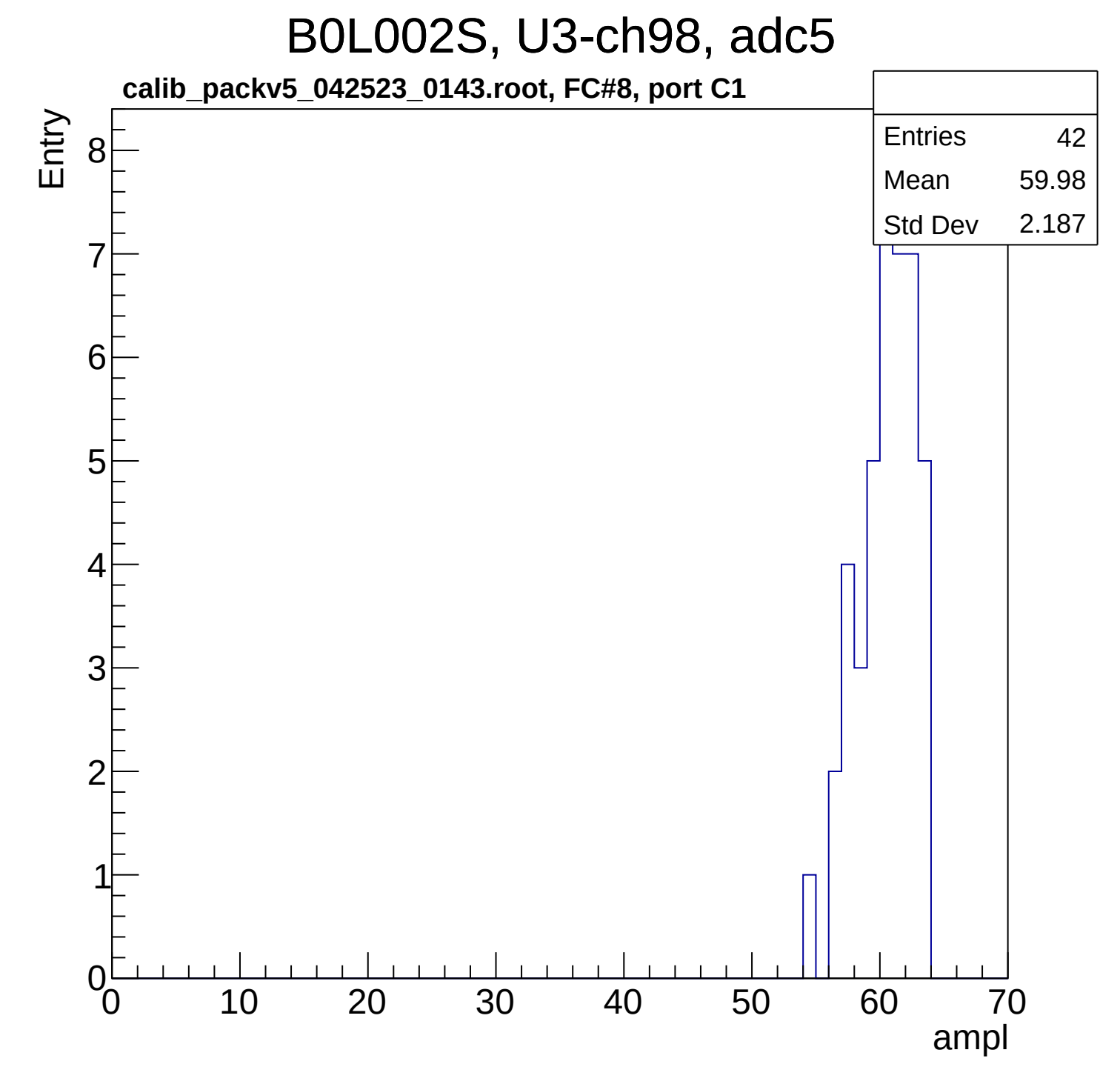

B0L002S, U3-ch98, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 29.34
Std Dev 3.184
Gaus mean : 29.5799
Gaus Width: 4.3489
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

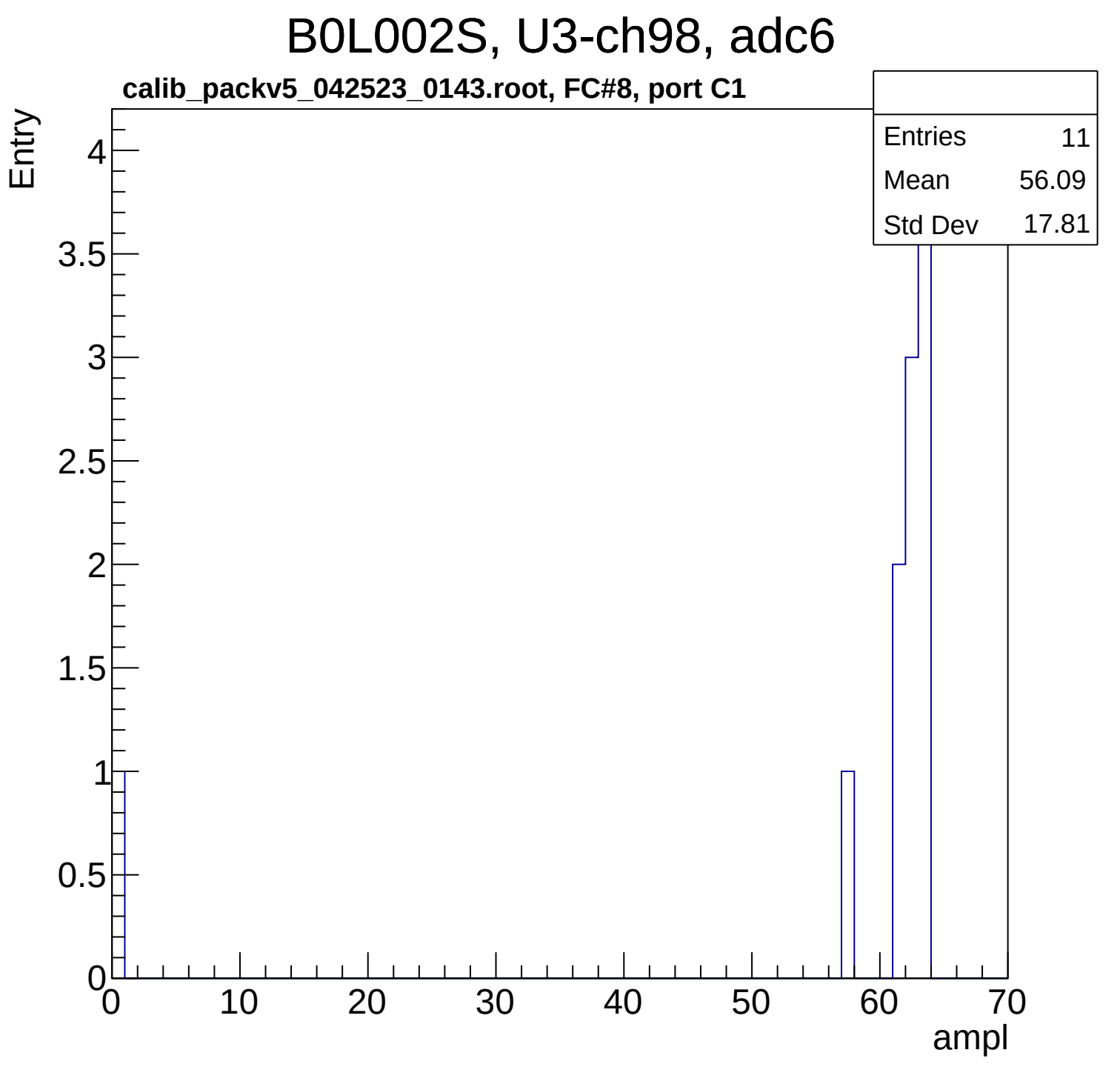

B0L002S, U3-ch98, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 98
Mean 36.14
Std Dev 4.14
Gaus mean : 36.8646
Gaus Width: 4.4502
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

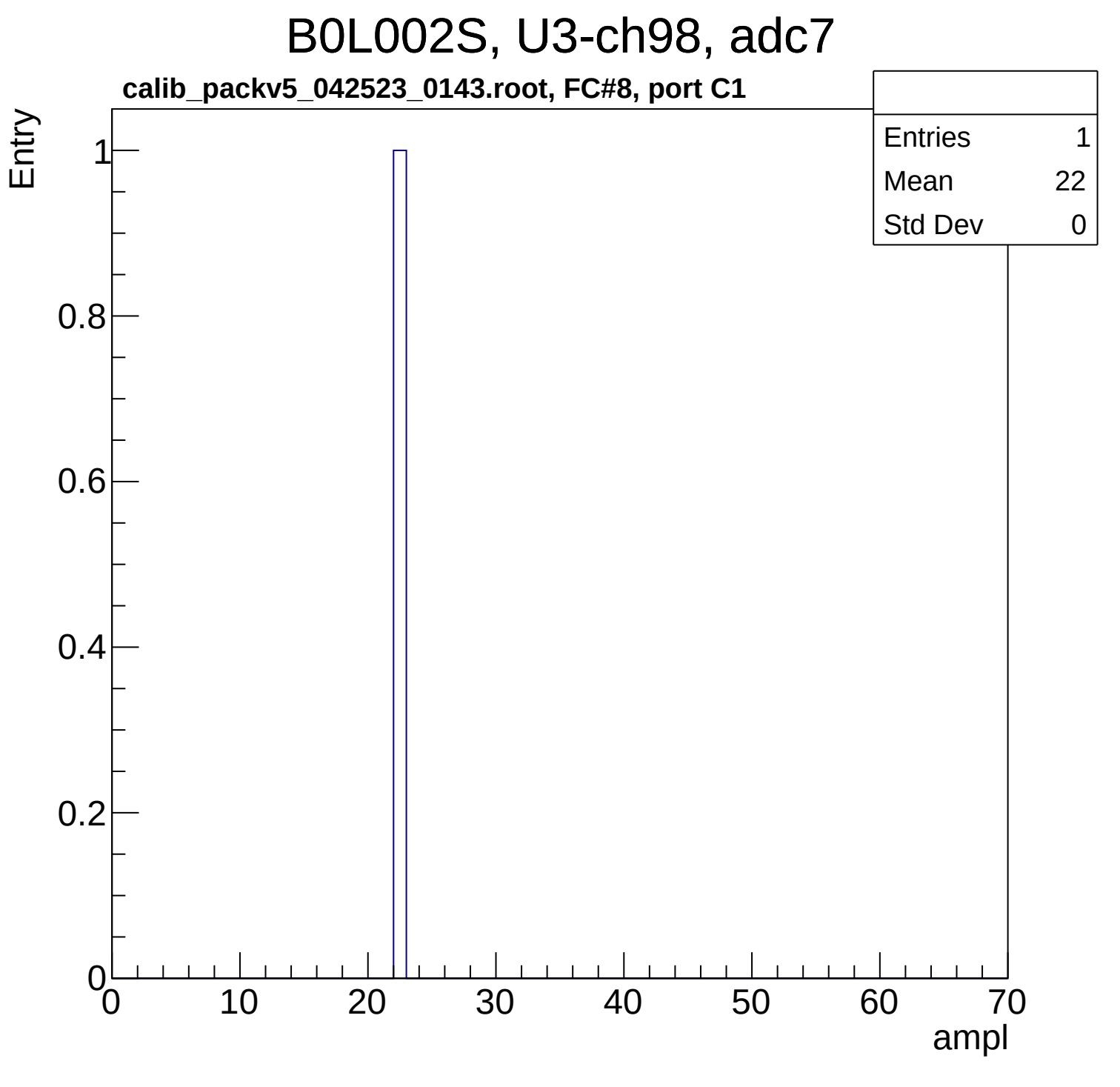

B0L002S, U3-ch98, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 43.36
Std Dev 2.985
Gaus mean : 43.6738
Gaus Width: 3.0948
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

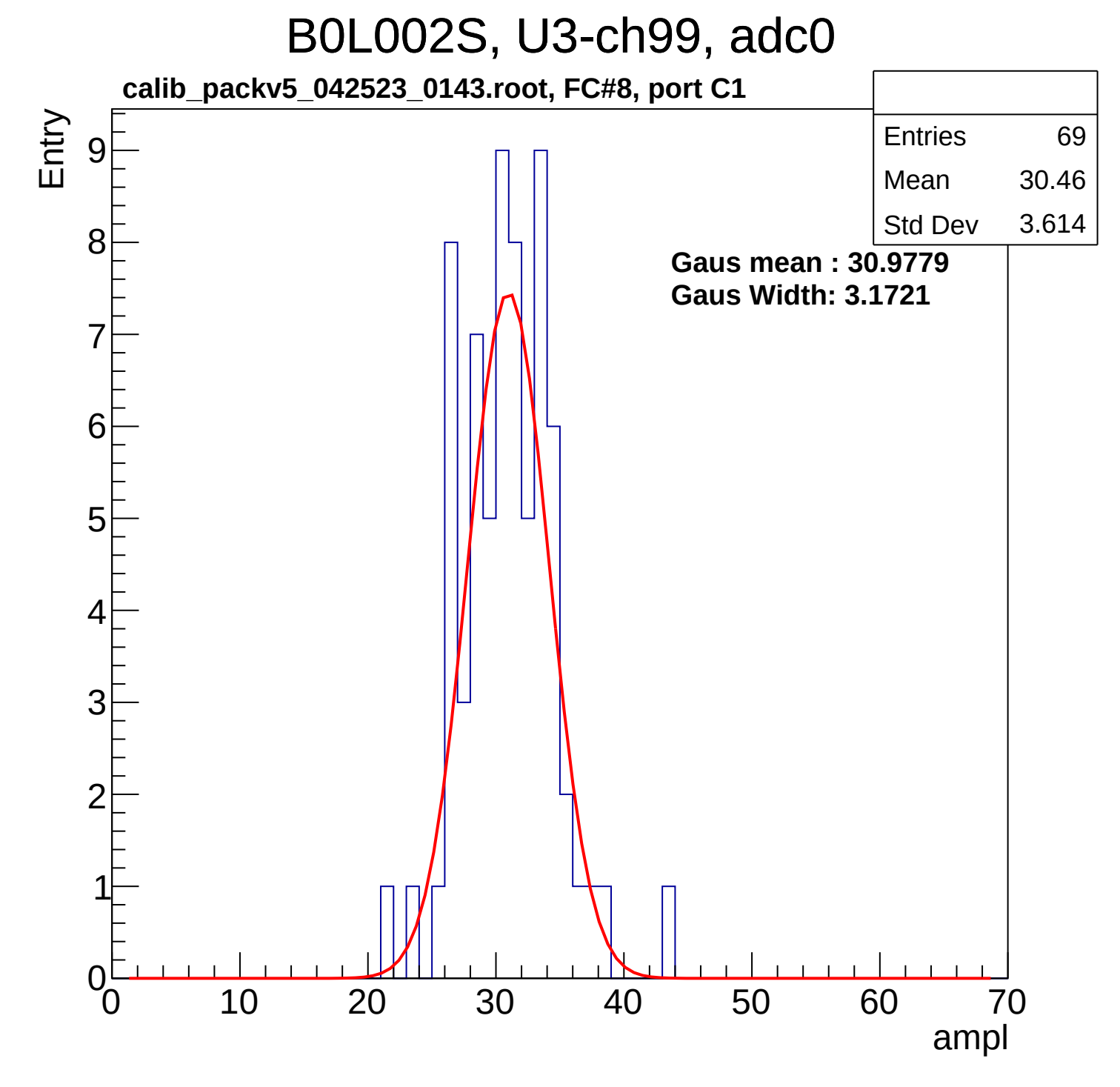

B0L002S, U3-ch98, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 49.82
Std Dev 3.19
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

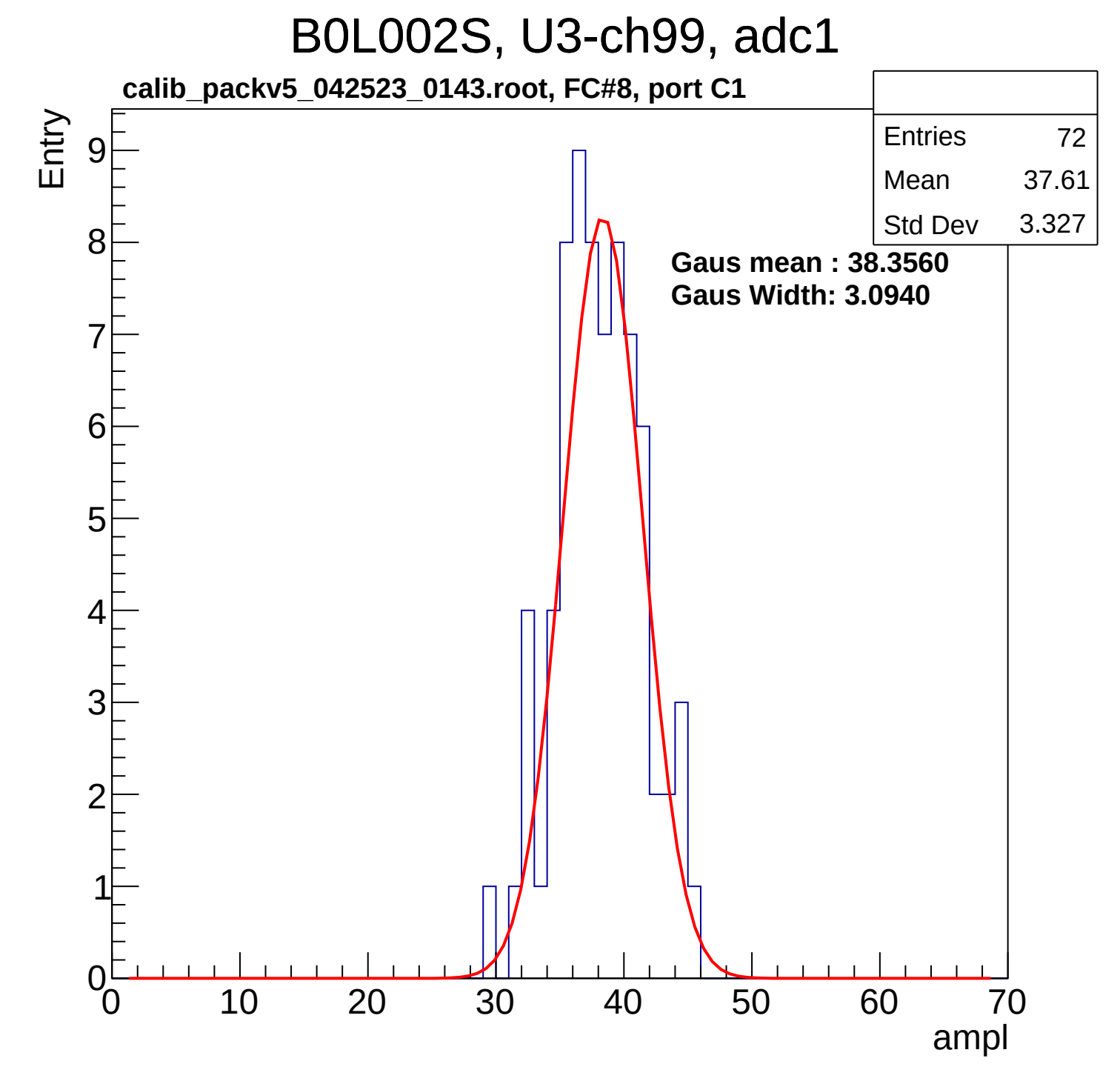

B0L002S, U3-ch98, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 56.39
Std Dev 2.596
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

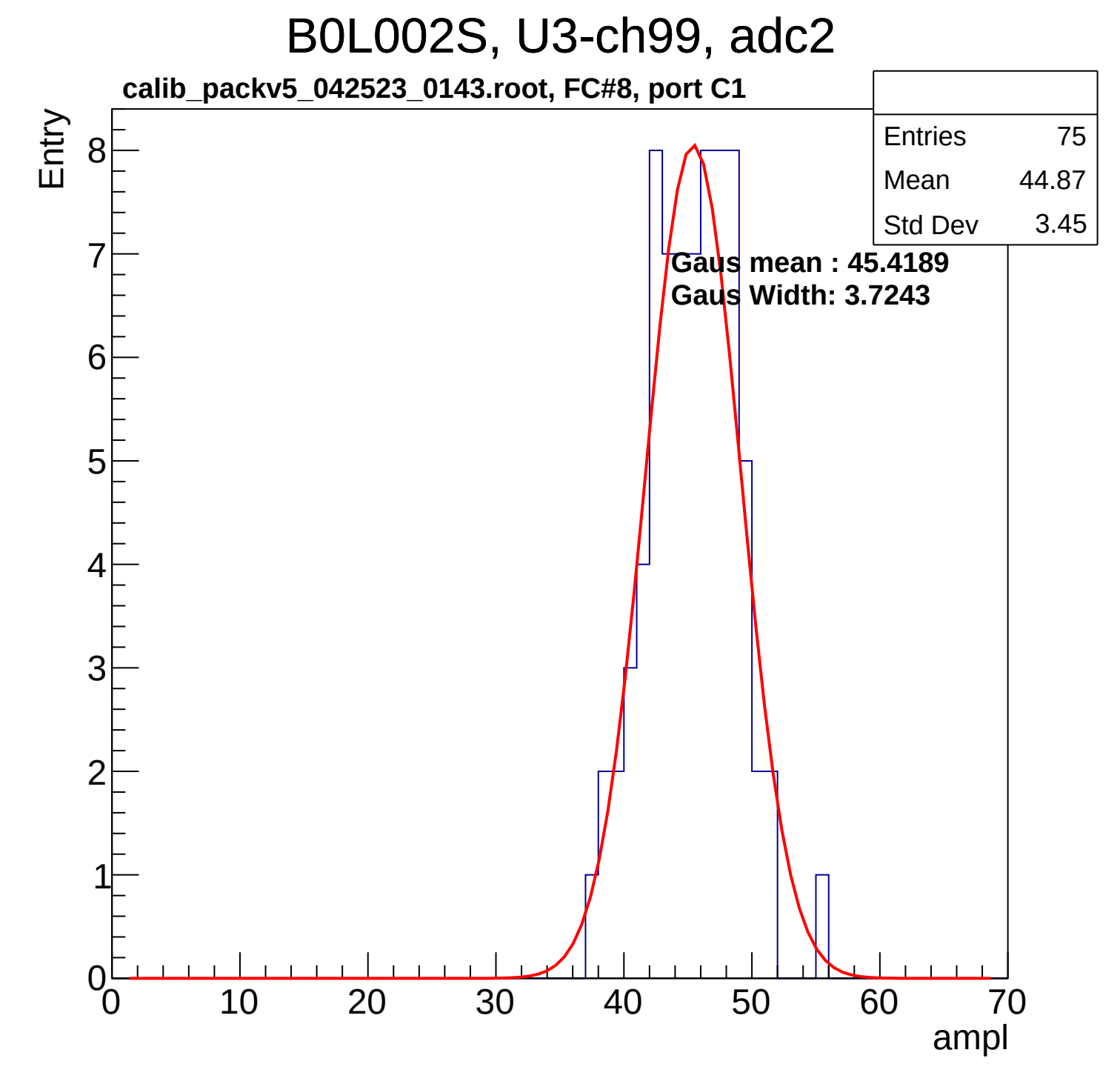

B0L002S, U3-ch98, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.98
Std Dev 2.187
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

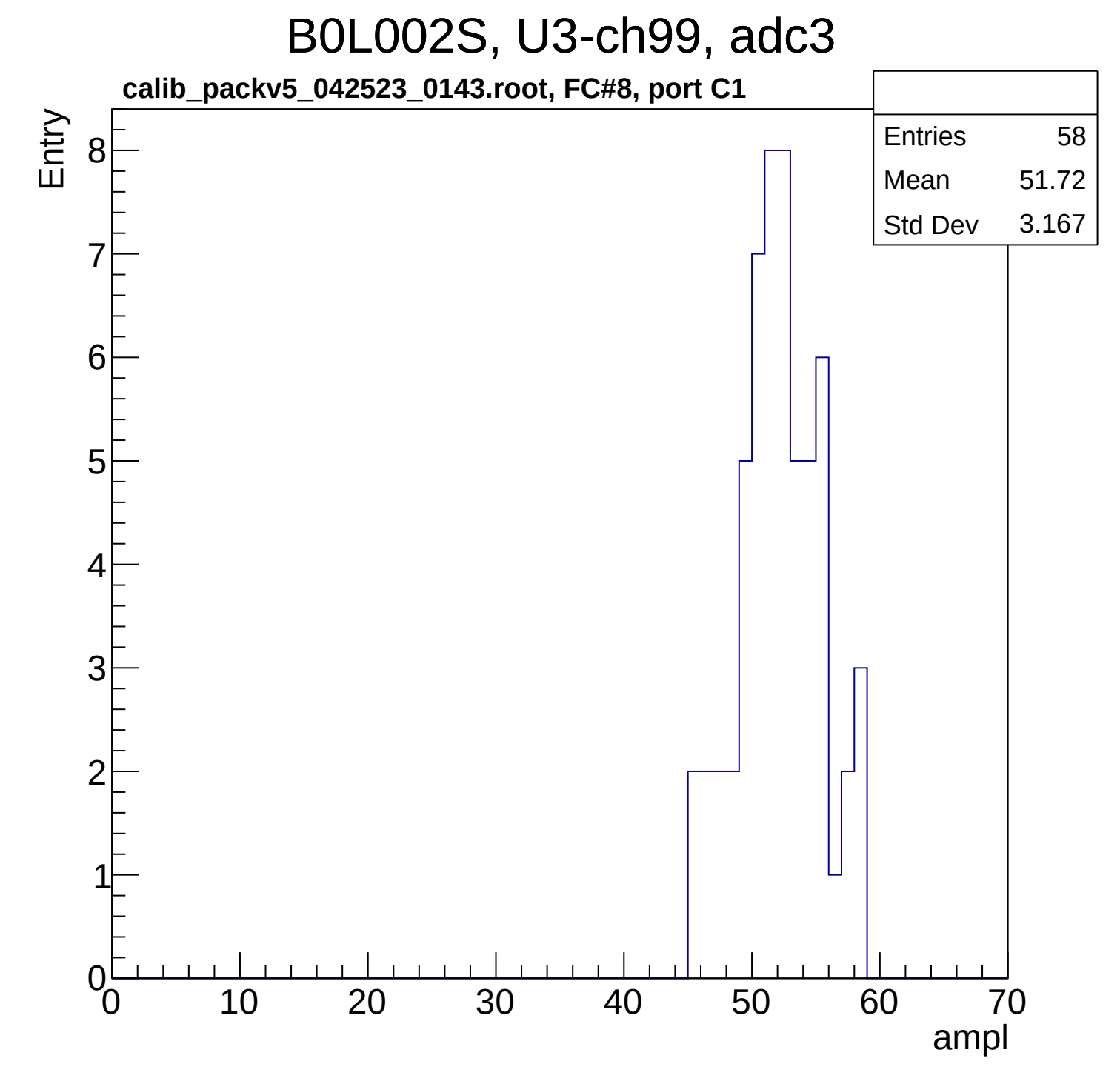

B0L002S, U3-ch98, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 11
Mean 56.09
Std Dev 17.81
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L002S, U3-ch98, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch99, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 30.46
Std Dev 3.614
Gaus mean : 30.9779
Gaus Width: 3.1721
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch99, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.61
Std Dev 3.327
Gaus mean : 38.3560
Gaus Width: 3.0940
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch99, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 44.87
Std Dev 3.45
Gaus mean : 45.4189
Gaus Width: 3.7243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch99, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.72
Std Dev 3.167
Entry
ampl
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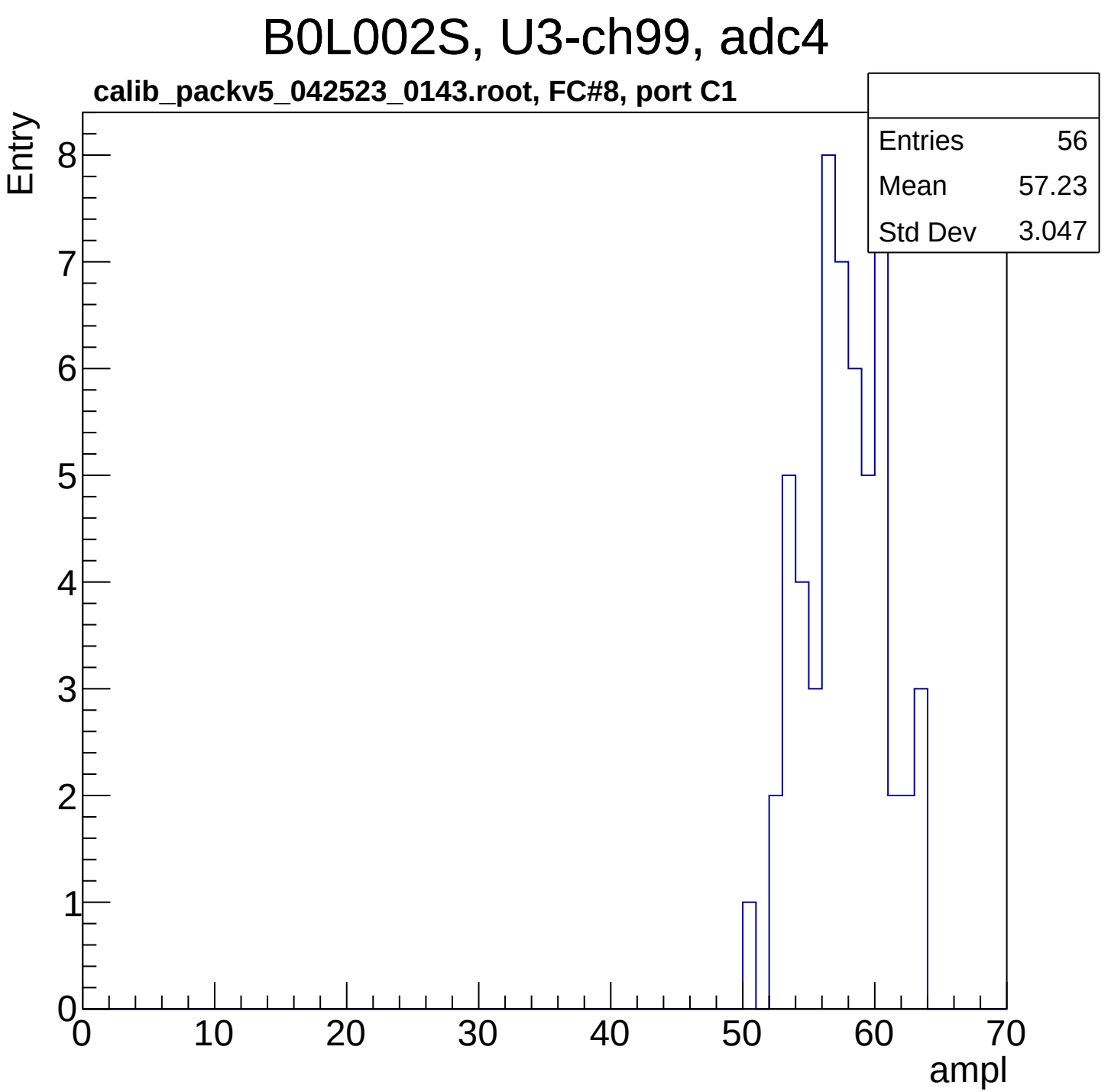
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch99, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.23
Std Dev 3.047
Entry
ampl
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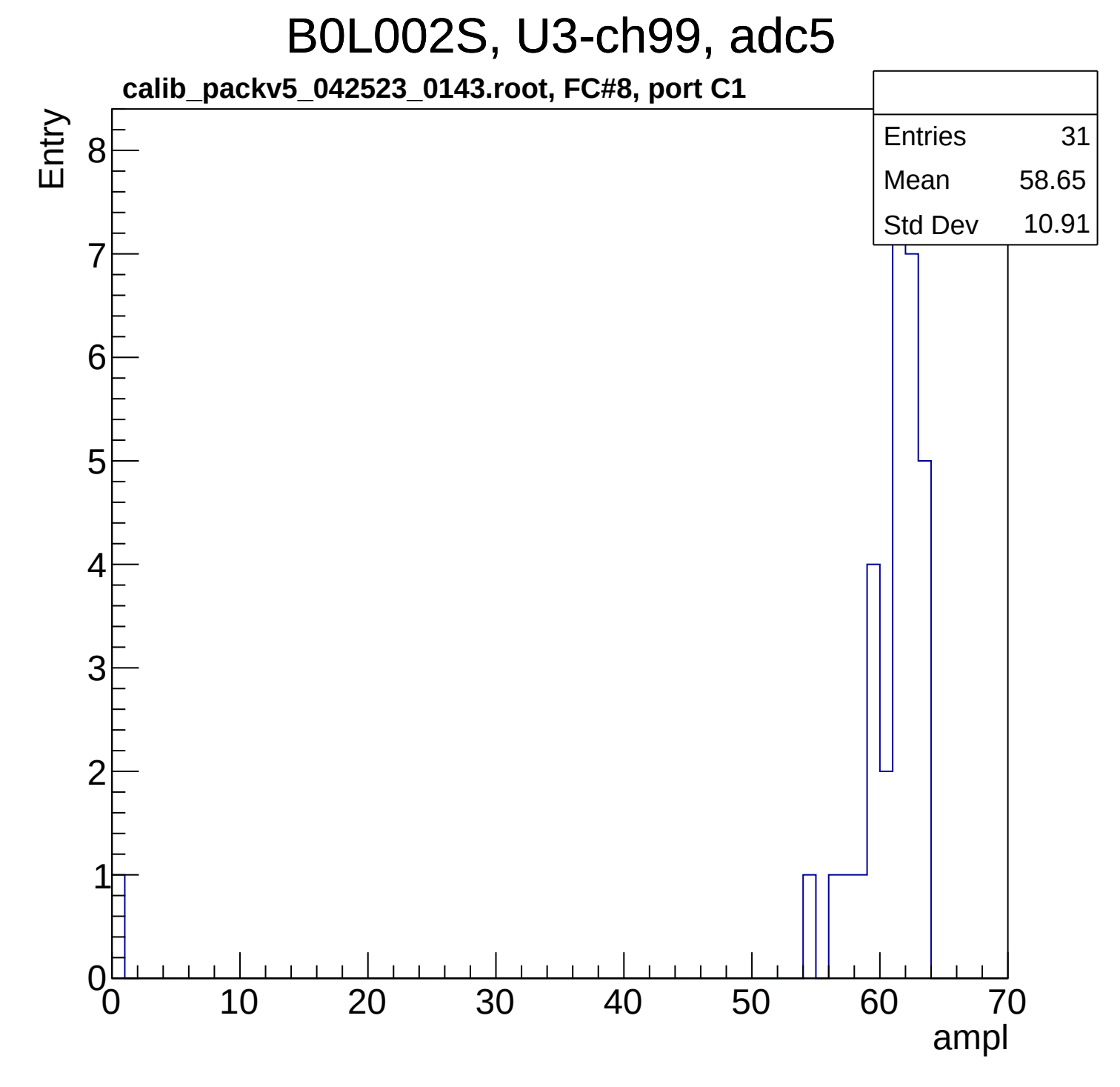
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch99, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 58.65
Std Dev 10.91
Entry
ampl
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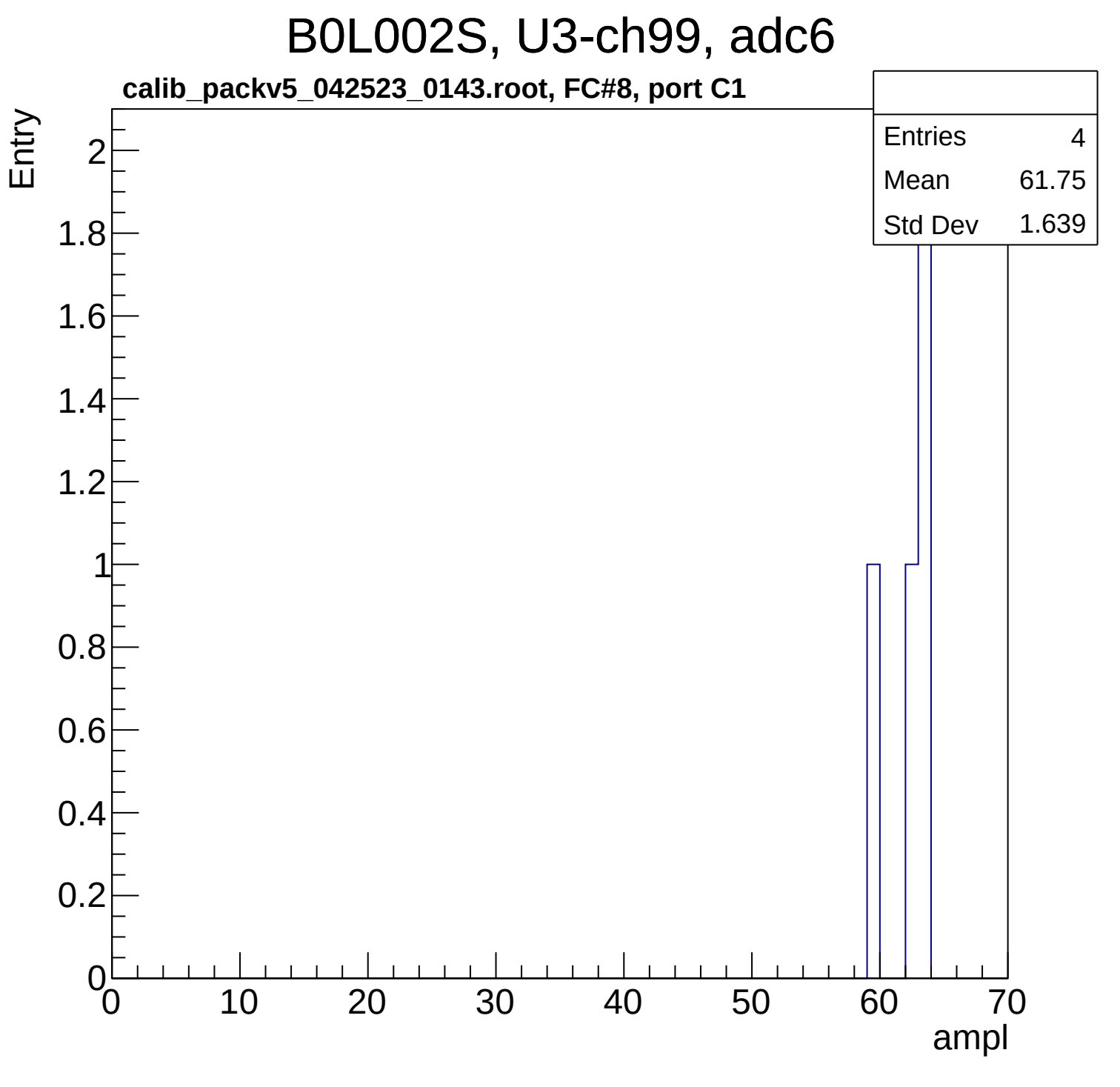
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch99, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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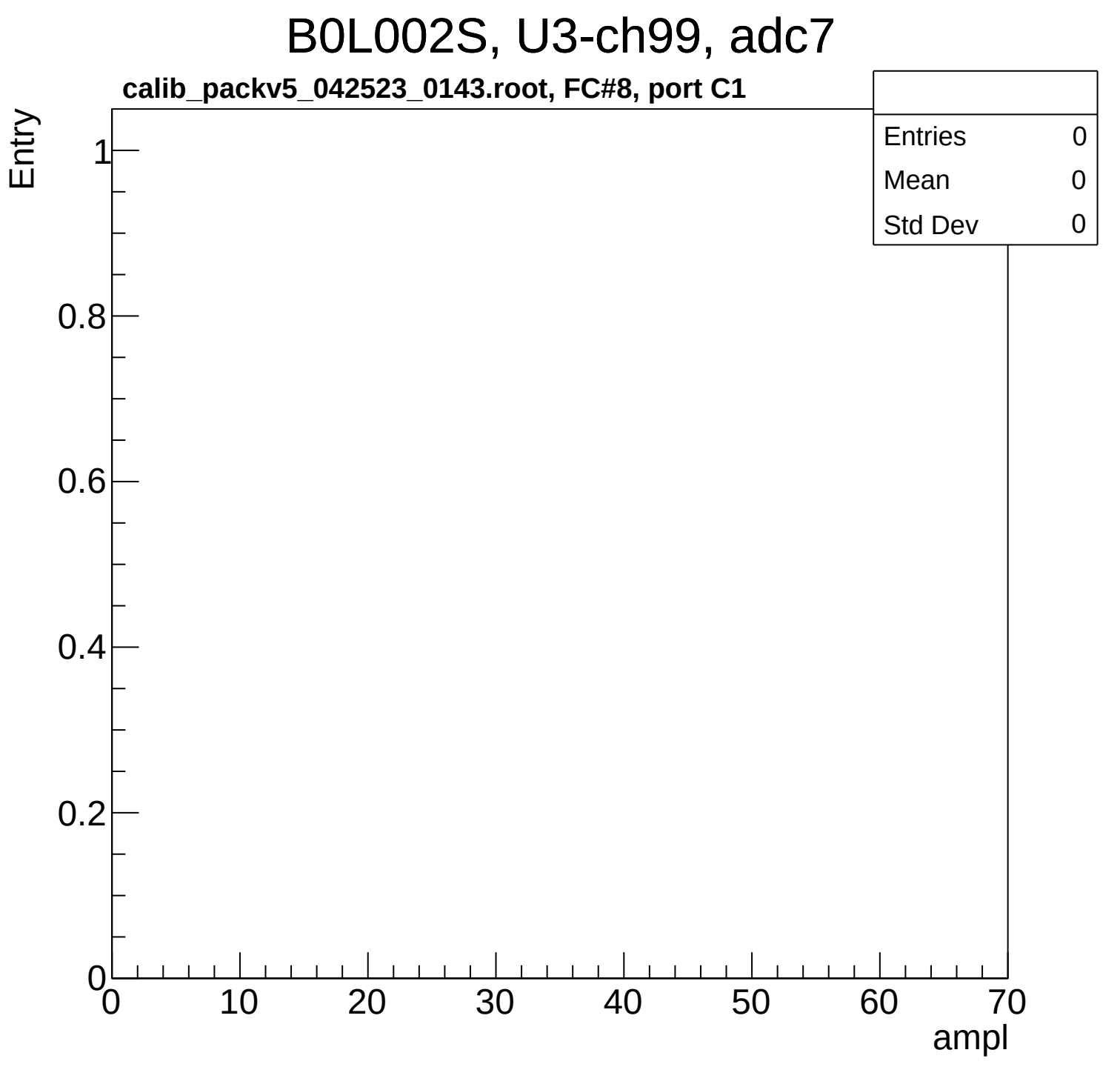
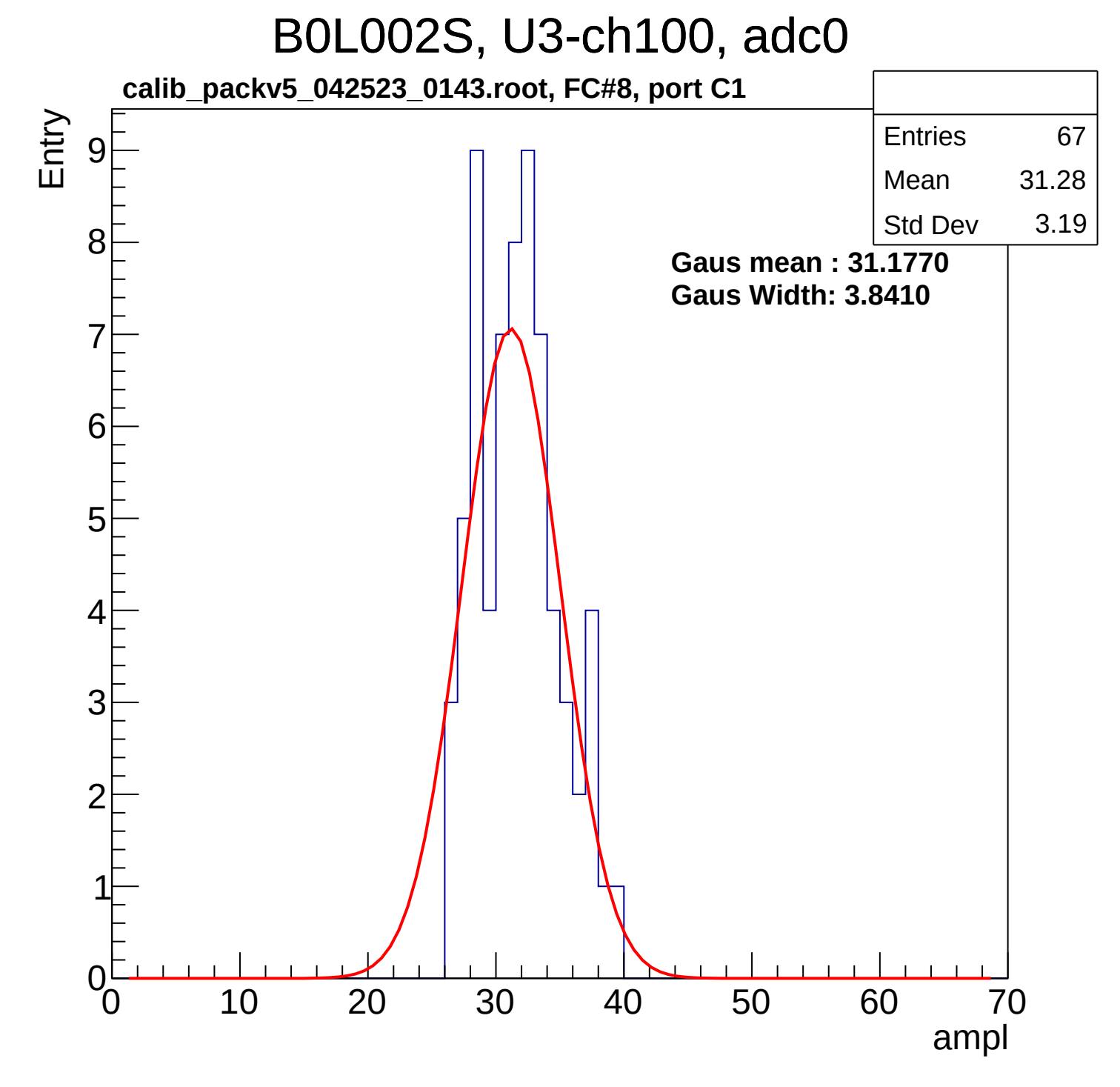
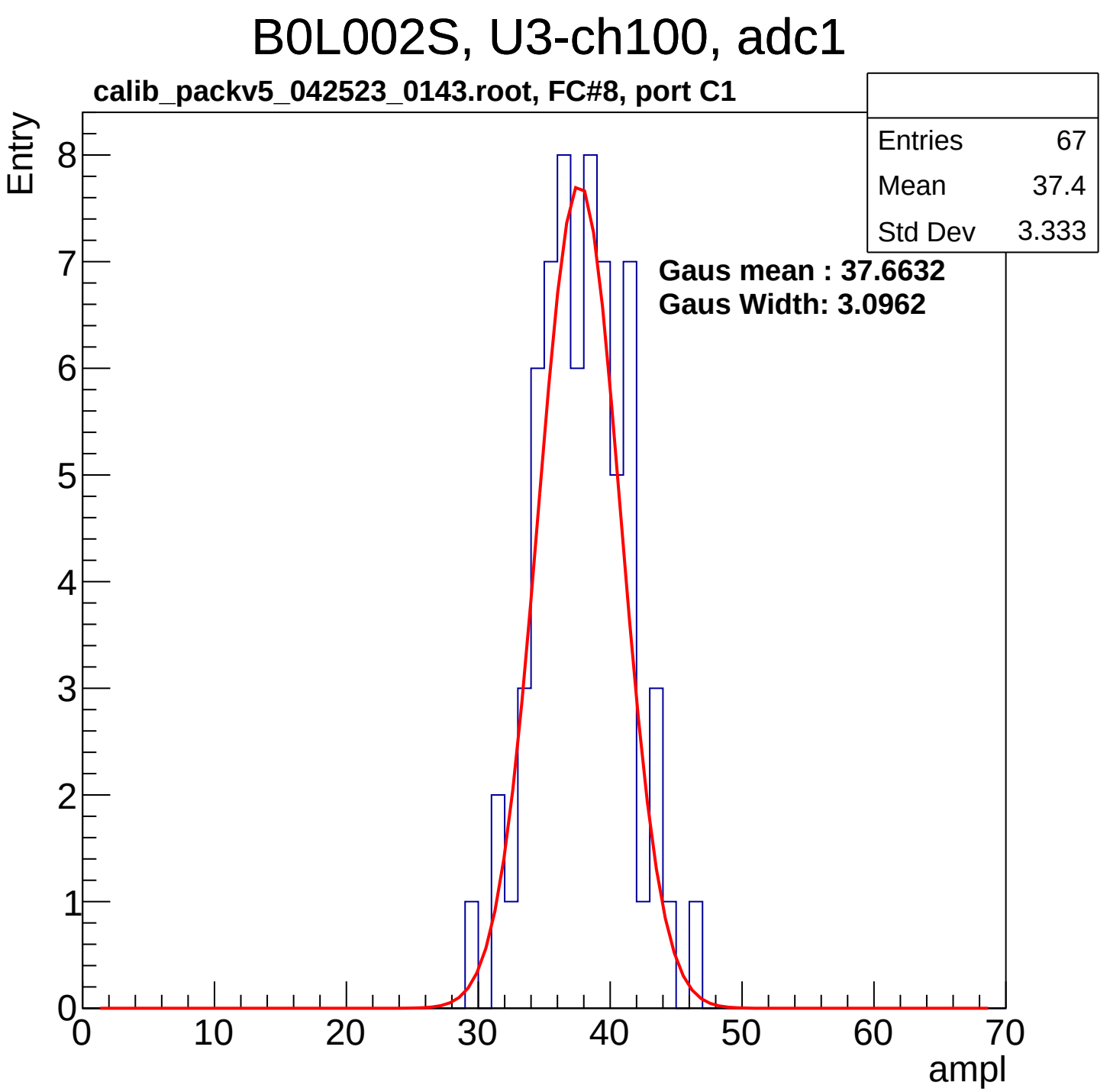
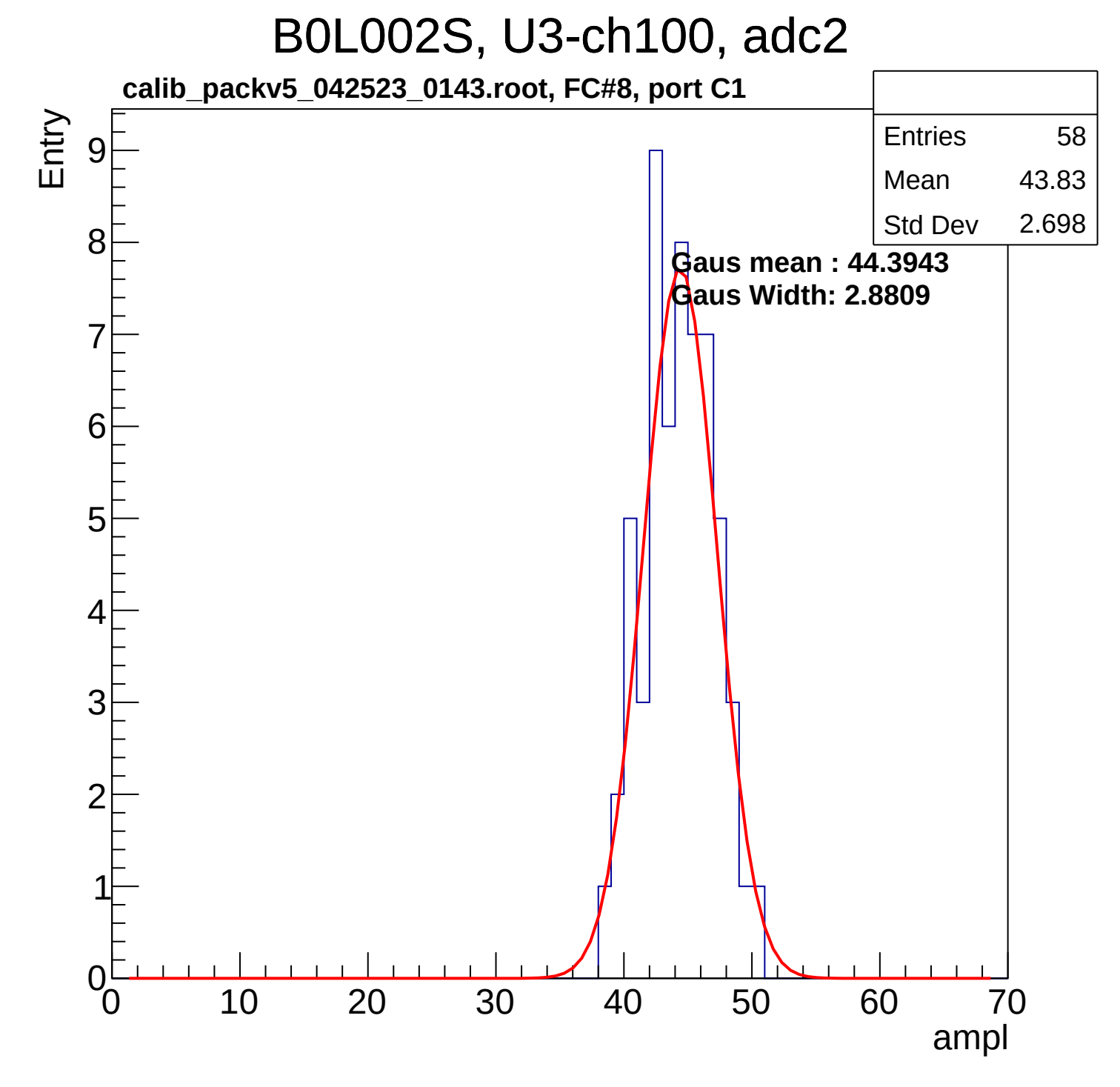
20
30
40
50
60
70


B0L002S, U3-ch99, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch100, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.28
Std Dev 3.19
Gaus mean : 31.1770
Gaus Width: 3.8410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch100, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.4
Std Dev 3.333
Gaus mean : 37.6632
Gaus Width: 3.0962
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch100, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.83
Std Dev 2.698
Gaus mean : 44.3943
Gaus Width: 2.8809
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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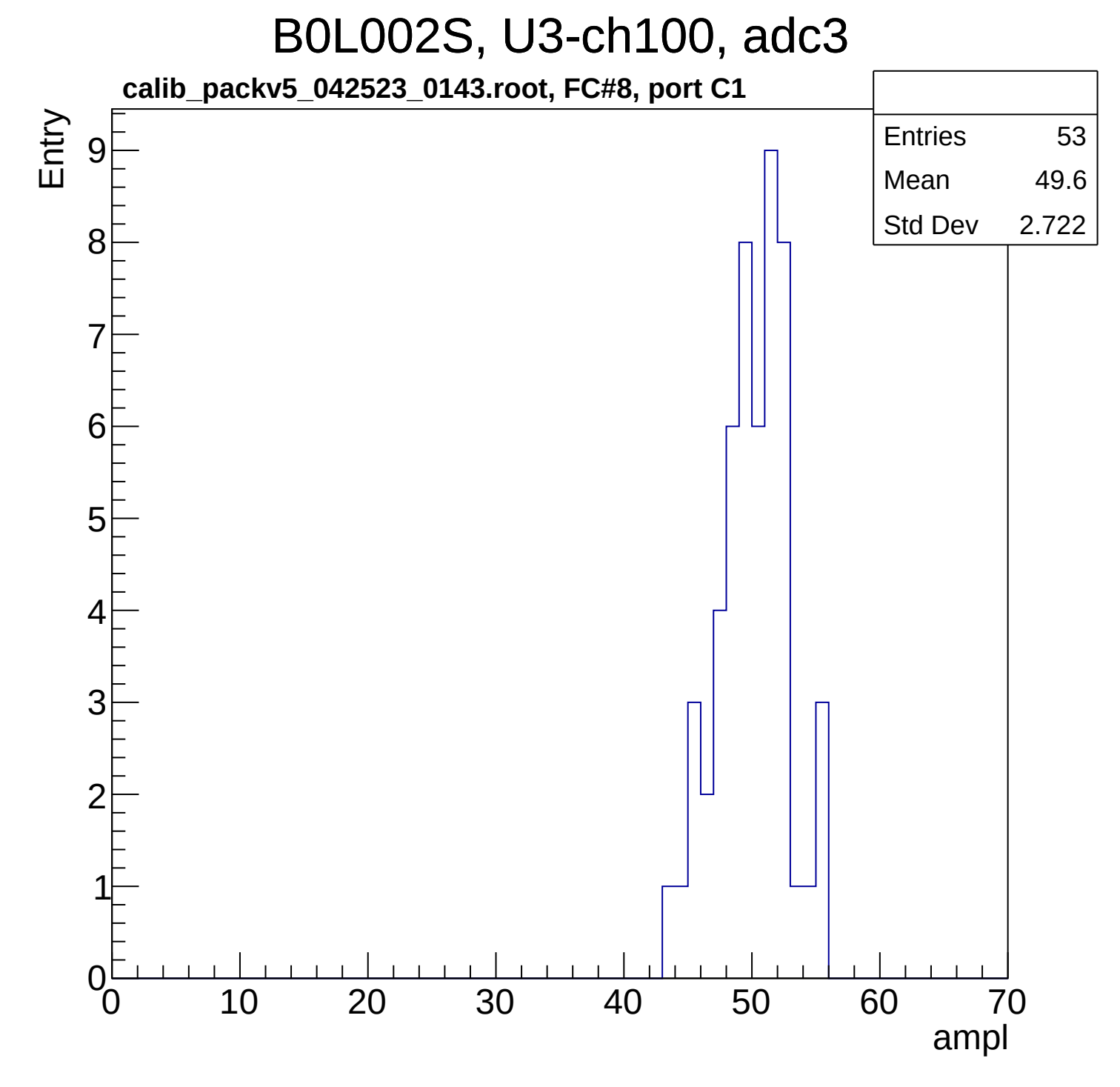
B0L002S, U3-ch100, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 49.6
Std Dev 2.722
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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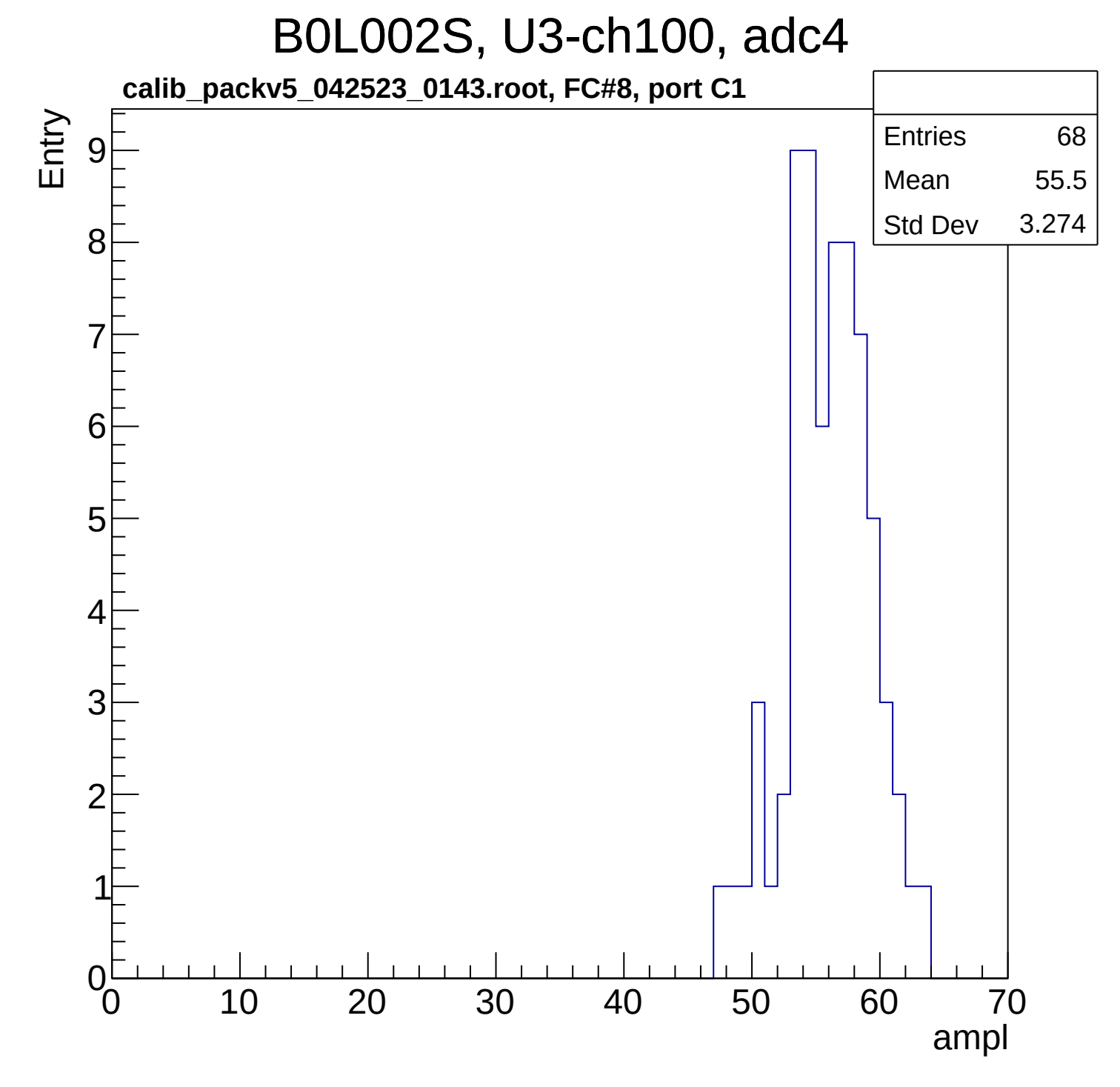
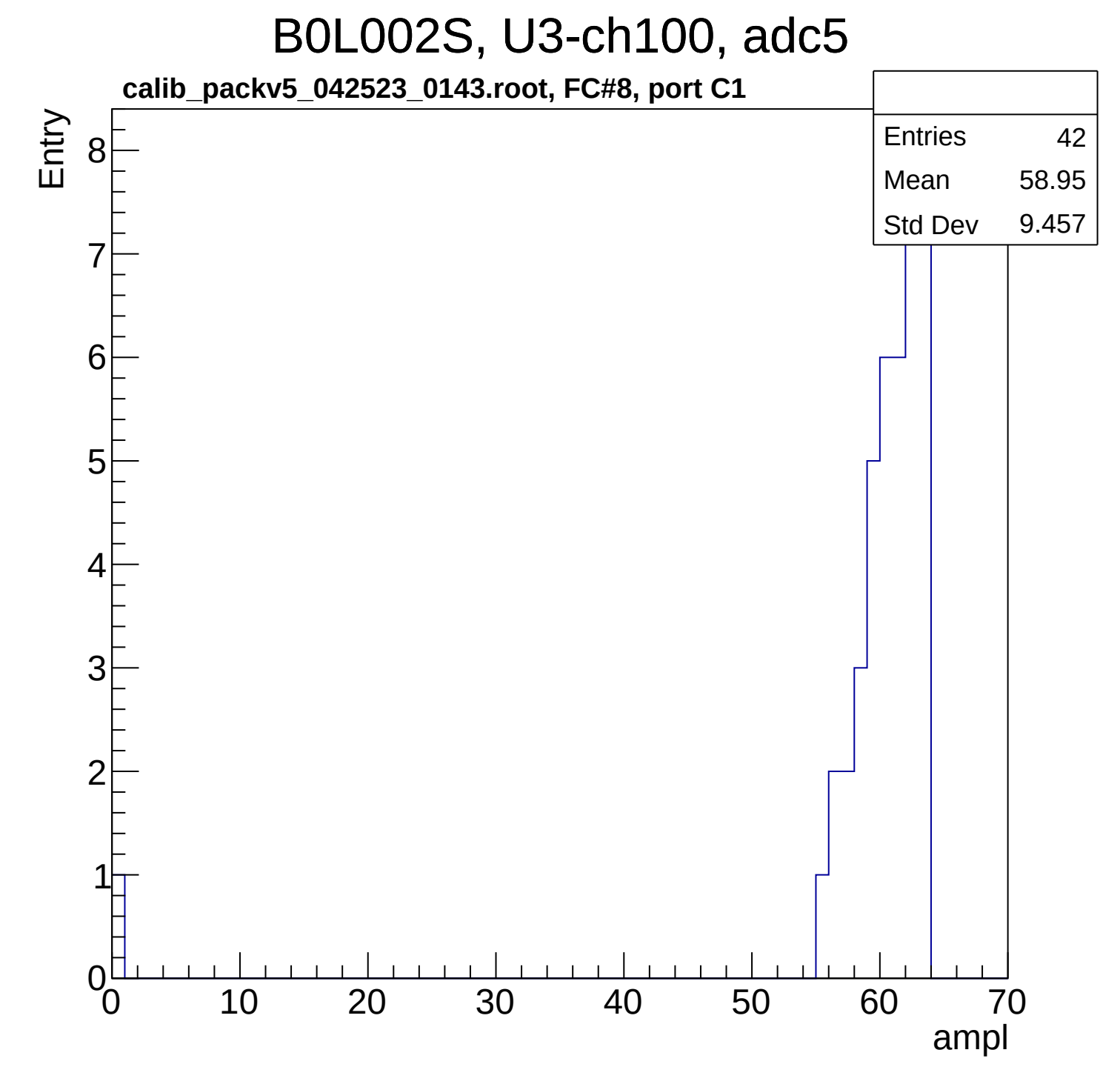
40
50
60
70


B0L002S, U3-ch100, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 55.5
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch100, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.95
Std Dev 9.457
Entry
ampl
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1
2
3
4
5
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0
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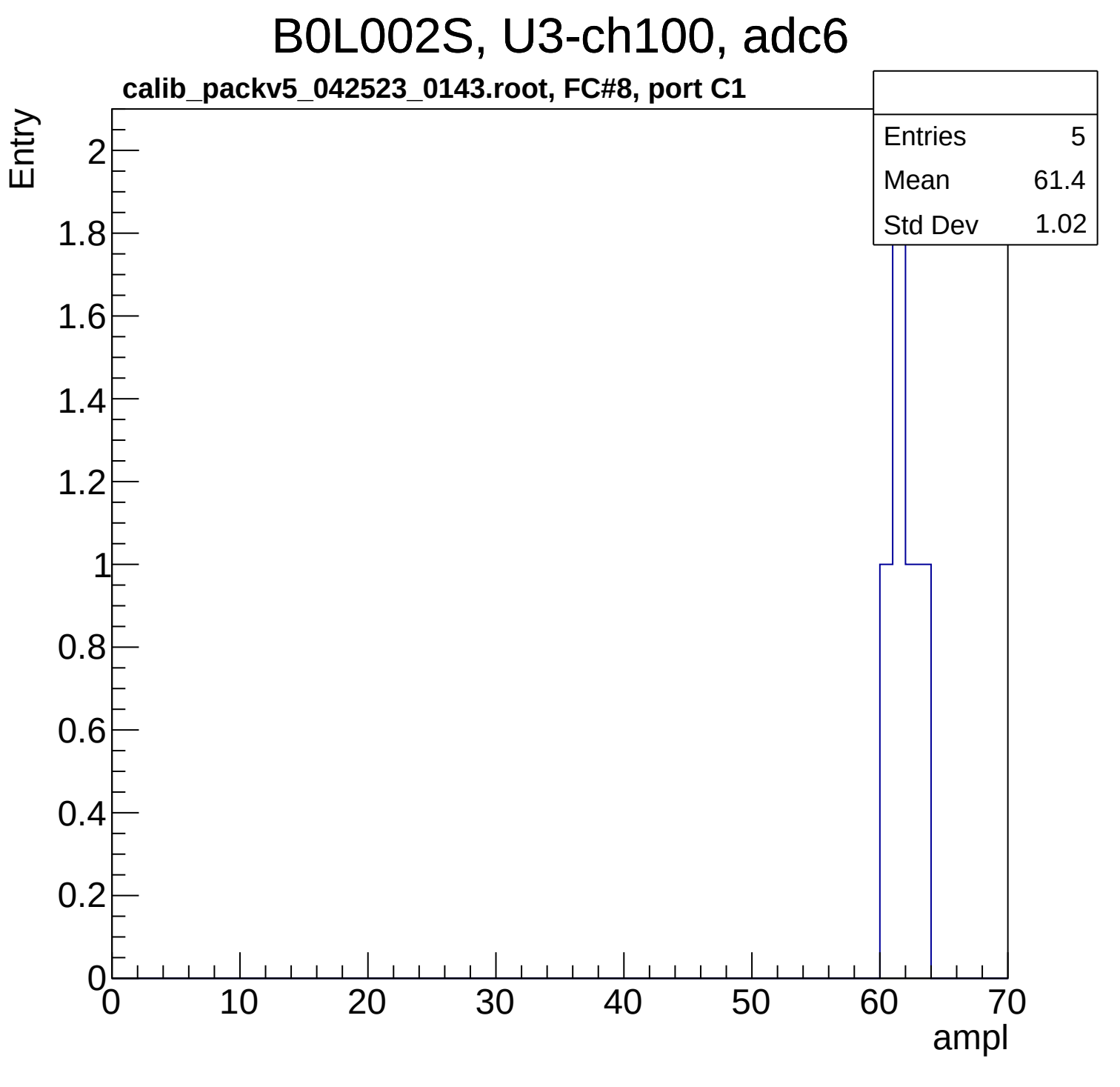
B0L002S, U3-ch100, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 61.4
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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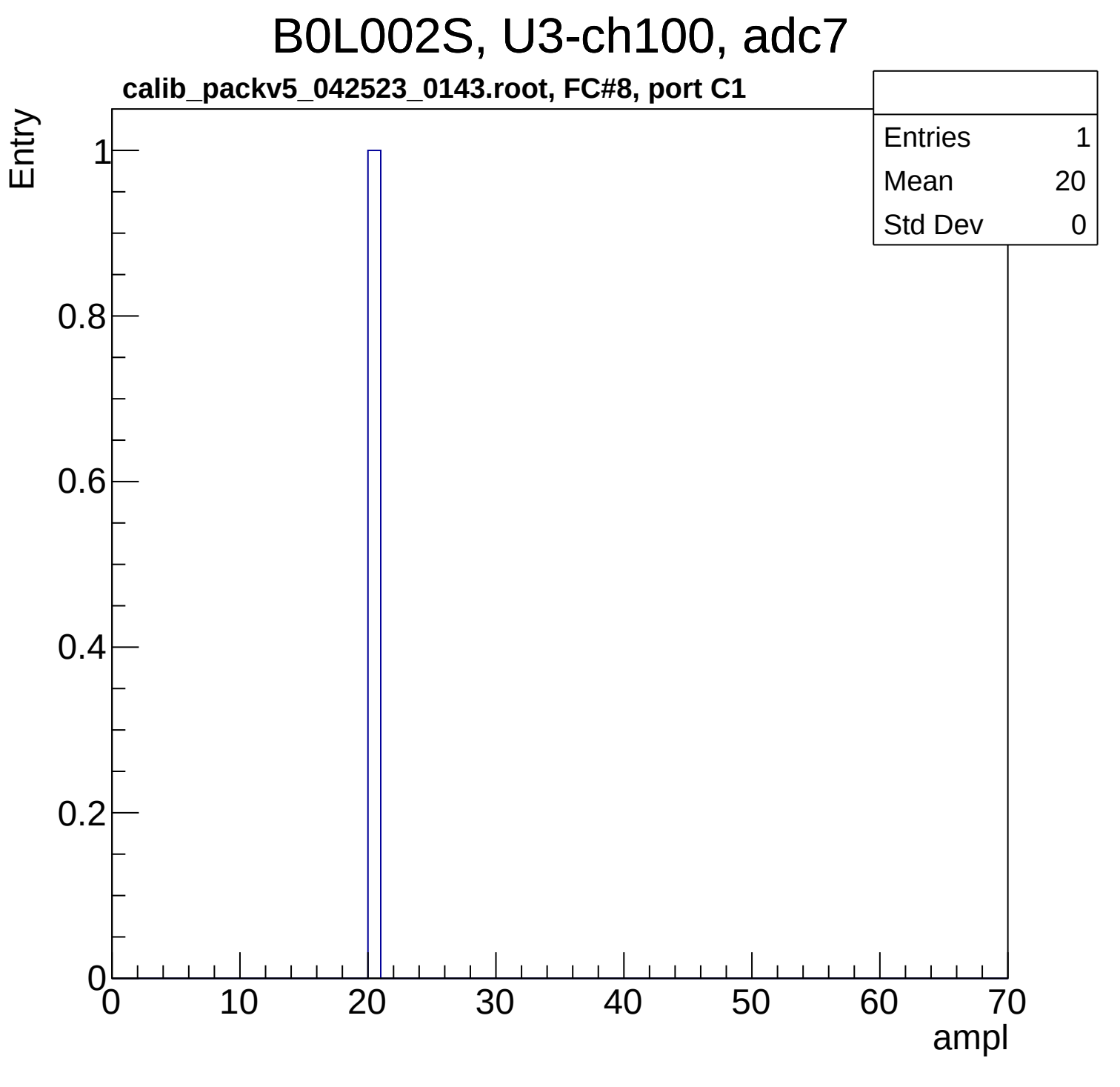
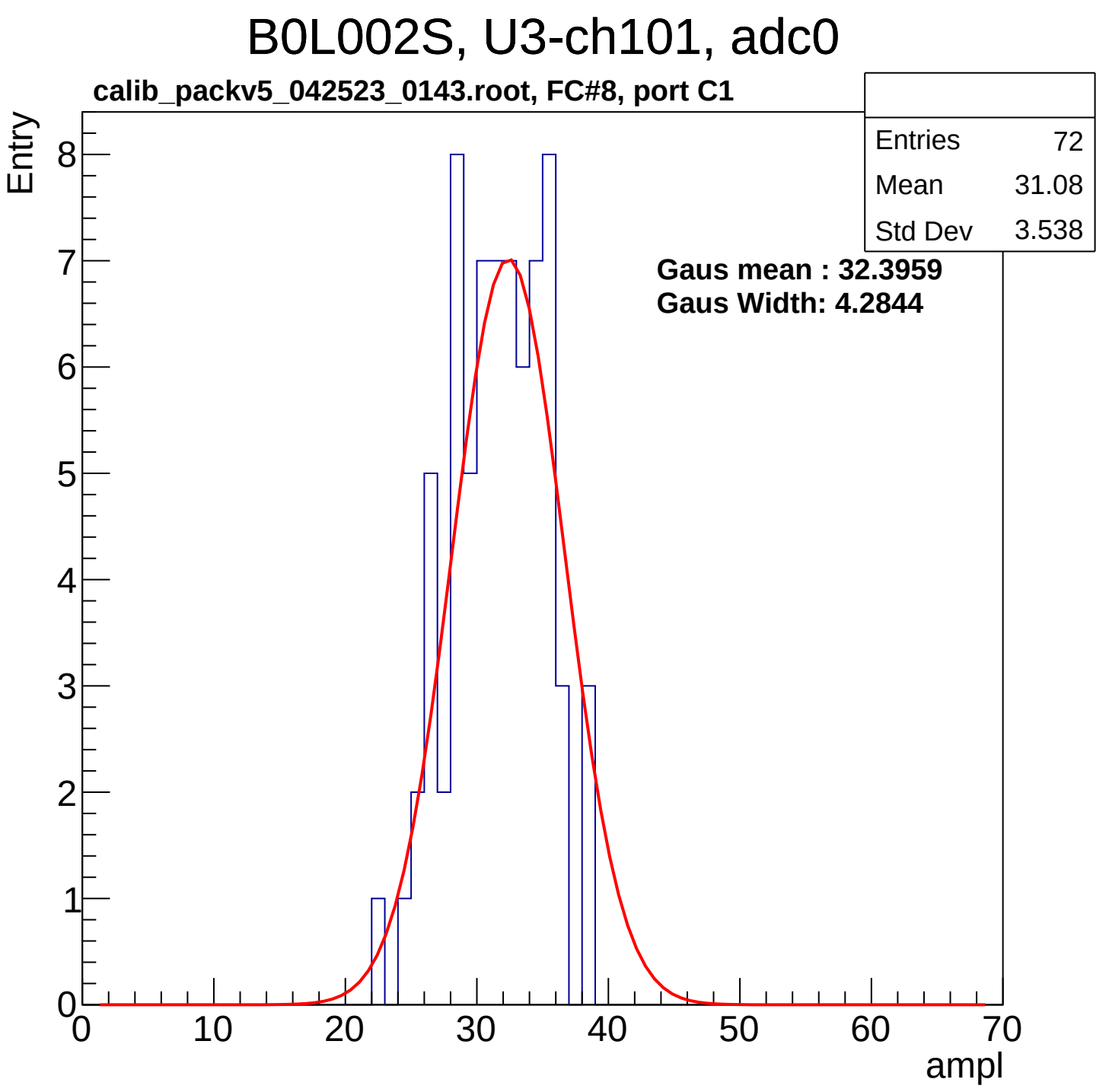

B0L002S, U3-ch100, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch101, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 31.08
Std Dev 3.538
Gaus mean : 32.3959
Gaus Width: 4.2844
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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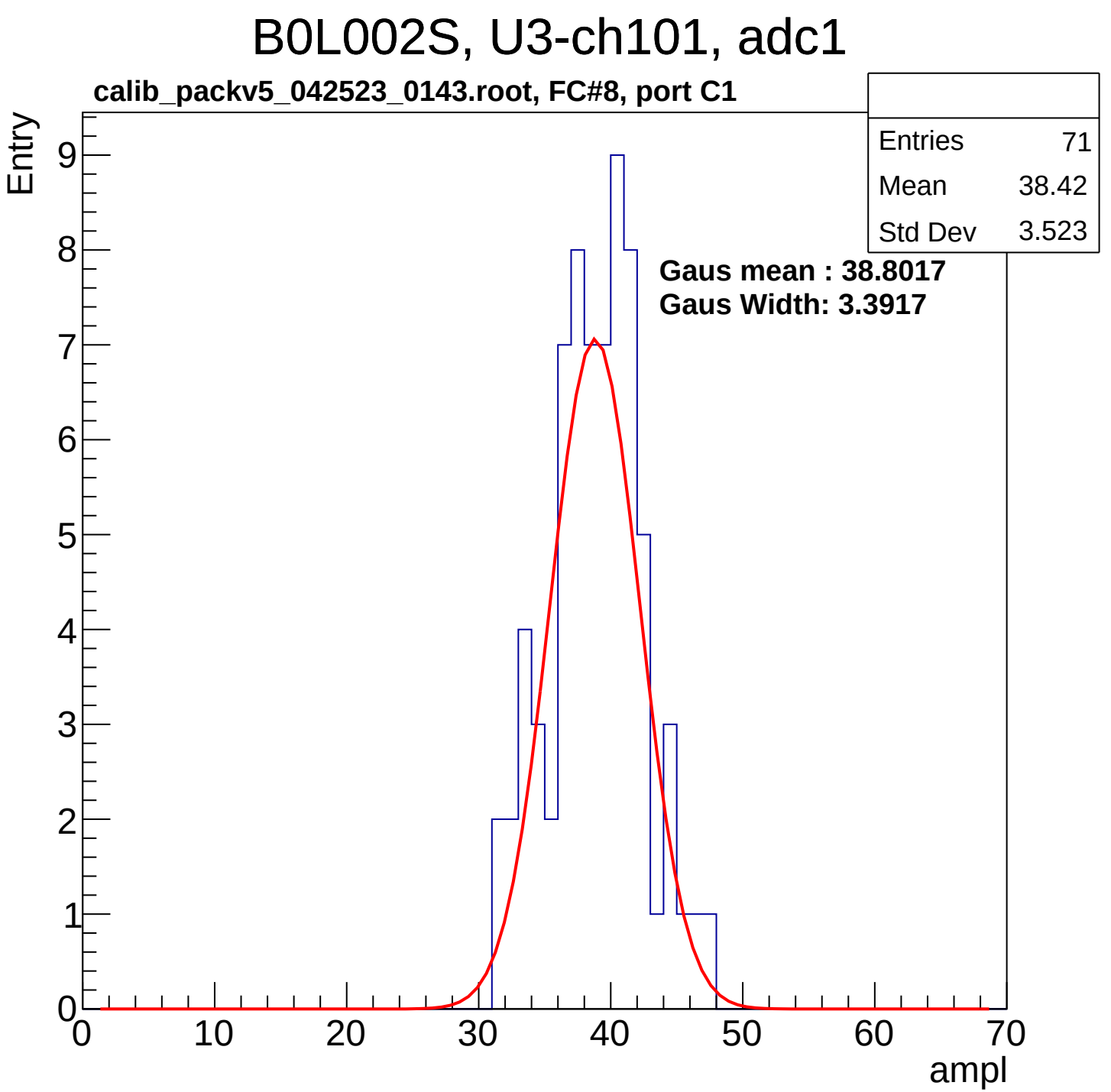
B0L002S, U3-ch101, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 38.42
Std Dev 3.523
Gaus mean : 38.8017
Gaus Width: 3.3917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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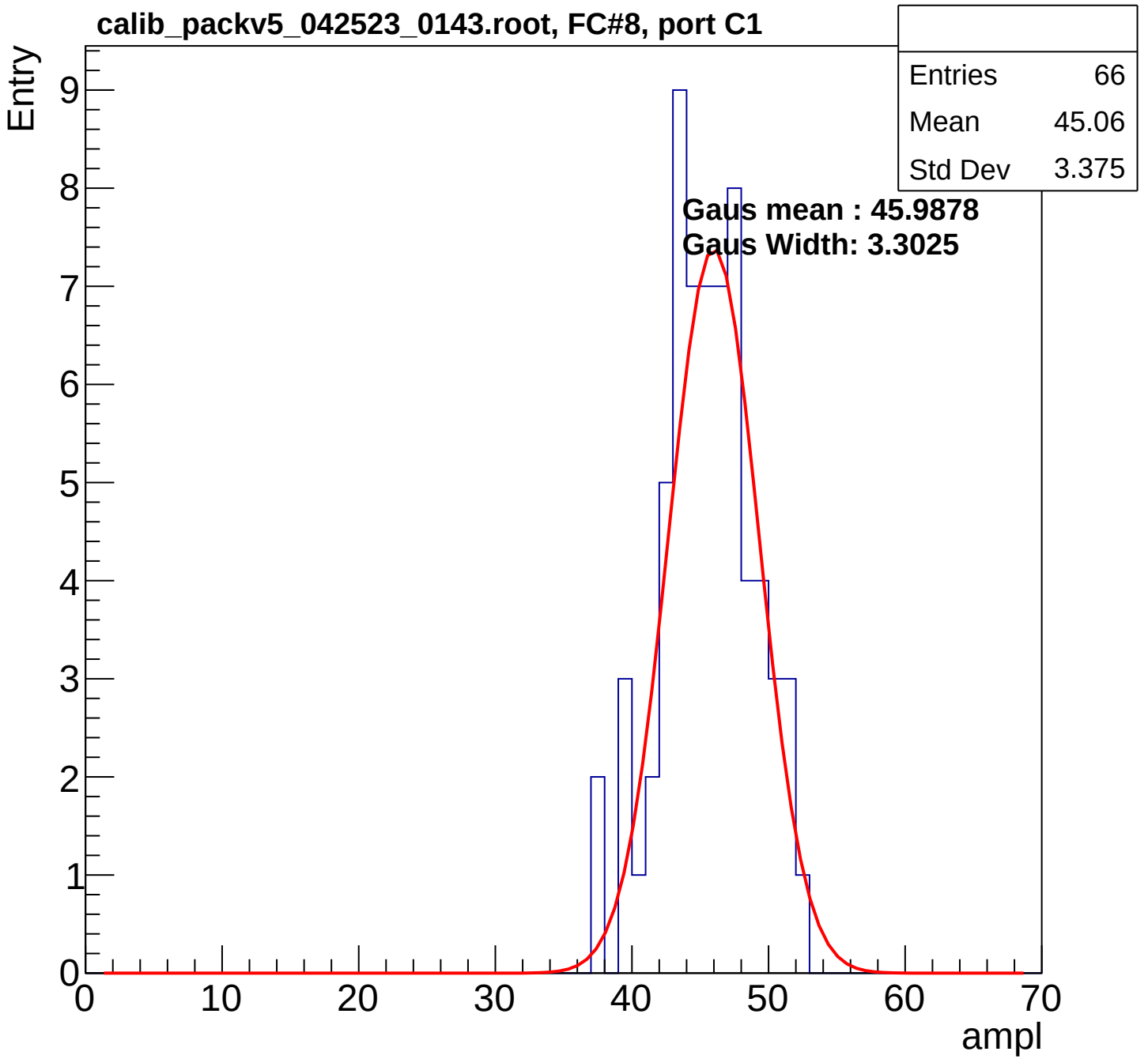
B0L002S, U3-ch101, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 45.06
Std Dev 3.375
Gaus mean : 45.9878
Gaus Width: 3.3025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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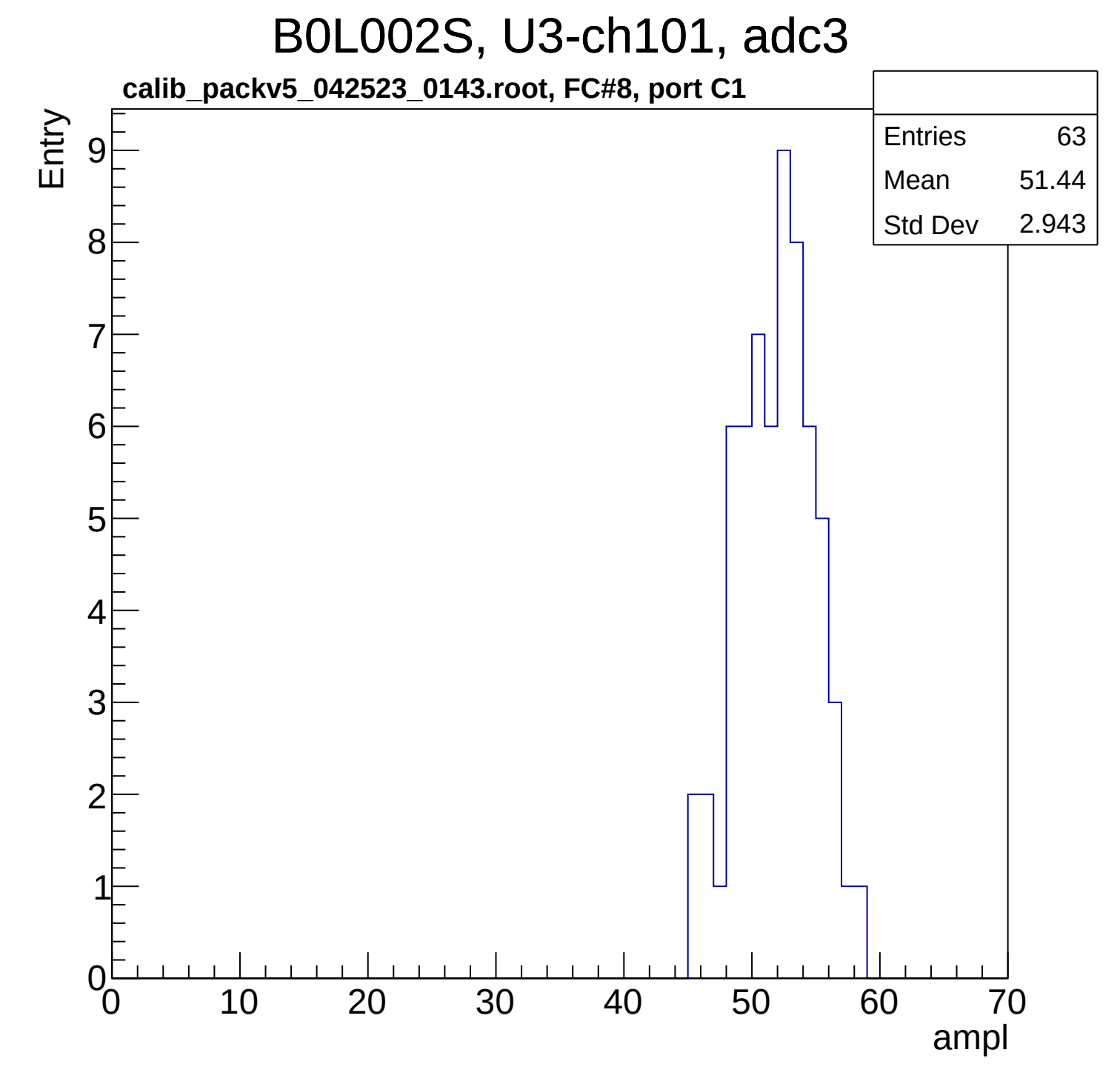
B0L002S, U3-ch101, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 51.44
Std Dev 2.943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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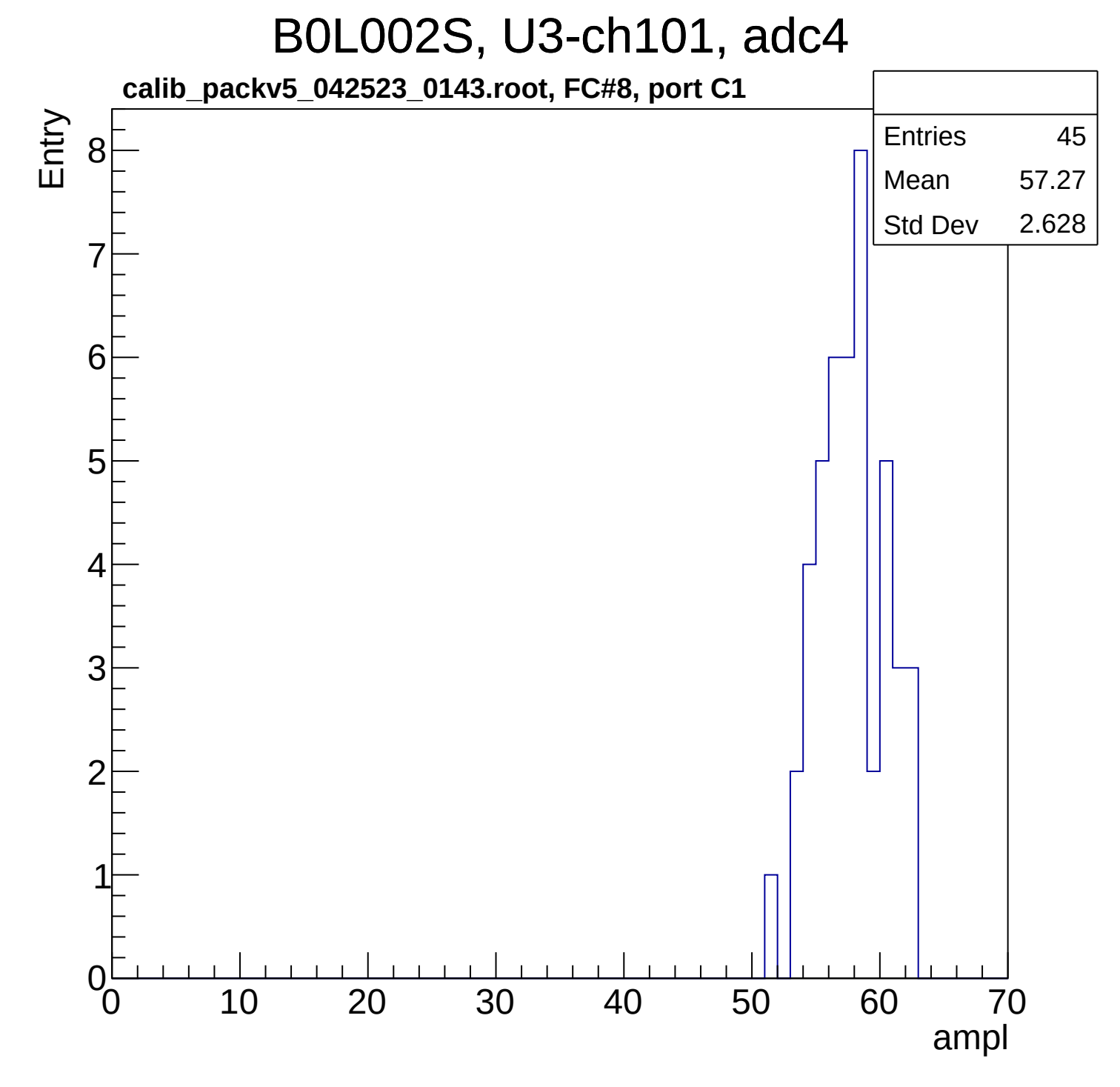
B0L002S, U3-ch101, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 57.27
Std Dev 2.628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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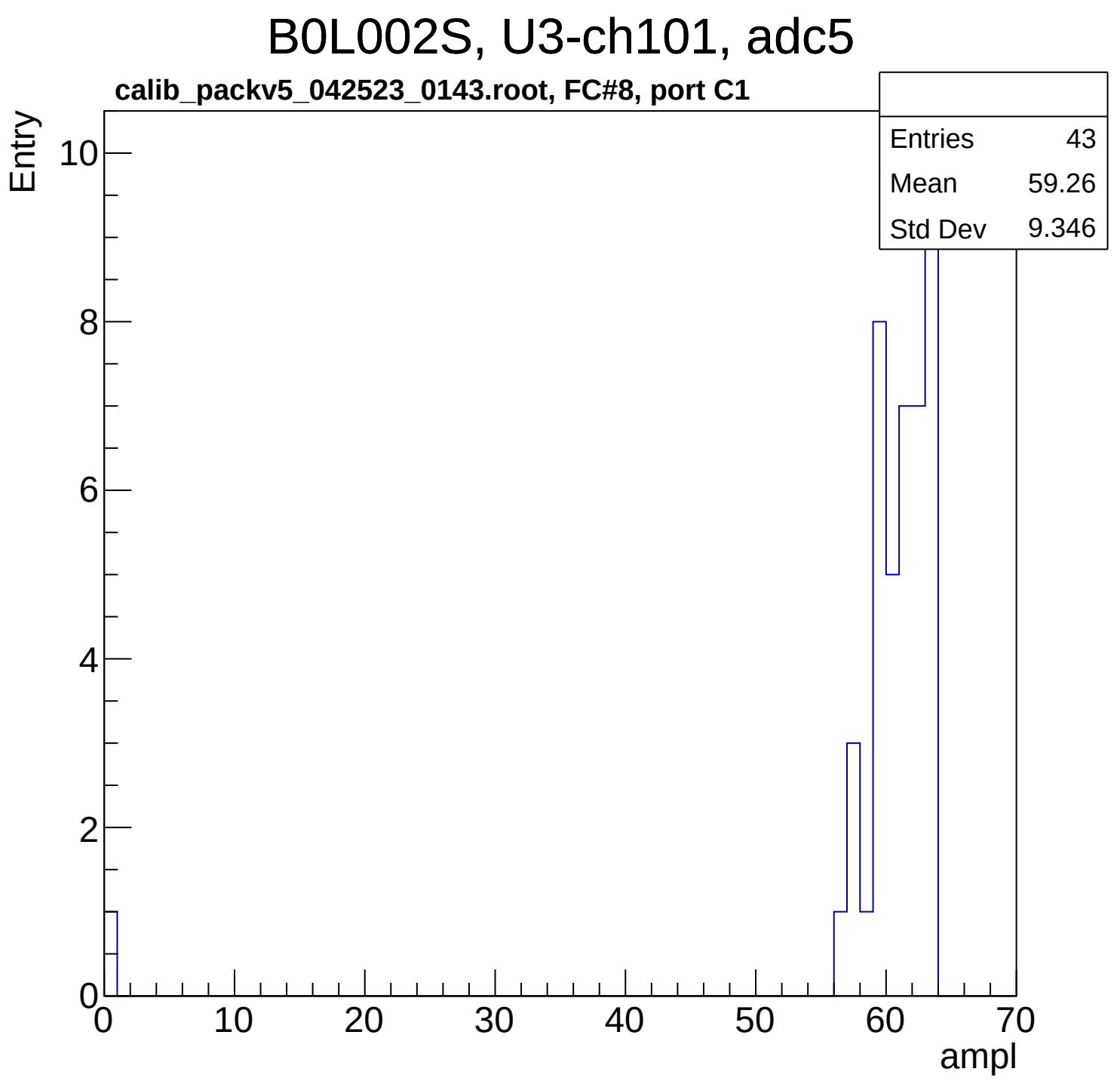
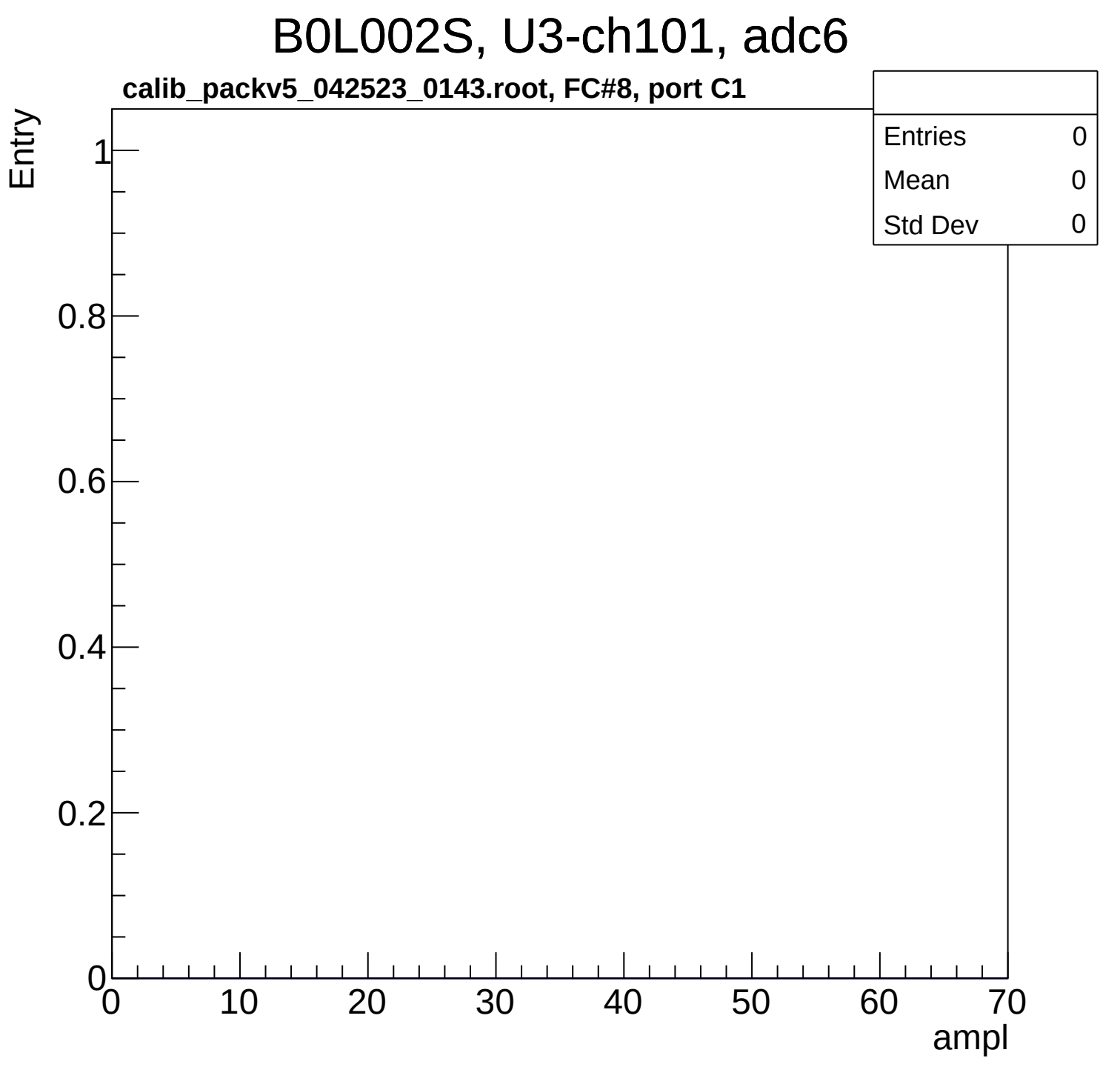
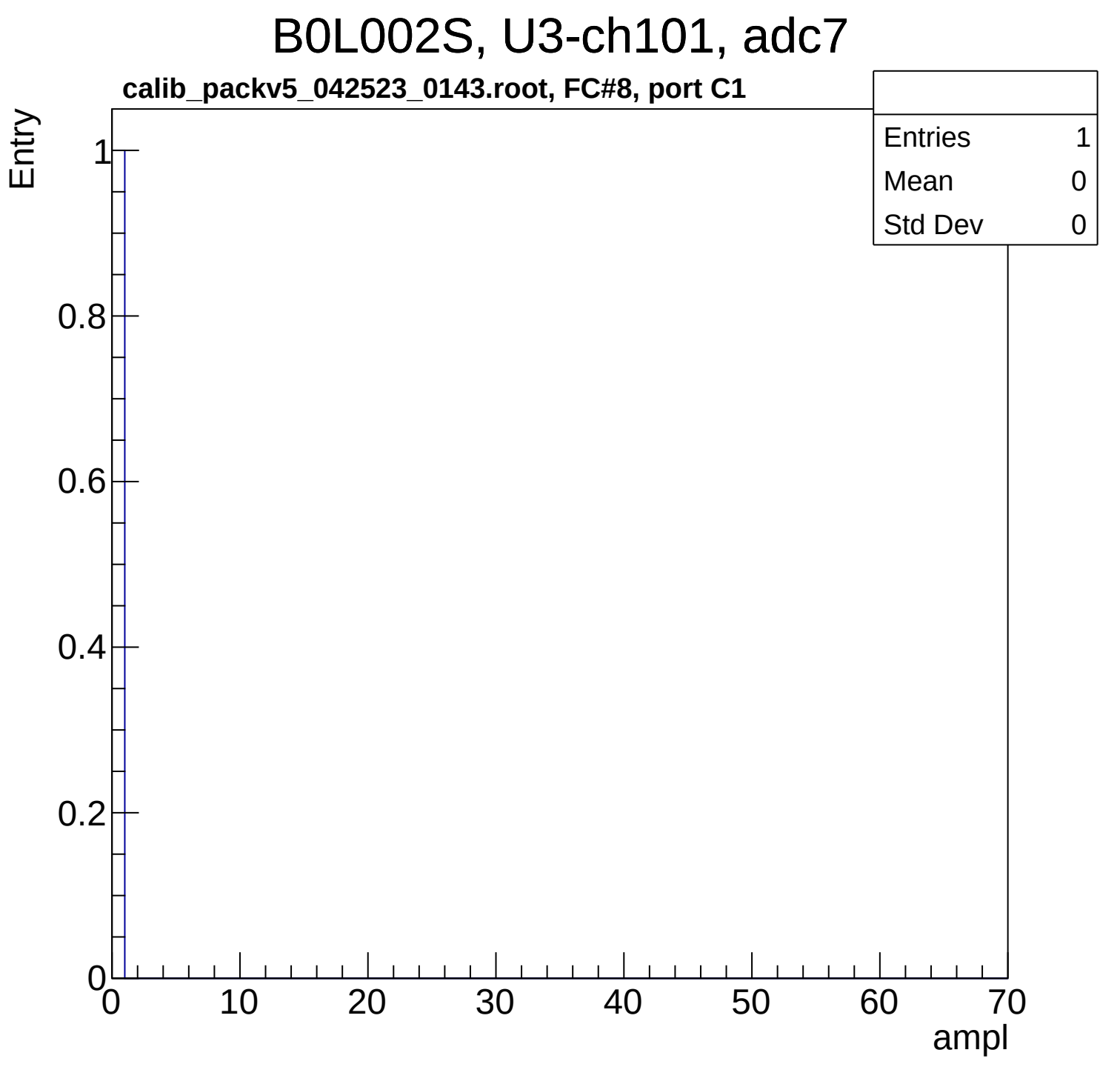
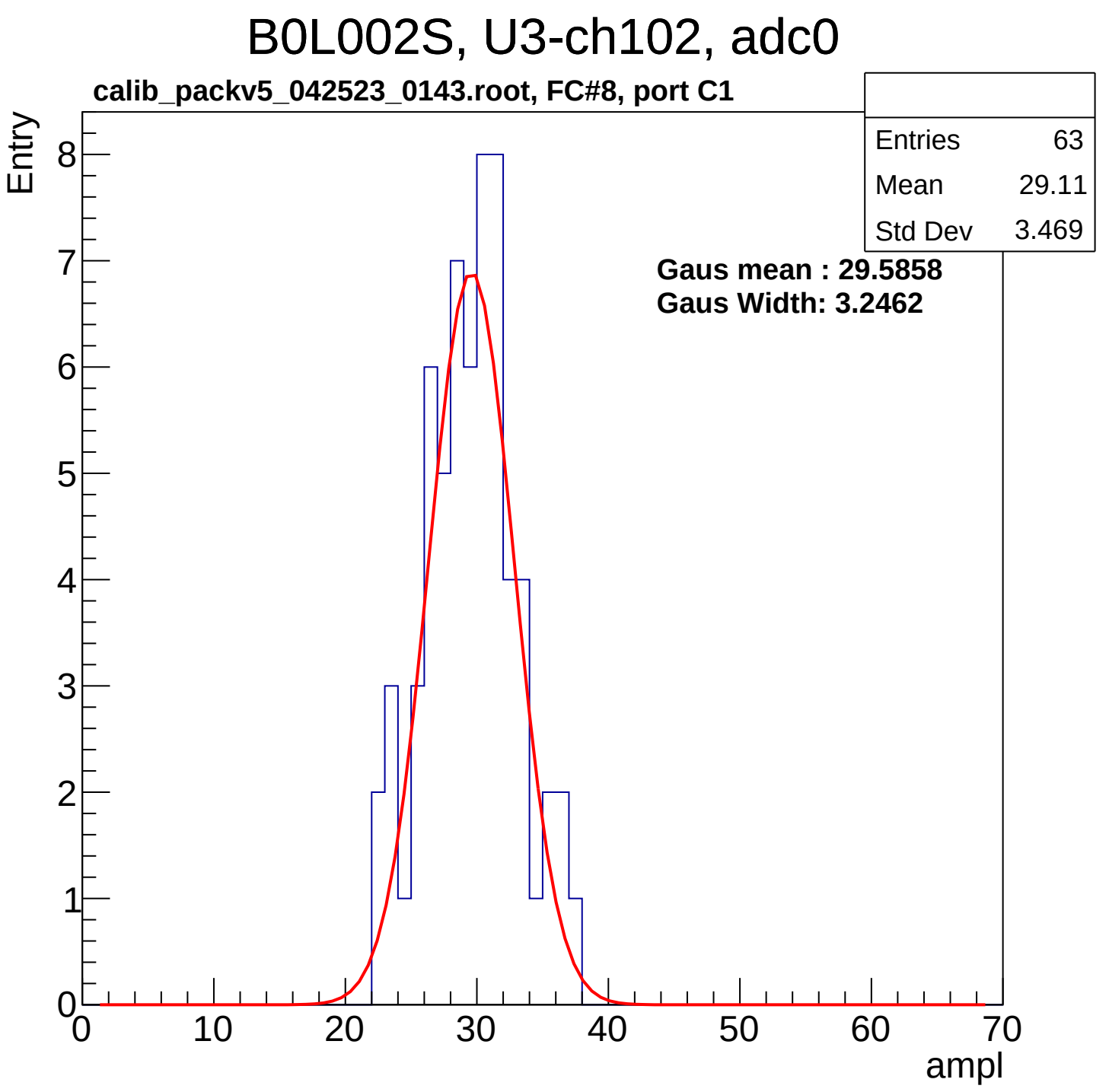
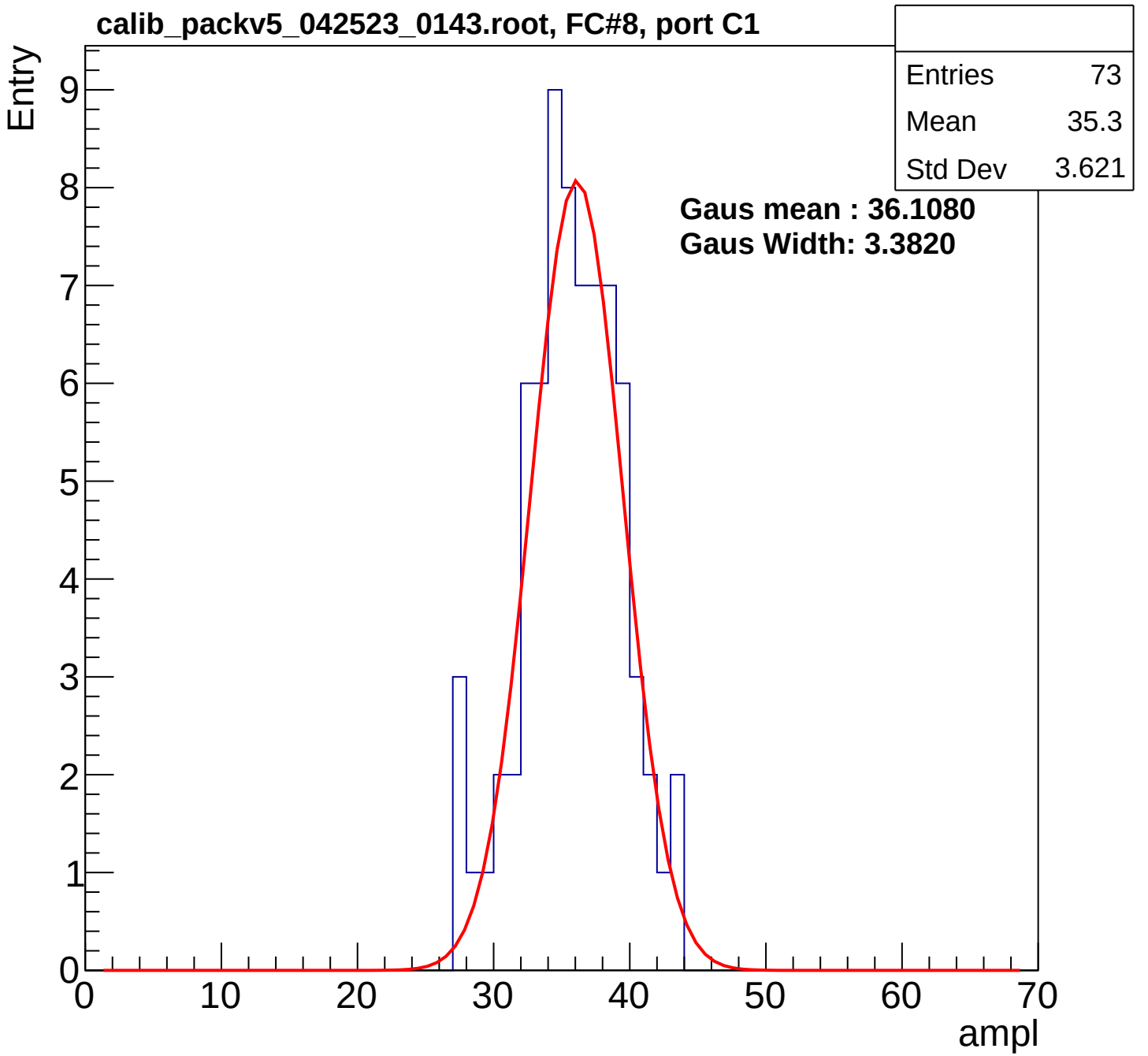

B0L002S, U3-ch101, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 59.26
Std Dev 9.346
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch101, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch101, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch102, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 29.11
Std Dev 3.469
Gaus mean : 29.5858
Gaus Width: 3.2462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch102, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 35.3
Std Dev 3.621
Gaus mean : 36.1080
Gaus Width: 3.3820
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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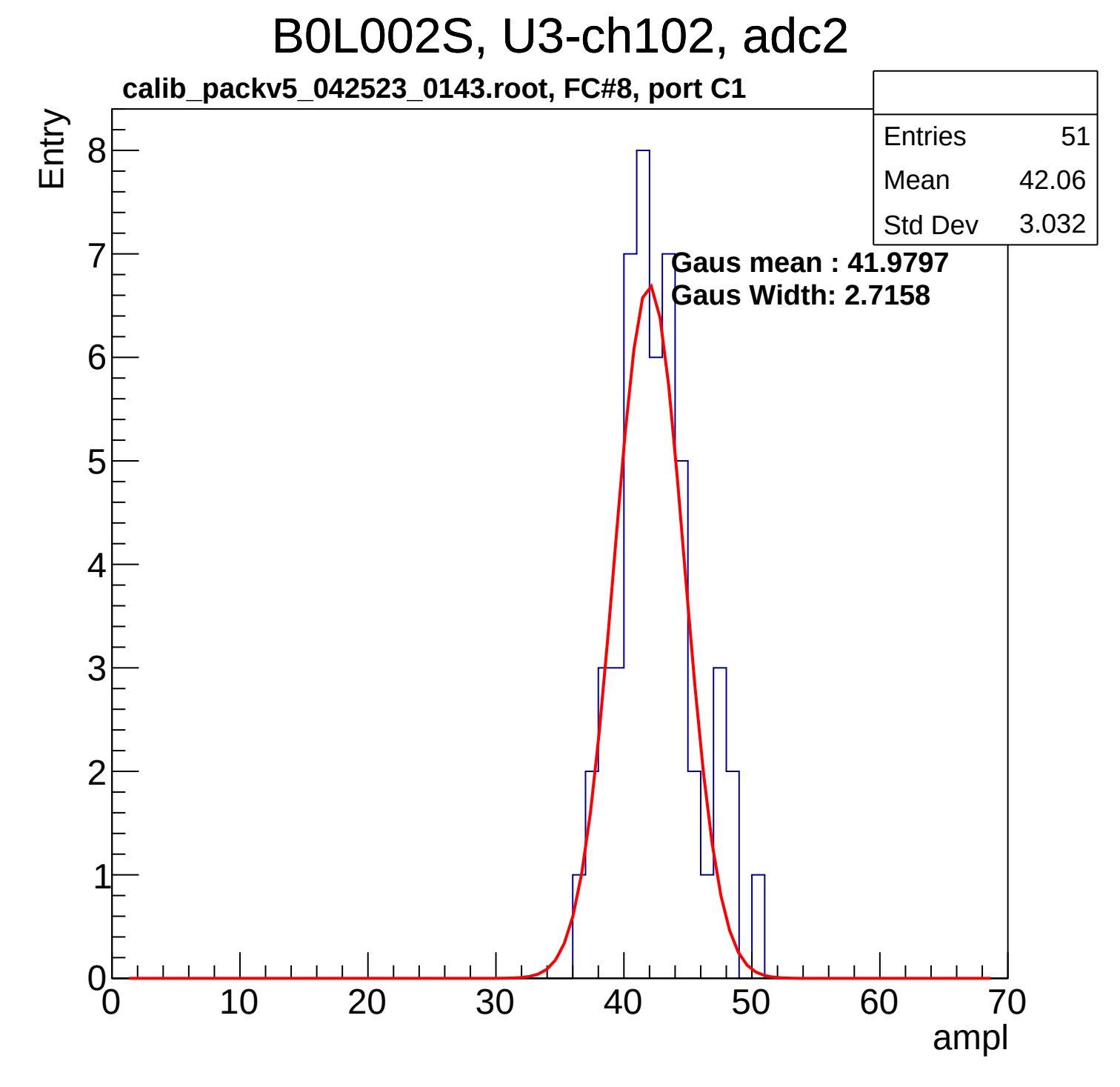
B0L002S, U3-ch102, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 42.06
Std Dev 3.032
Gaus mean : 41.9797
Gaus Width: 2.7158
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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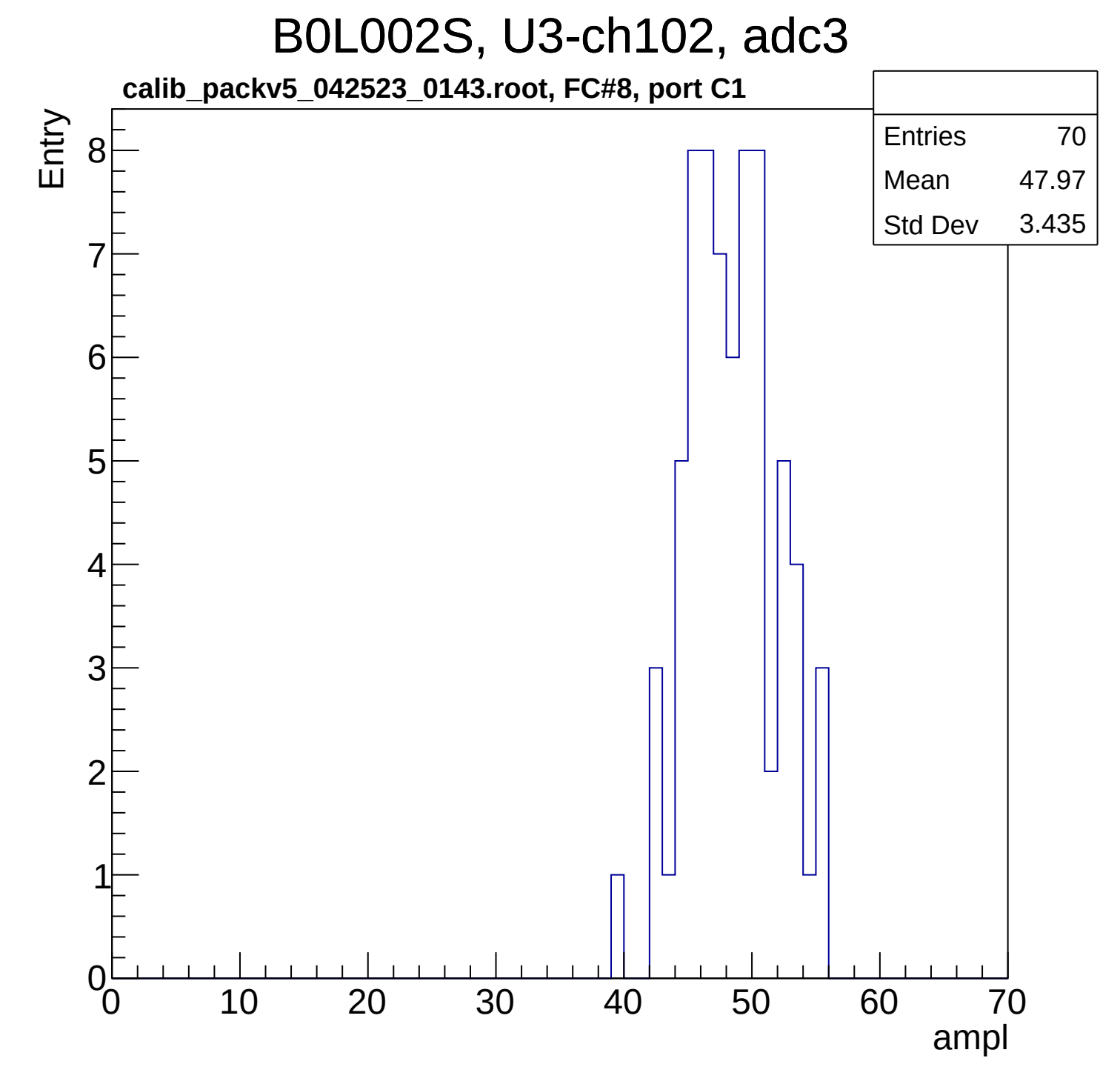
B0L002S, U3-ch102, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 47.97
Std Dev 3.435
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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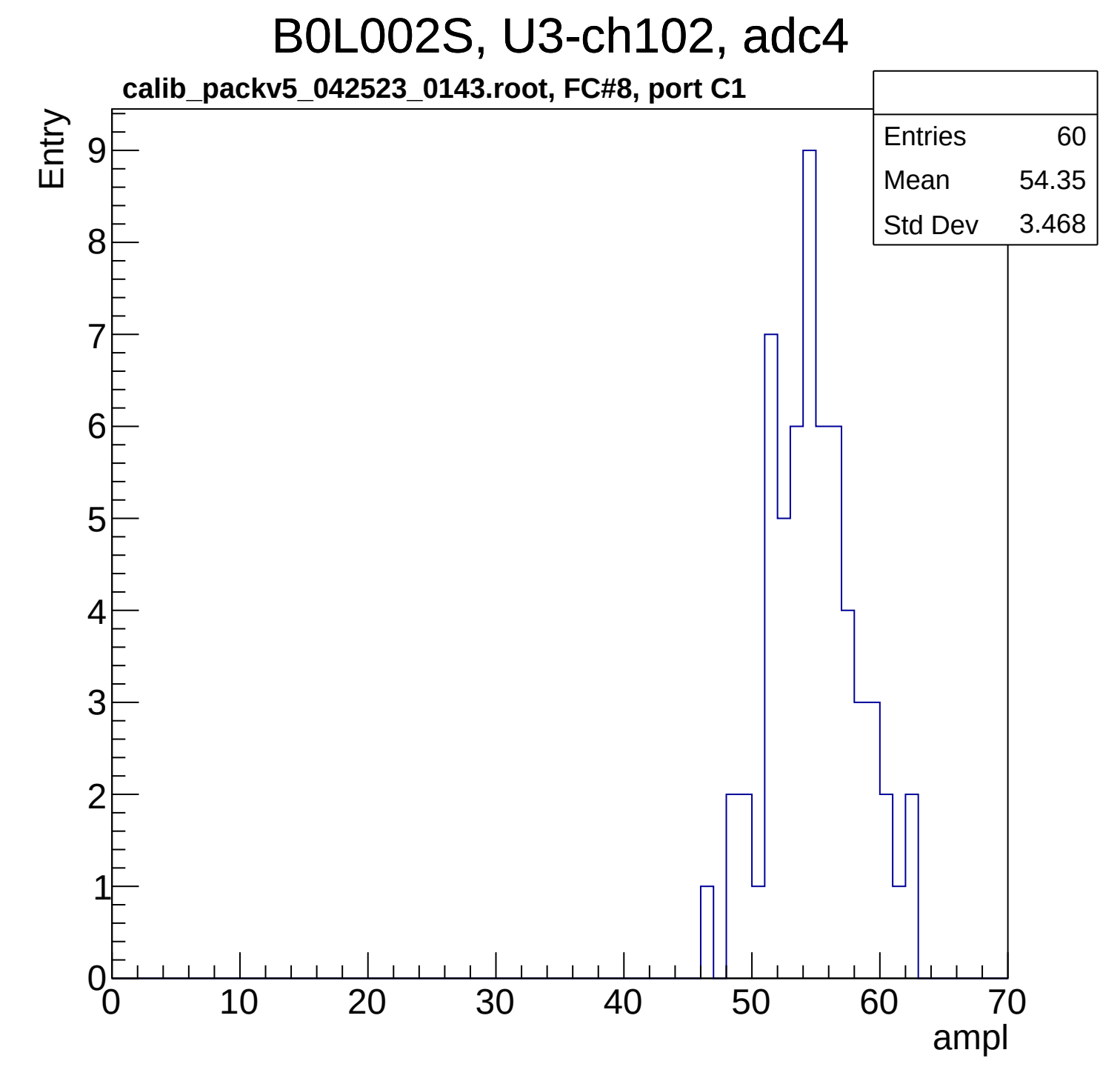
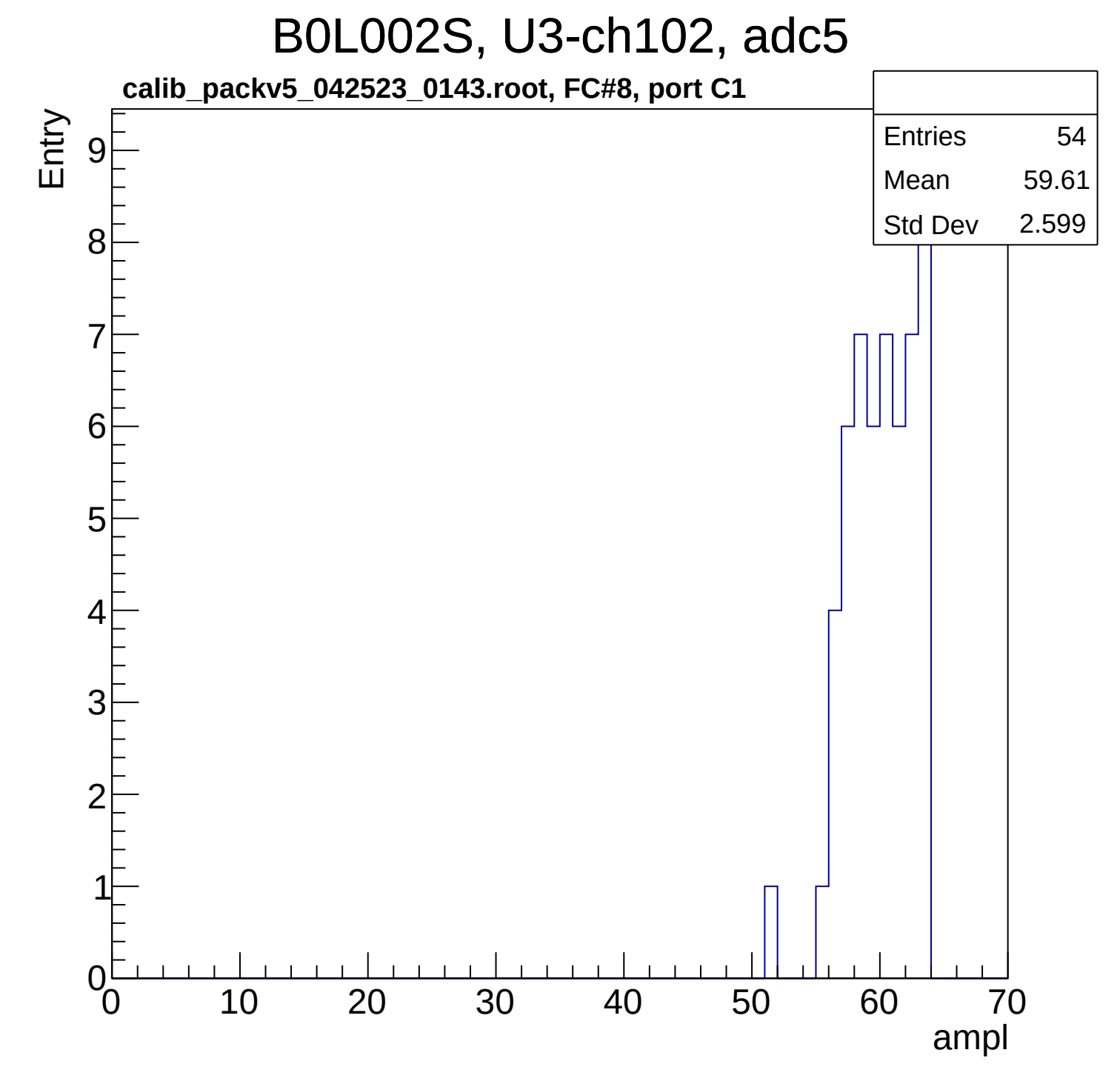

B0L002S, U3-ch102, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 54.35
Std Dev 3.468
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch102, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 59.61
Std Dev 2.599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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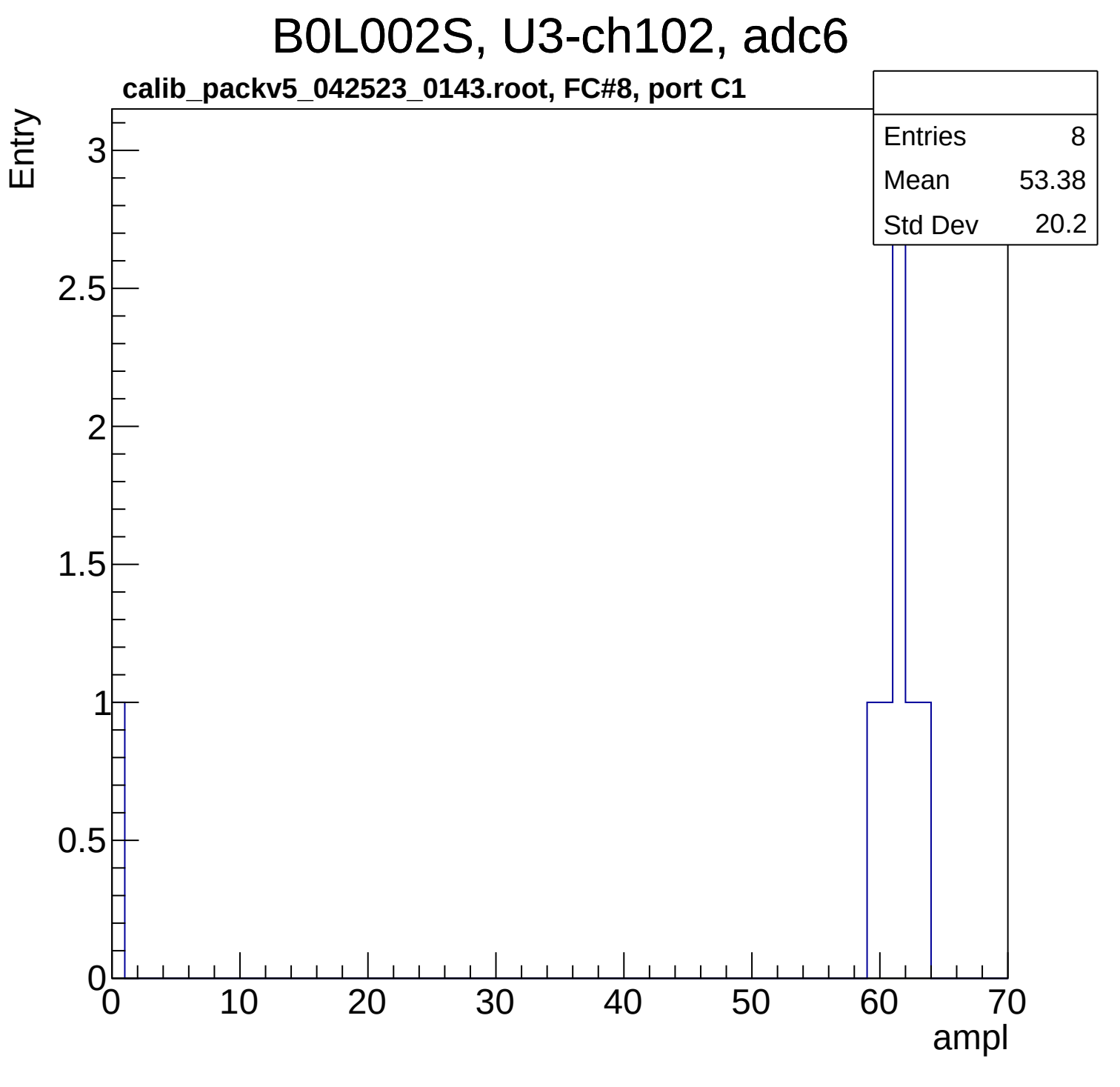
B0L002S, U3-ch102, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 53.38
Std Dev 20.2
Entry
ampl
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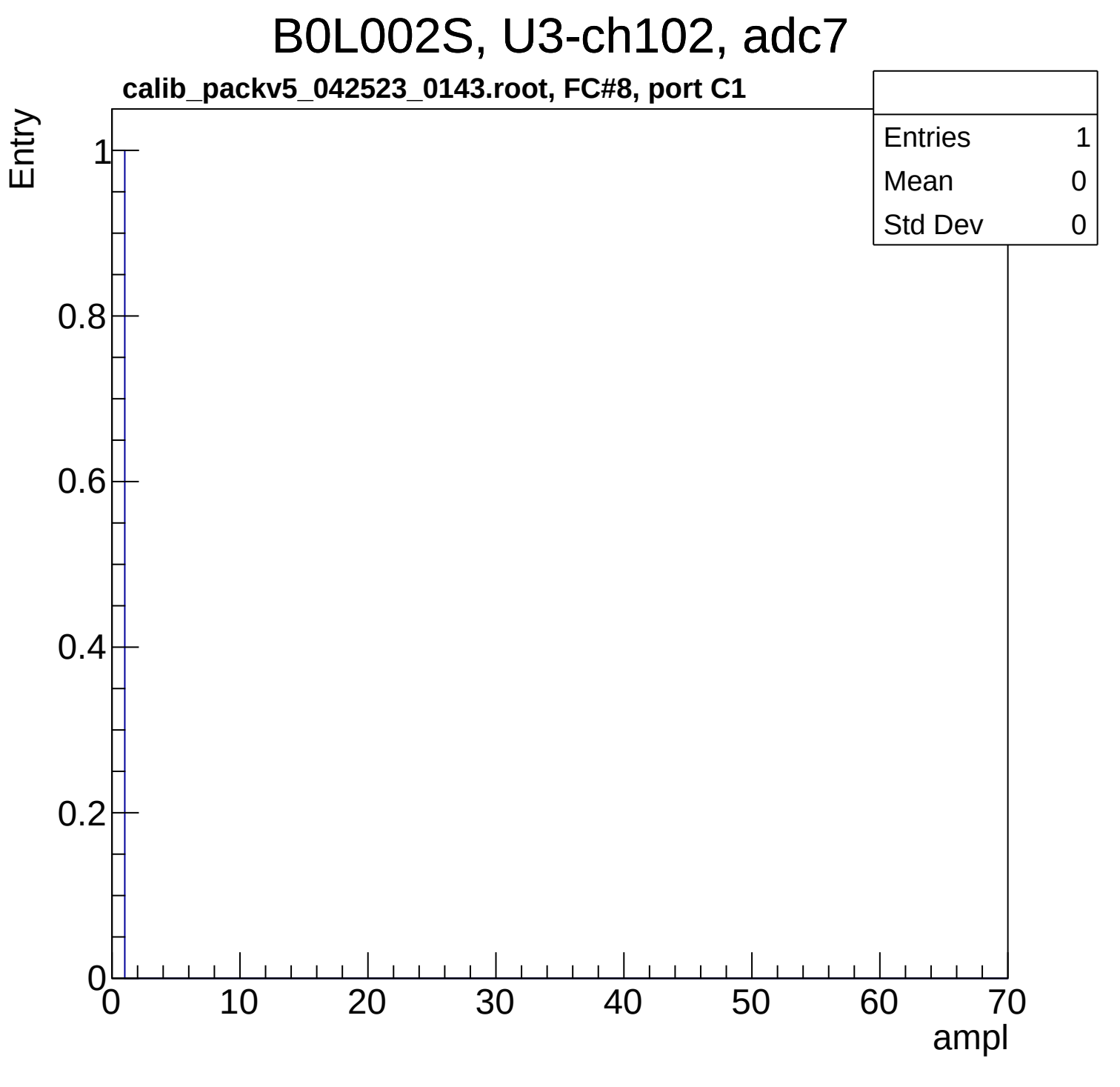
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch102, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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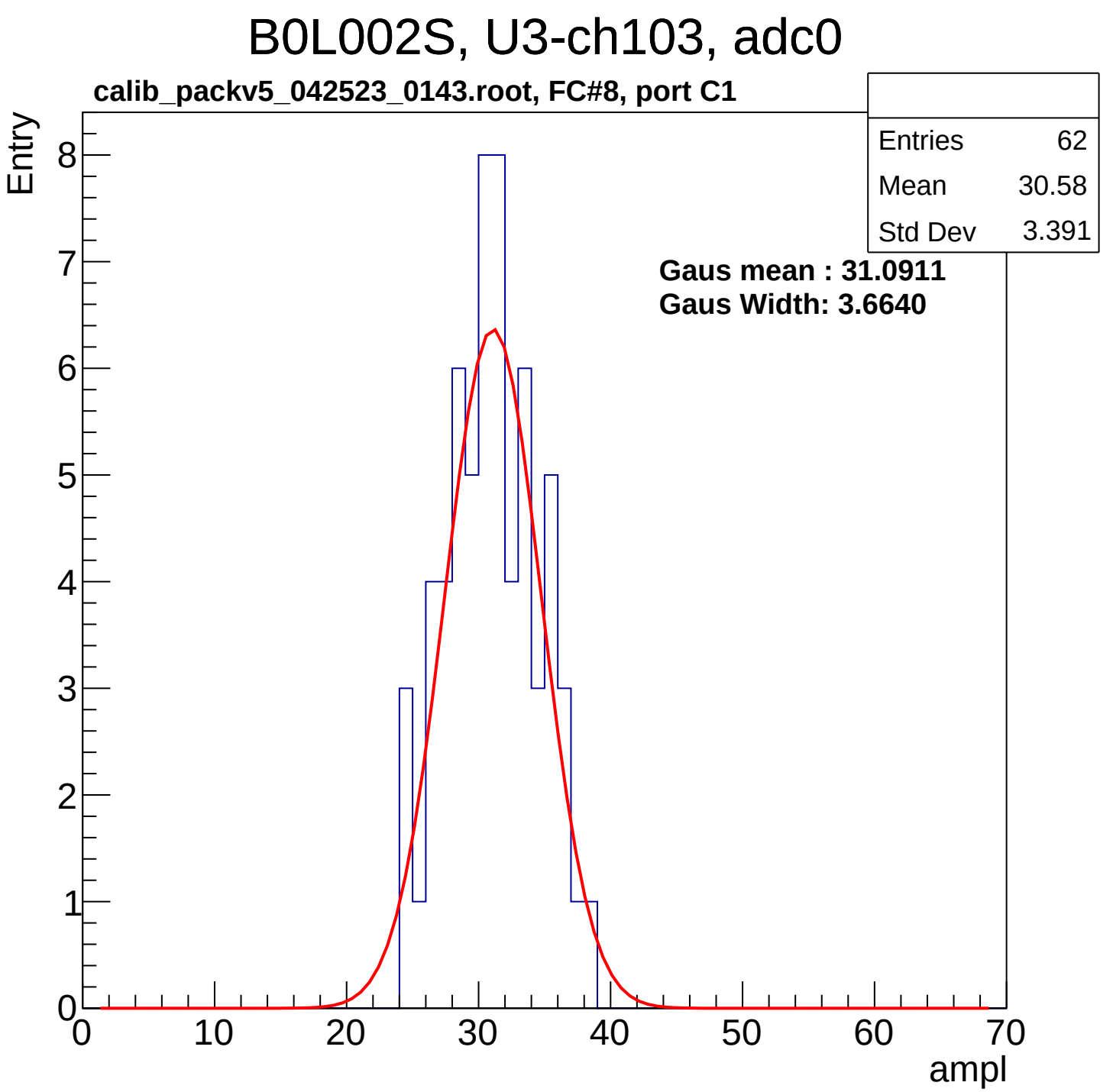
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch103, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 30.58
Std Dev 3.391
Gaus mean : 31.0911
Gaus Width: 3.6640
Entry
ampl
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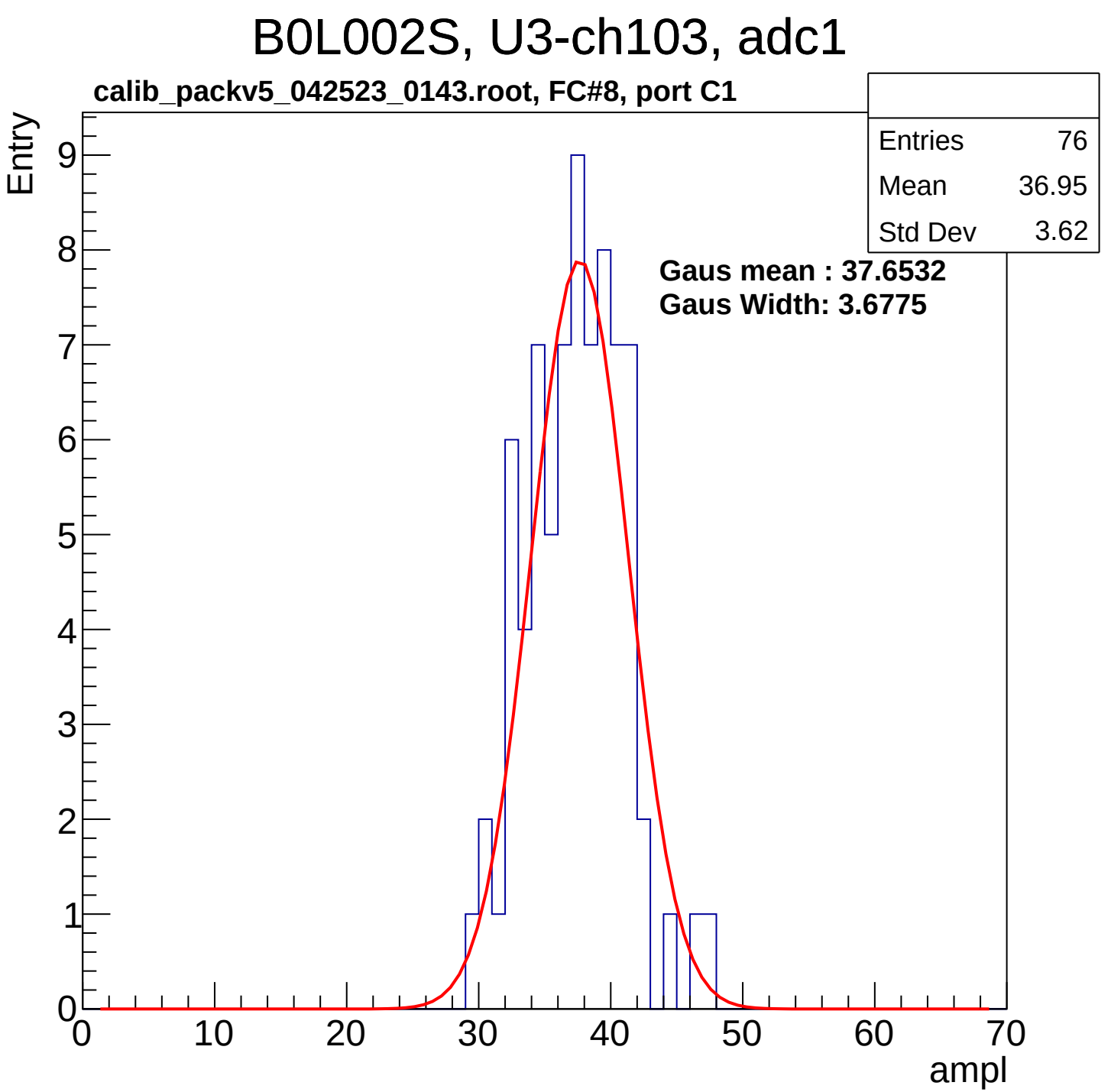
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch103, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 36.95
Std Dev 3.62
Gaus mean : 37.6532
Gaus Width: 3.6775
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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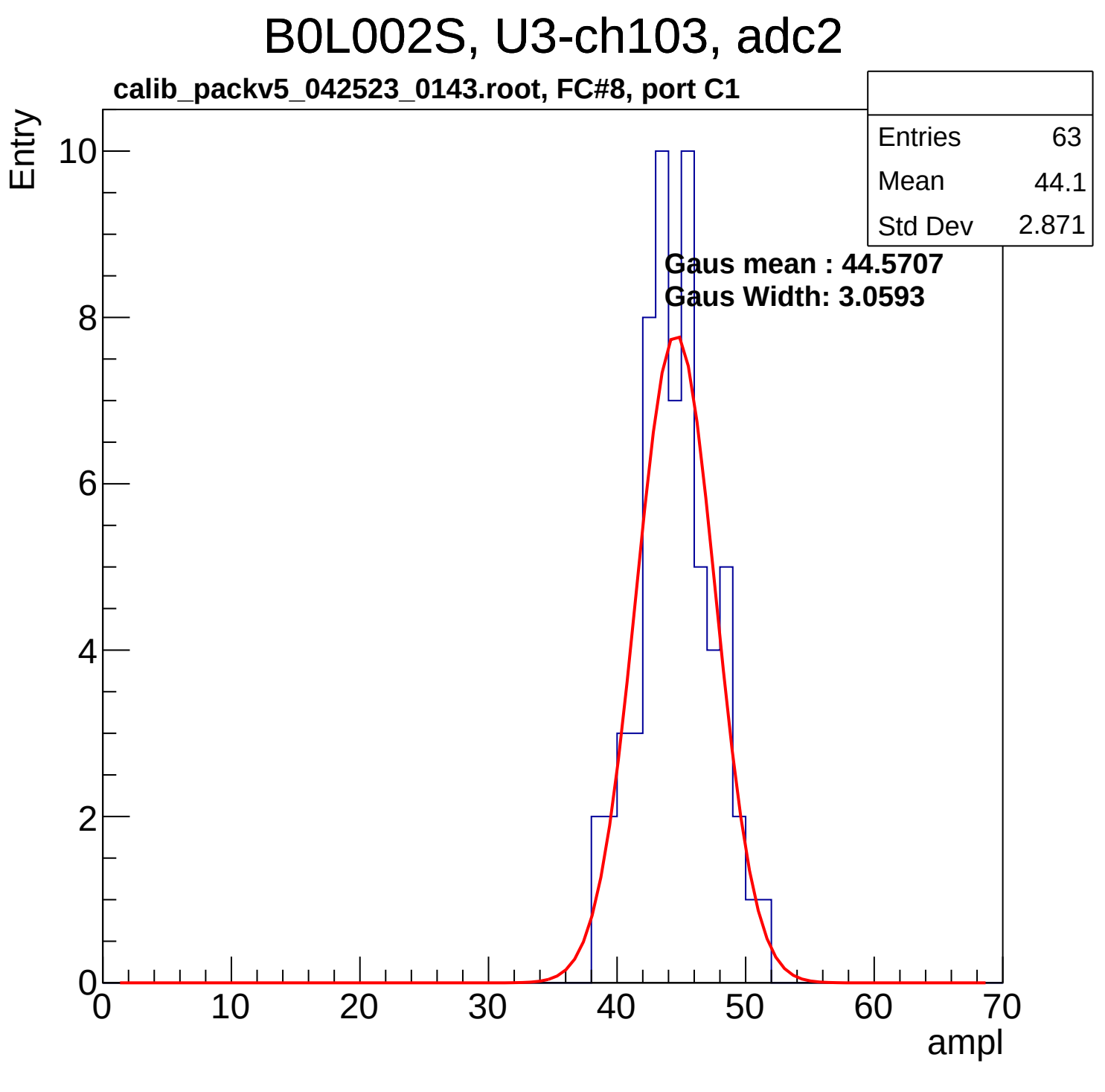
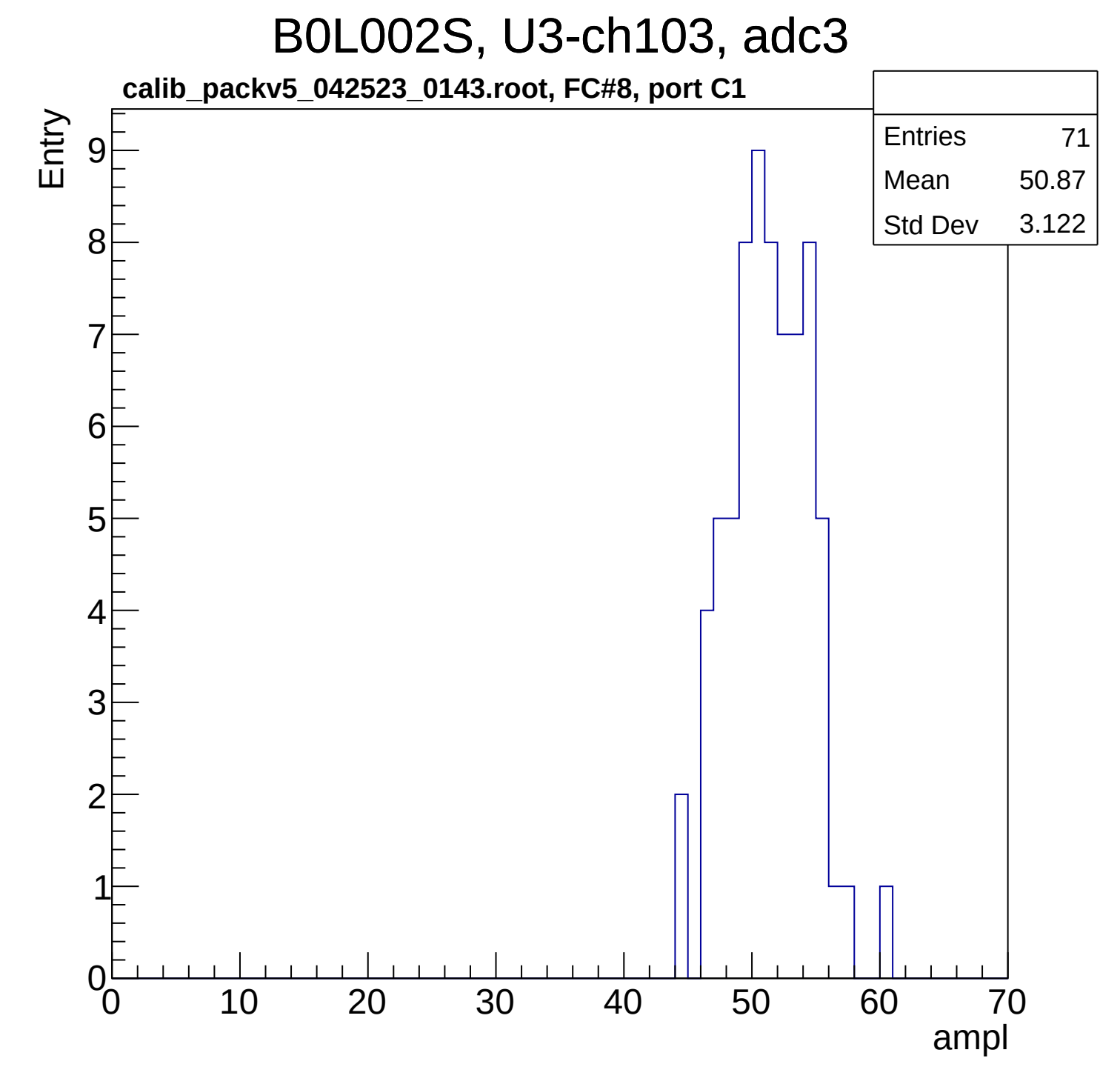
40
50
60
70


B0L002S, U3-ch103, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 44.1
Std Dev 2.871
Gaus mean : 44.5707
Gaus Width: 3.0593
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch103, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 50.87
Std Dev 3.122
Entry
ampl
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4
5
6
7
8
9
0
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20
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40
50
60
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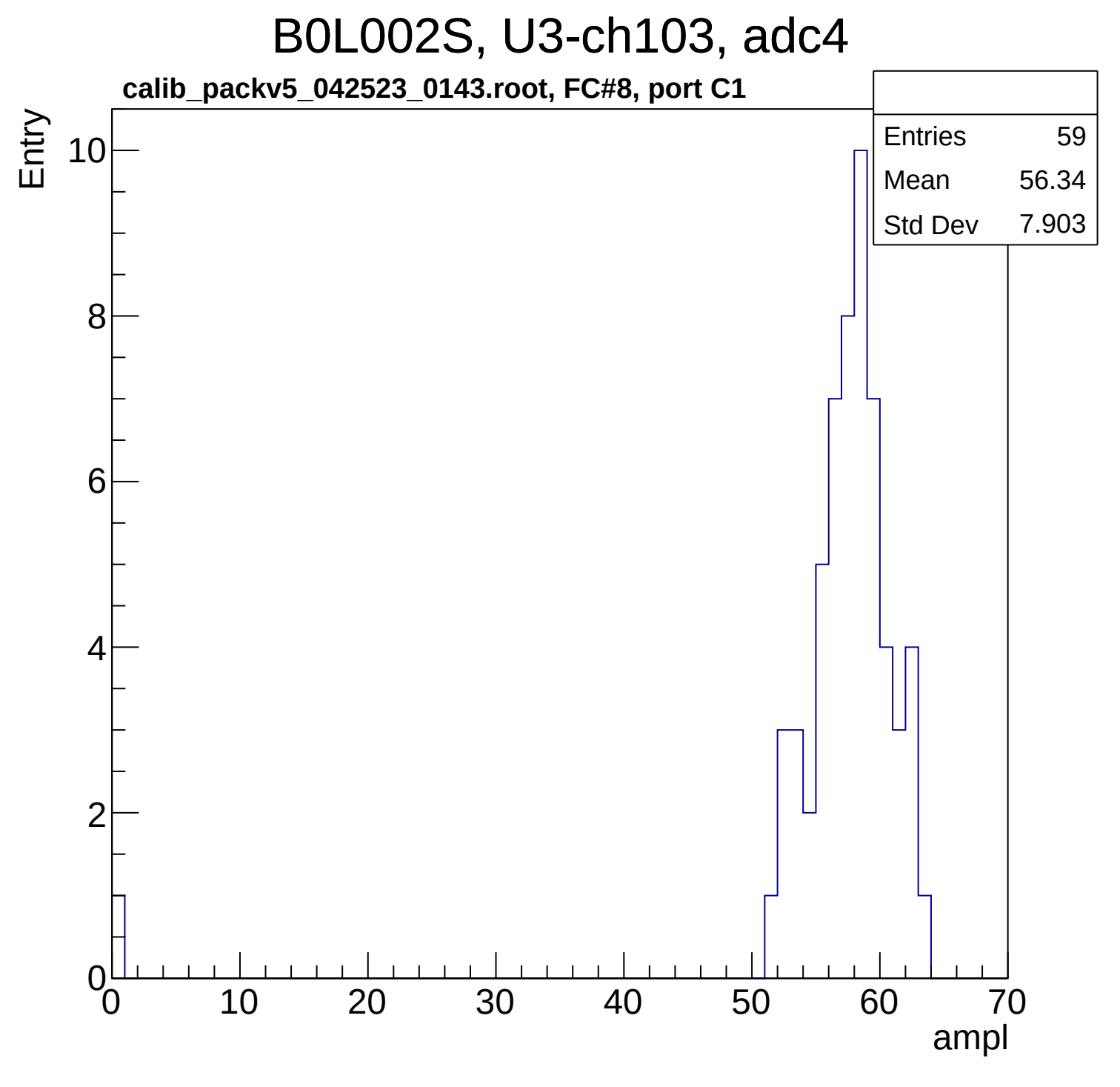
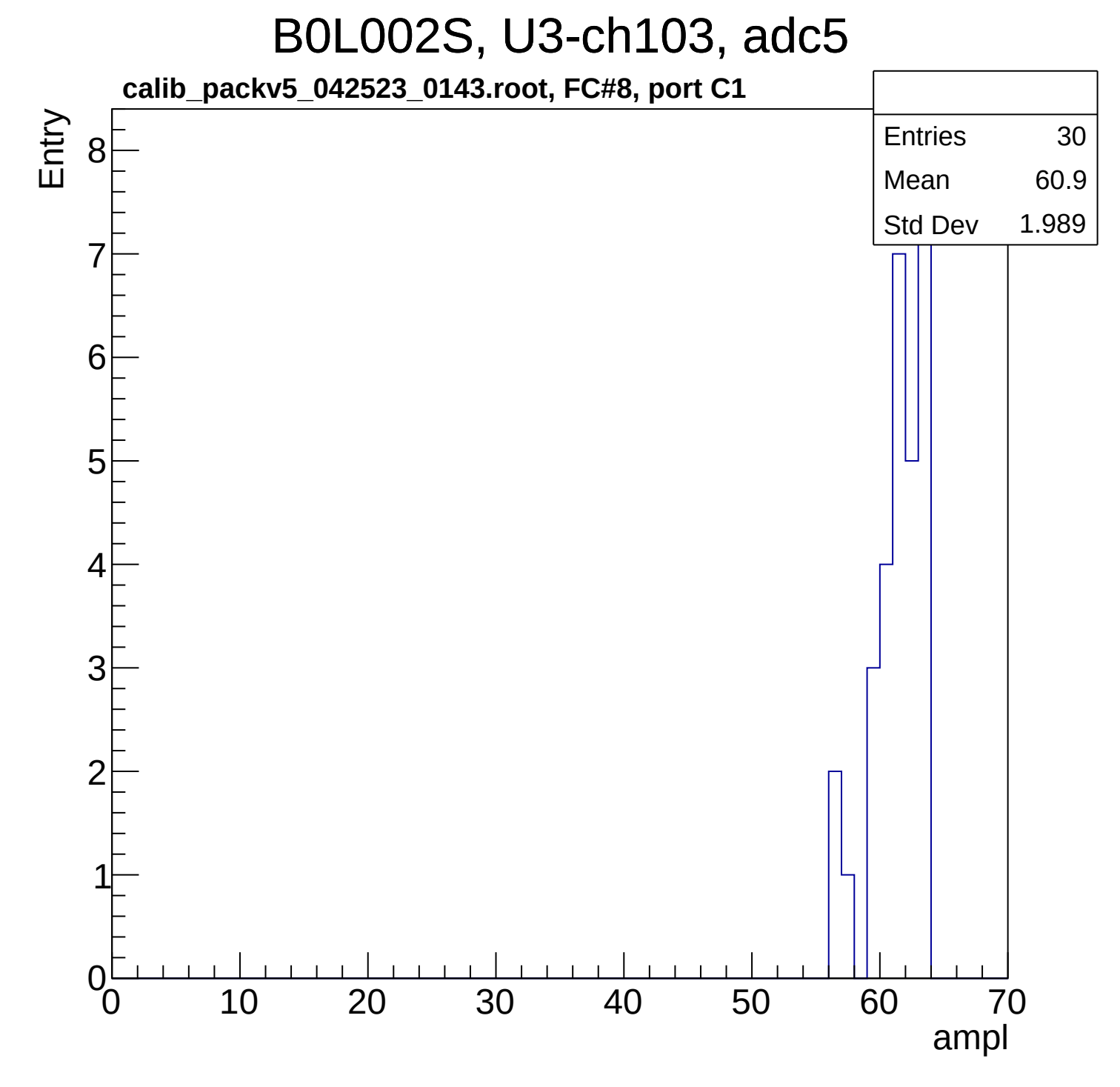

B0L002S, U3-ch103, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.34
Std Dev 7.903
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch103, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 60.9
Std Dev 1.989
Entry
ampl
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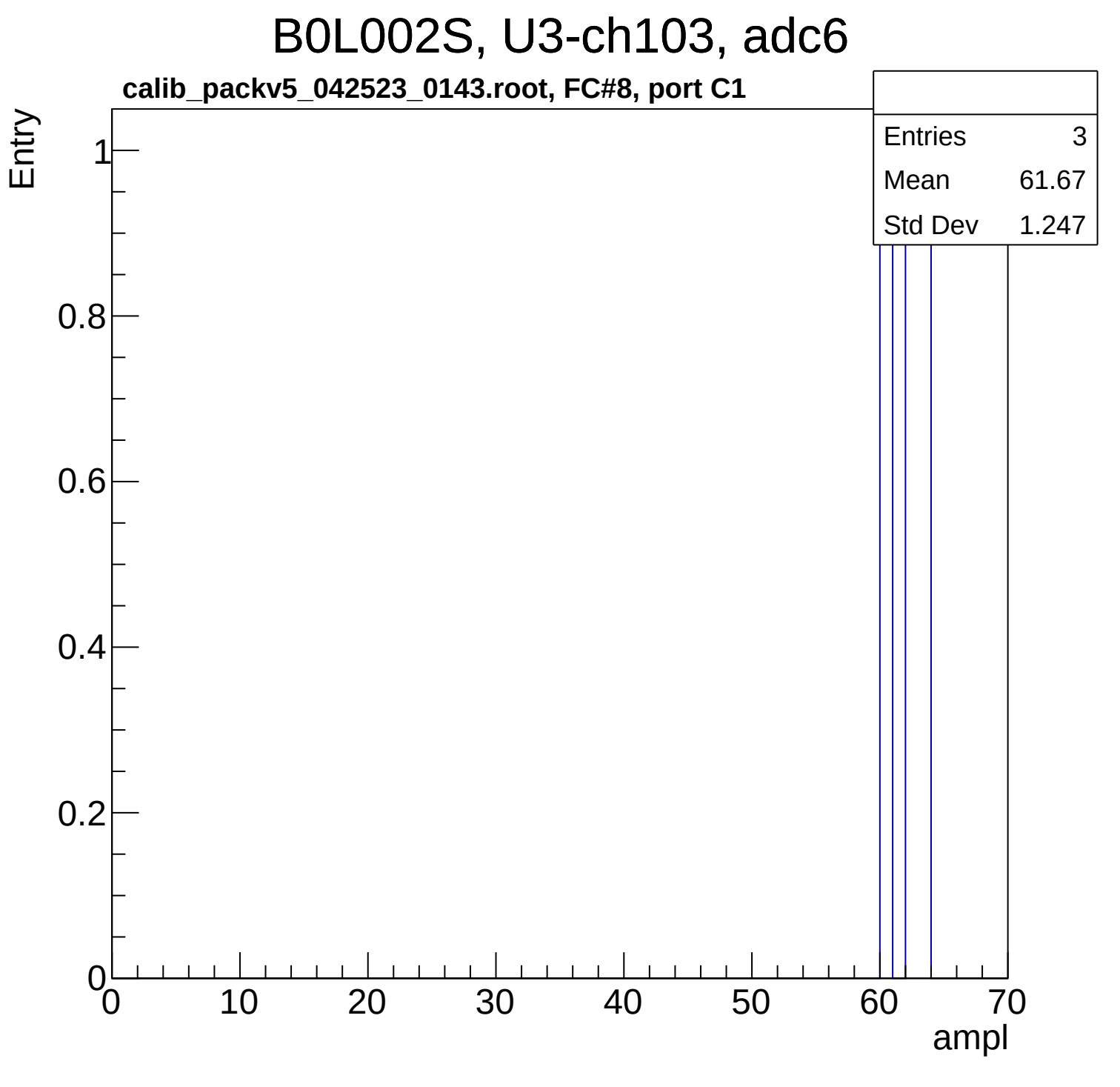
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch103, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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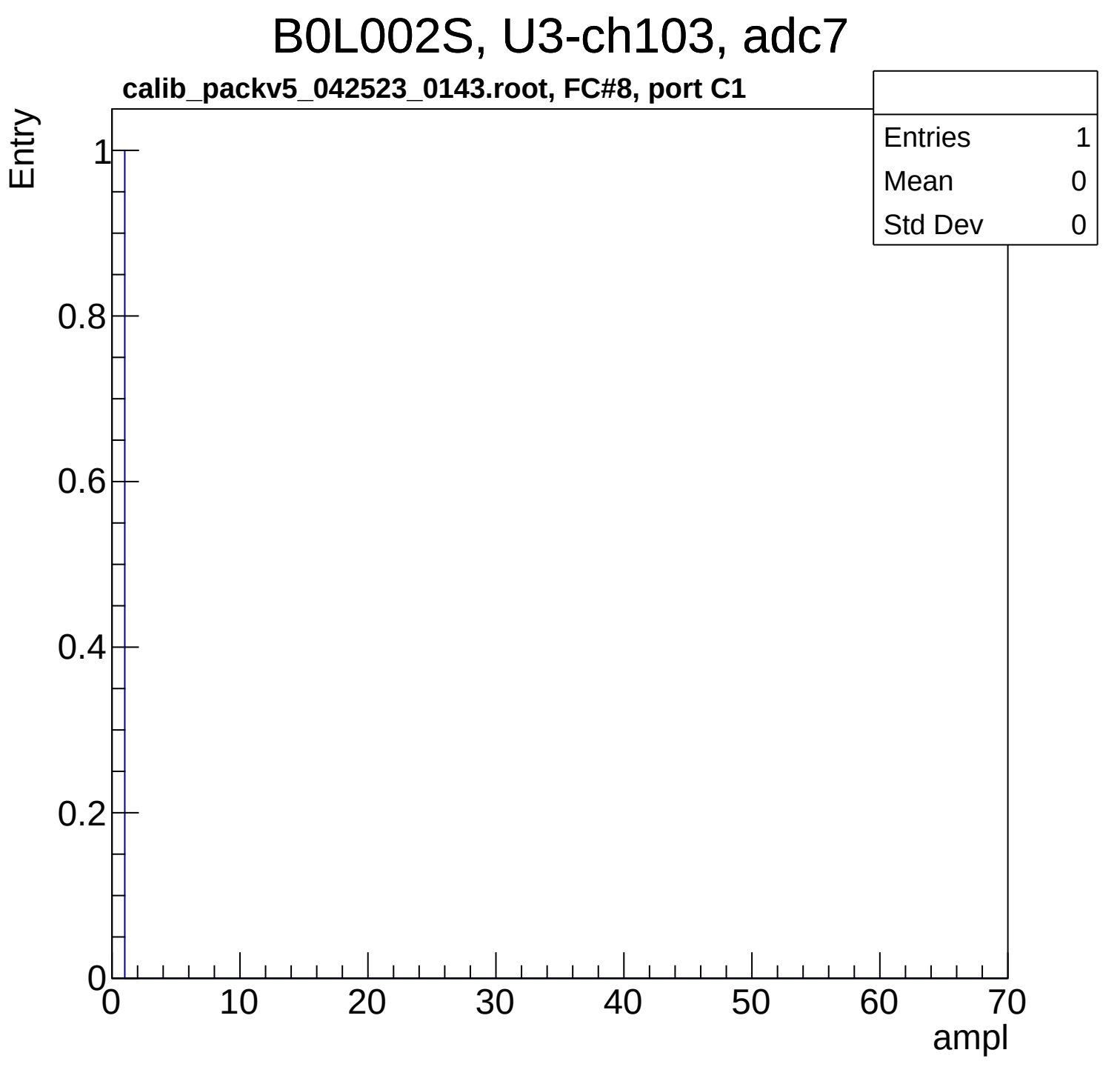
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch103, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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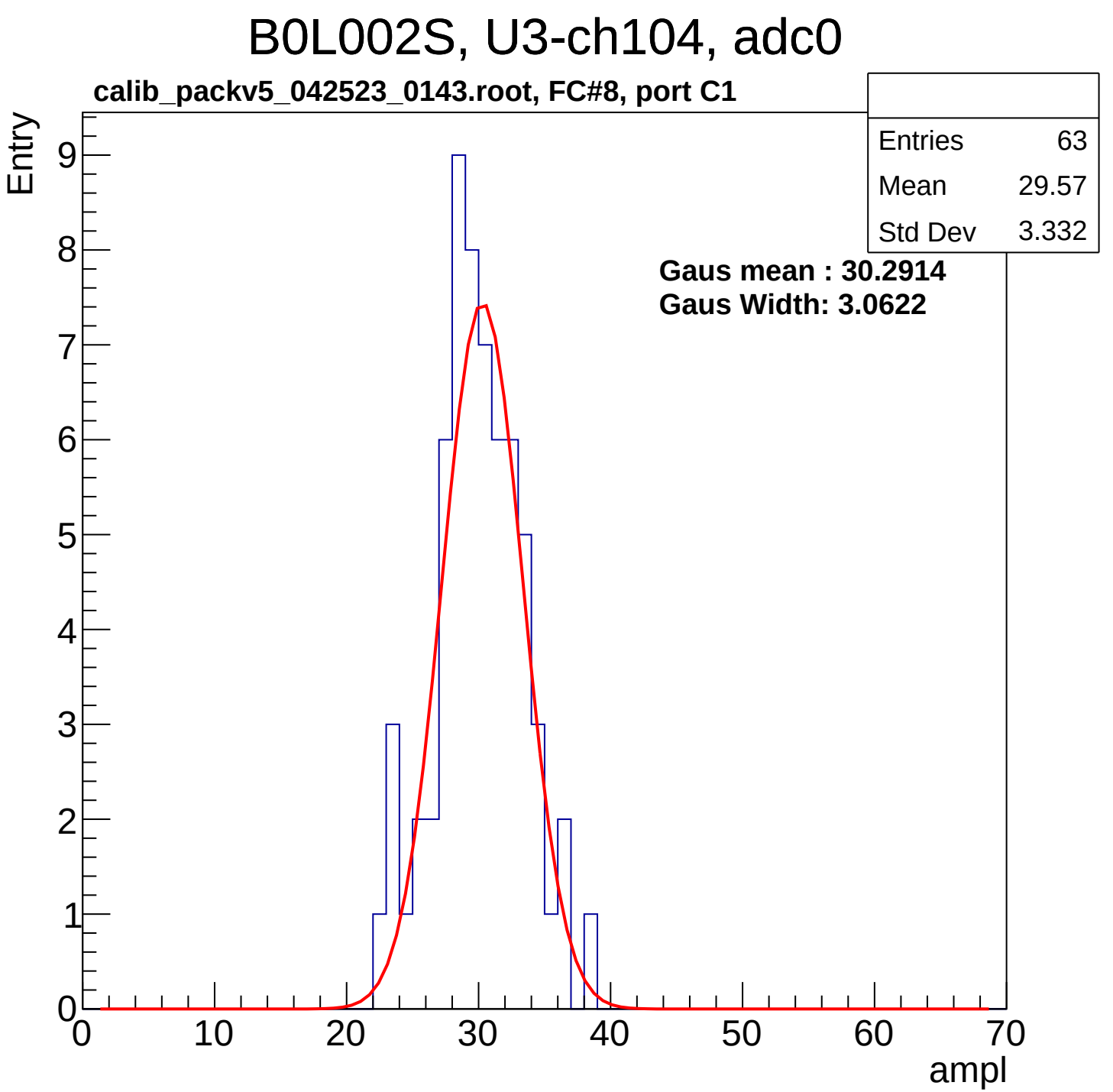
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch104, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 29.57
Std Dev 3.332
Gaus mean : 30.2914
Gaus Width: 3.0622
Entry
ampl
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2
3
4
5
6
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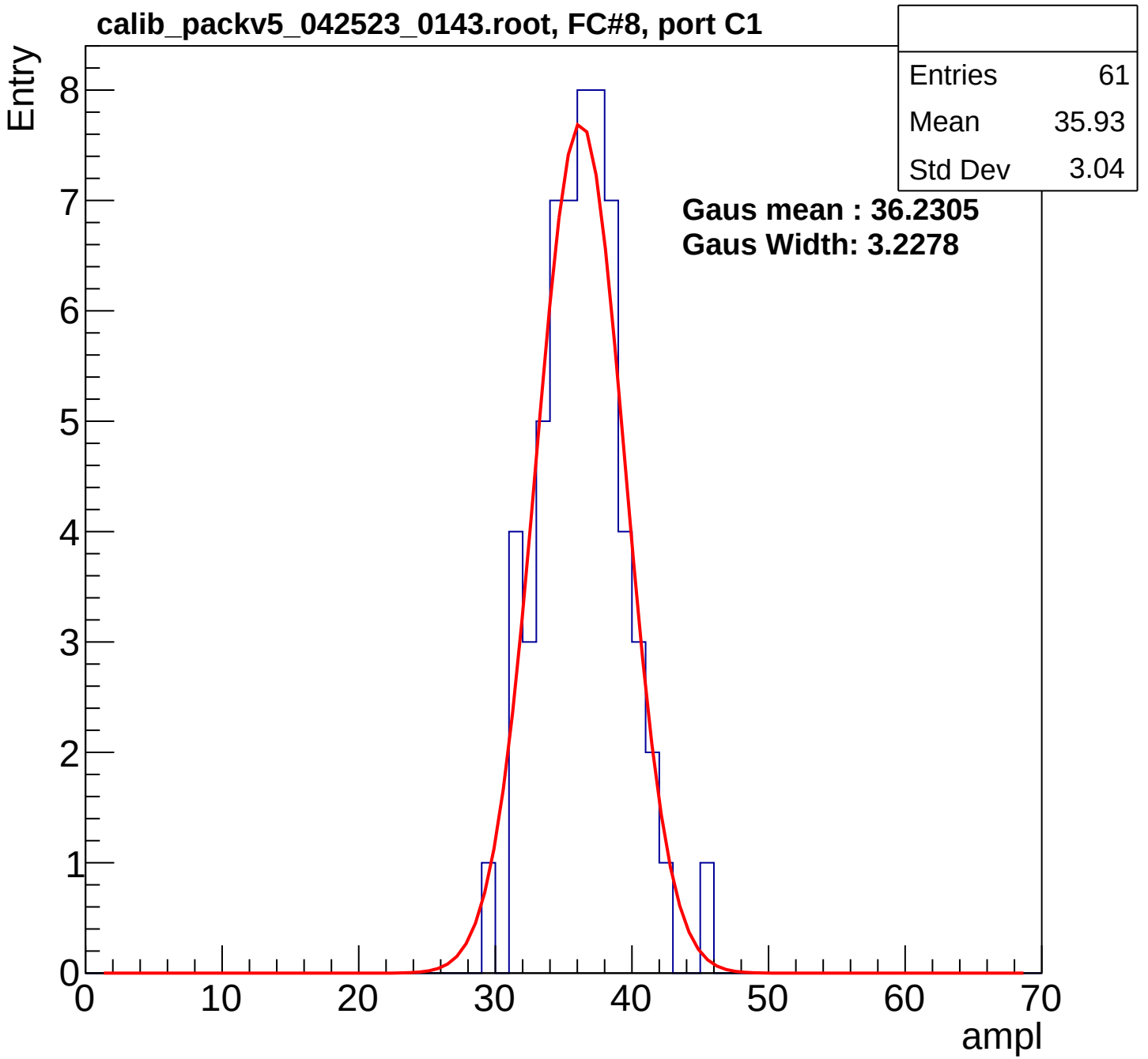
B0L002S, U3-ch104, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 35.93
Std Dev 3.04
Gaus mean : 36.2305
Gaus Width: 3.2278
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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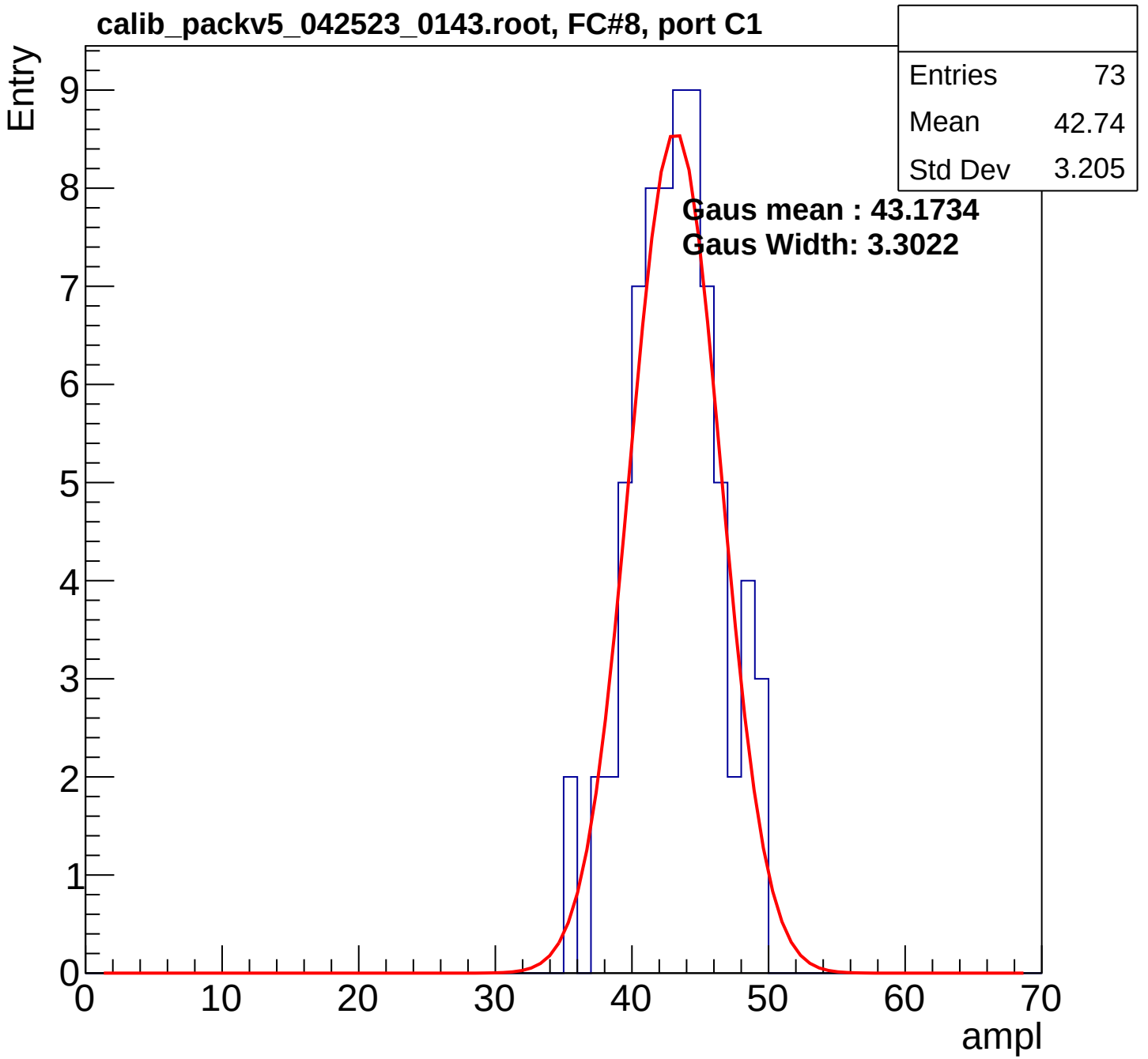
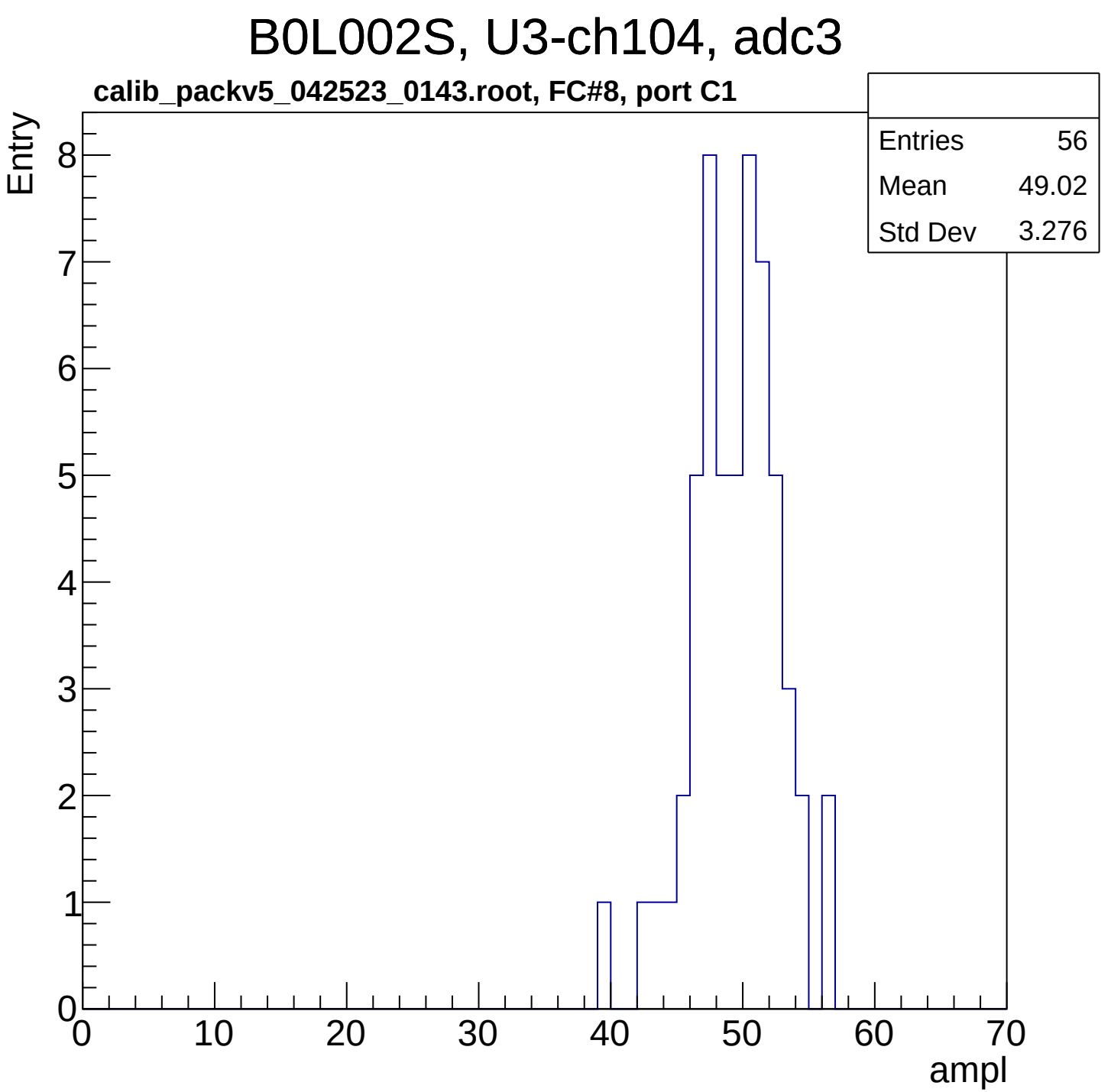

B0L002S, U3-ch104, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 42.74
Std Dev 3.205
Gaus mean : 43.1734
Gaus Width: 3.3022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch104, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 49.02
Std Dev 3.276
Entry
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2
3
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6
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8
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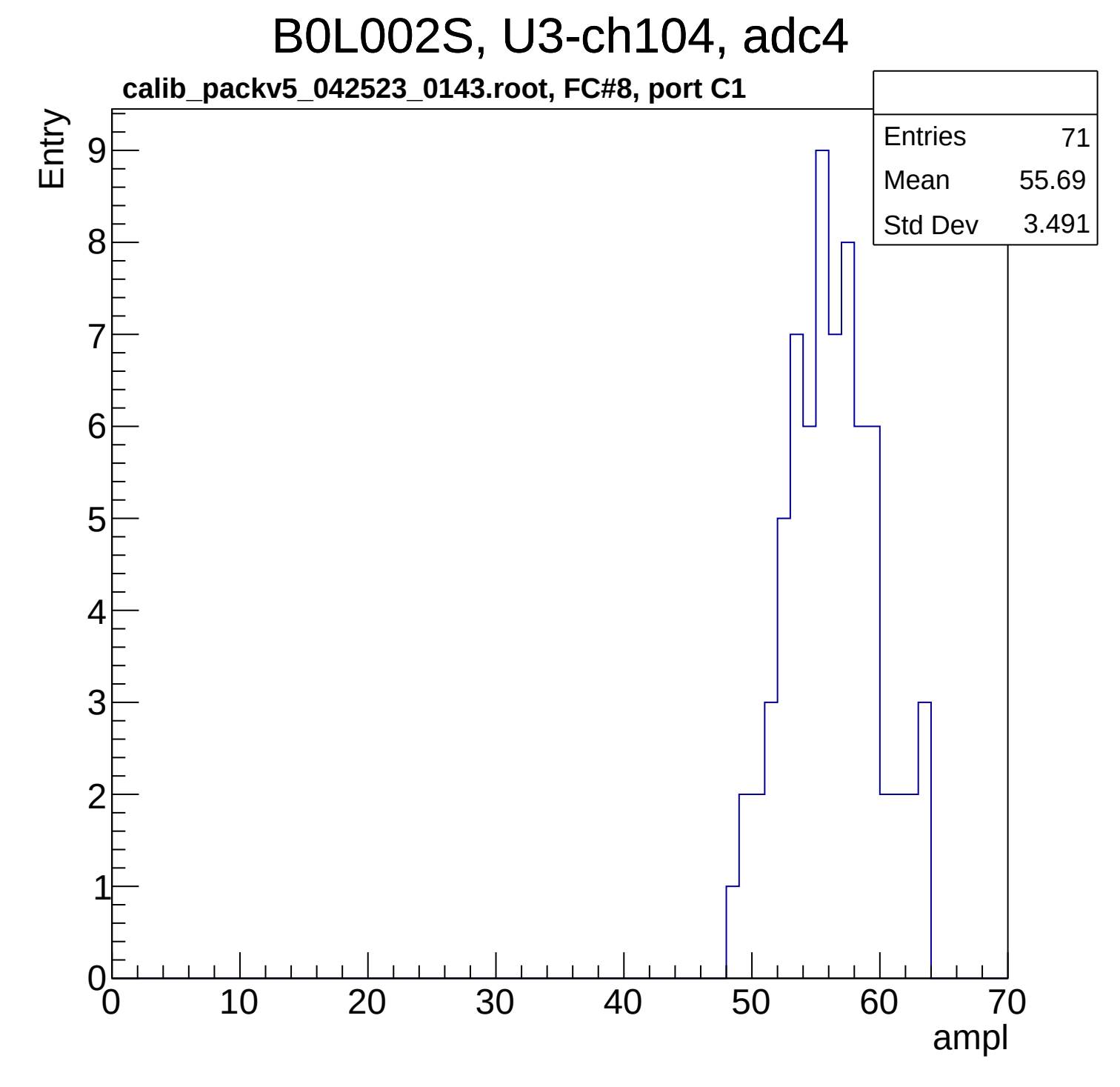
70
ampl


B0L002S, U3-ch104, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 55.69
Std Dev 3.491
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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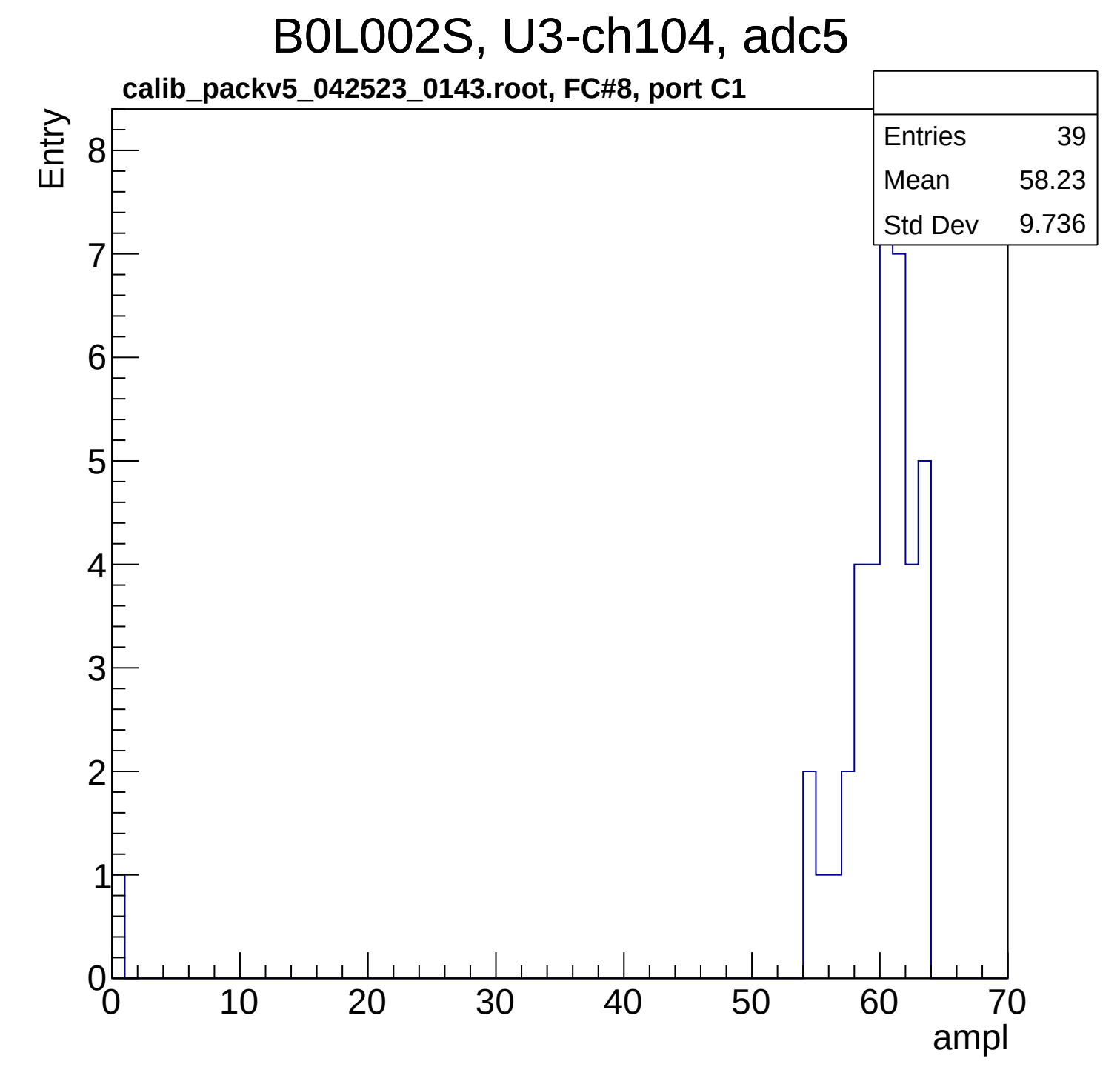
B0L002S, U3-ch104, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.23
Std Dev 9.736
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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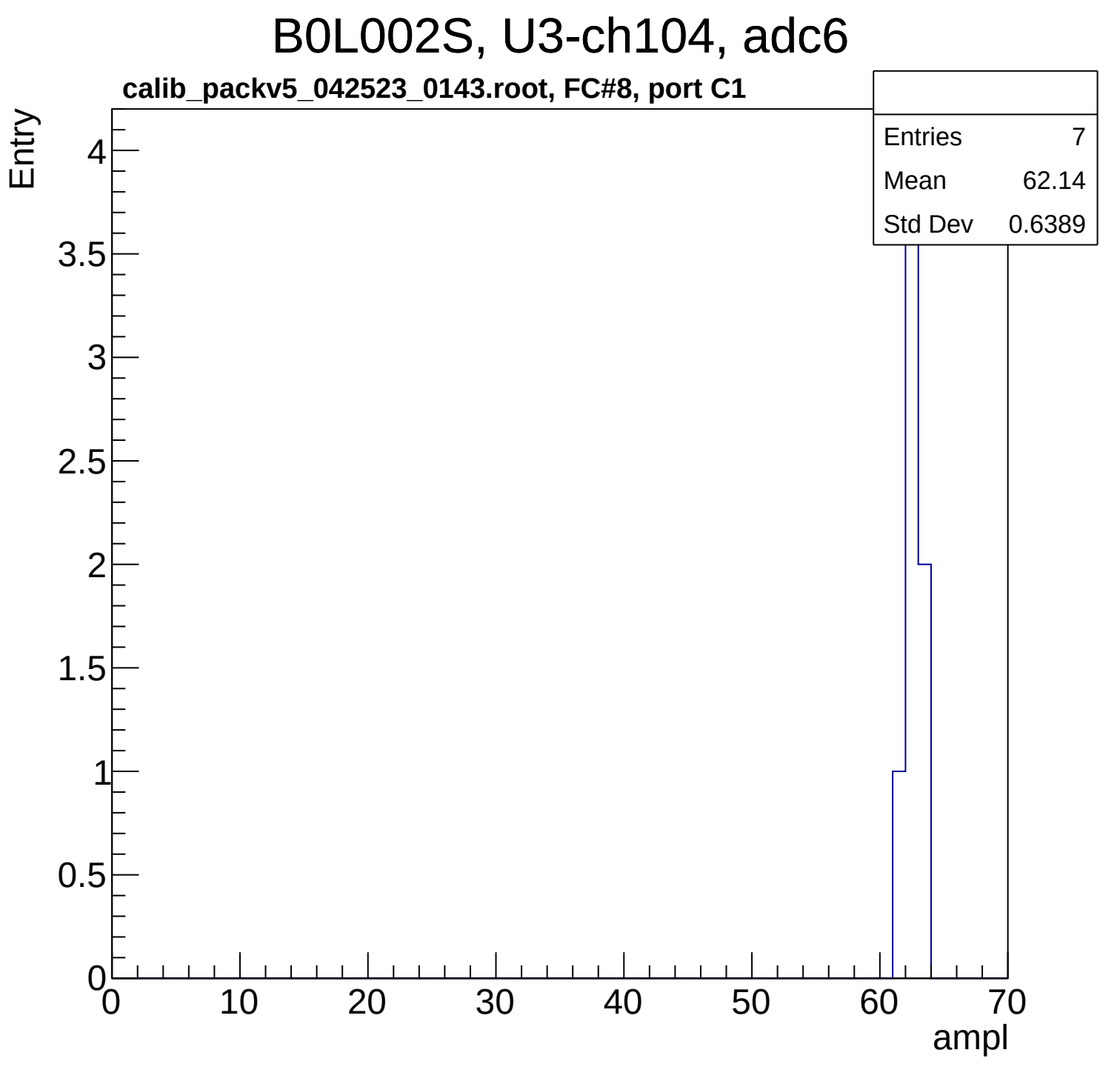
B0L002S, U3-ch104, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 62.14
Std Dev 0.6389
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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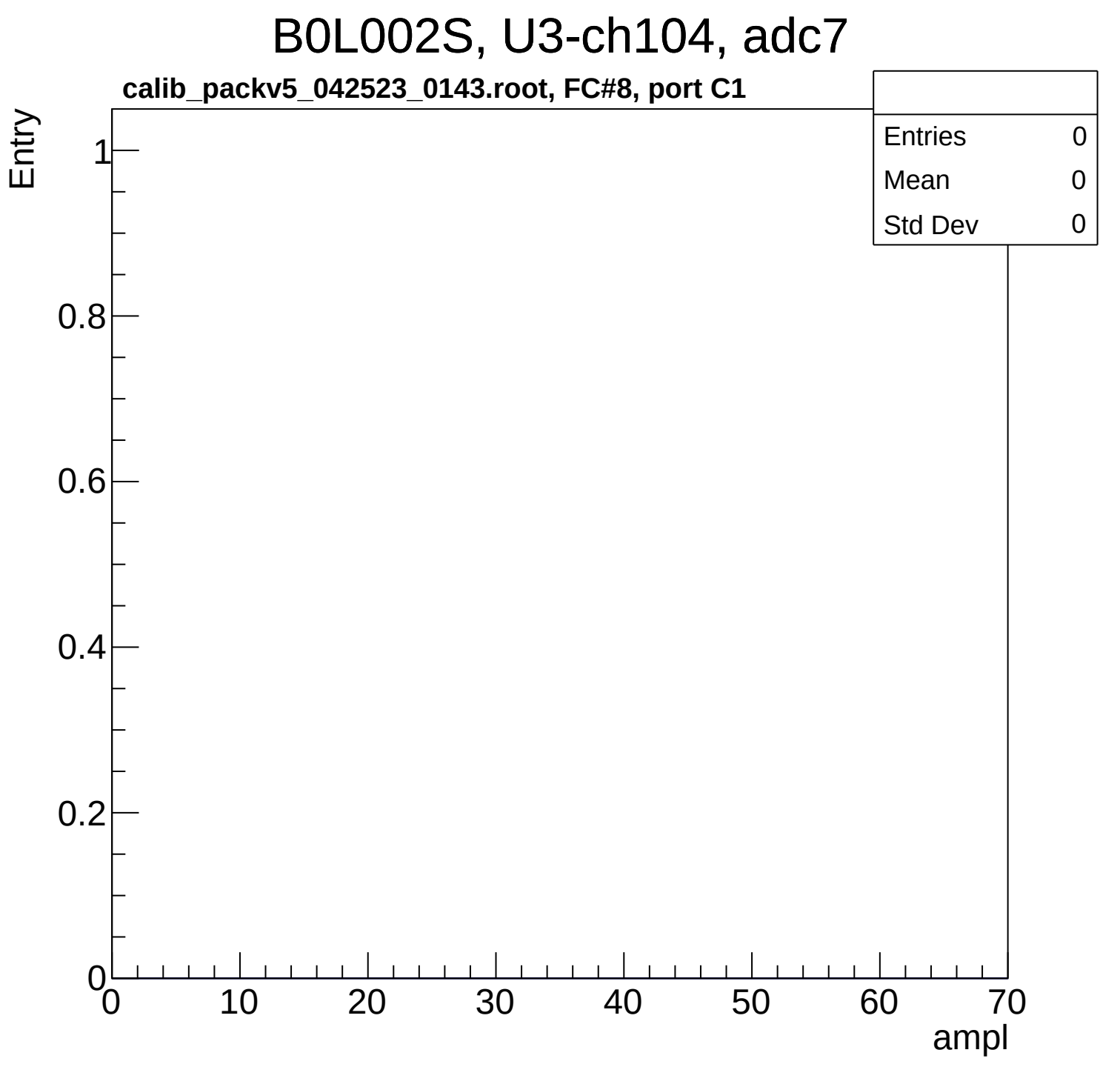
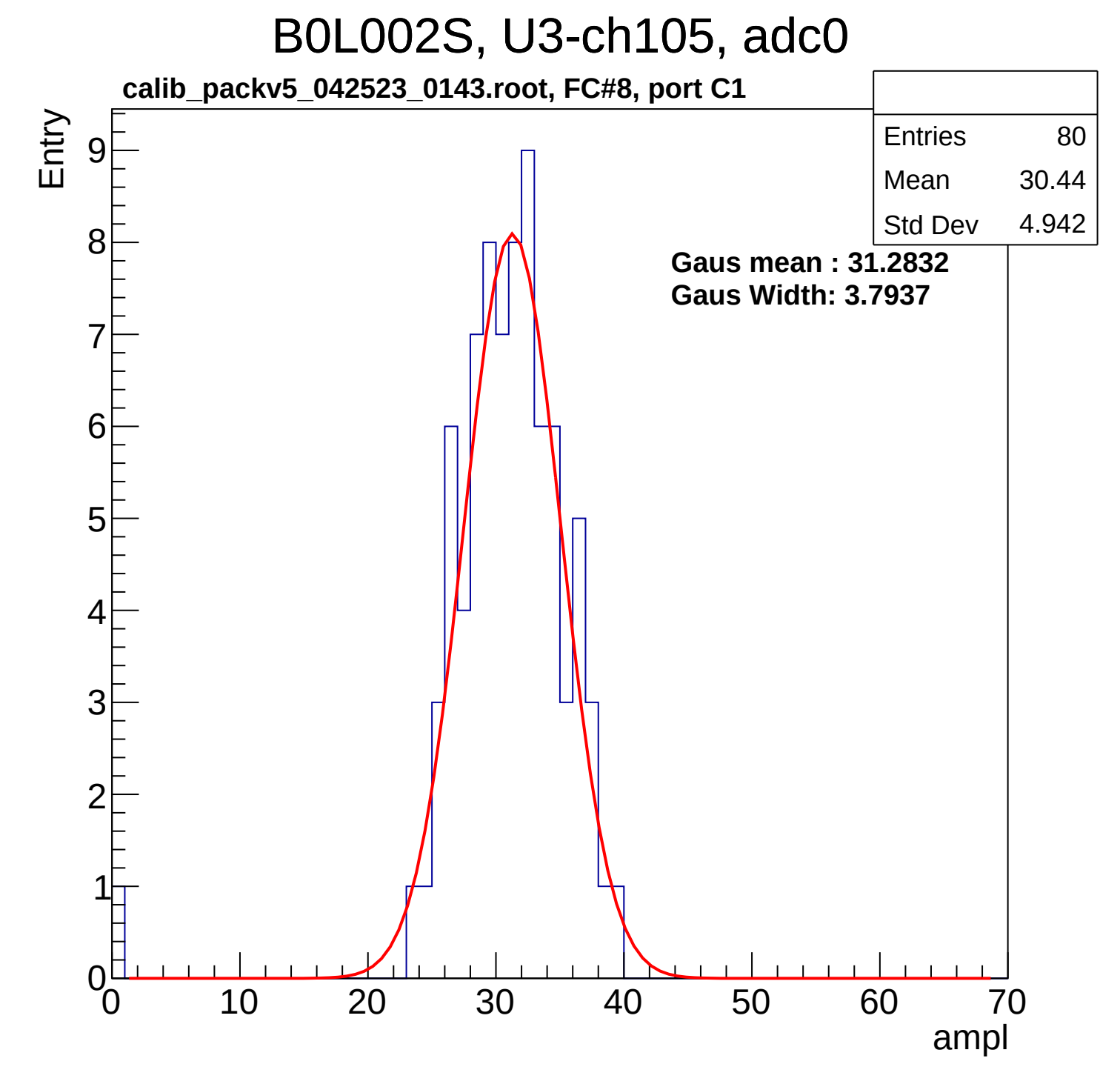

B0L002S, U3-ch104, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch105, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.44
Std Dev 4.942
Gaus mean : 31.2832
Gaus Width: 3.7937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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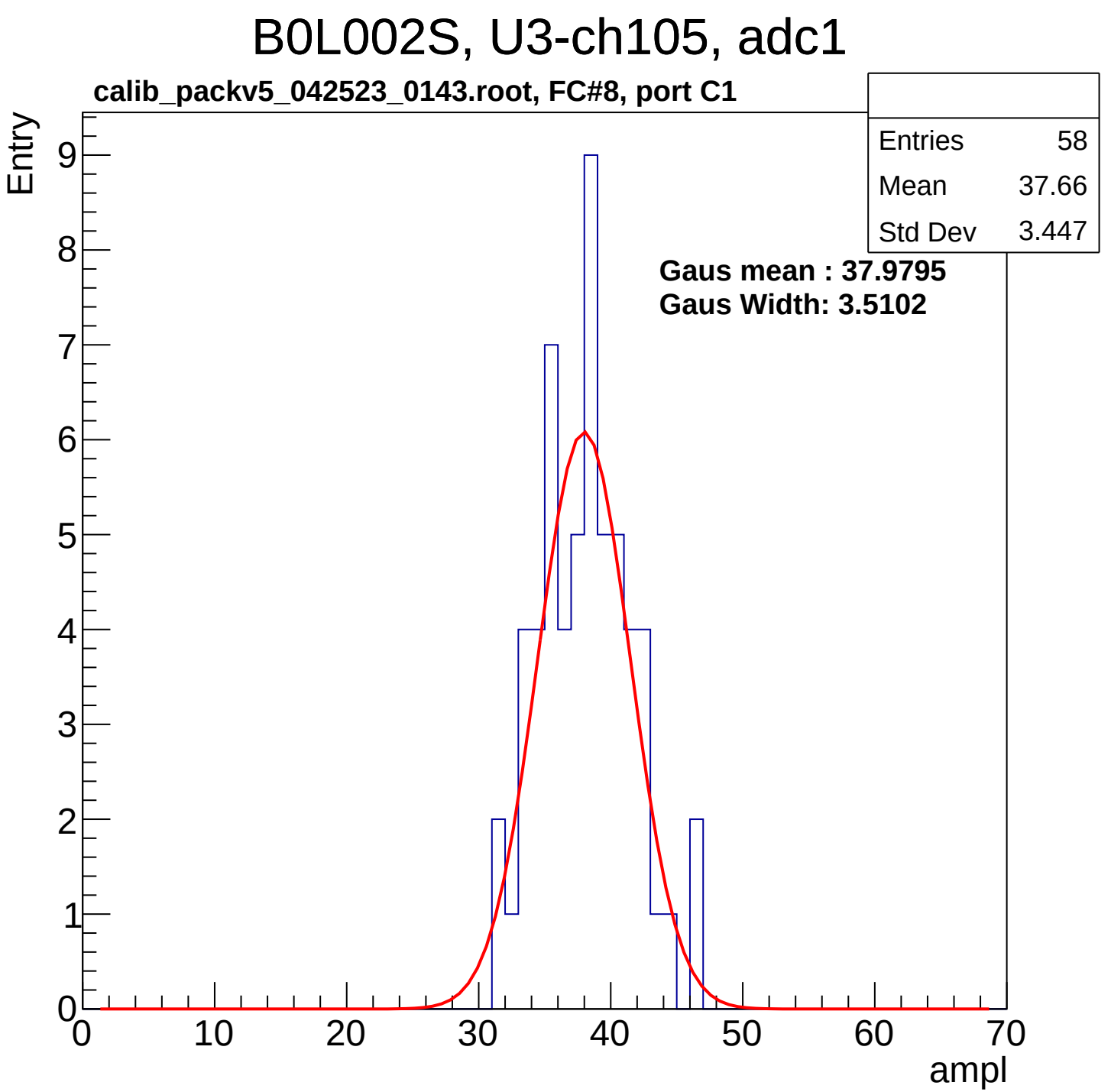
B0L002S, U3-ch105, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 37.66
Std Dev 3.447
Gaus mean : 37.9795
Gaus Width: 3.5102
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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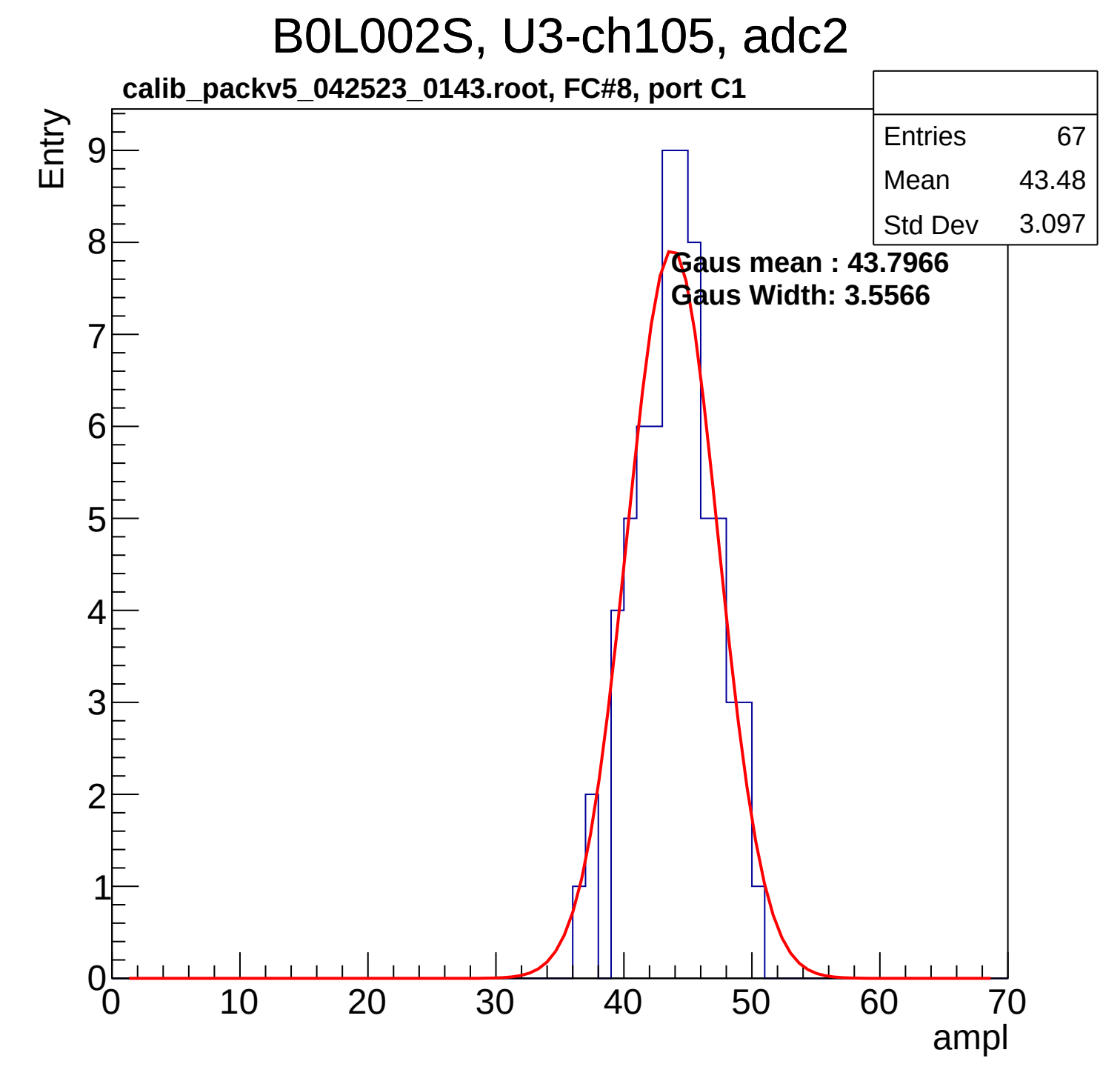
B0L002S, U3-ch105, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.48
Std Dev 3.097
Gaus mean : 43.7966
Gaus Width: 3.5566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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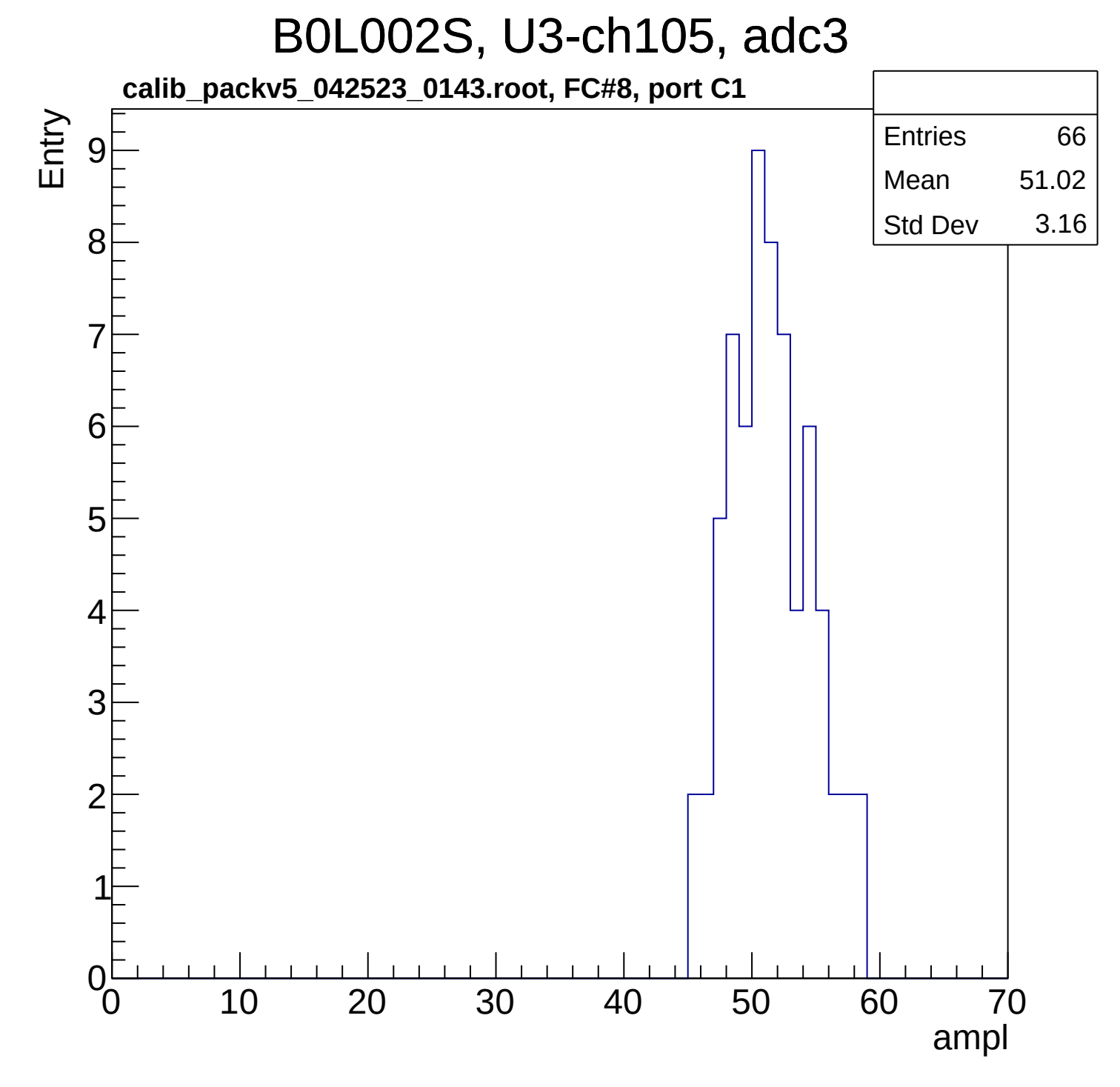
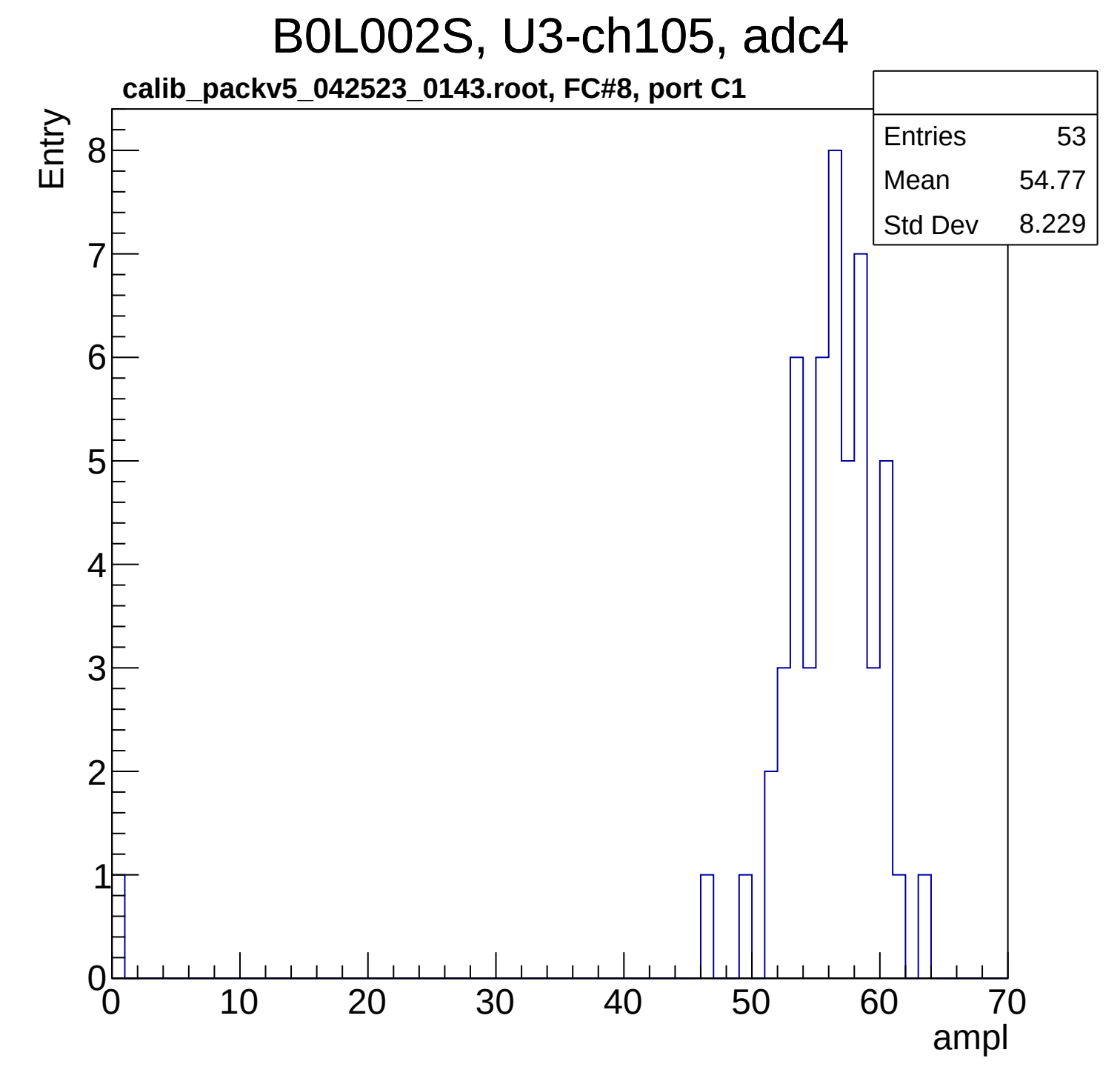
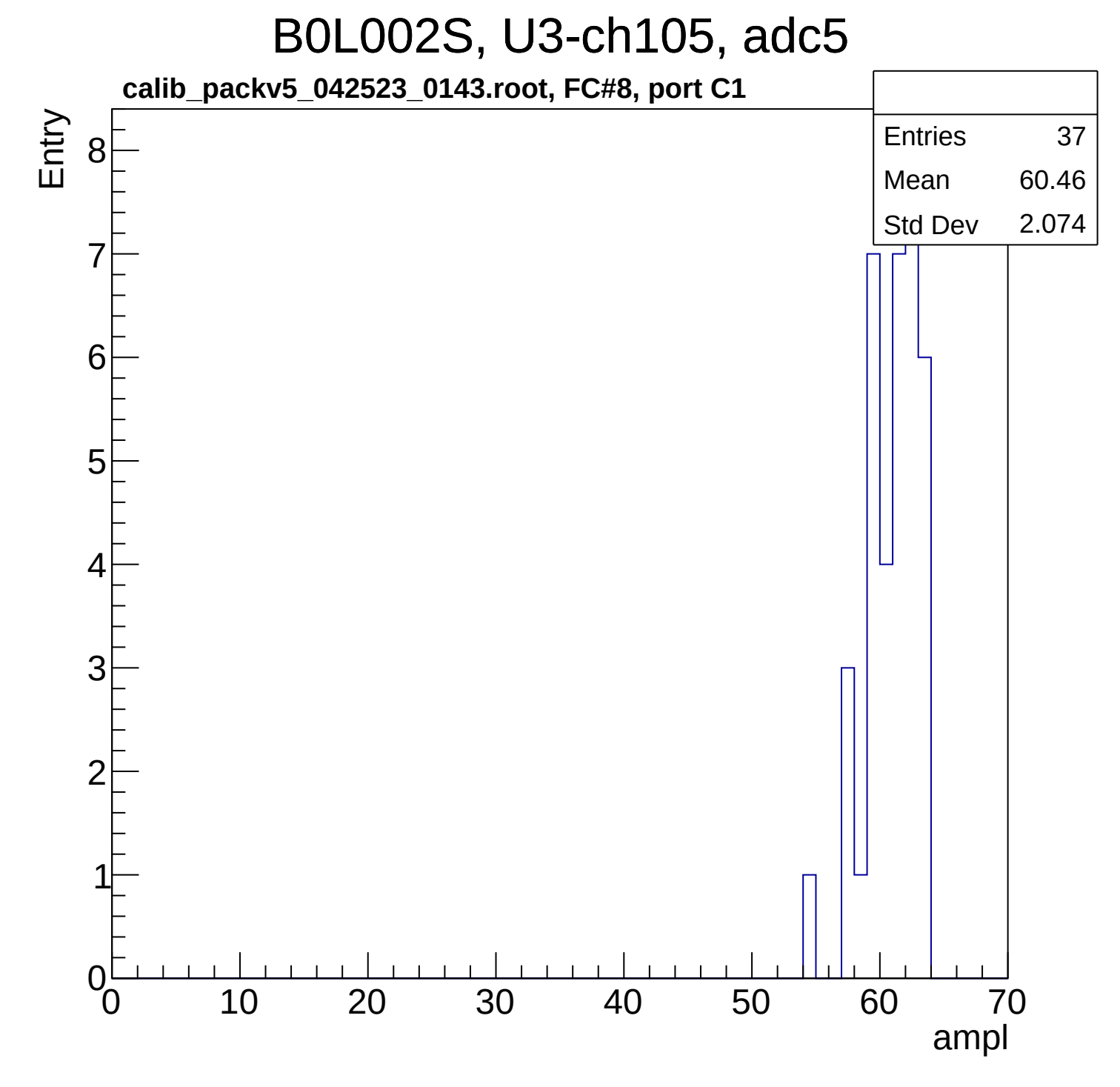

B0L002S, U3-ch105, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.02
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch105, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 54.77
Std Dev 8.229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch105, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 60.46
Std Dev 2.074
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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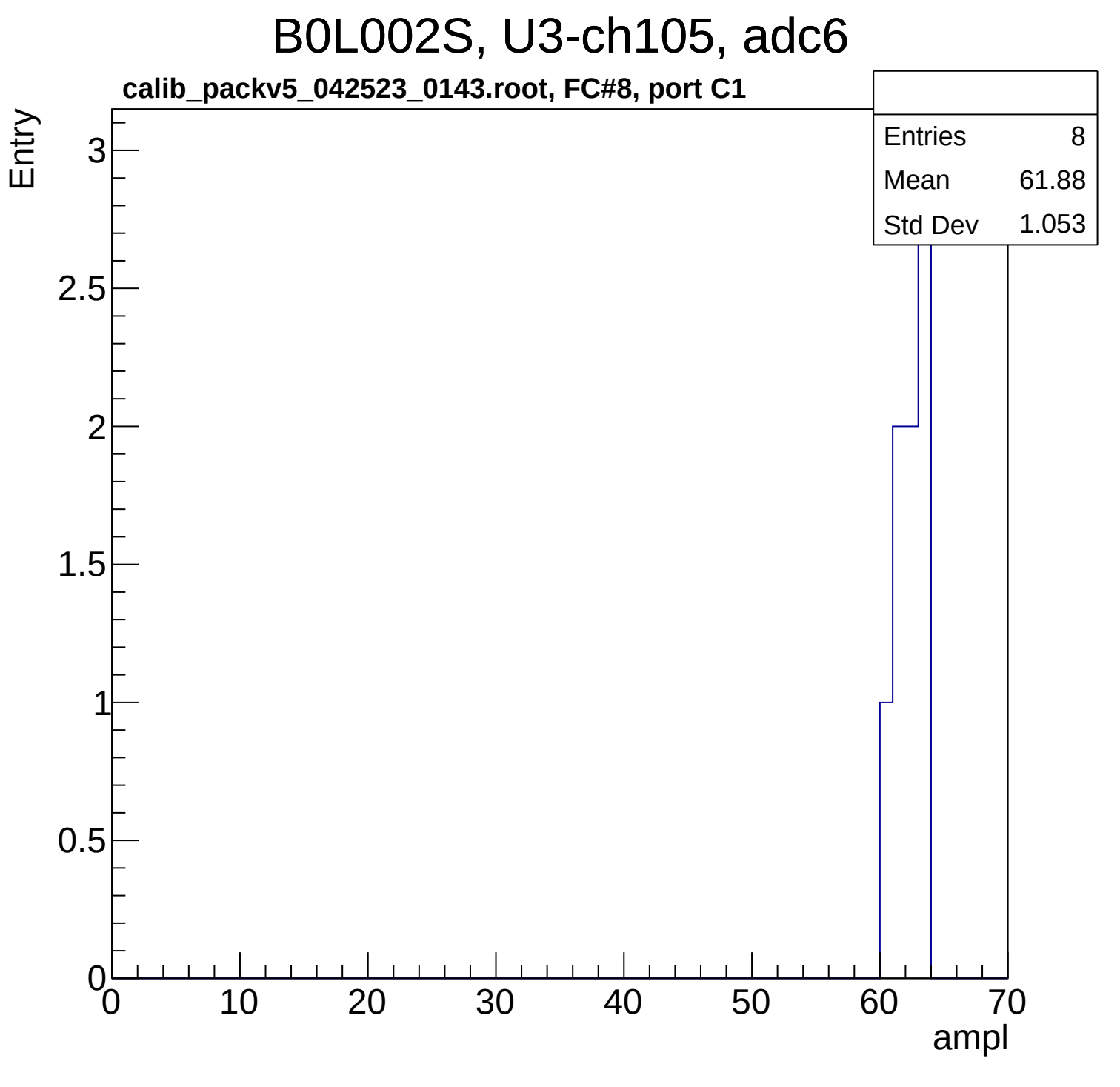
B0L002S, U3-ch105, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 61.88
Std Dev 1.053
Entry
ampl
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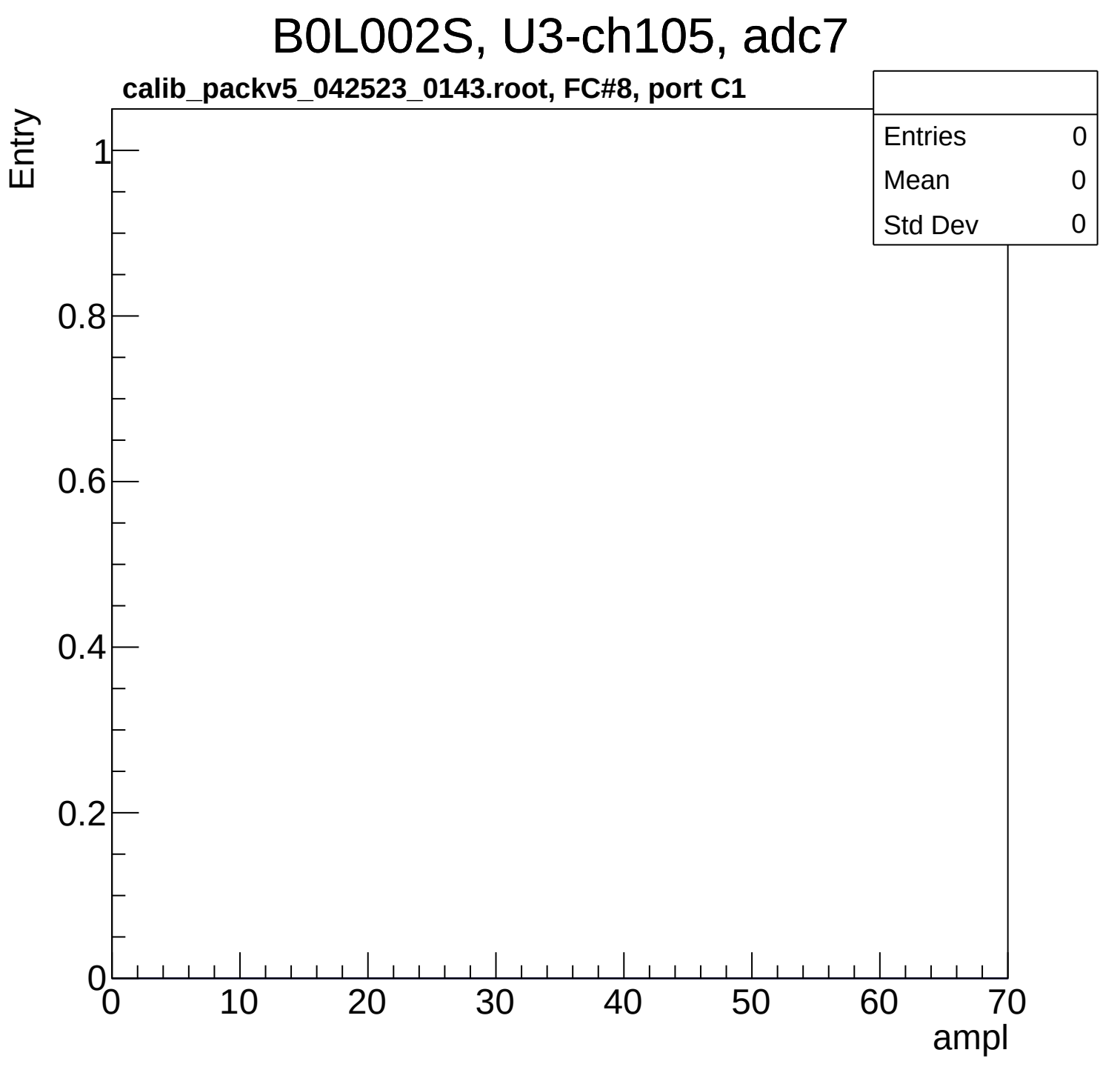
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L002S, U3-ch105, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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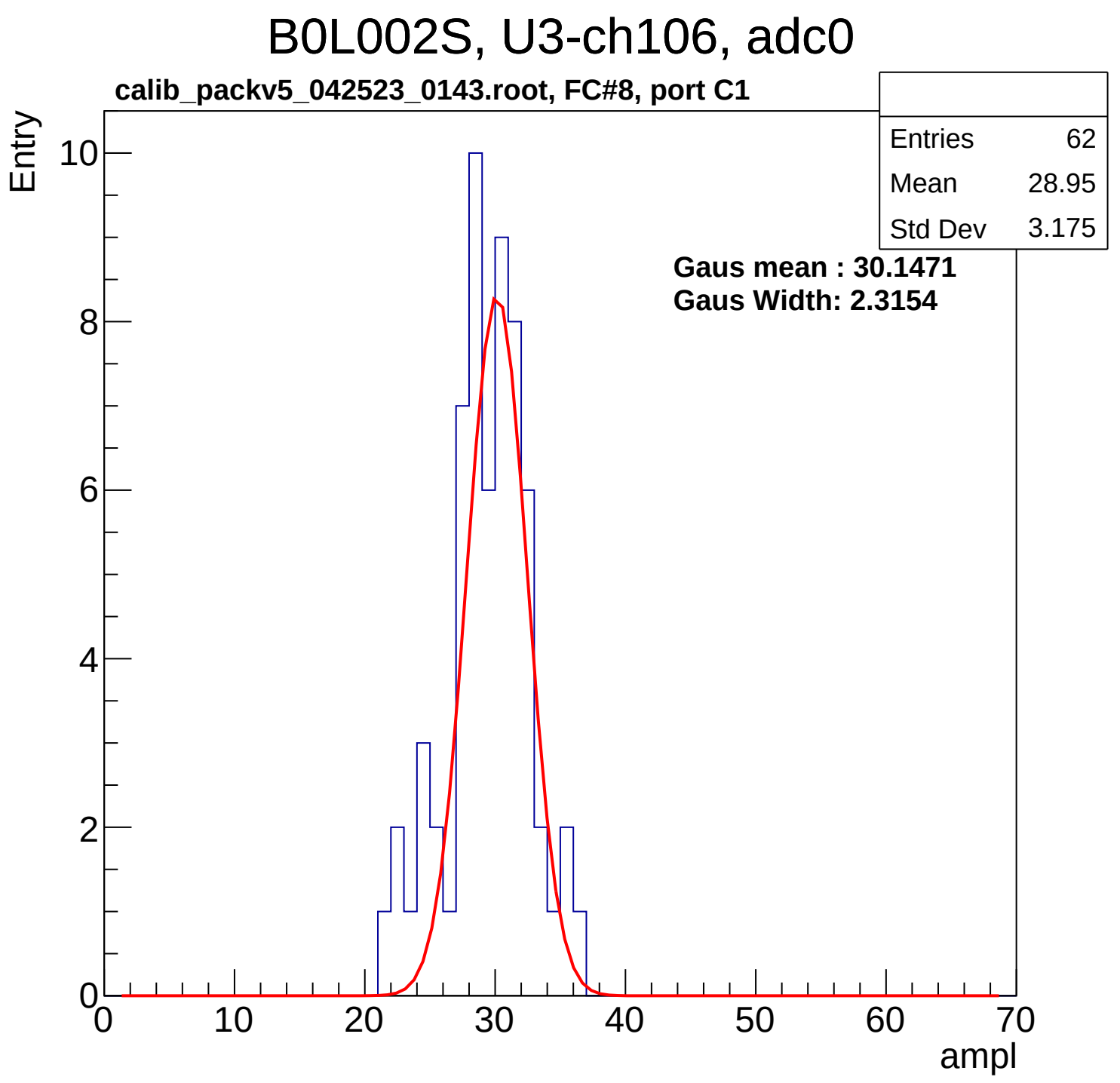
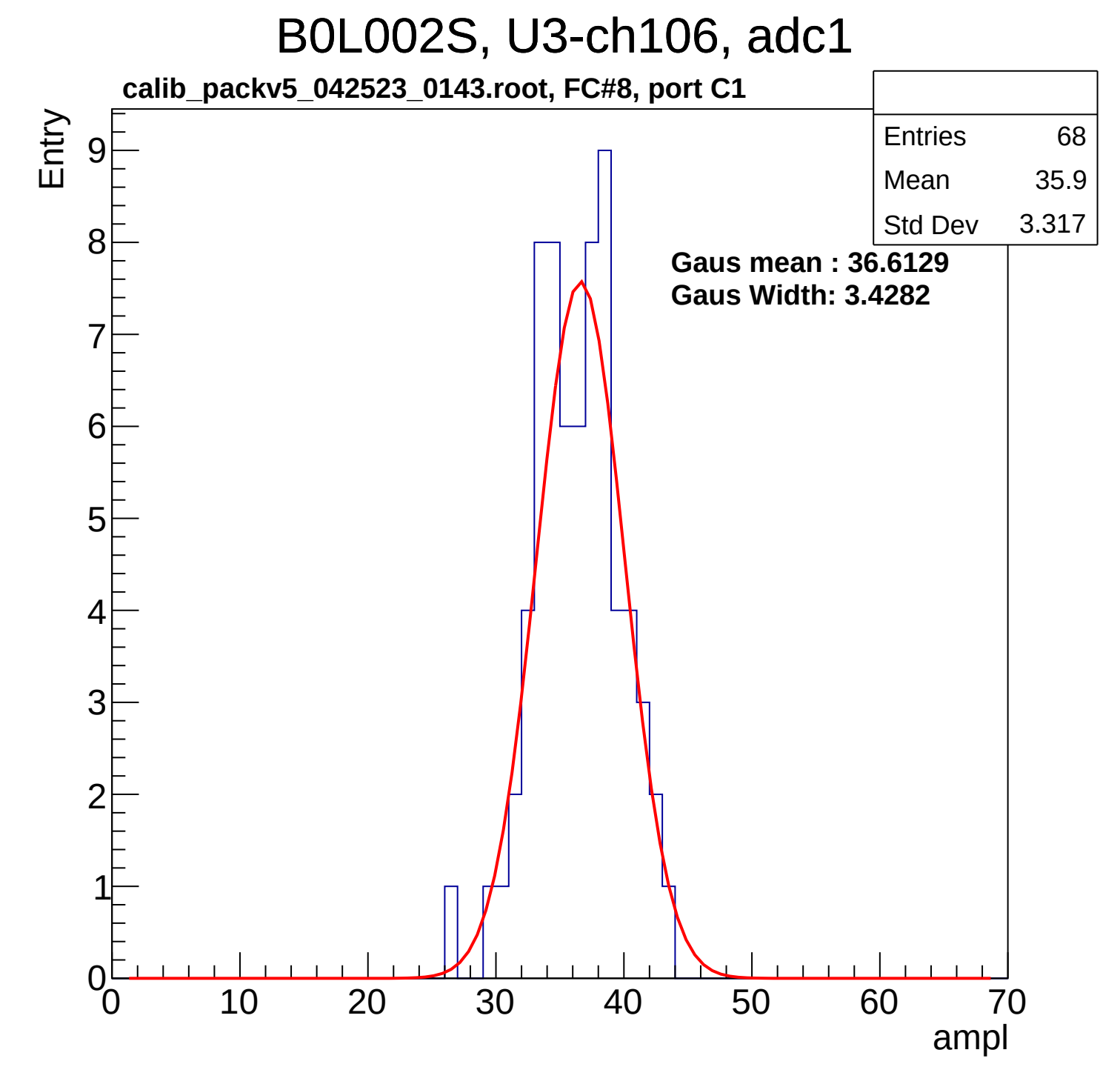
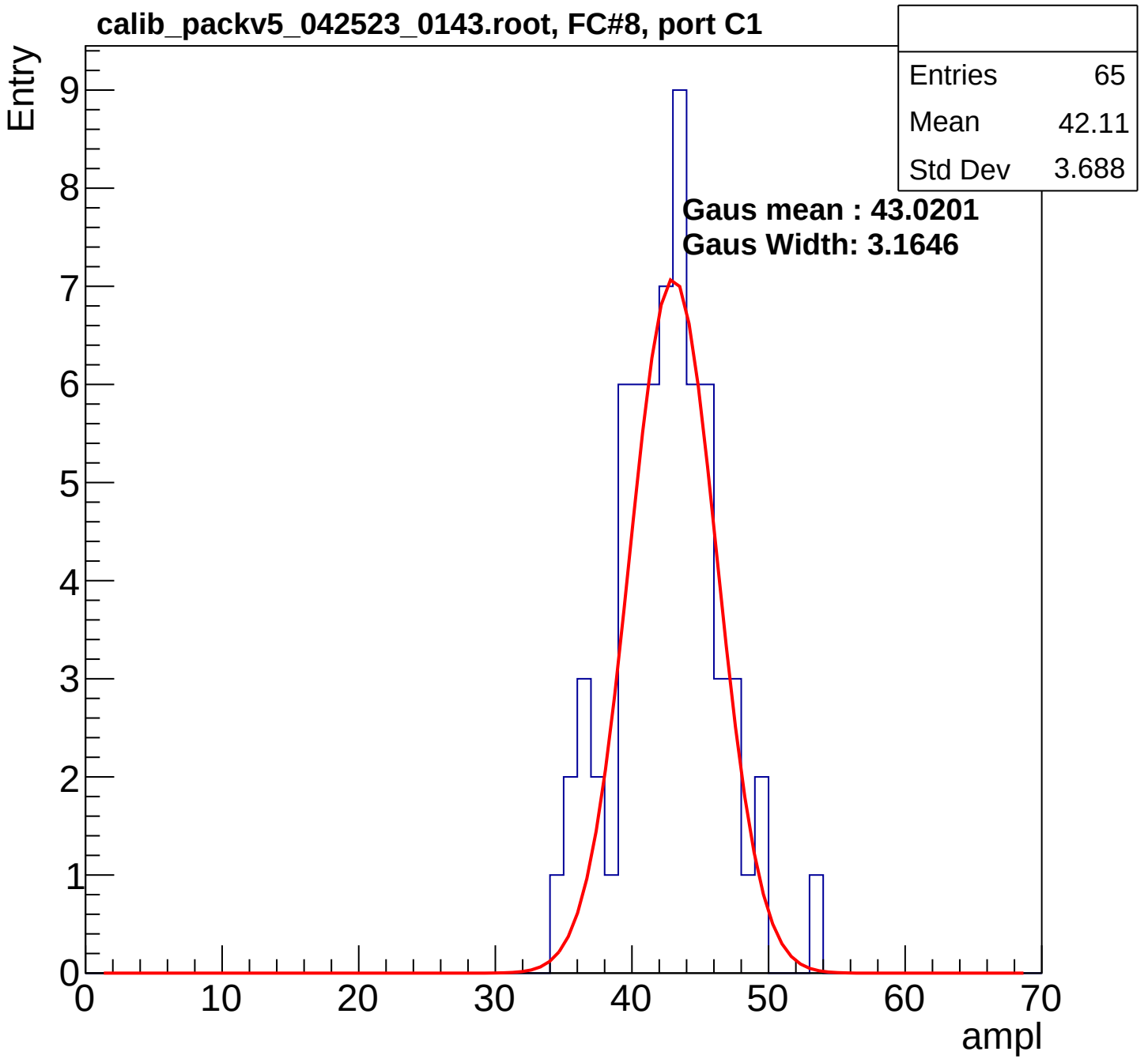
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch106, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 28.95
Std Dev 3.175
Gaus mean : 30.1471
Gaus Width: 2.3154
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch106, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 35.9
Std Dev 3.317
Gaus mean : 36.6129
Gaus Width: 3.4282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch106, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 42.11
Std Dev 3.688
Gaus mean : 43.0201
Gaus Width: 3.1646
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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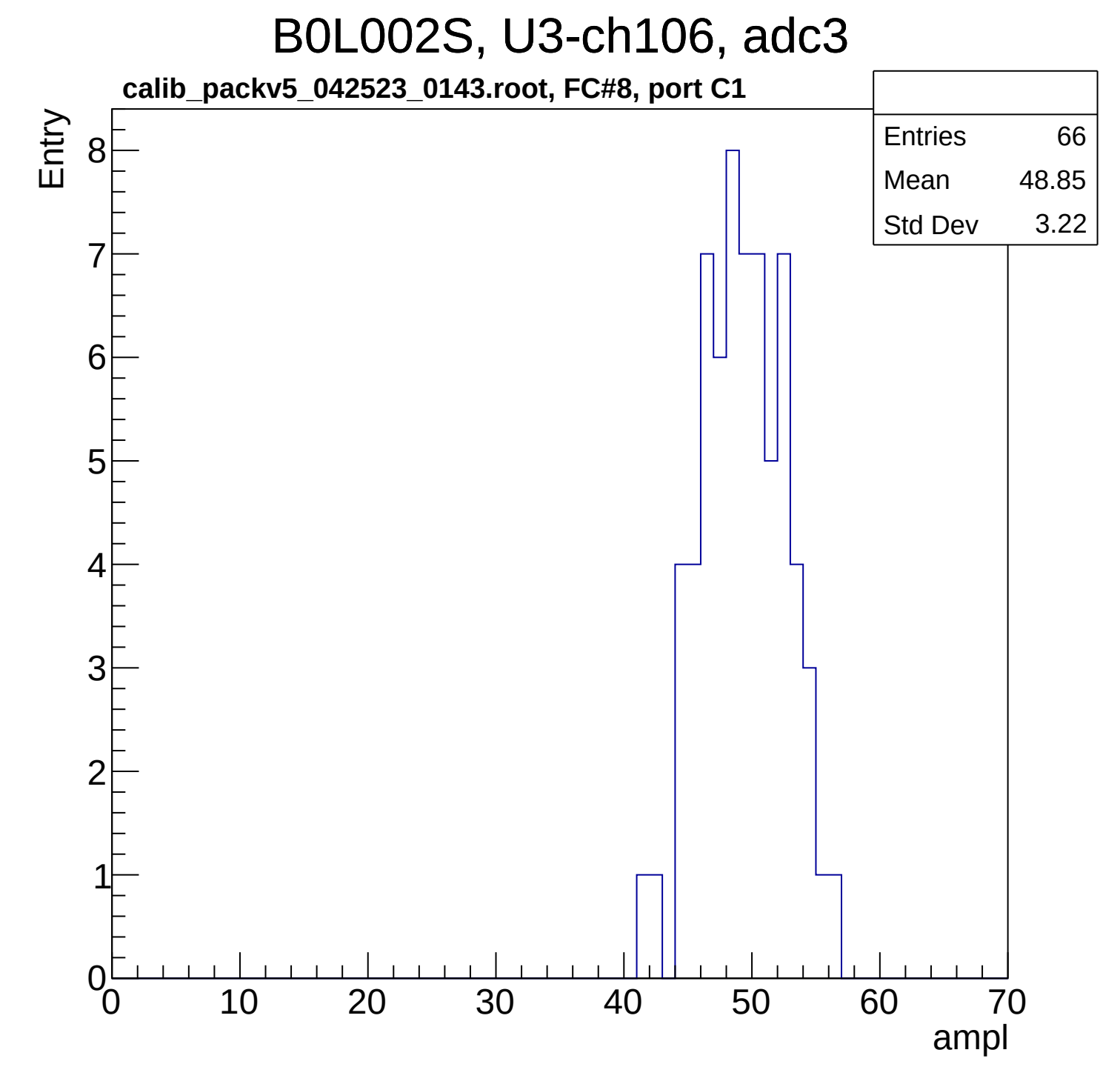
B0L002S, U3-ch106, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 48.85
Std Dev 3.22
Entry
ampl
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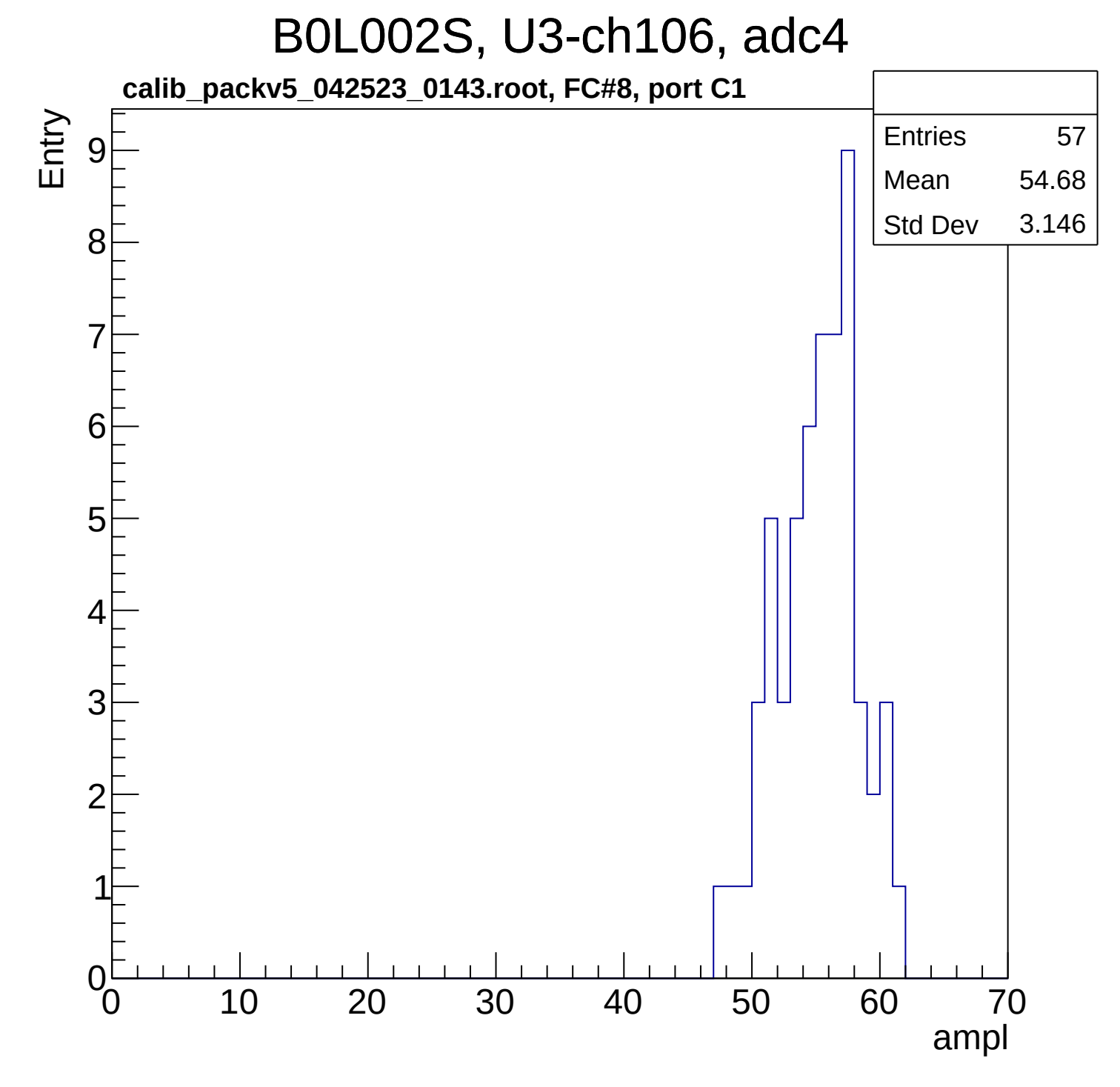
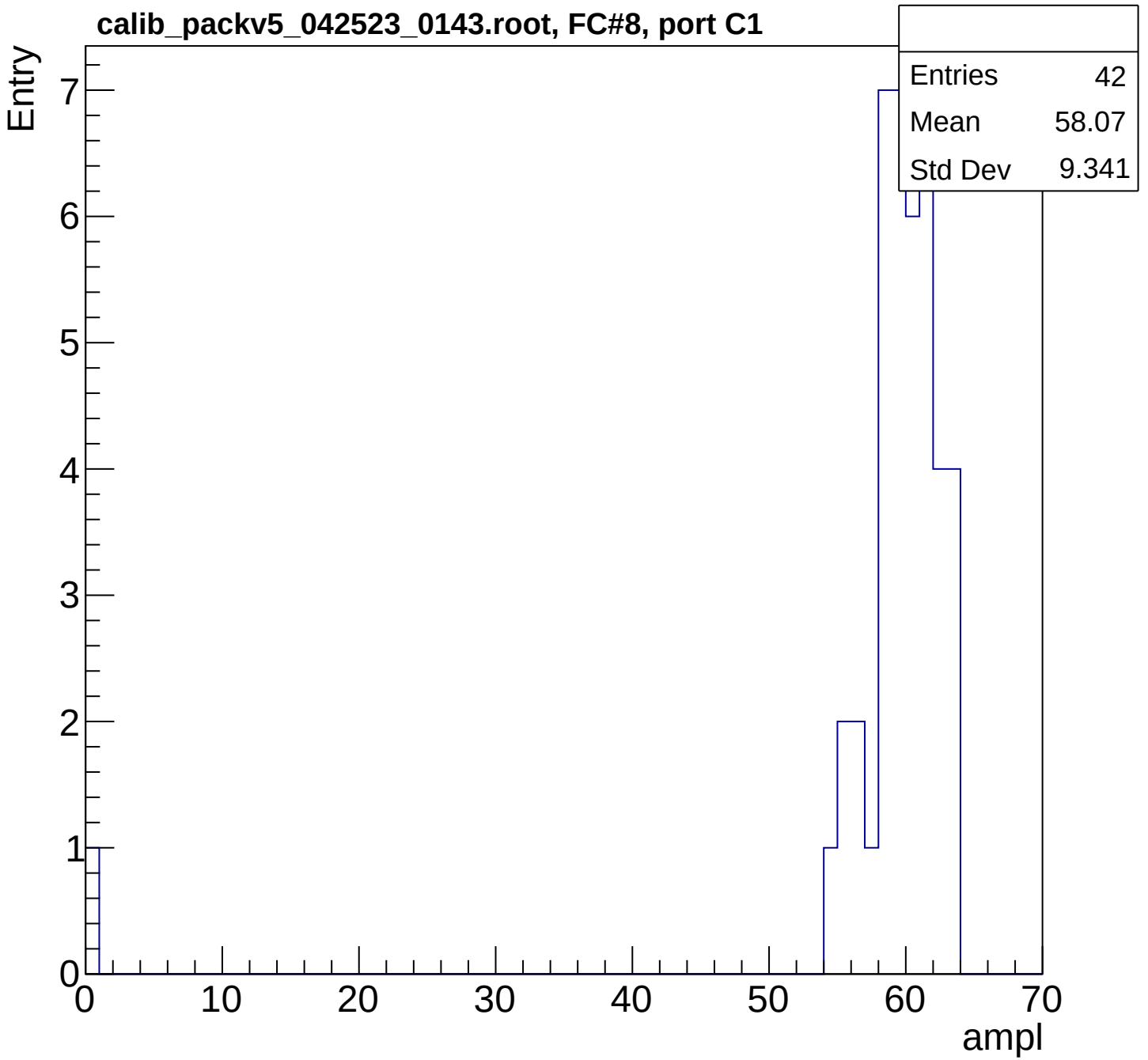
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch106, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 54.68
Std Dev 3.146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch106, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.07
Std Dev 9.341
Entry
ampl
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1
2
3
4
5
6
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0
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20
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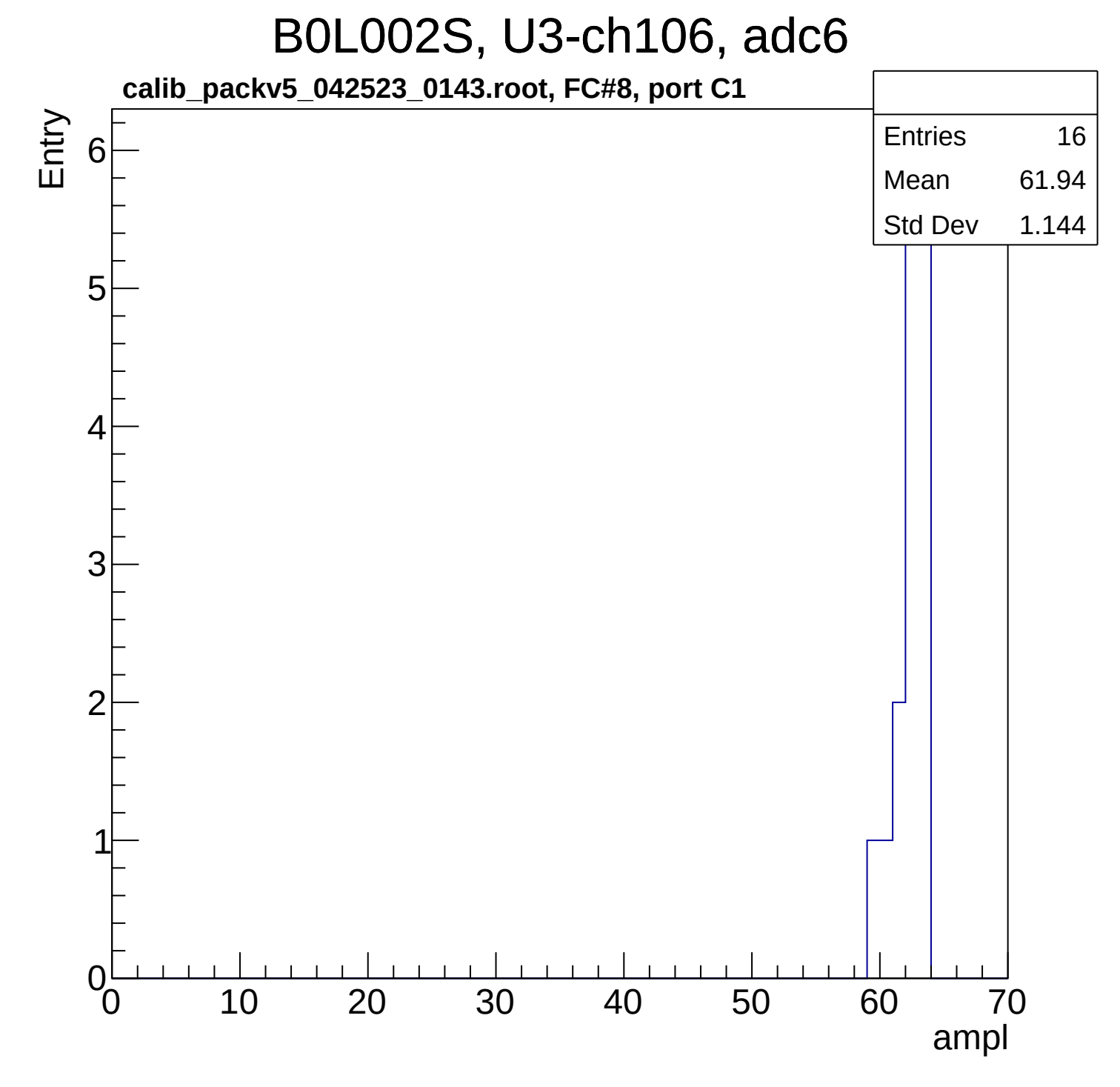
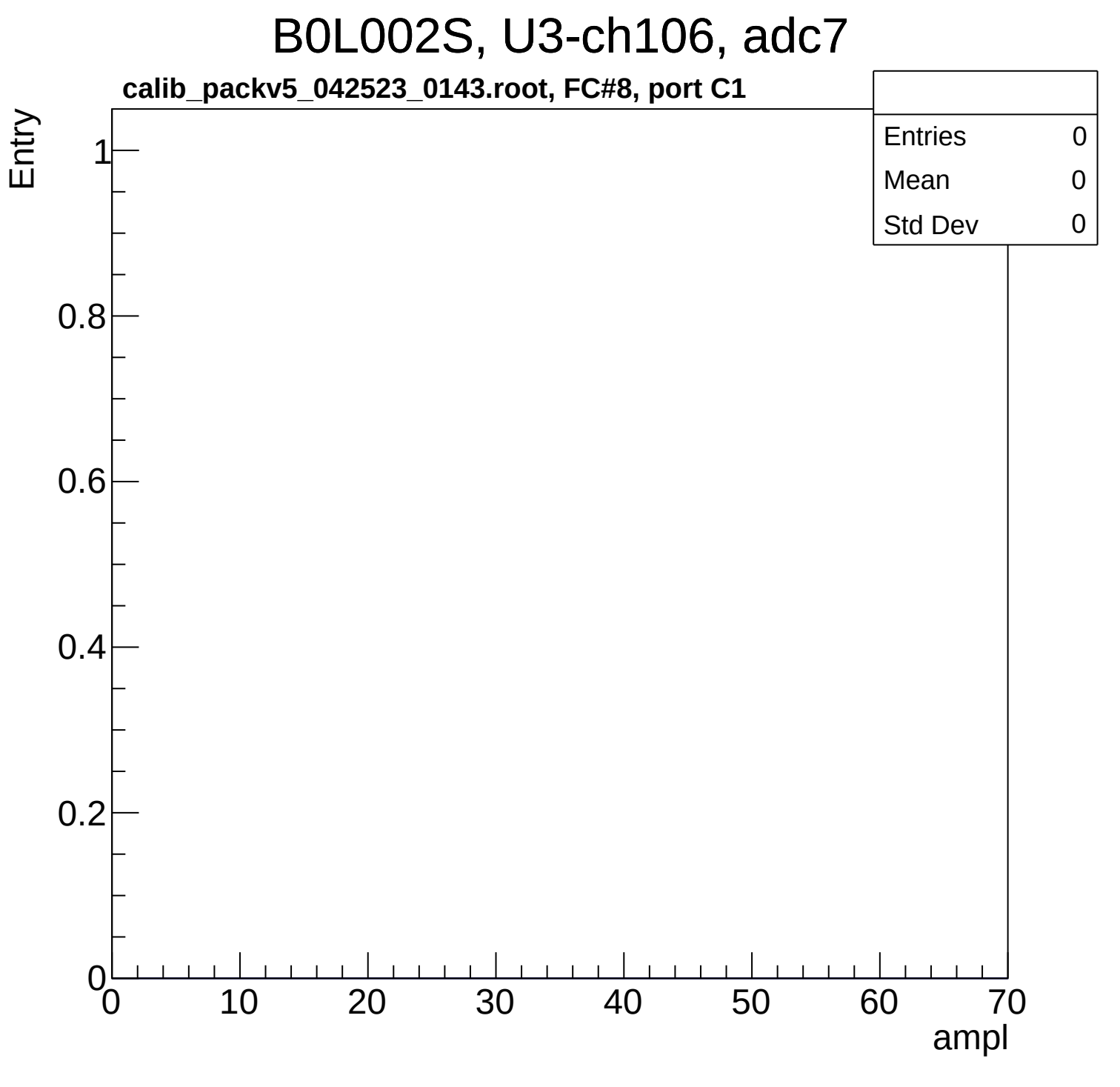
40
50
60
70


B0L002S, U3-ch106, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 61.94
Std Dev 1.144
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U3-ch106, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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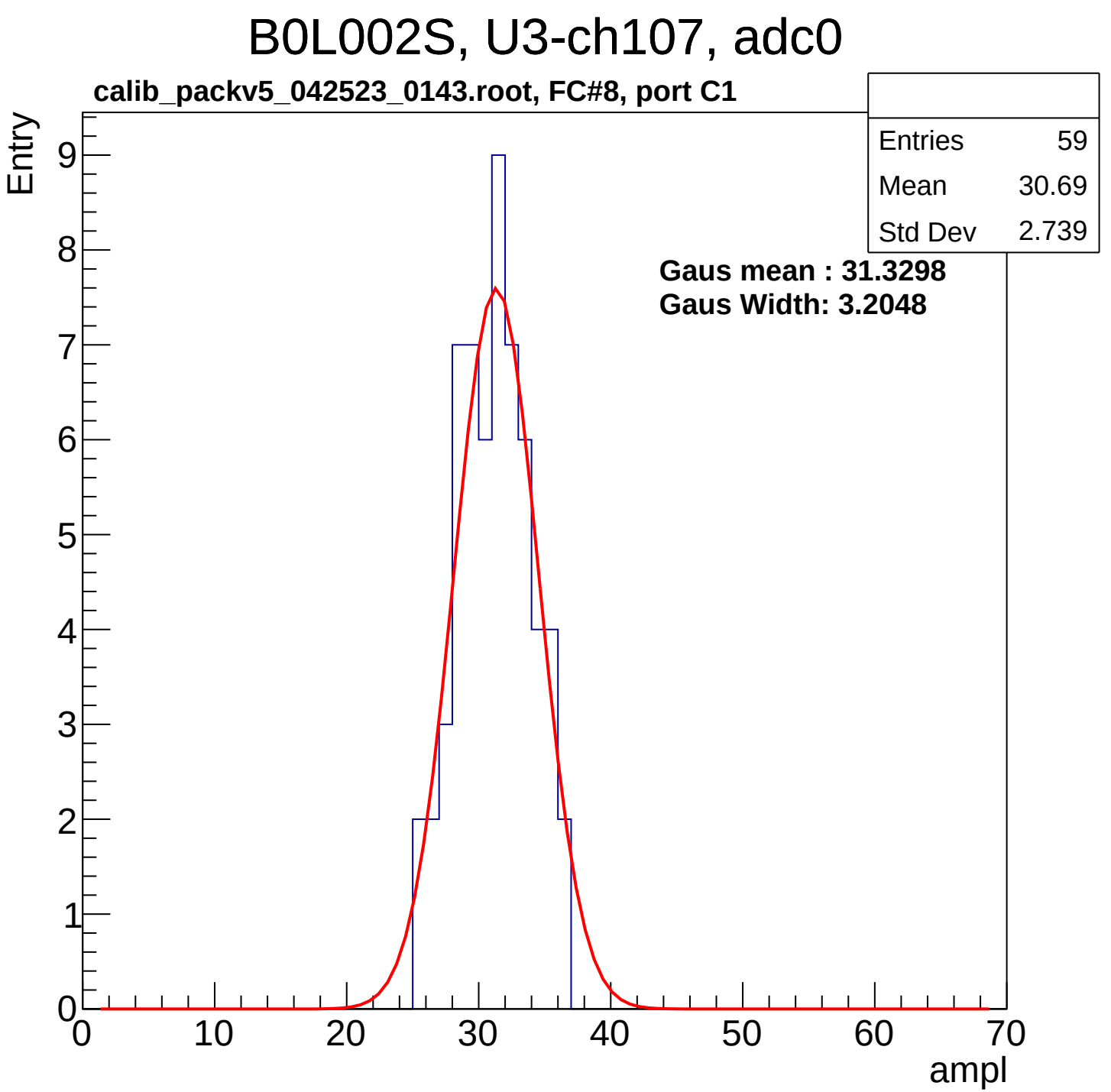
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch107, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 30.69
Std Dev 2.739
Gaus mean : 31.3298
Gaus Width: 3.2048
Entry
ampl
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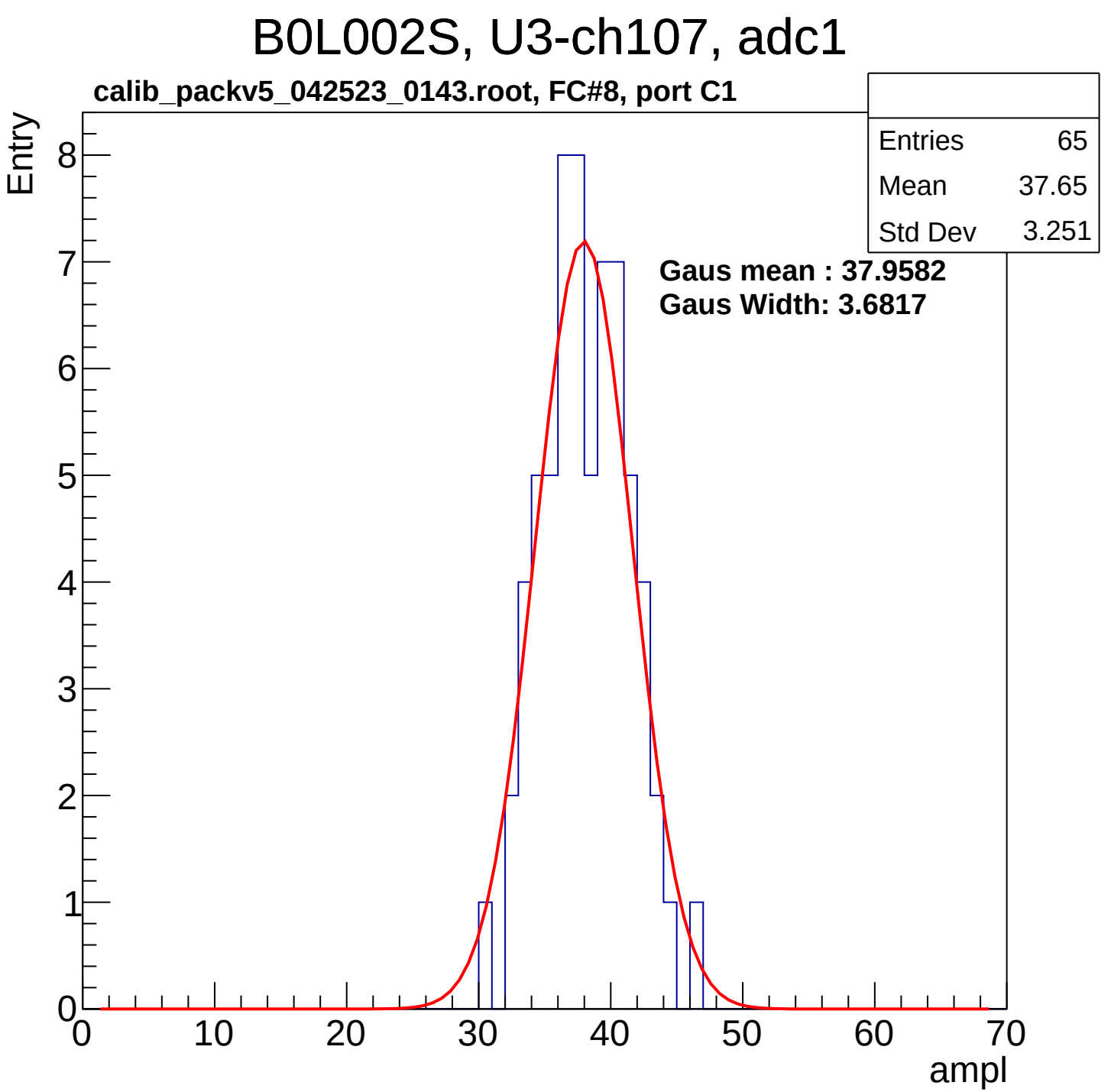
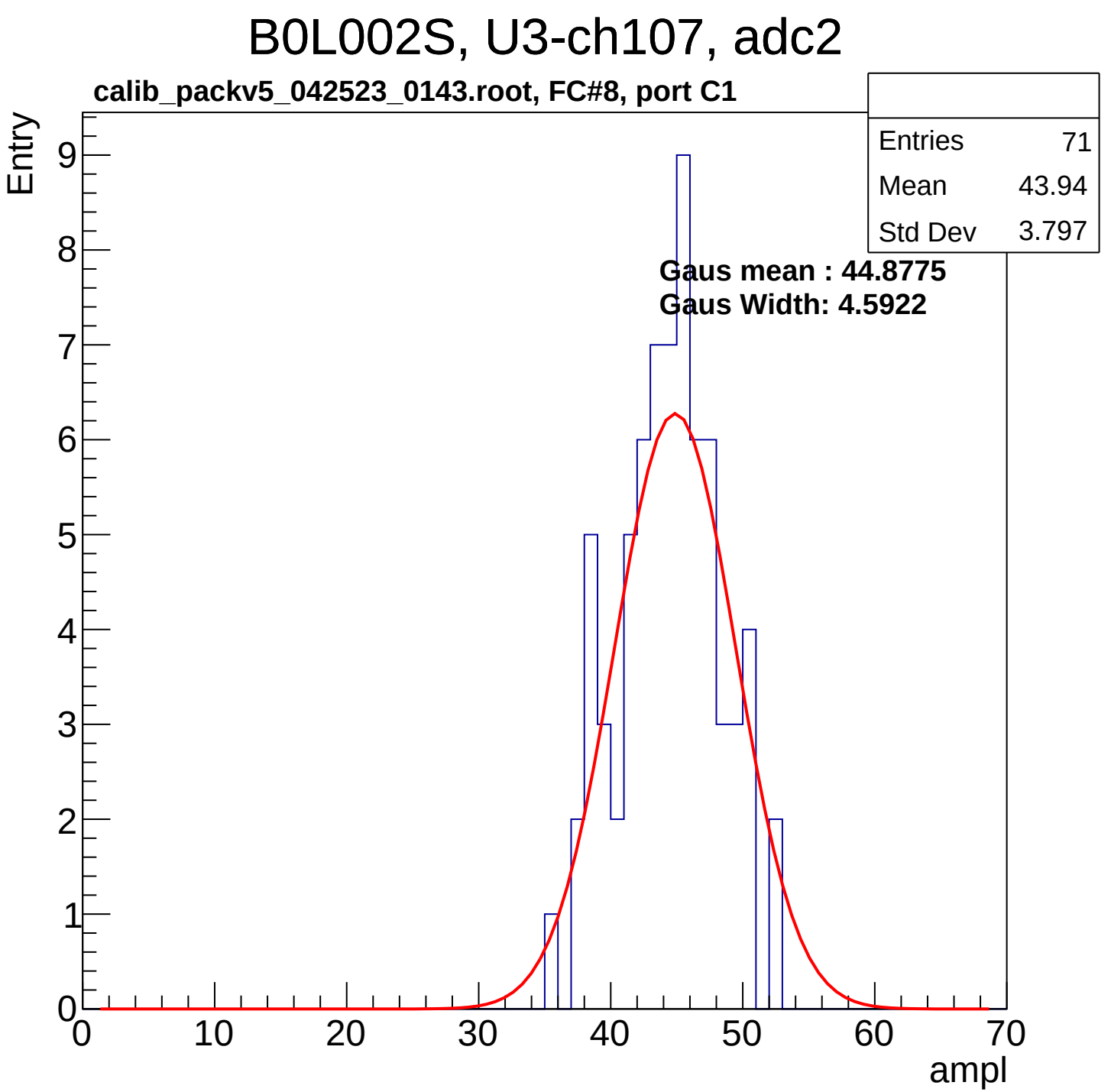
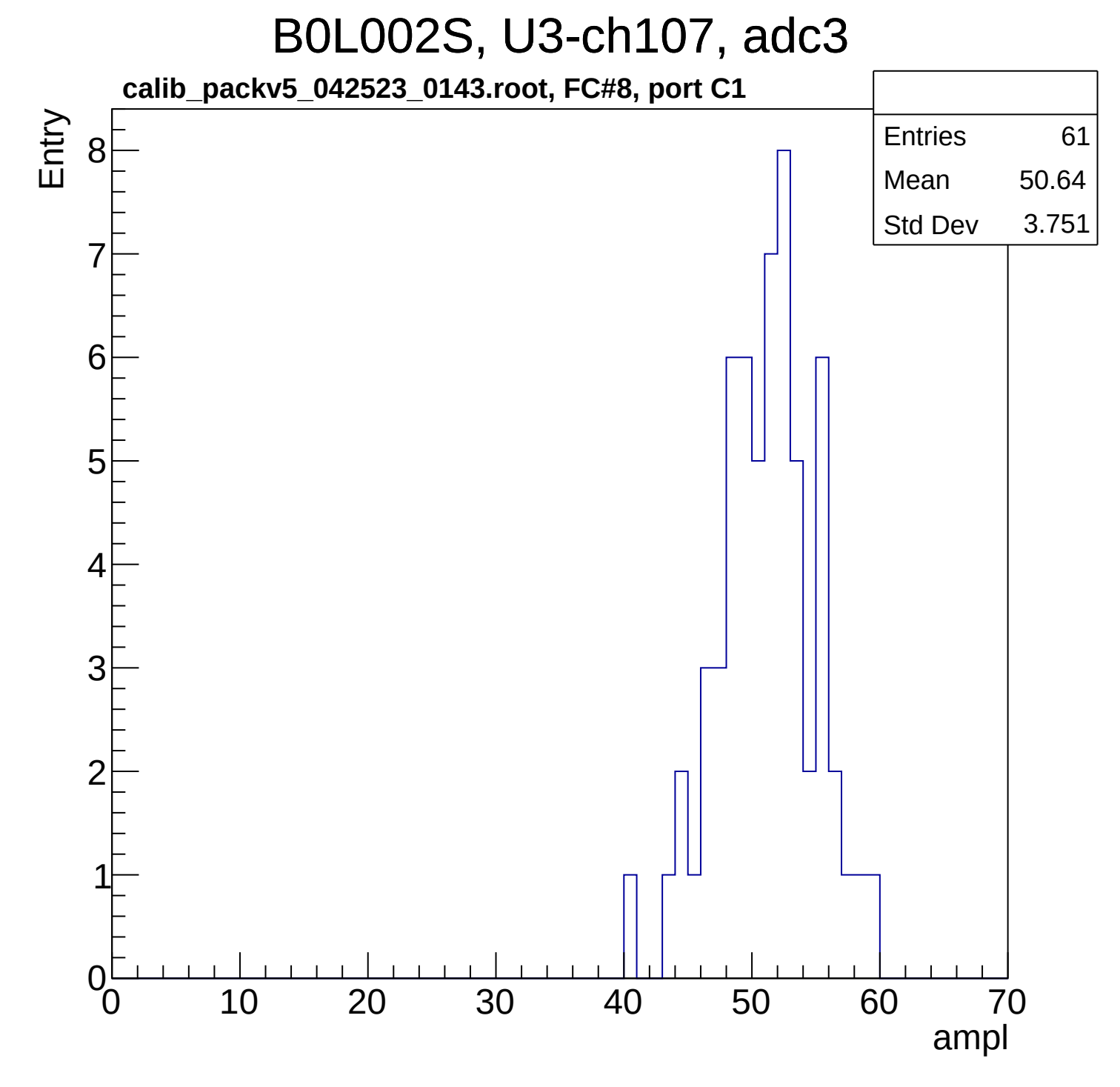
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch107, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.65
Std Dev 3.251
Gaus mean : 37.9582
Gaus Width: 3.6817
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch107, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 43.94
Std Dev 3.797
Gaus mean : 44.8775
Gaus Width: 4.5922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch107, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 50.64
Std Dev 3.751
Entry
ampl
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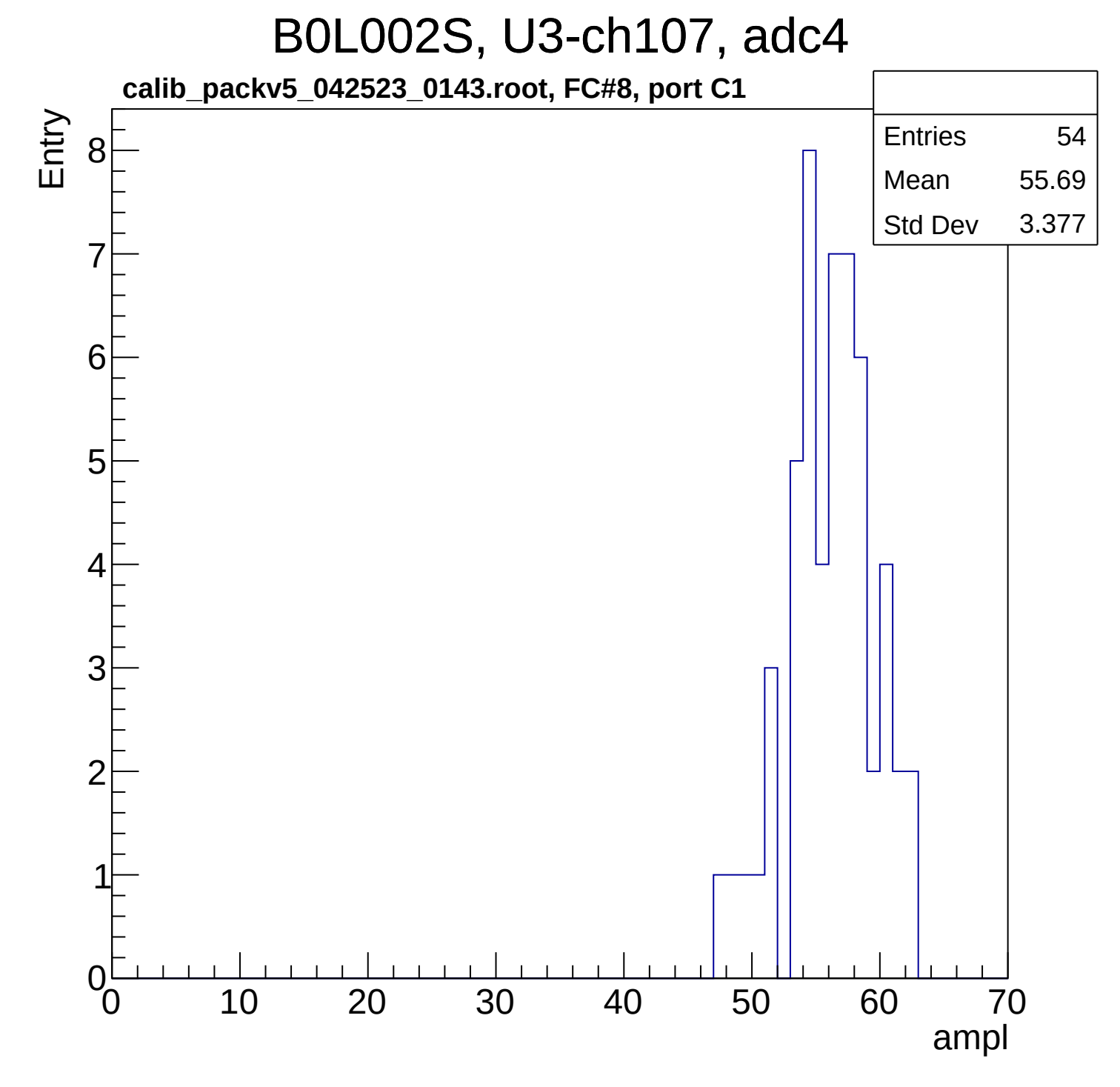
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch107, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 55.69
Std Dev 3.377
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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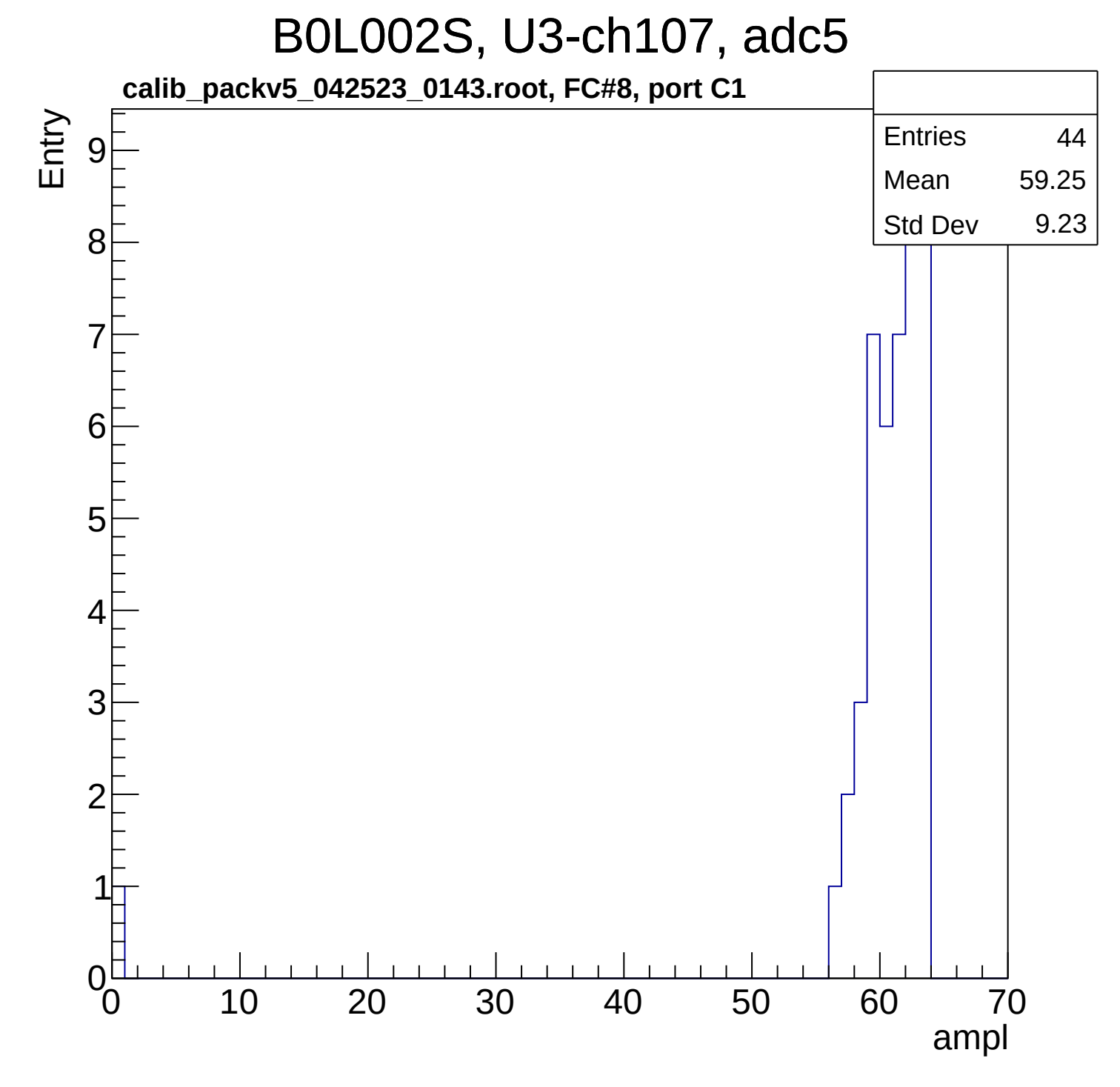
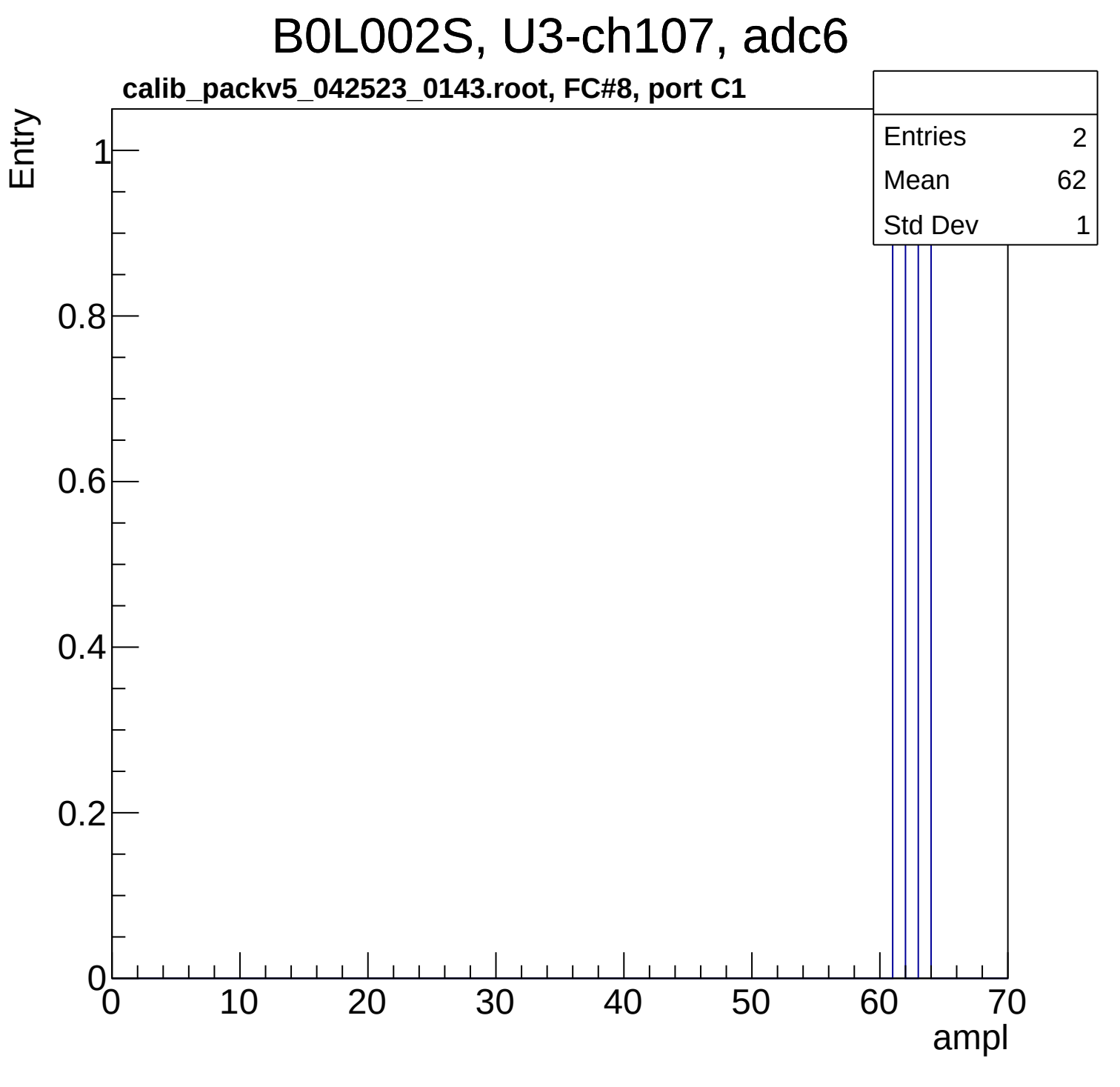
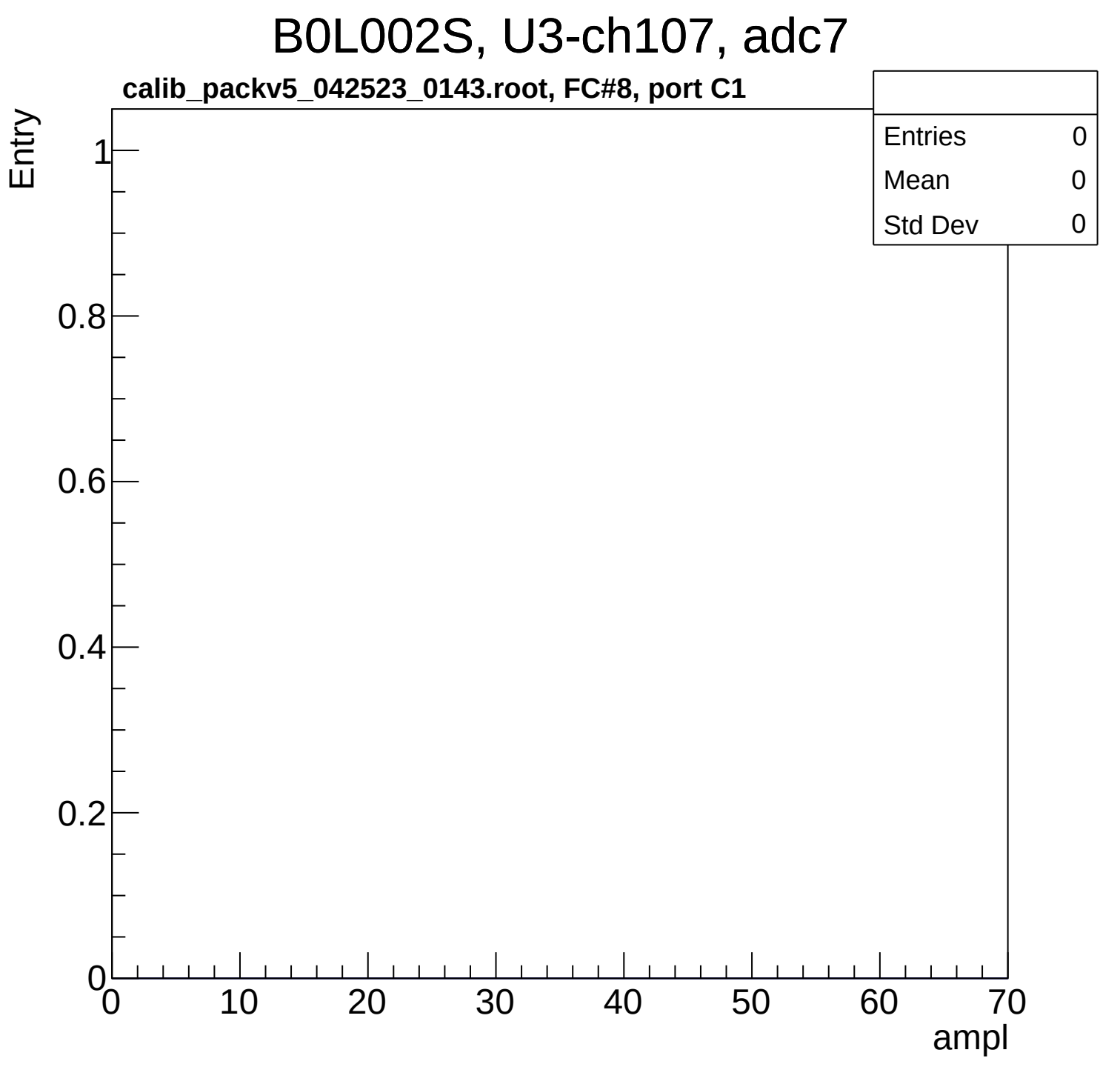

B0L002S, U3-ch107, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 59.25
Std Dev 9.23
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch107, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch107, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
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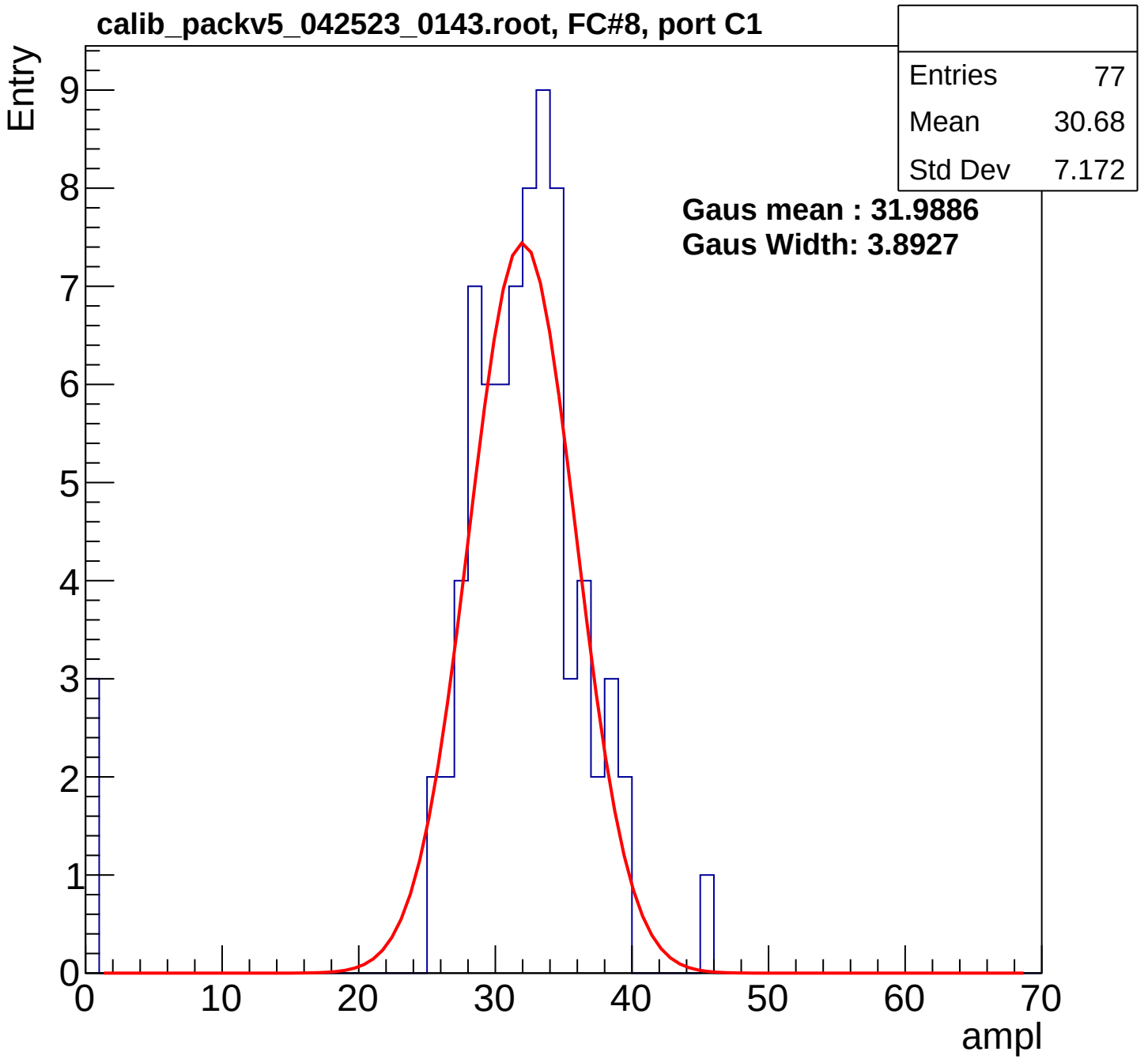
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch108, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.68
Std Dev 7.172
Gaus mean : 31.9886
Gaus Width: 3.8927
Entry
ampl
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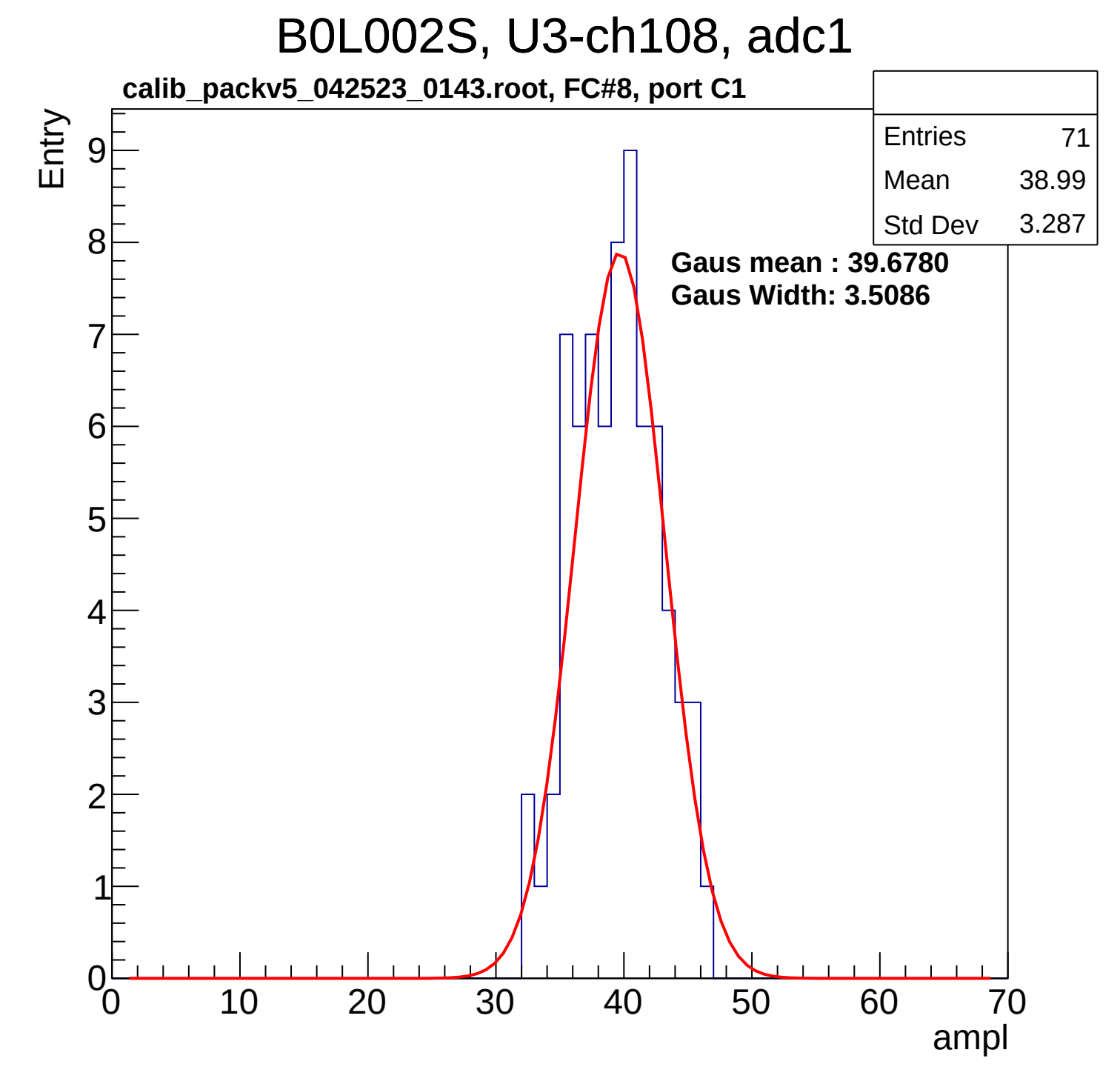
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L002S, U3-ch108, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 38.99
Std Dev 3.287
Gaus mean : 39.6780
Gaus Width: 3.5086
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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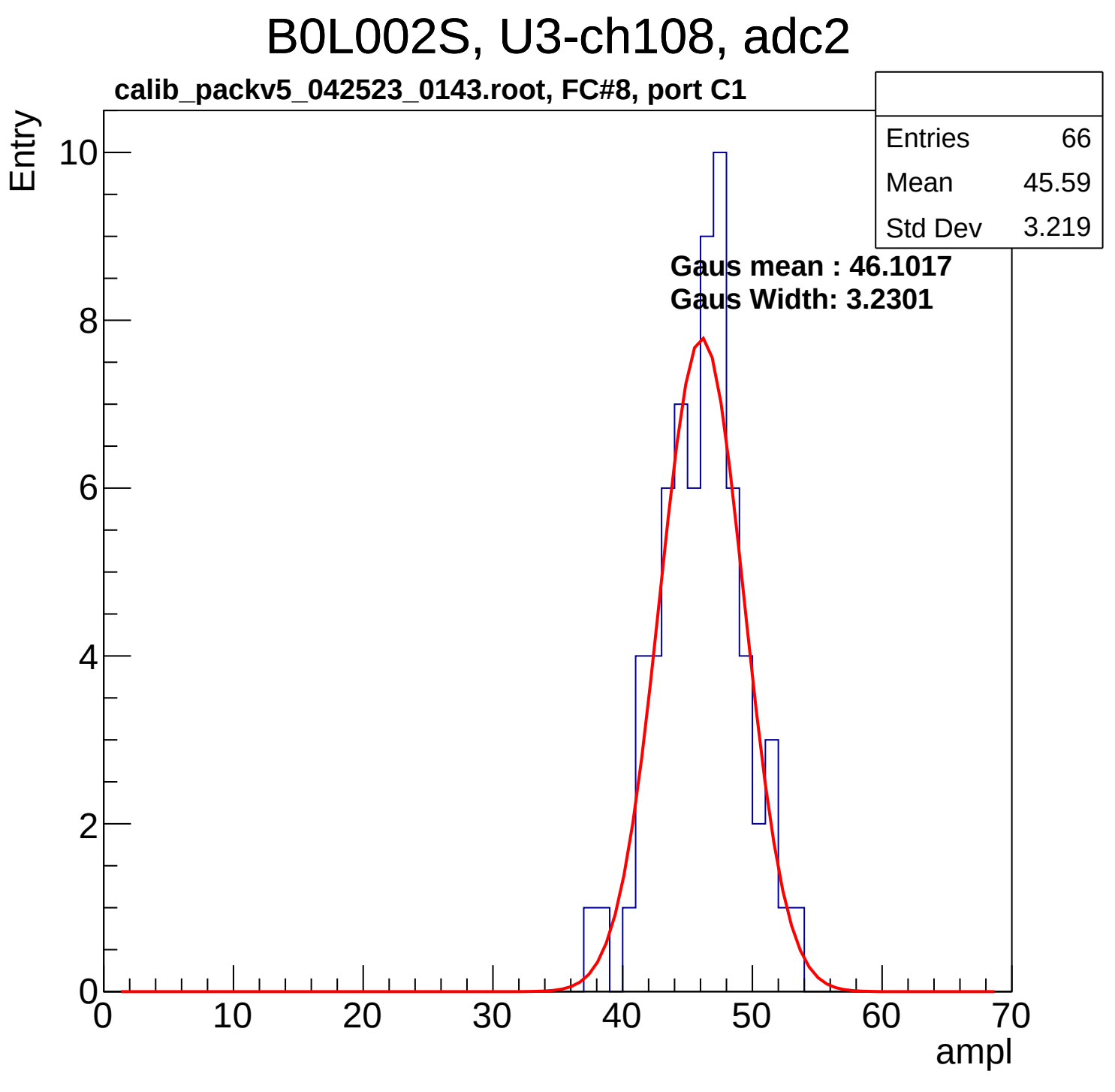
B0L002S, U3-ch108, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 45.59
Std Dev 3.219
Gaus mean : 46.1017
Gaus Width: 3.2301
Entry
ampl
0
2
4
6
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0
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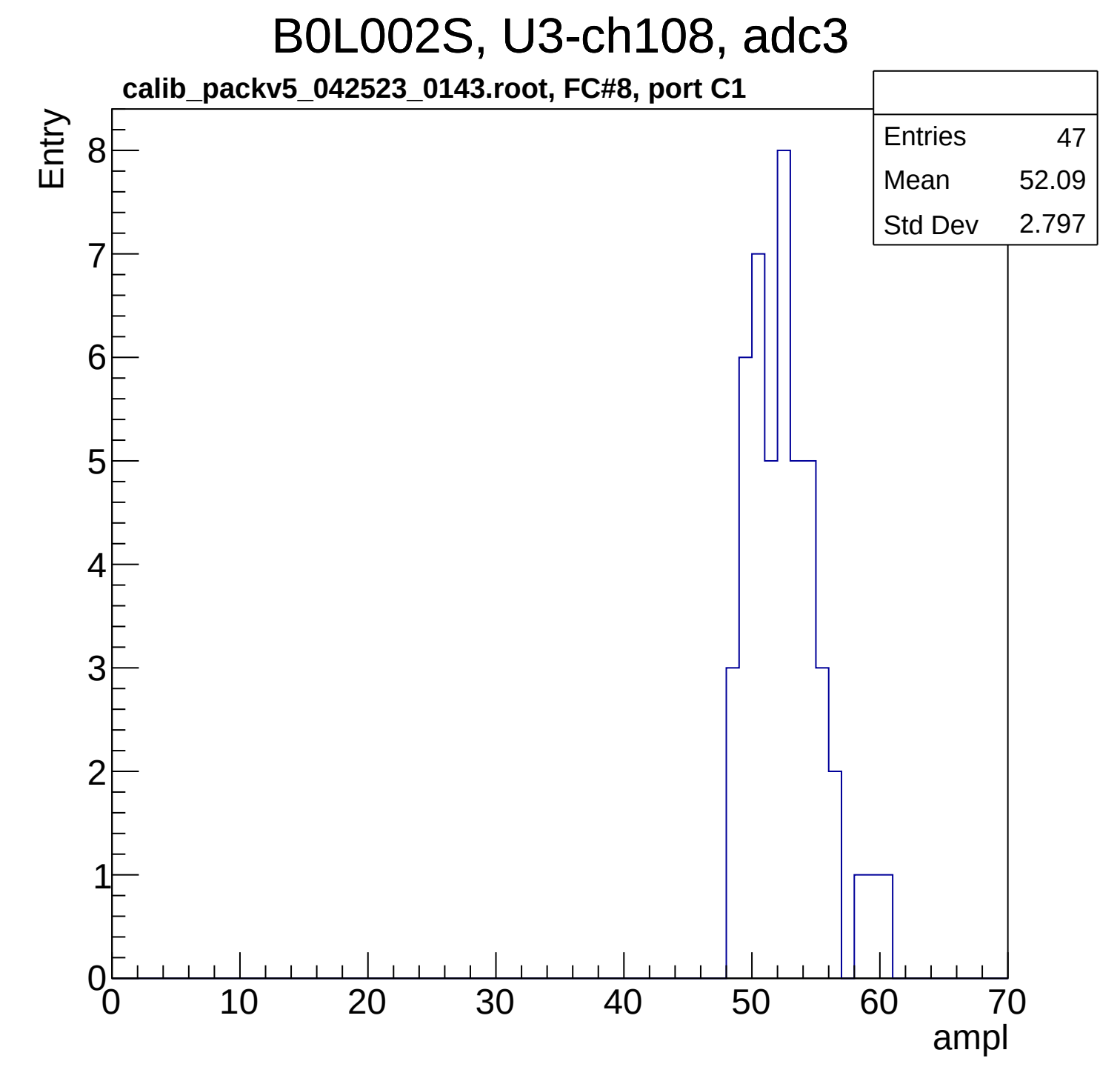
B0L002S, U3-ch108, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 52.09
Std Dev 2.797
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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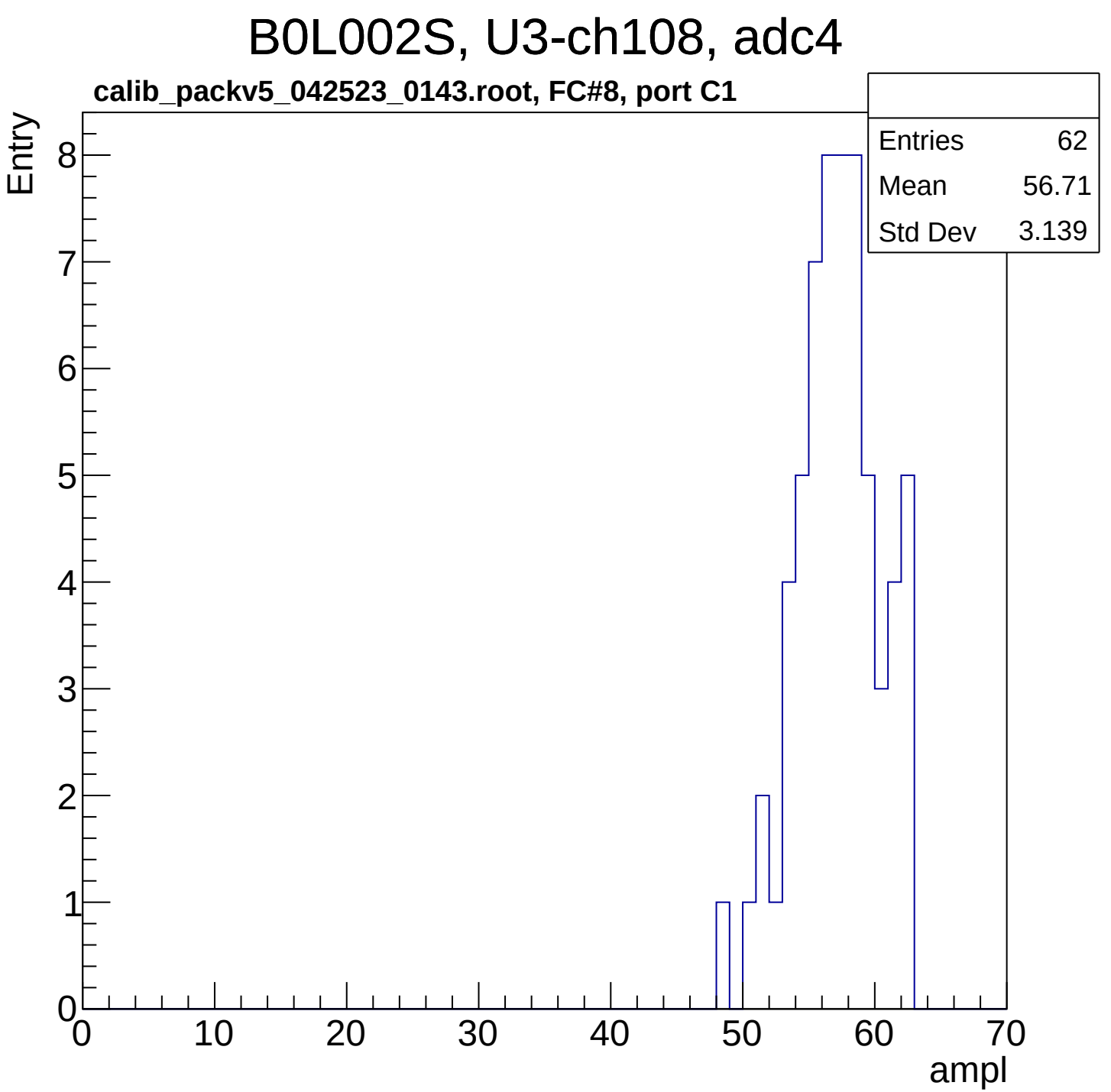
B0L002S, U3-ch108, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 56.71
Std Dev 3.139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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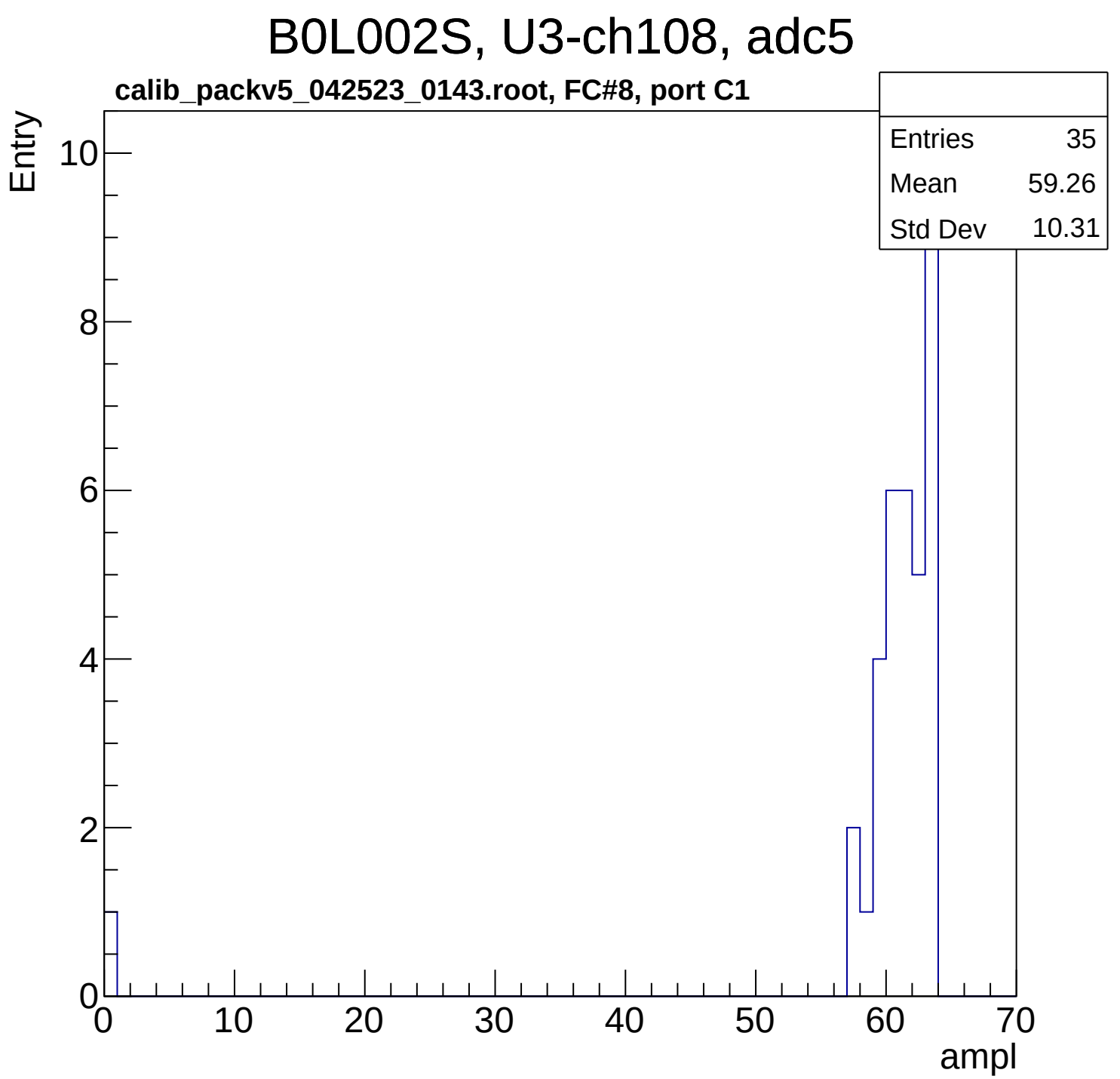
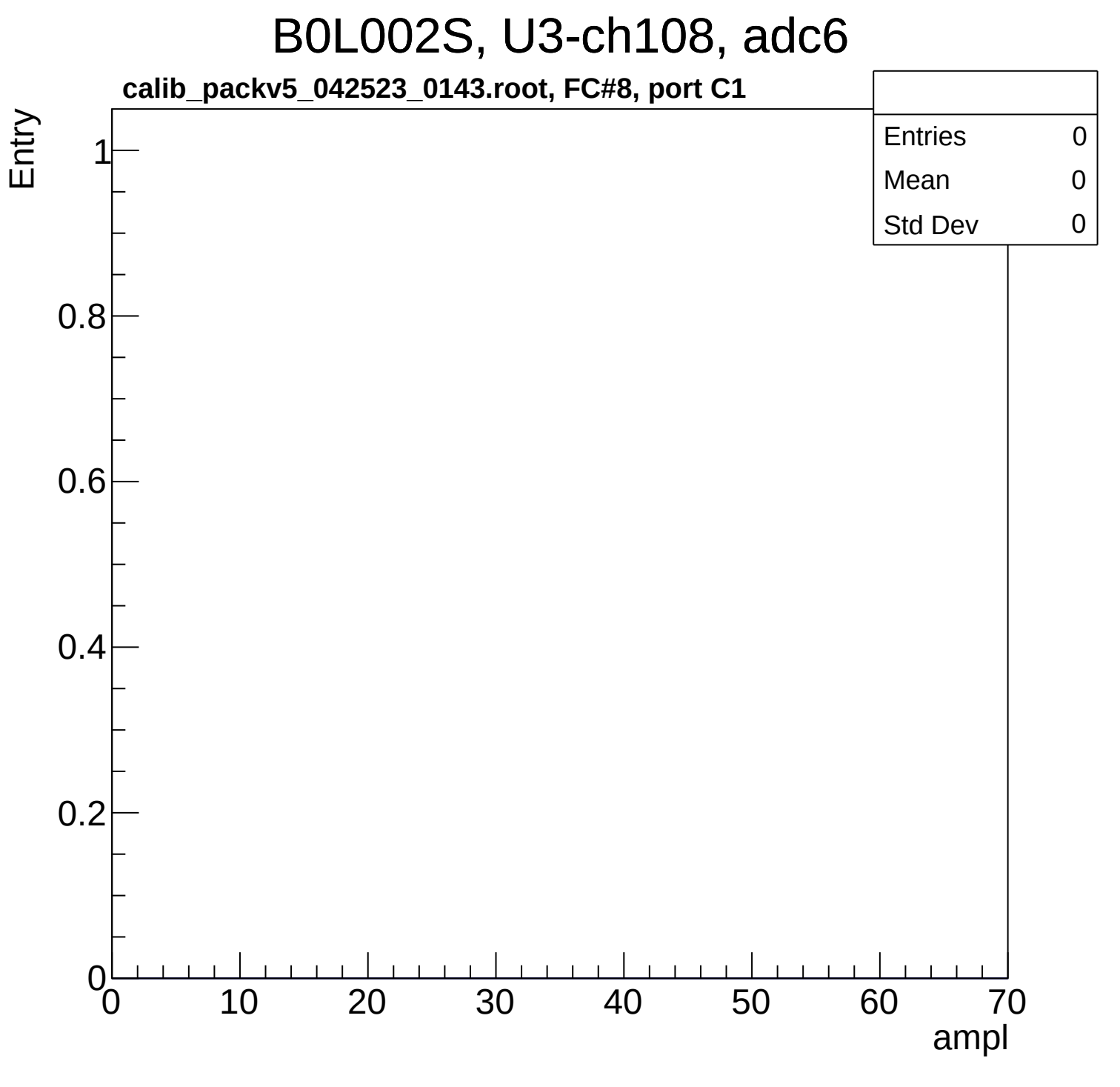
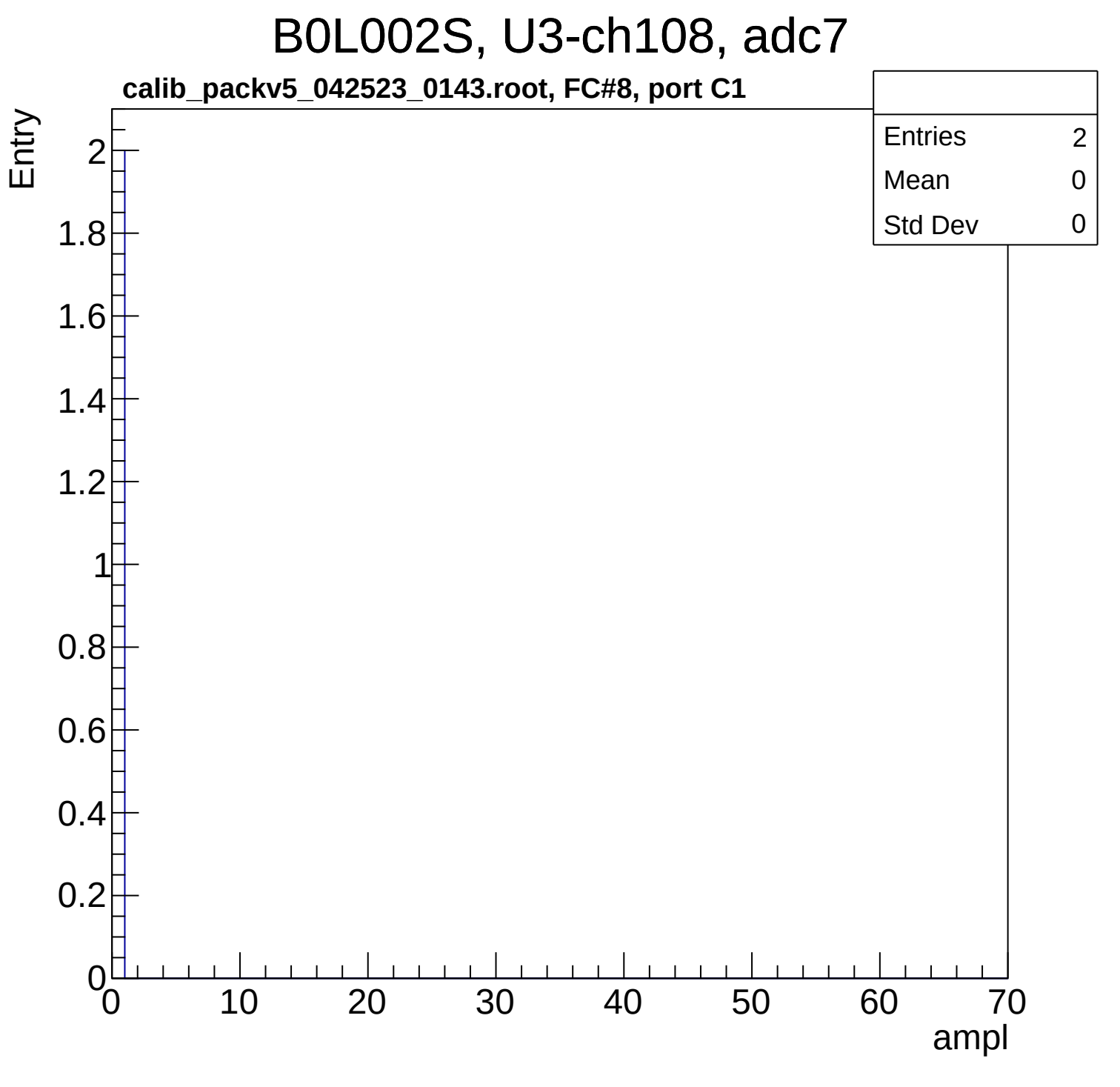

B0L002S, U3-ch108, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.26
Std Dev 10.31
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch108, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch108, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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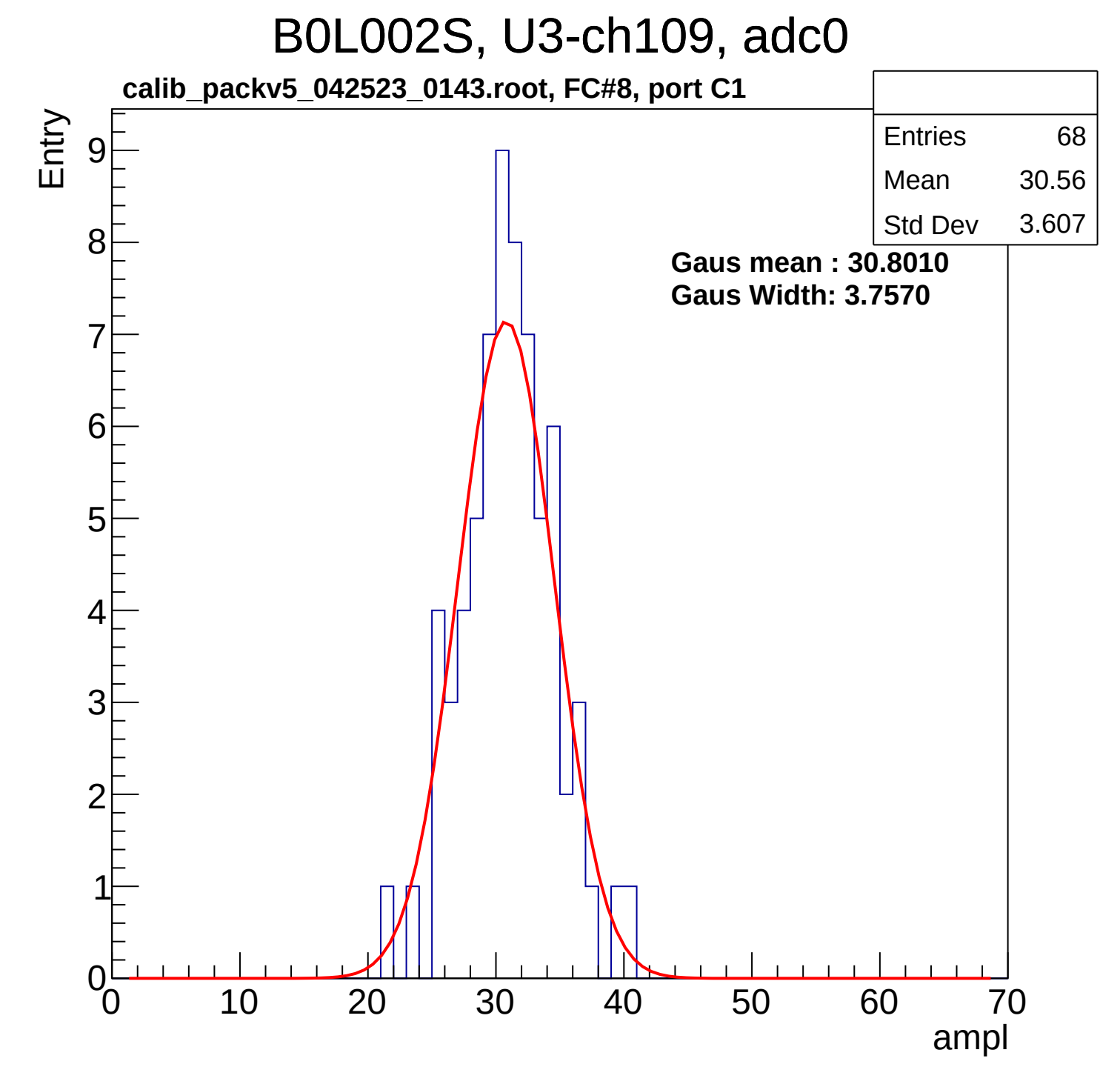
B0L002S, U3-ch109, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 30.56
Std Dev 3.607
Gaus mean : 30.8010
Gaus Width: 3.7570
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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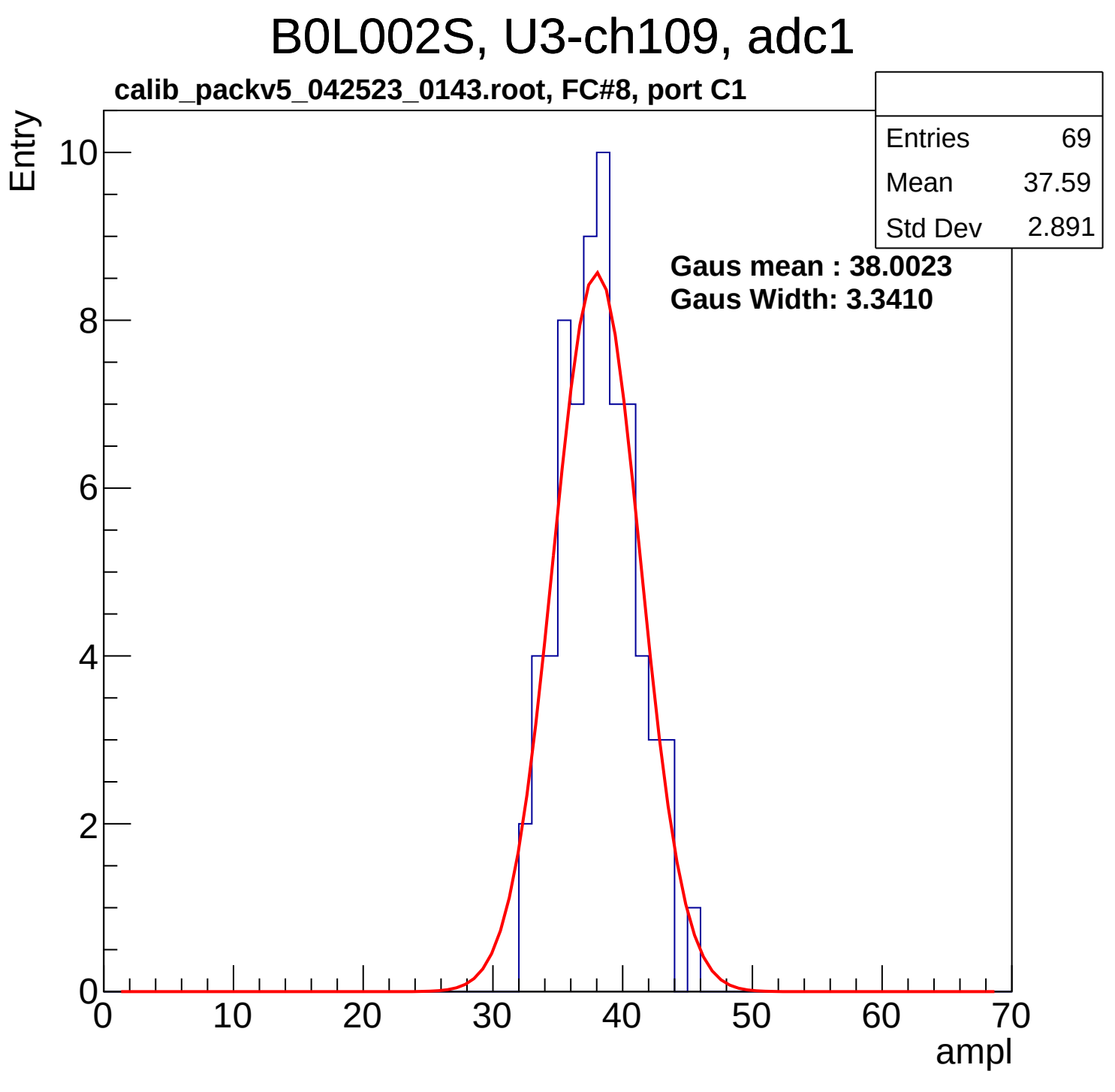
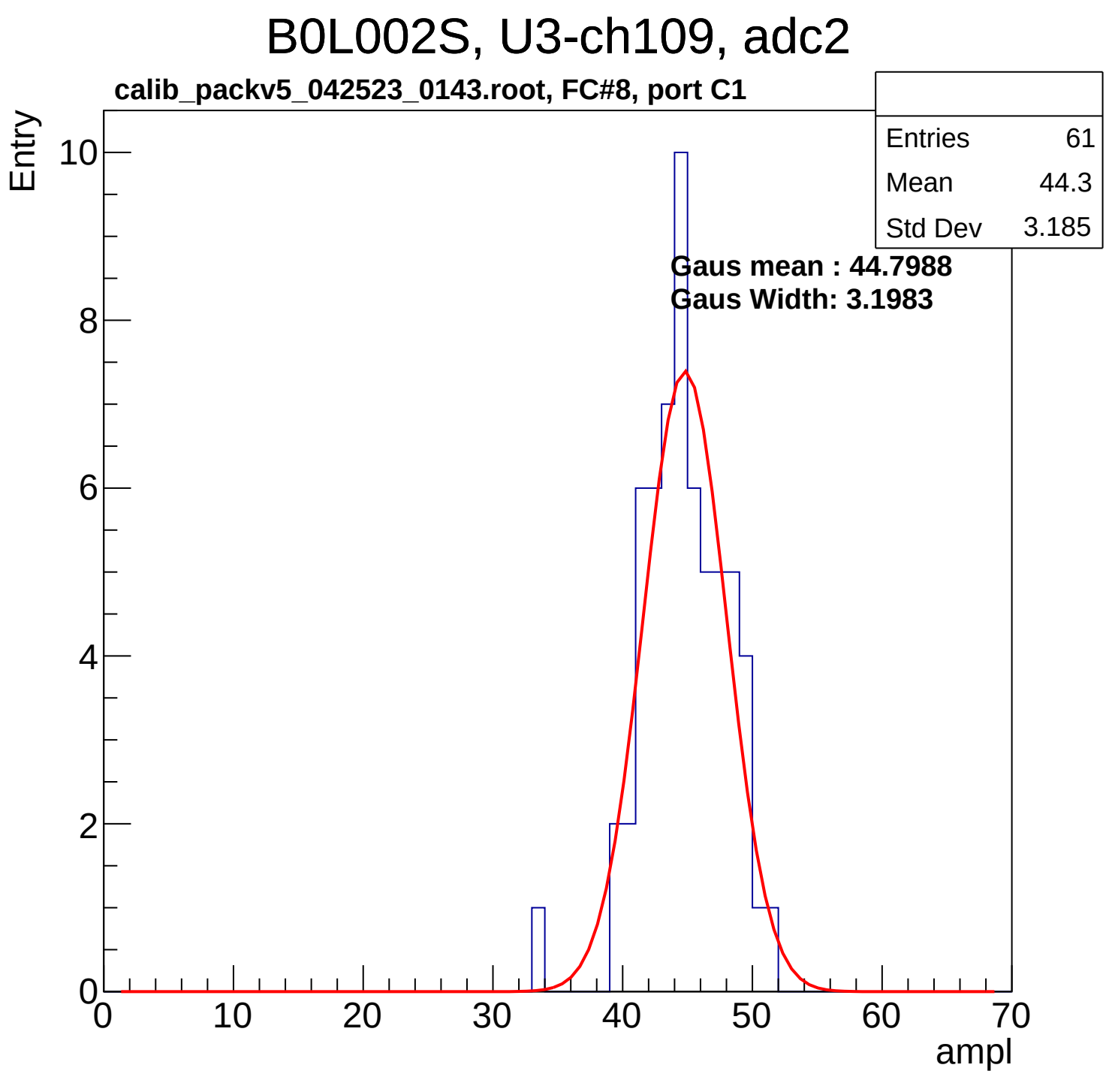
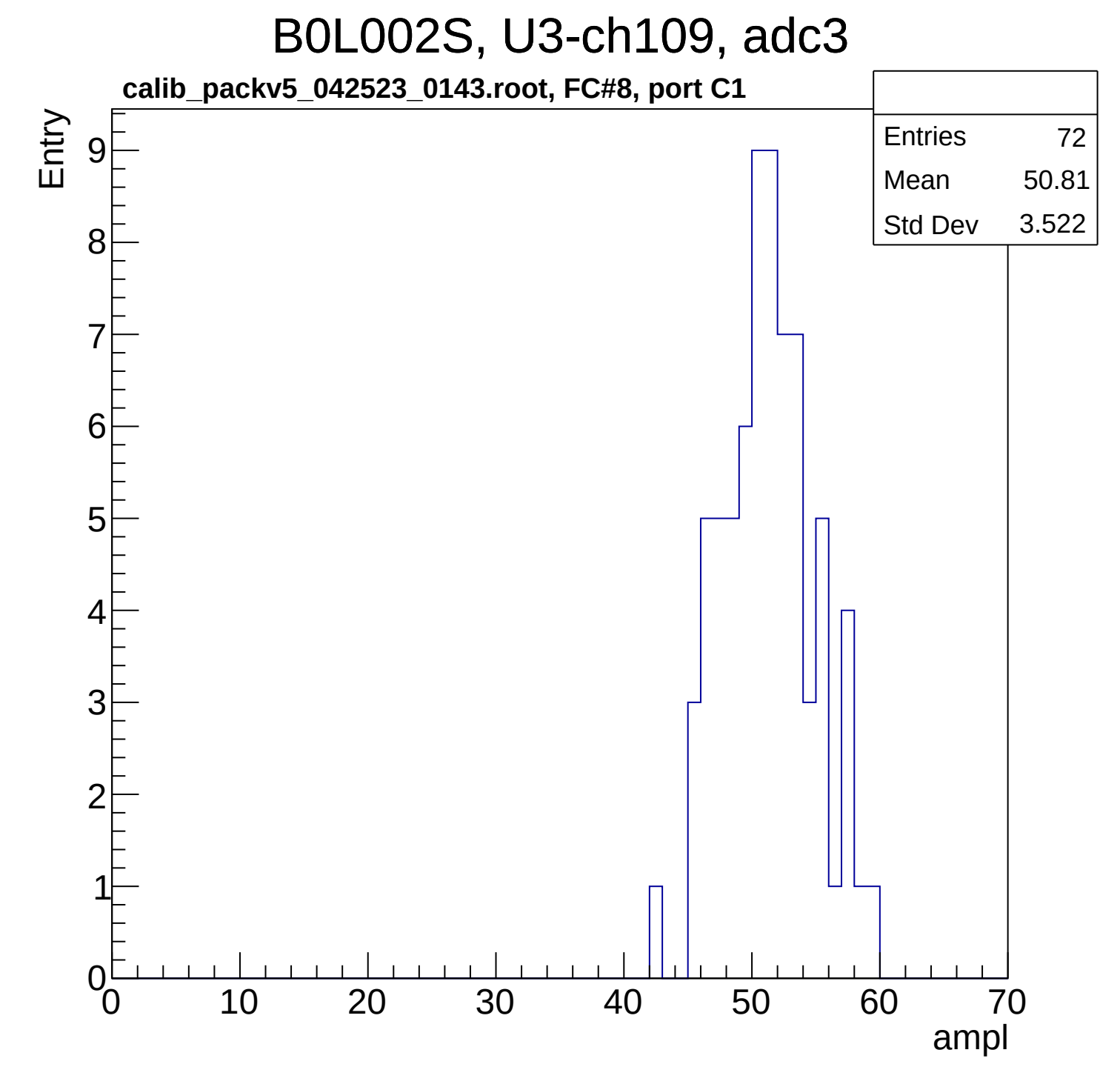
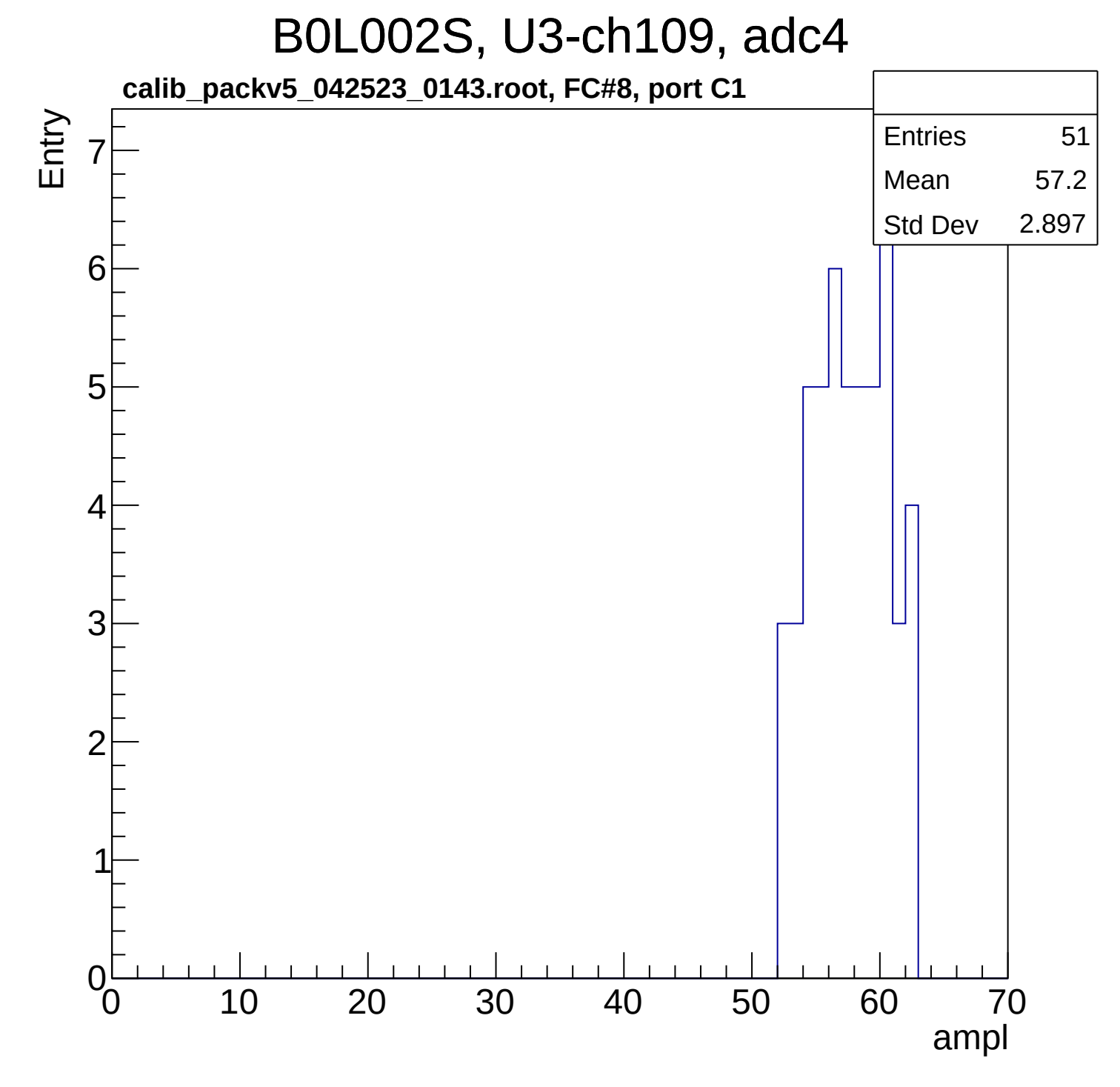
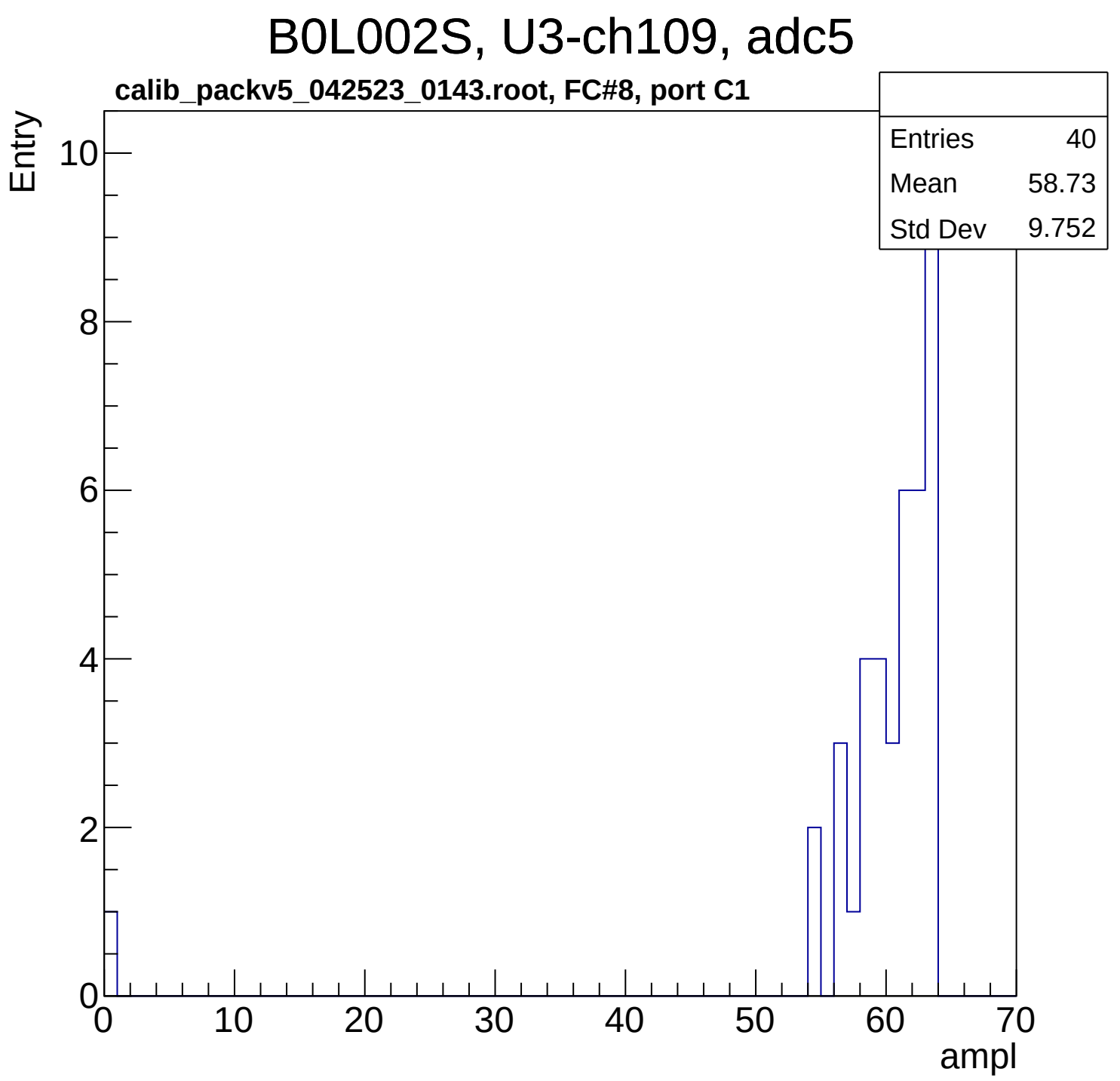
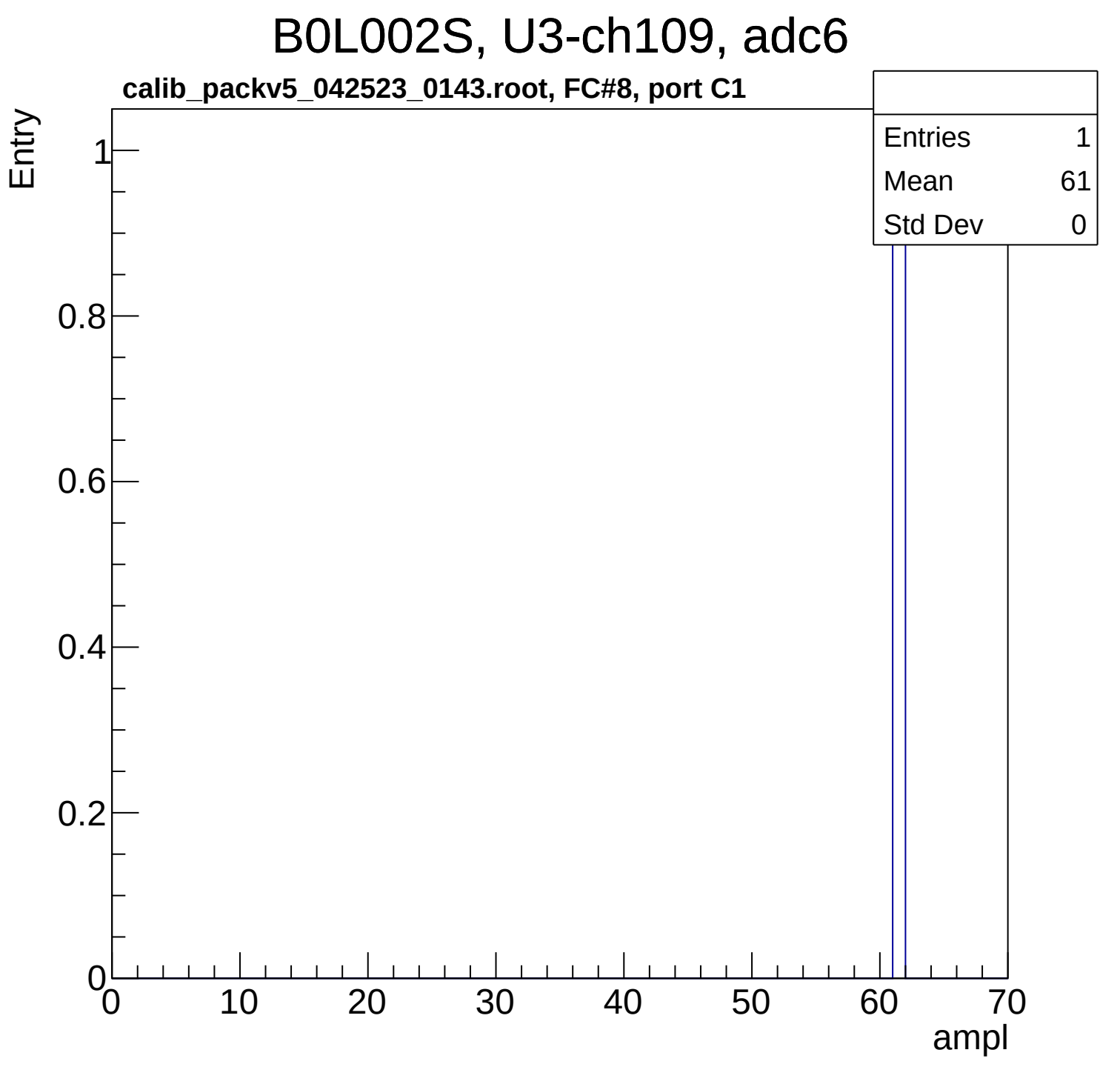

B0L002S, U3-ch109, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.59
Std Dev 2.891
Gaus mean : 38.0023
Gaus Width: 3.3410
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

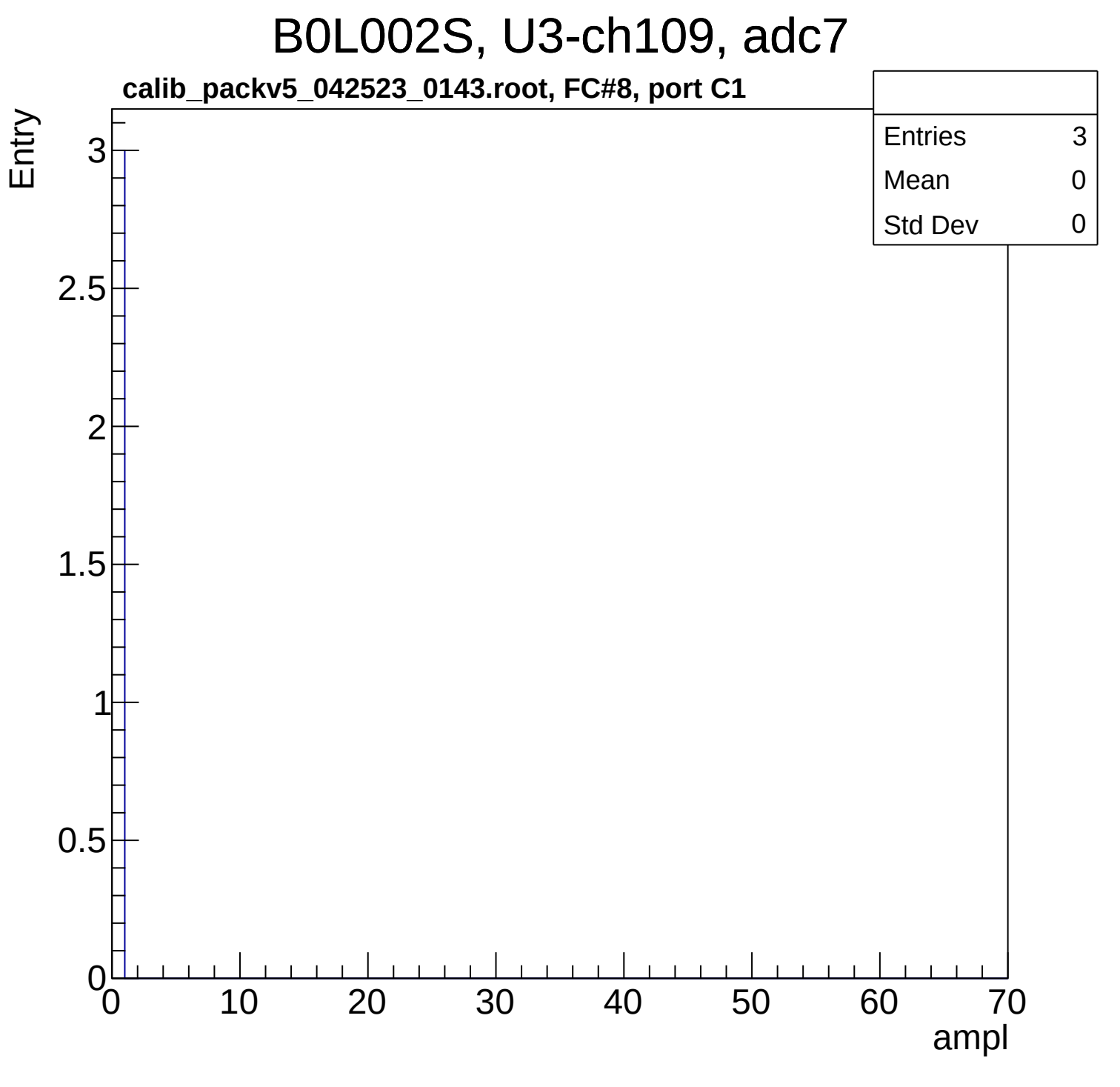

B0L002S, U3-ch109, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.3
Std Dev 3.185
Gaus mean : 44.7988
Gaus Width: 3.1983
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

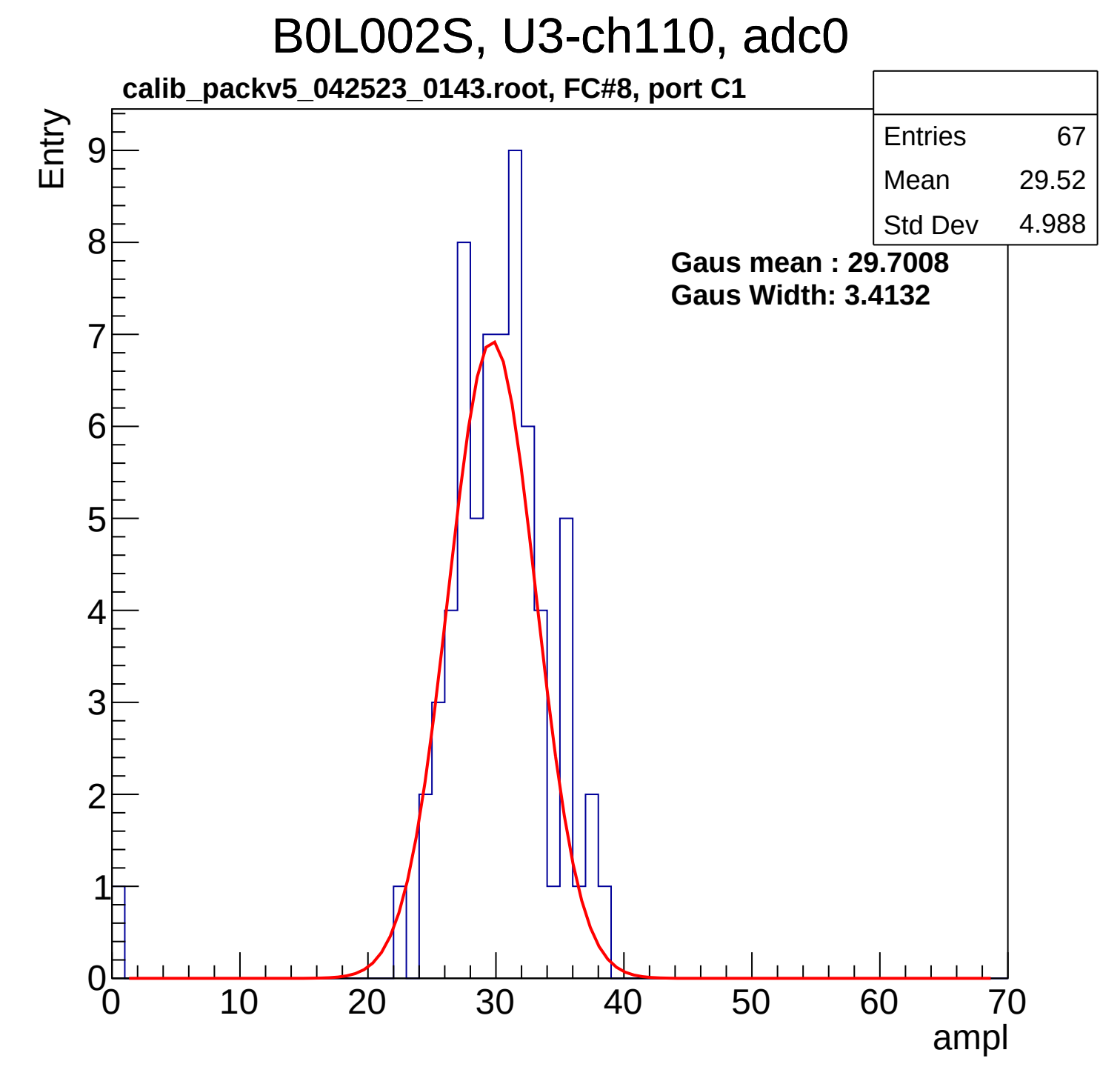

B0L002S, U3-ch109, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.81
Std Dev 3.522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

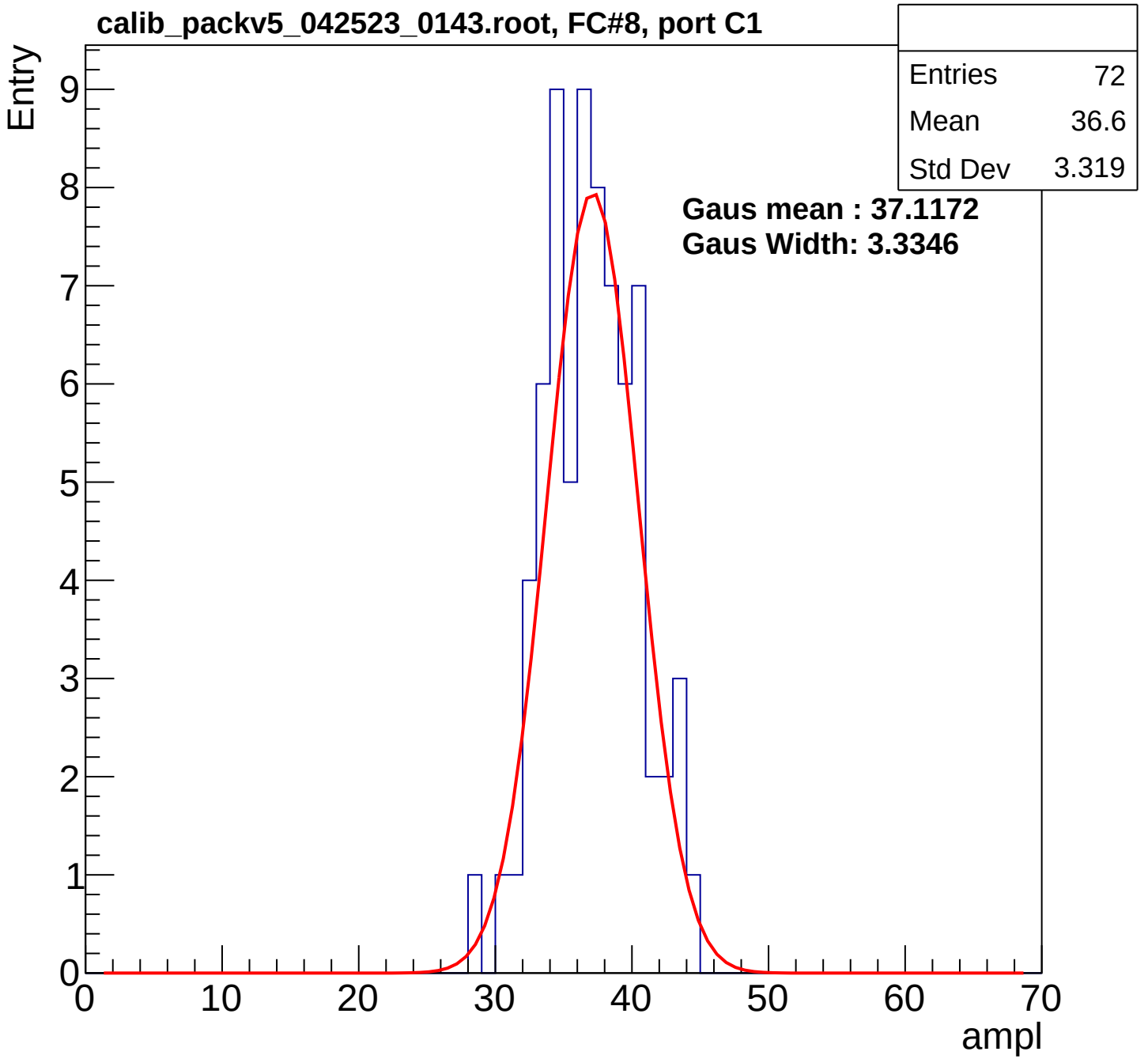

B0L002S, U3-ch109, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.2
Std Dev 2.897
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

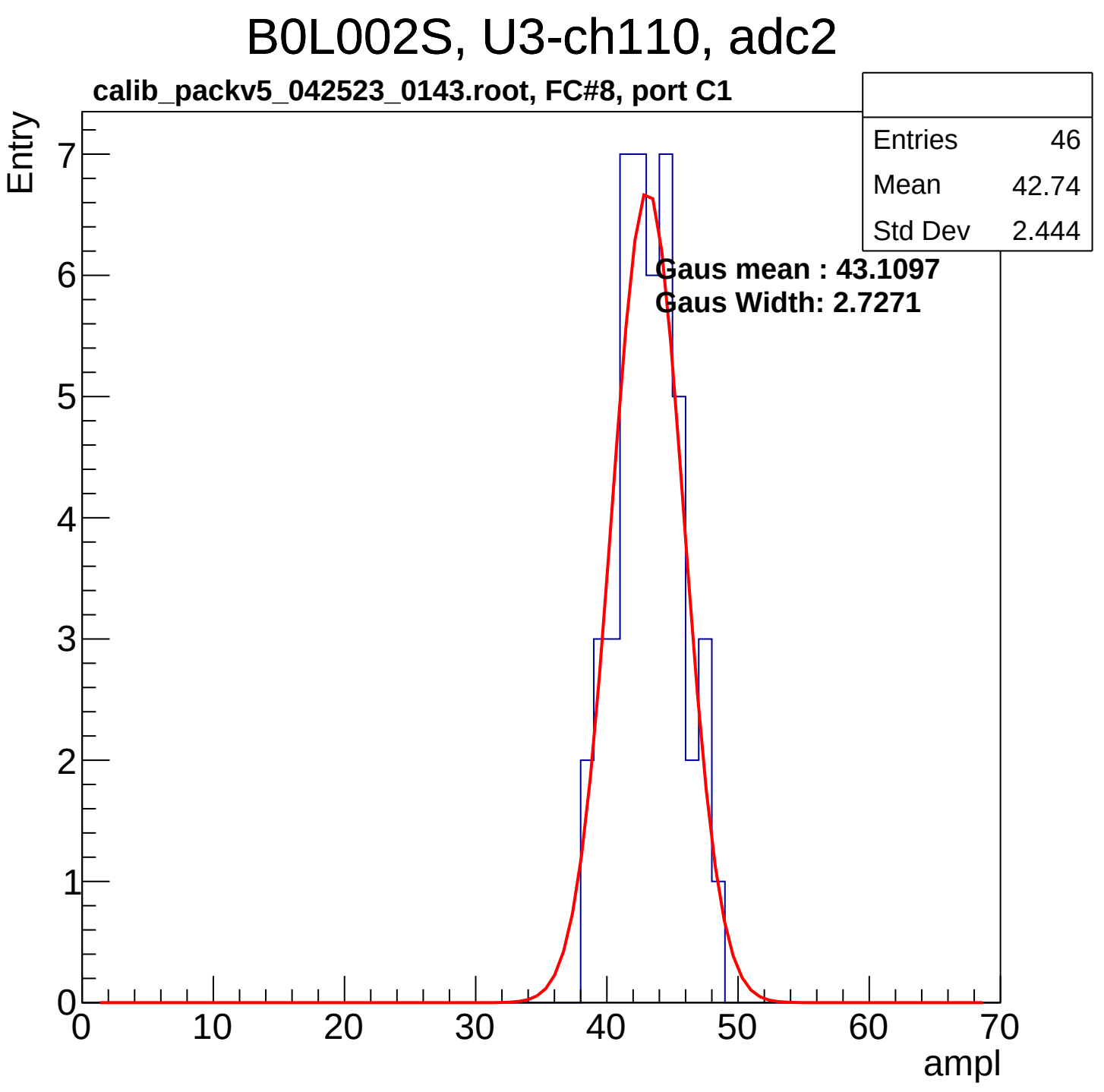

B0L002S, U3-ch109, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.73
Std Dev 9.752
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch109, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch109, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch110, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.52
Std Dev 4.988
Gaus mean : 29.7008
Gaus Width: 3.4132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch110, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 36.6
Std Dev 3.319
Gaus mean : 37.1172
Gaus Width: 3.3346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch110, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 42.74
Std Dev 2.444
Gaus mean : 43.1097
Gaus Width: 2.7271
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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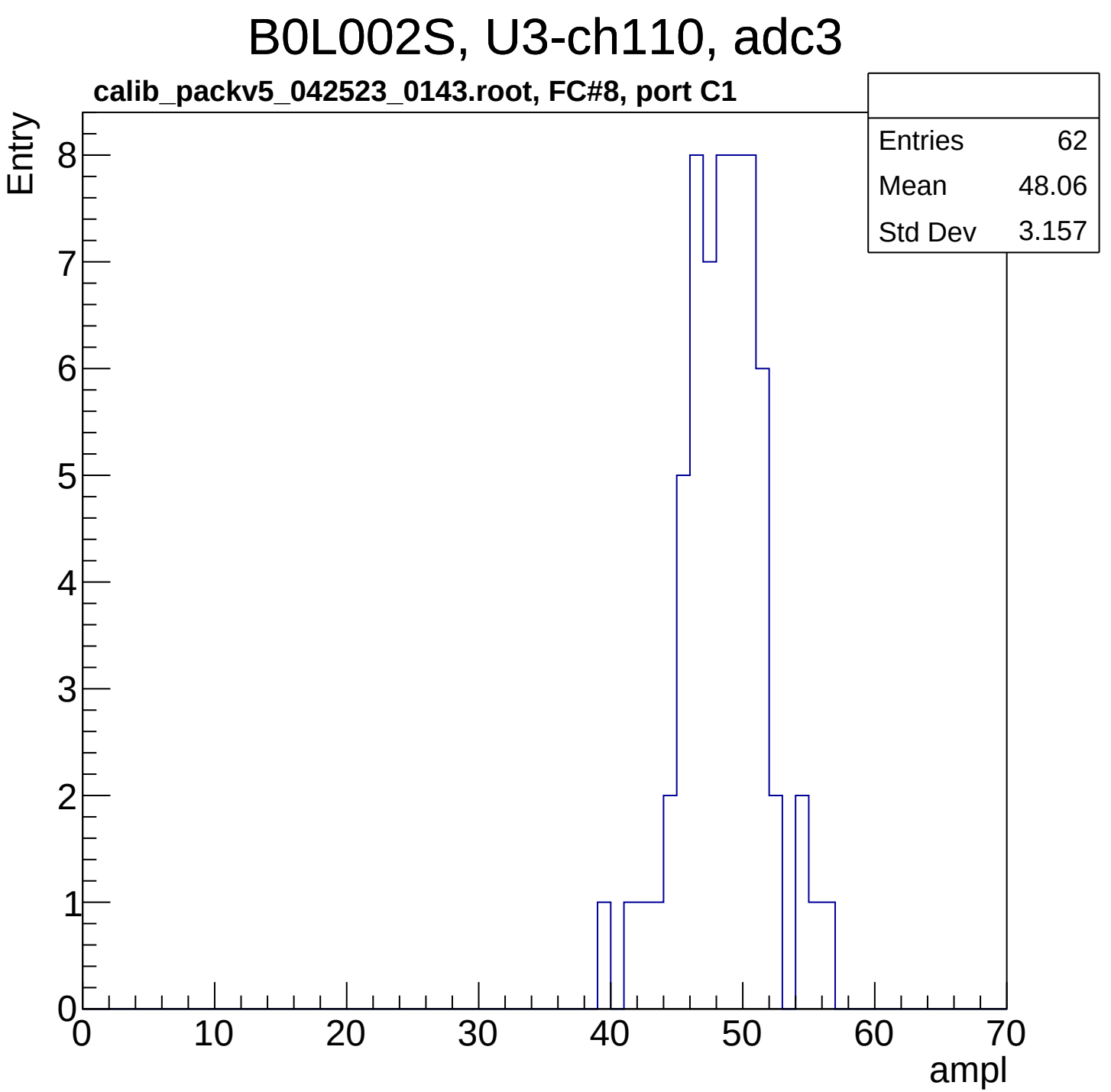
B0L002S, U3-ch110, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 48.06
Std Dev 3.157
Entry
ampl
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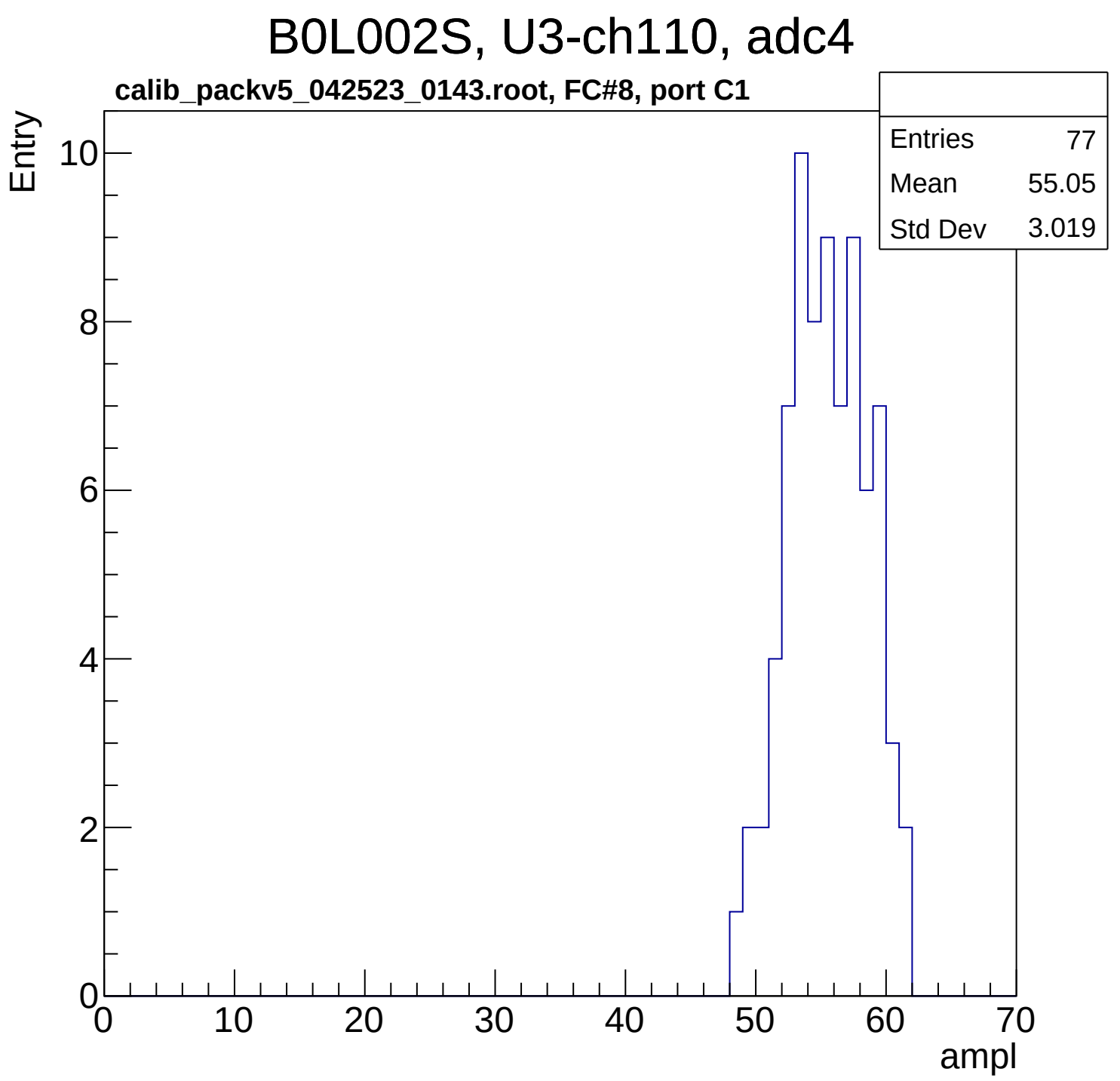
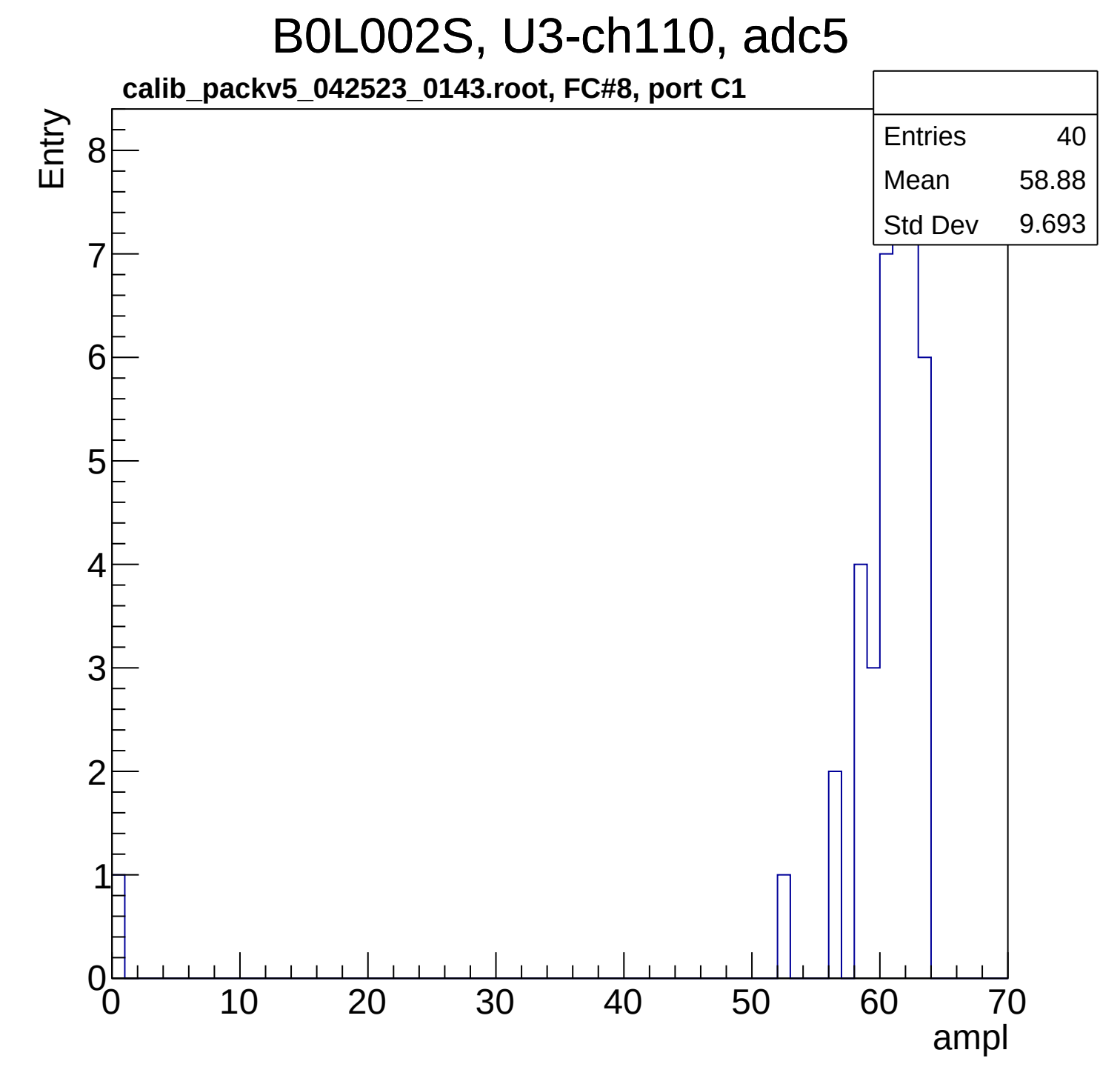
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch110, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 55.05
Std Dev 3.019
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch110, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 58.88
Std Dev 9.693
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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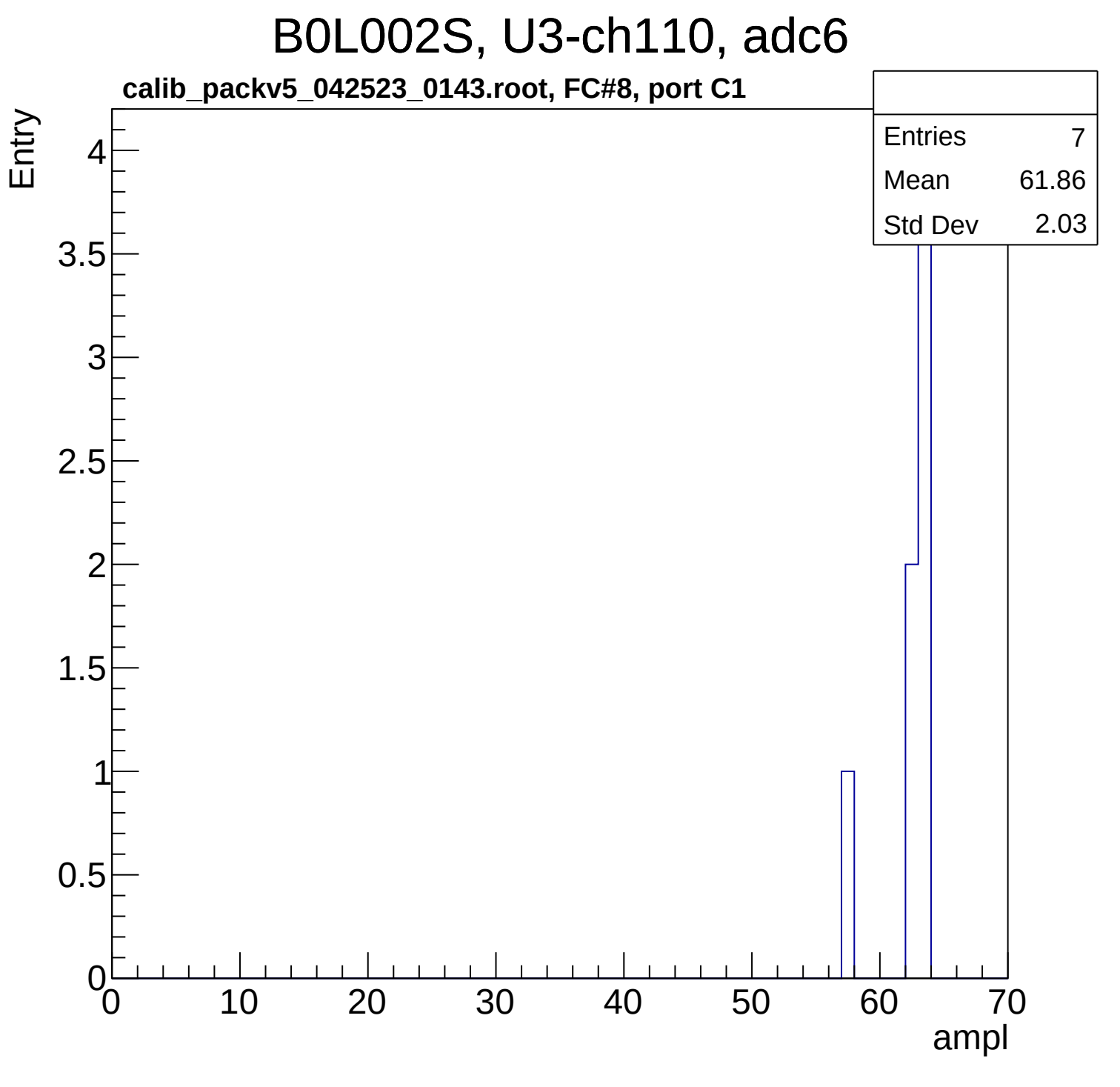
B0L002S, U3-ch110, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 61.86
Std Dev 2.03
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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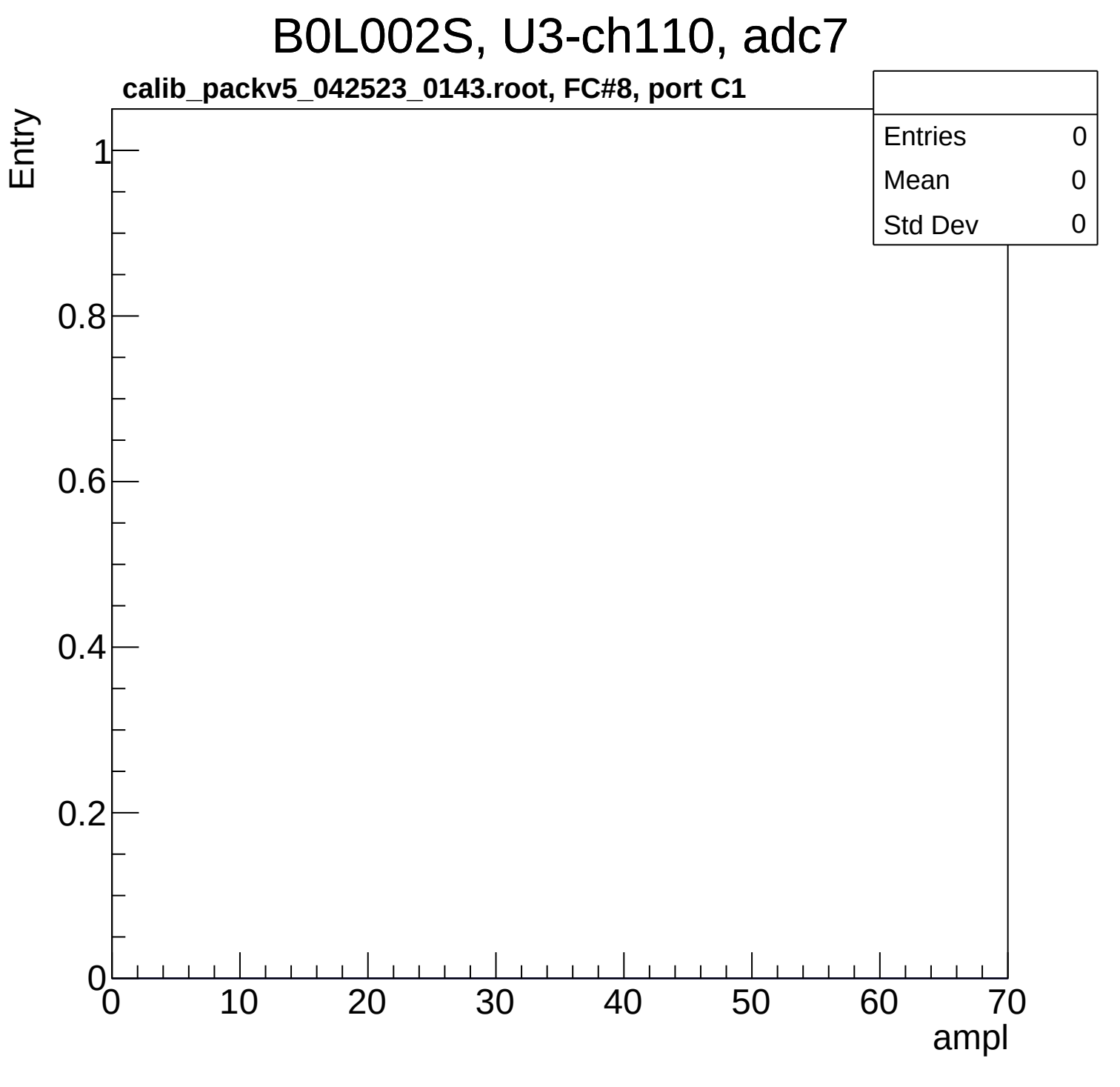
40
50
60
70


B0L002S, U3-ch110, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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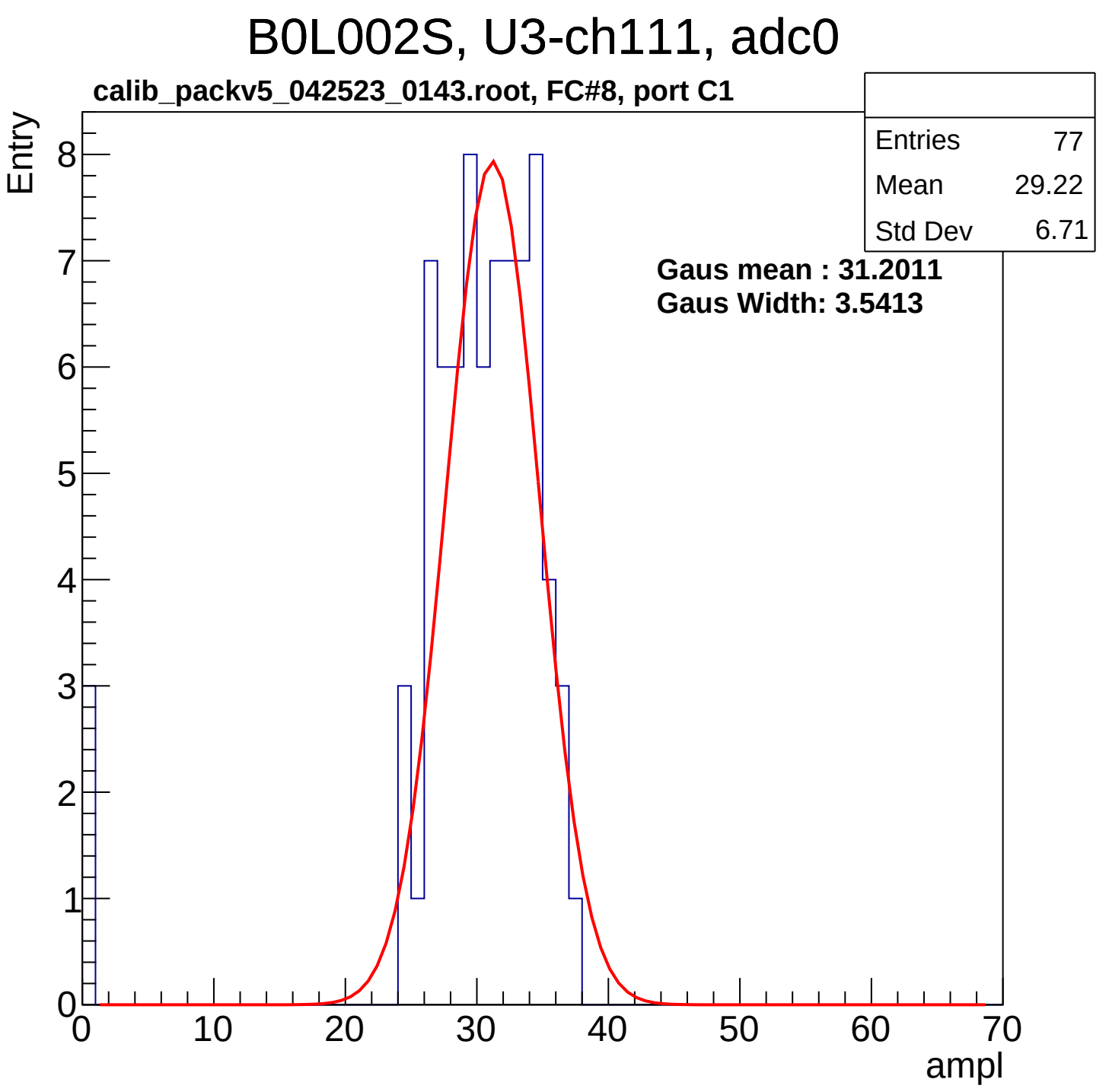
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch111, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 29.22
Std Dev 6.71
Gaus mean : 31.2011
Gaus Width: 3.5413
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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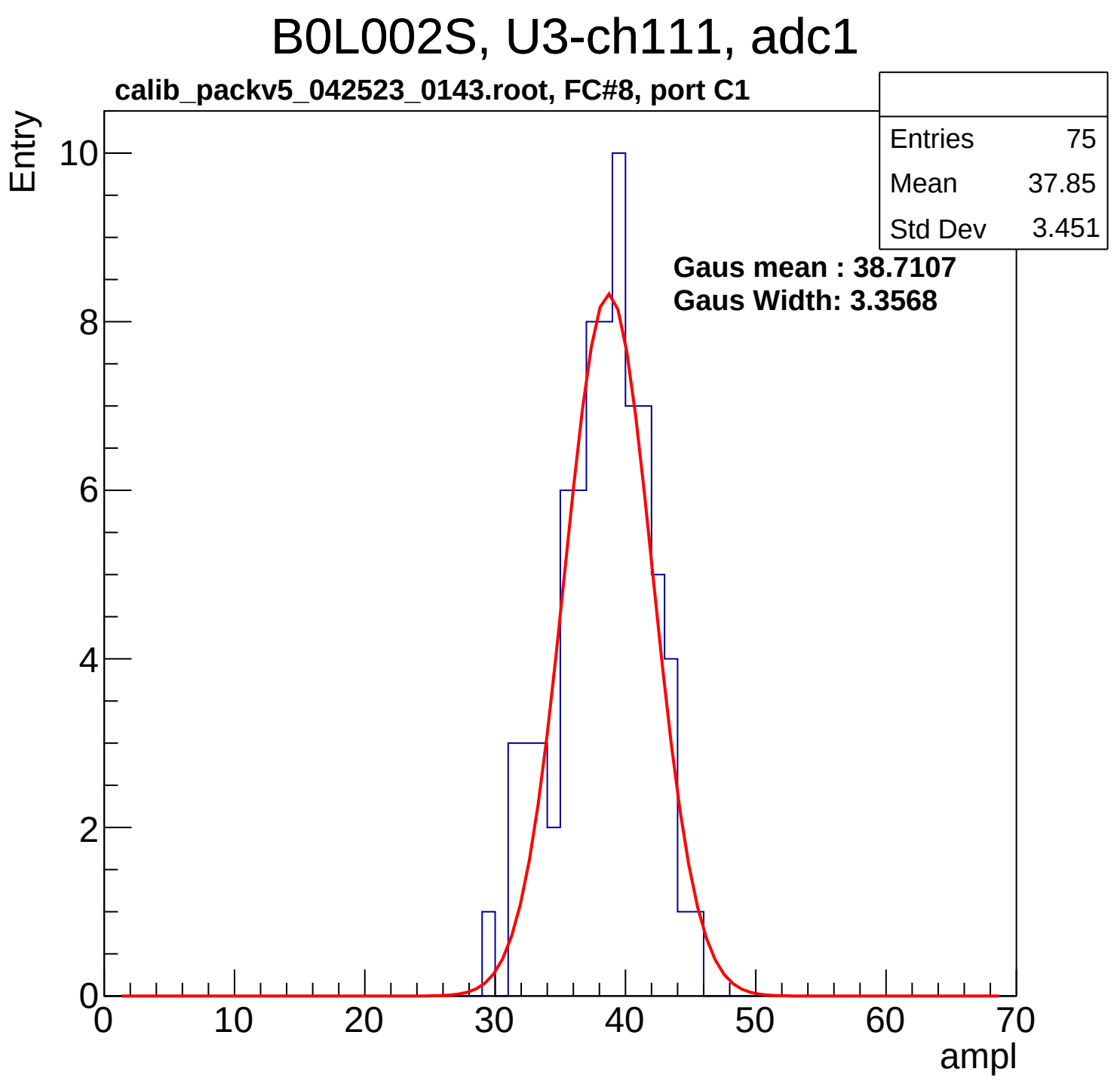
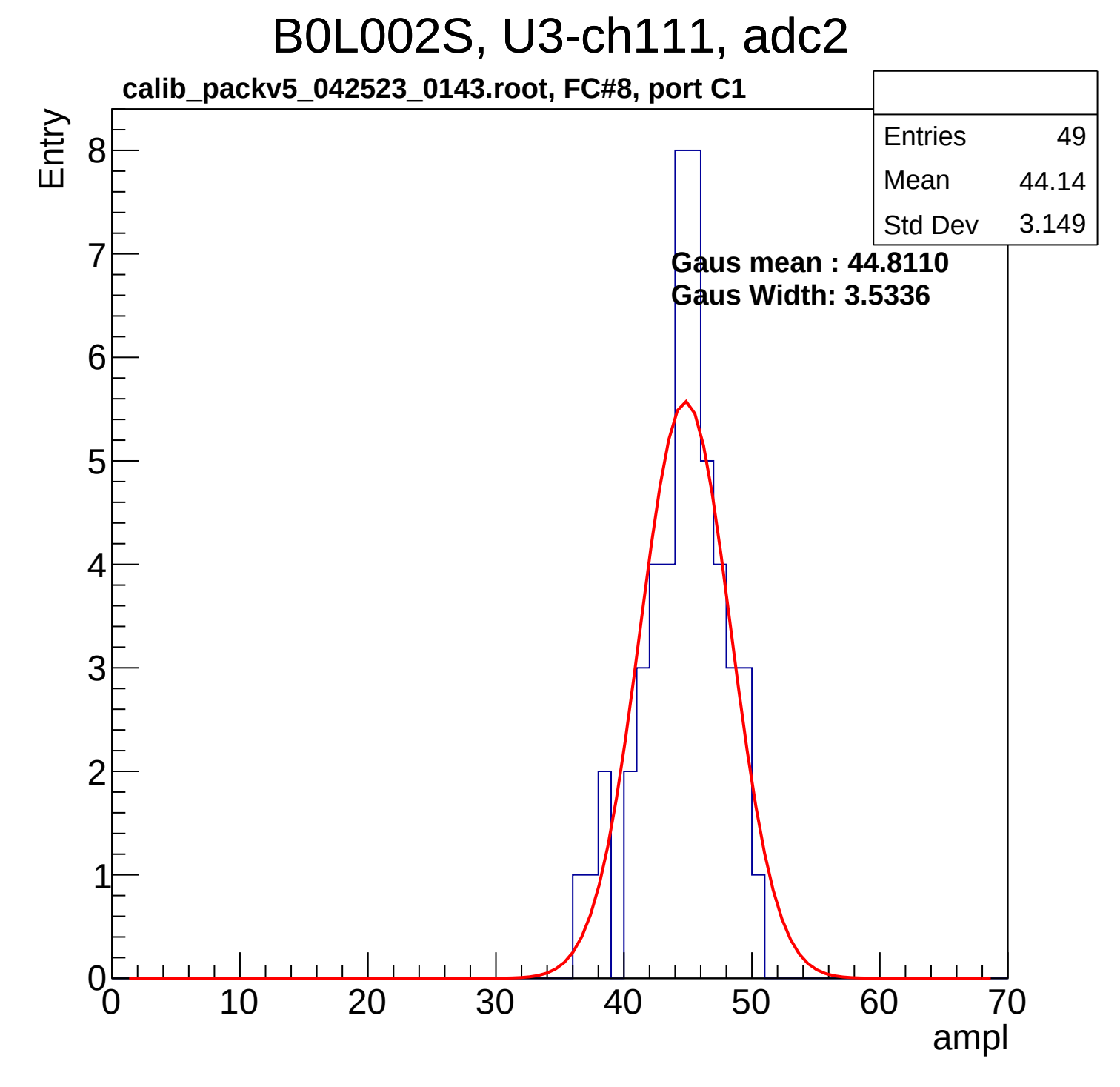
40
50
60
70


B0L002S, U3-ch111, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.85
Std Dev 3.451
Gaus mean : 38.7107
Gaus Width: 3.3568
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch111, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 44.14
Std Dev 3.149
Gaus mean : 44.8110
Gaus Width: 3.5336
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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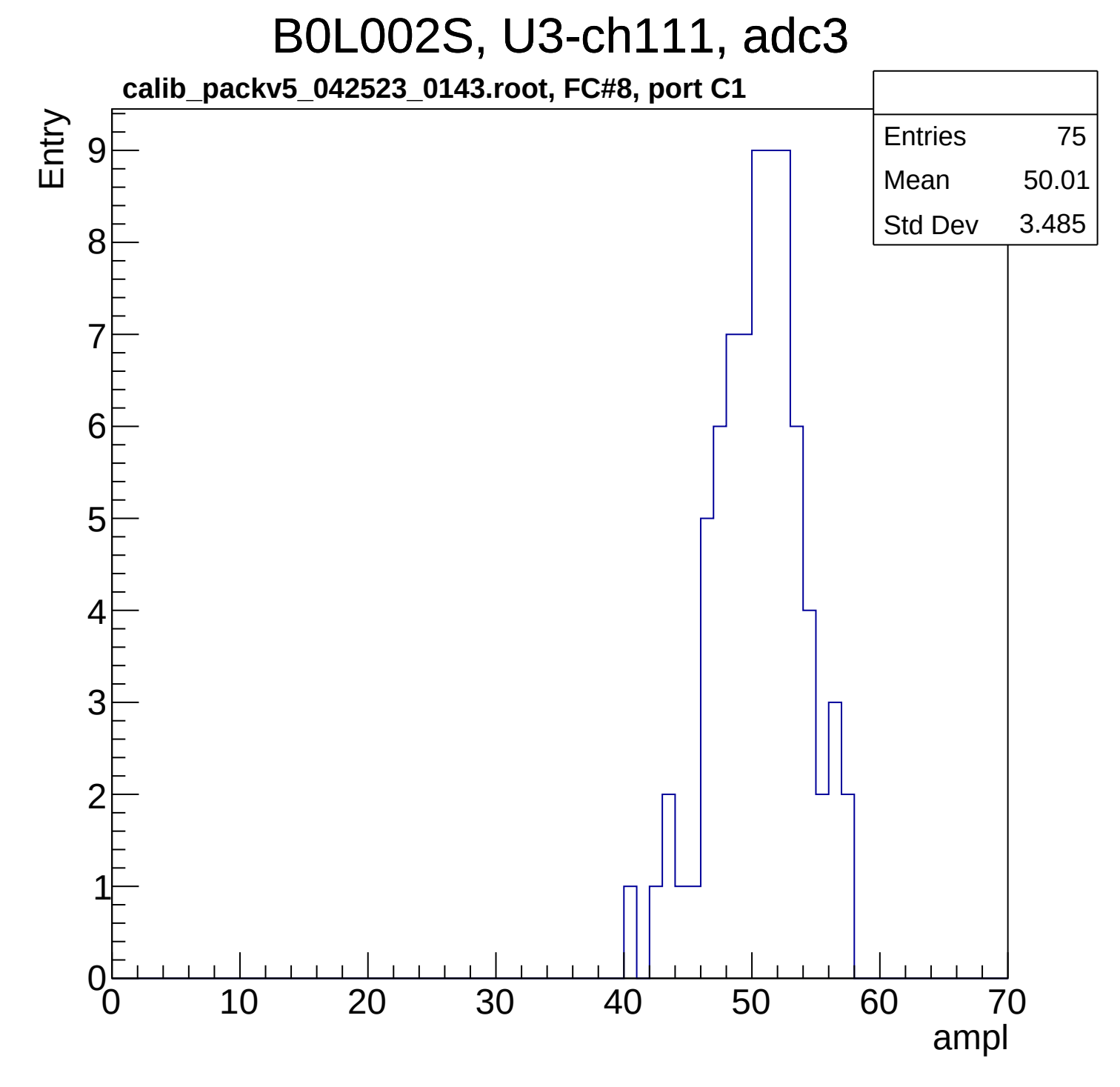
B0L002S, U3-ch111, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.01
Std Dev 3.485
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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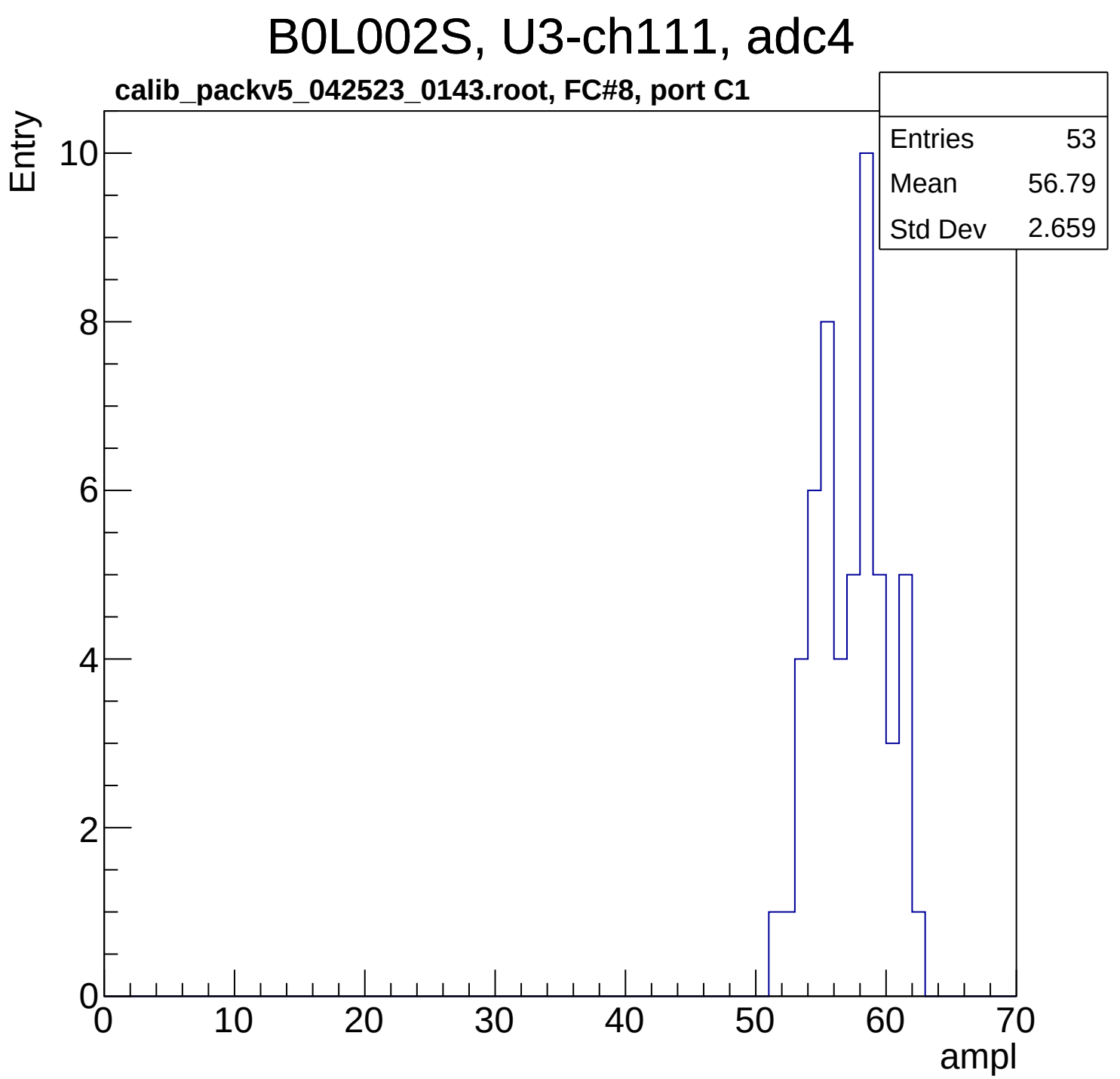
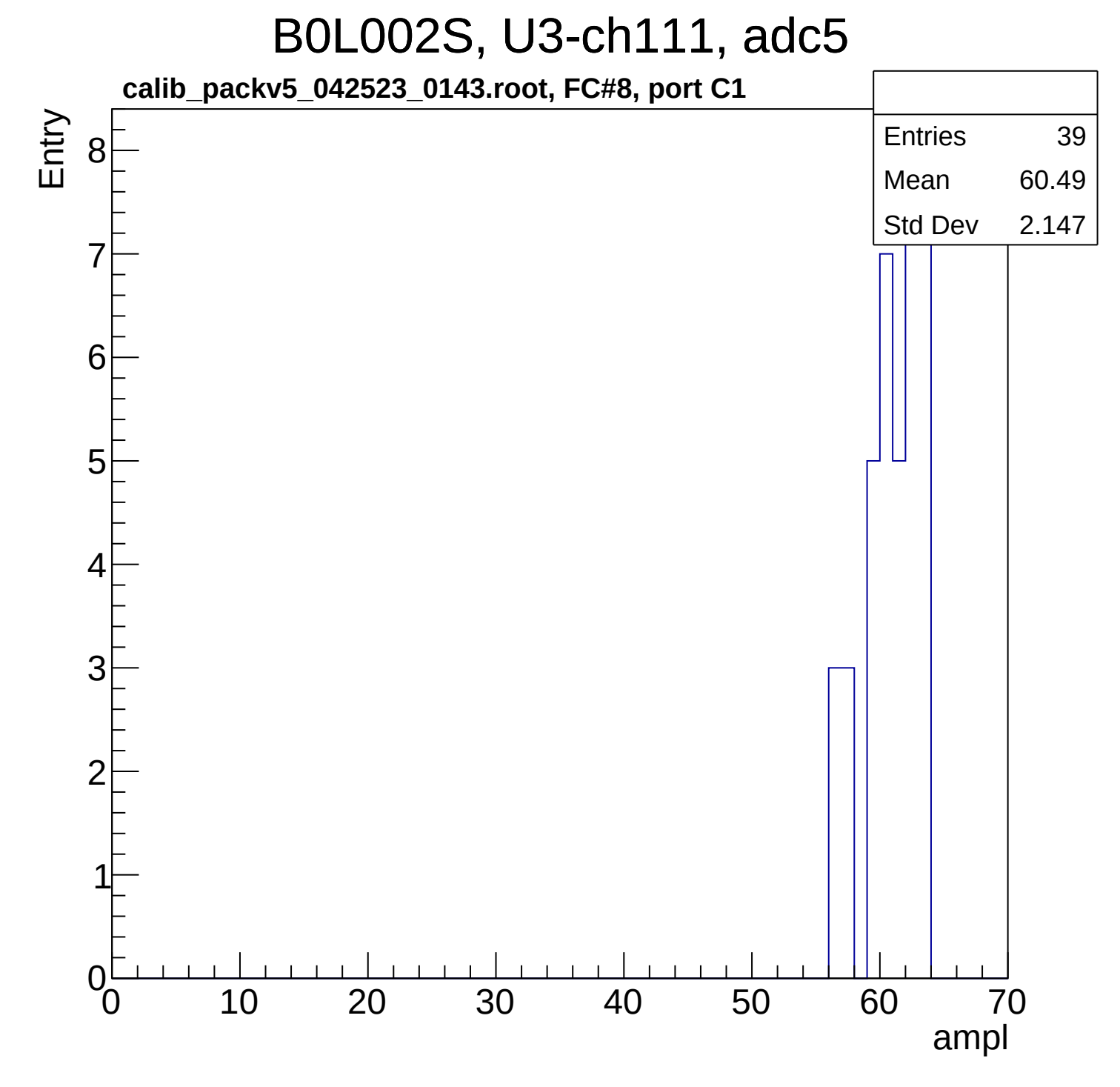

B0L002S, U3-ch111, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 56.79
Std Dev 2.659
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch111, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 60.49
Std Dev 2.147
Entry
ampl
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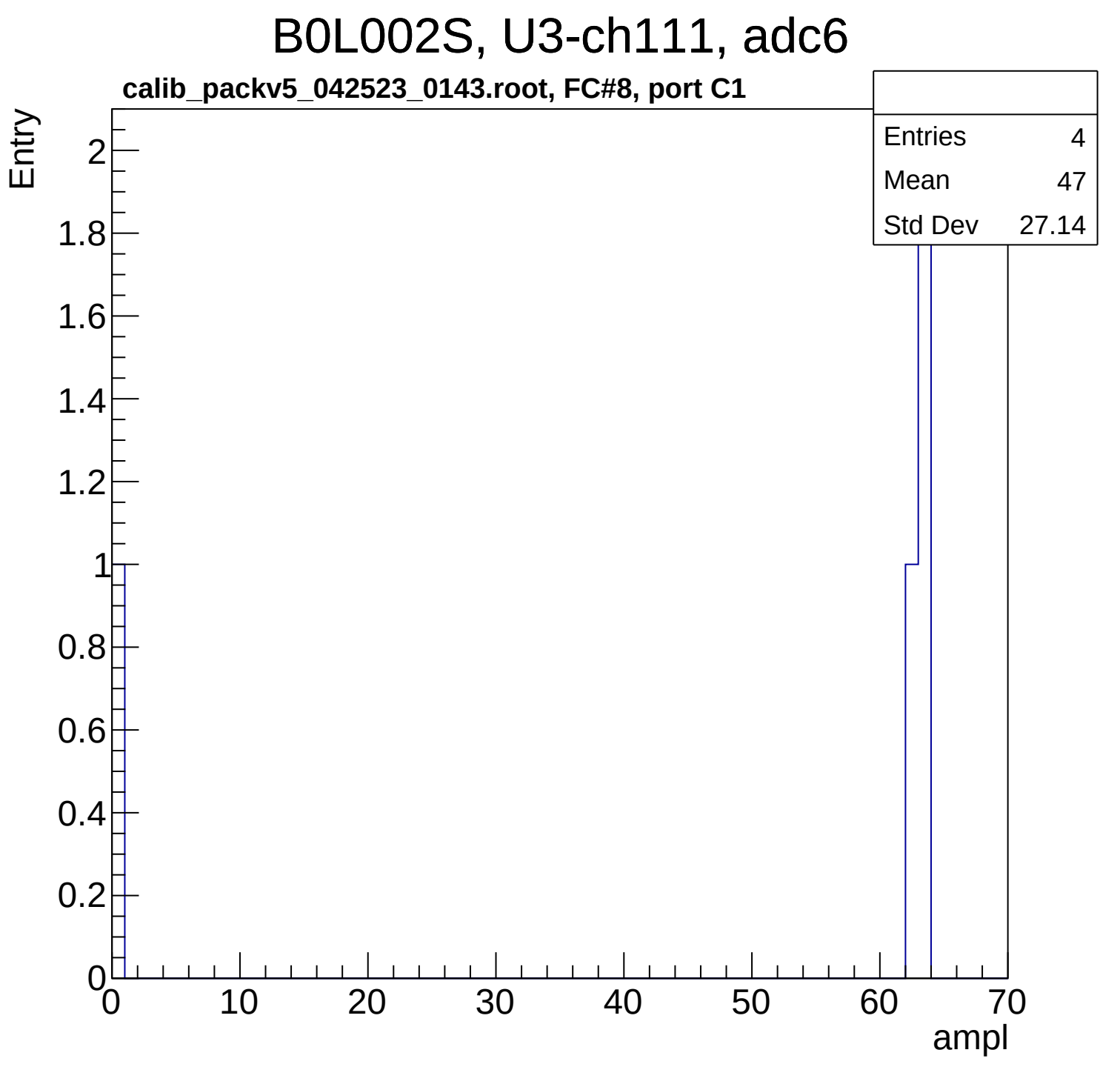
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch111, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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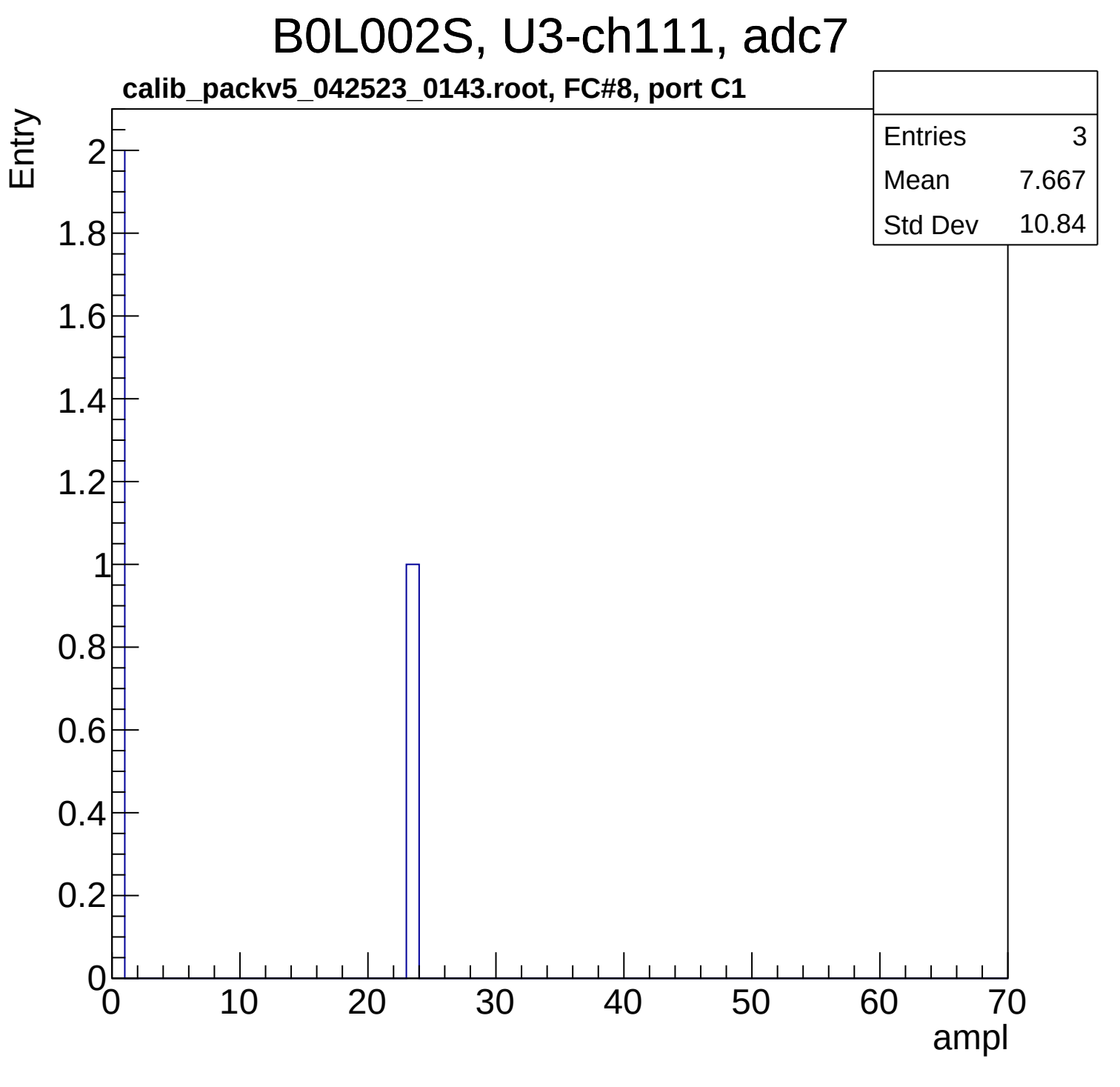
20
30
40
50
60
70


B0L002S, U3-ch111, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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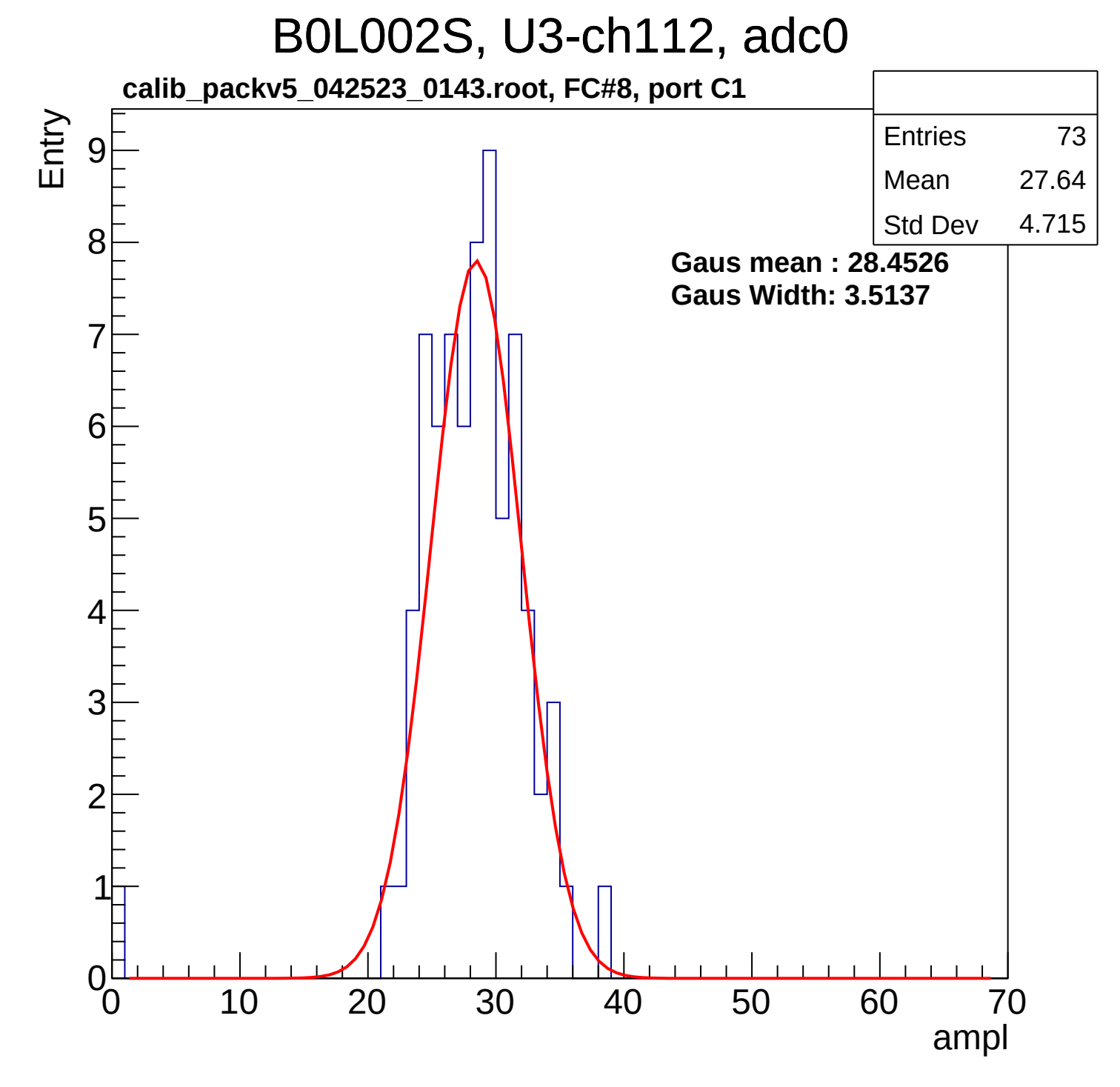
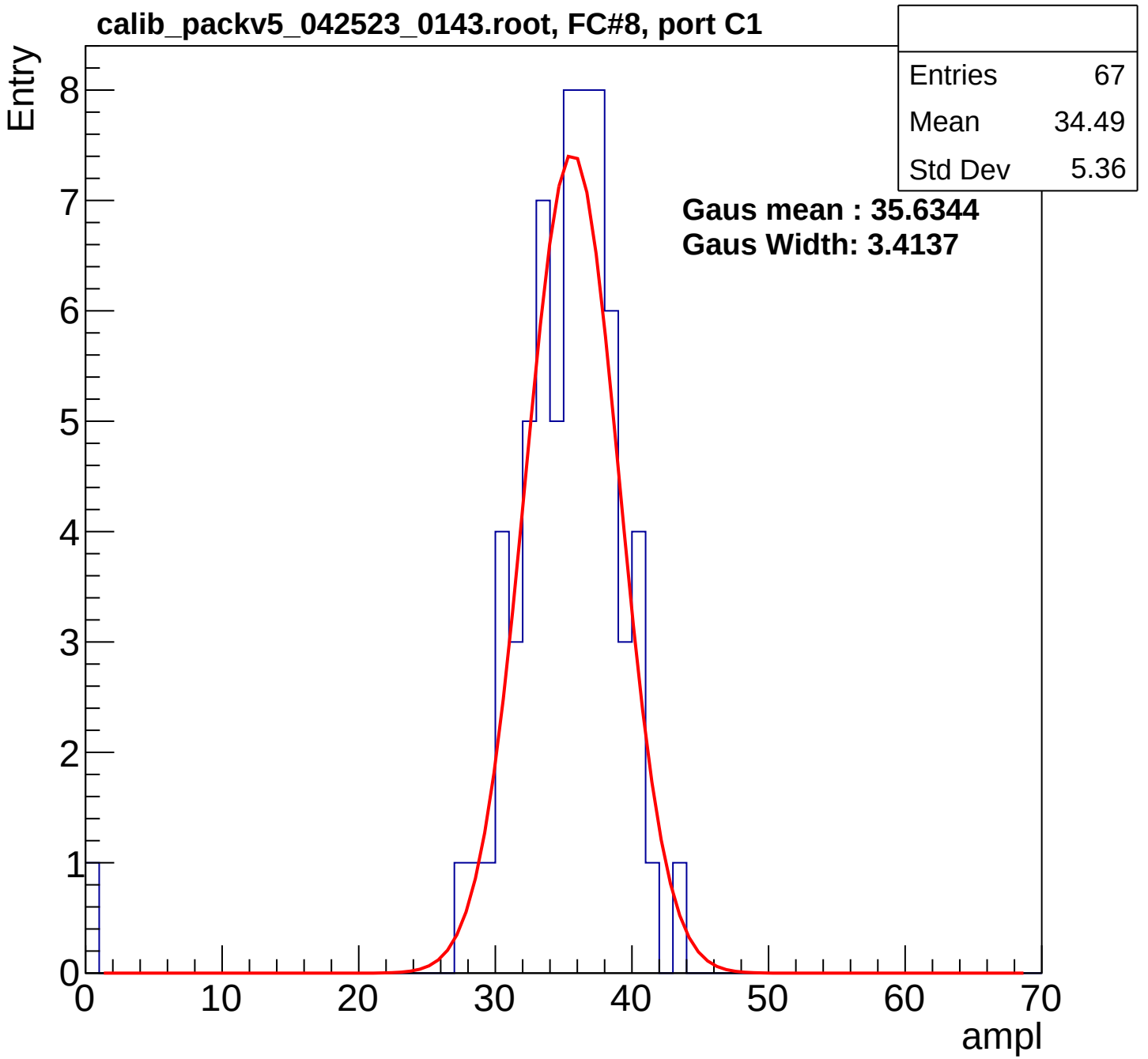

B0L002S, U3-ch112, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 27.64
Std Dev 4.715
Gaus mean : 28.4526
Gaus Width: 3.5137
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch112, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 34.49
Std Dev 5.36
Gaus mean : 35.6344
Gaus Width: 3.4137
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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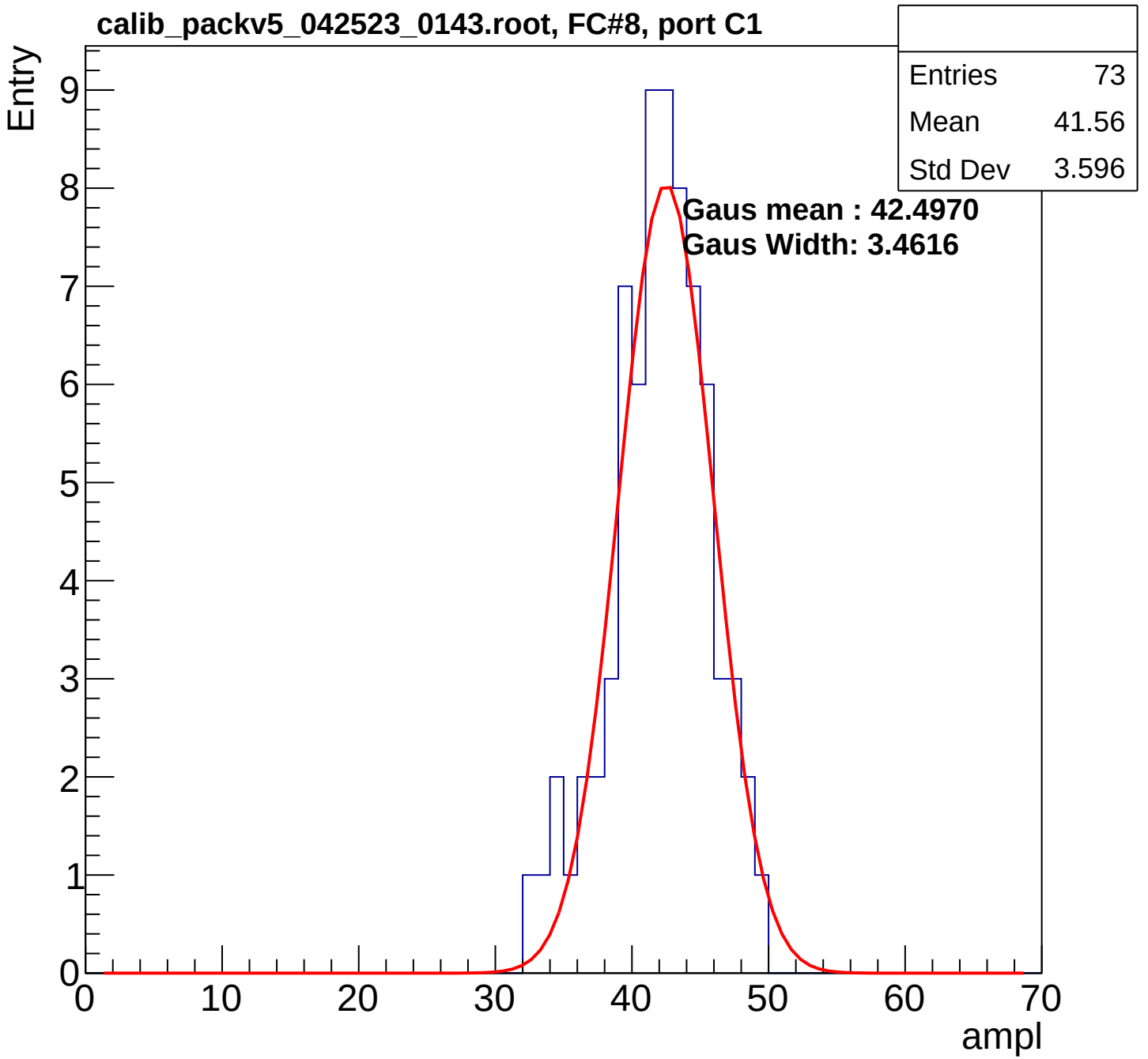
B0L002S, U3-ch112, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 41.56
Std Dev 3.596
Gaus mean : 42.4970
Gaus Width: 3.4616
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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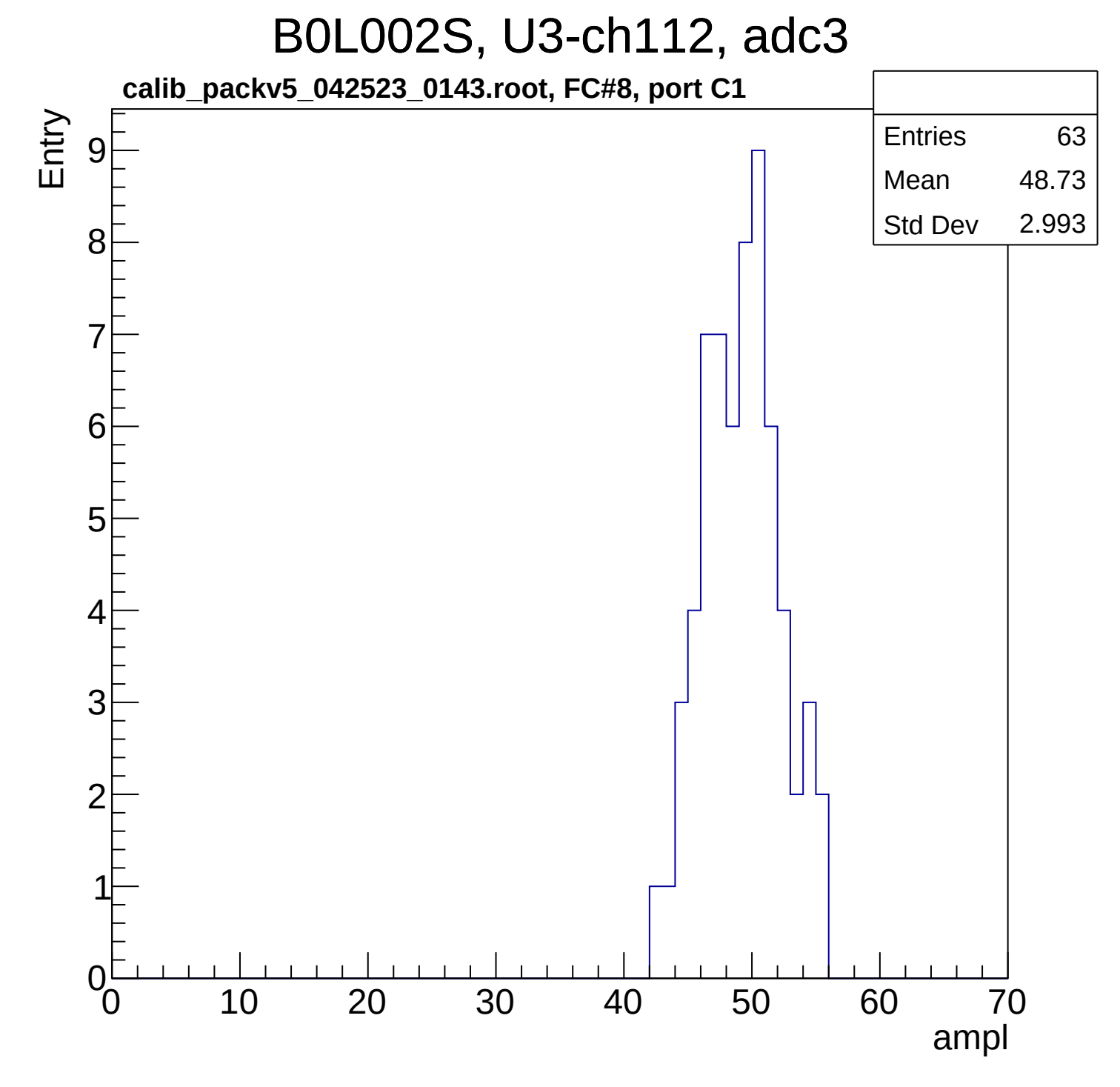
B0L002S, U3-ch112, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 48.73
Std Dev 2.993
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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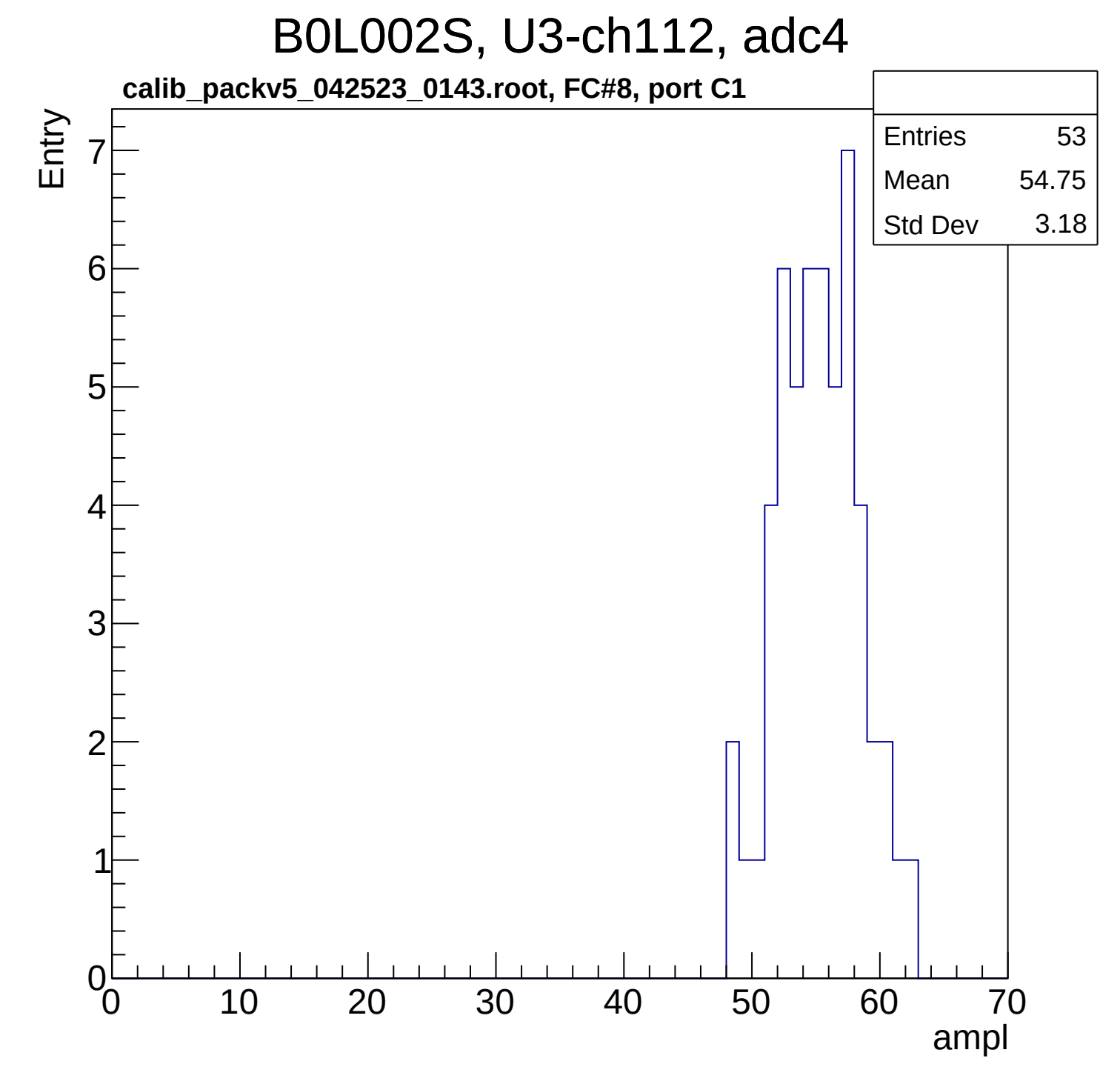
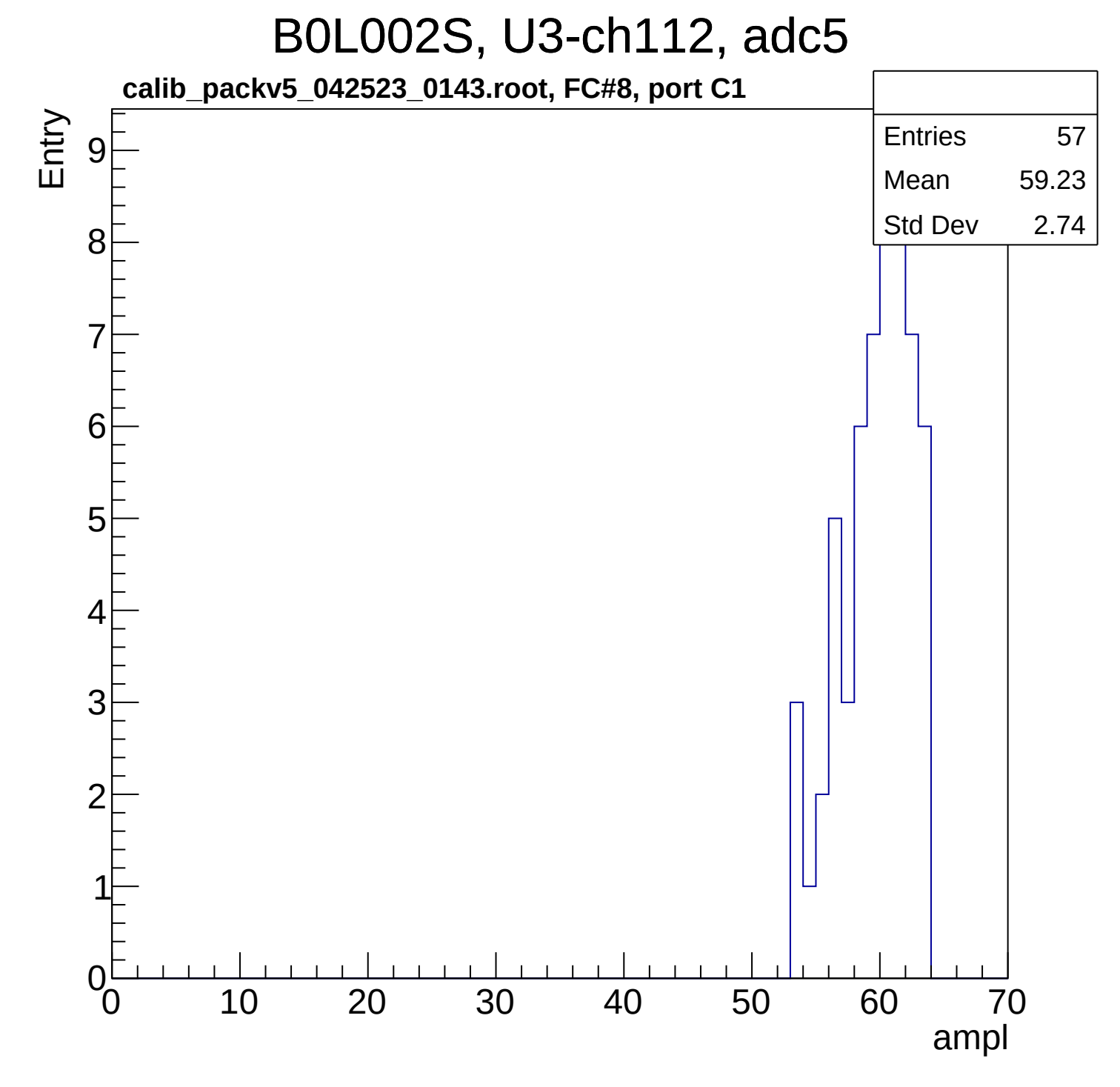

B0L002S, U3-ch112, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 54.75
Std Dev 3.18
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch112, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 59.23
Std Dev 2.74
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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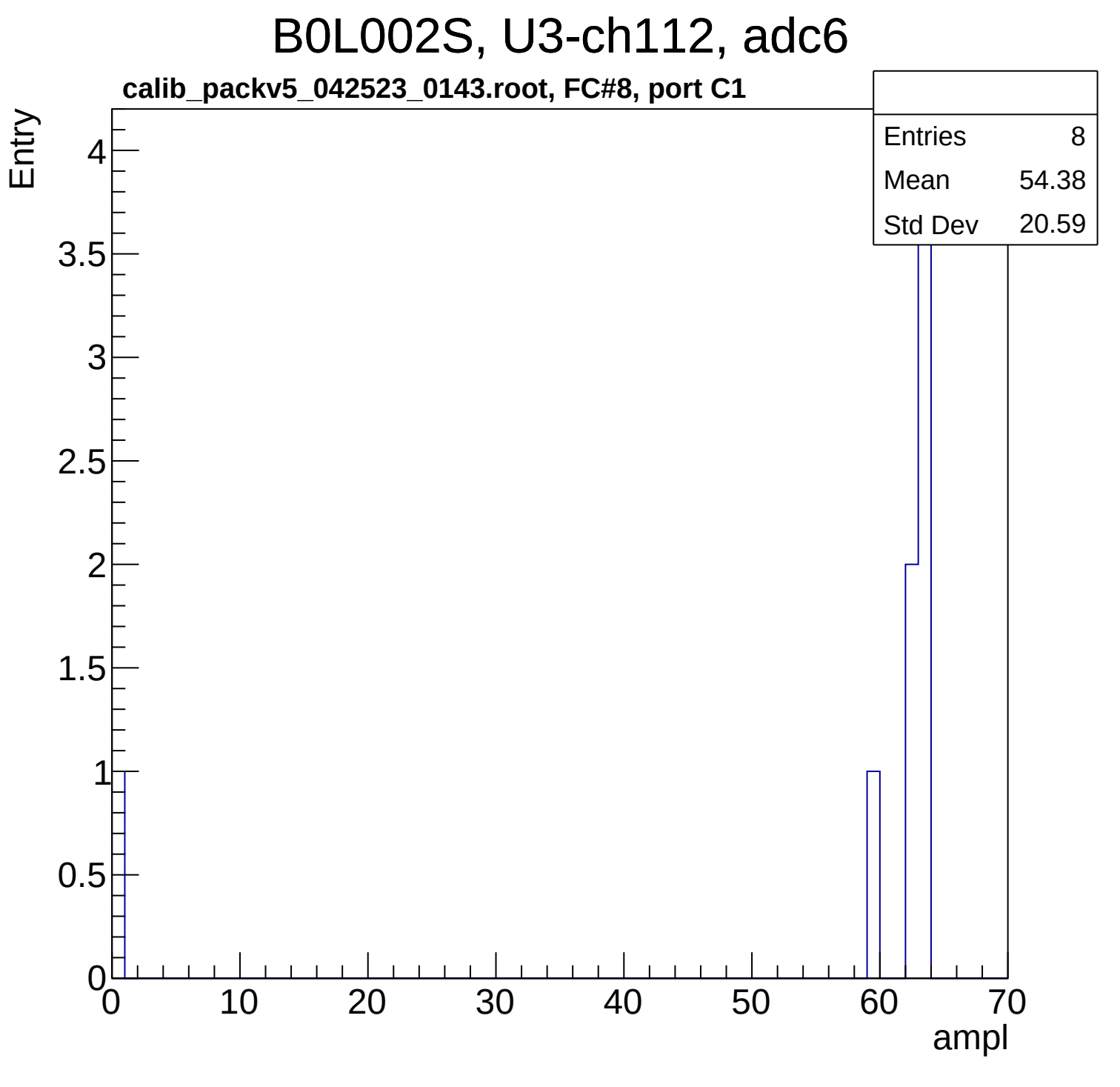
B0L002S, U3-ch112, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 54.38
Std Dev 20.59
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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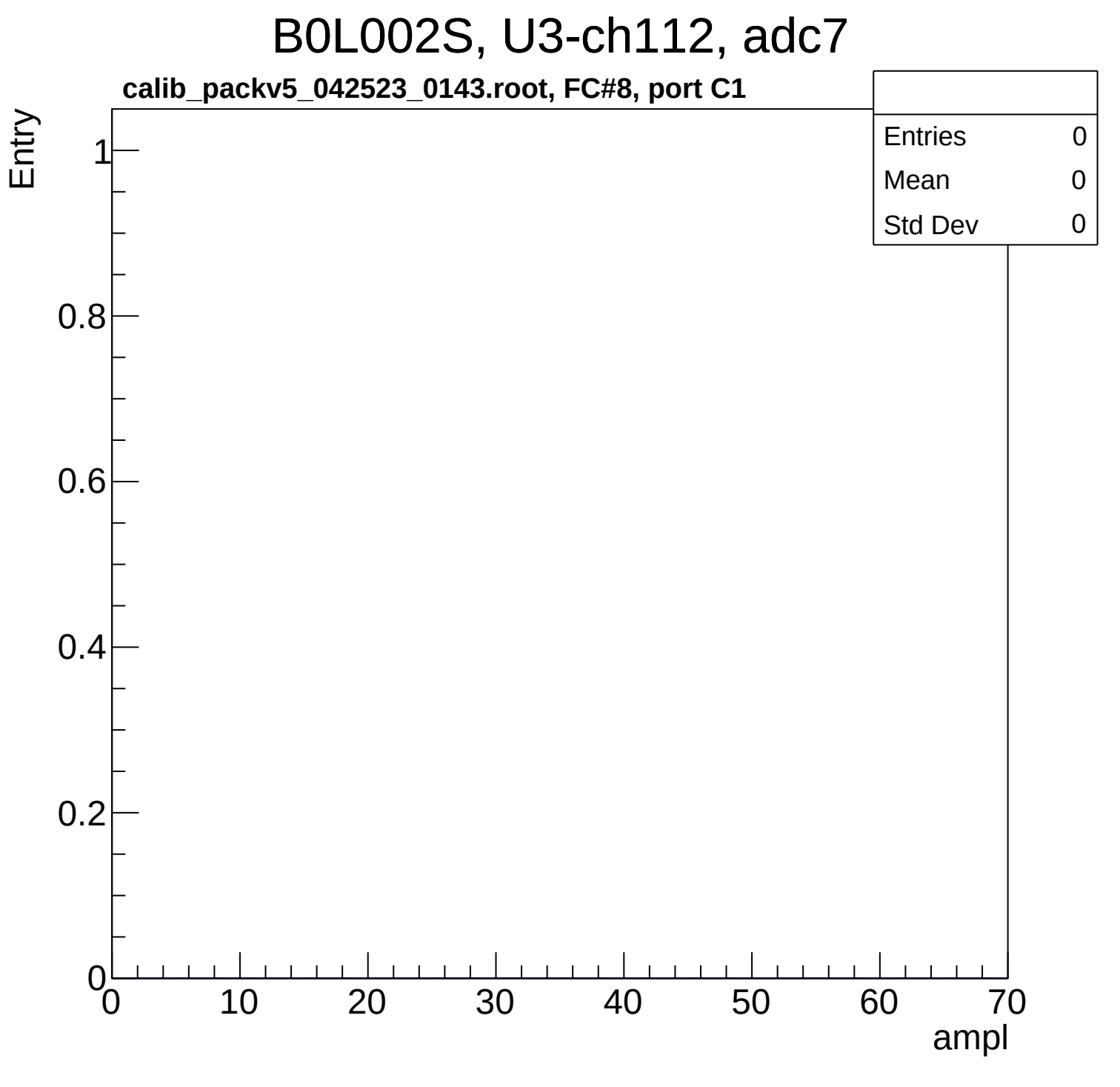
B0L002S, U3-ch112, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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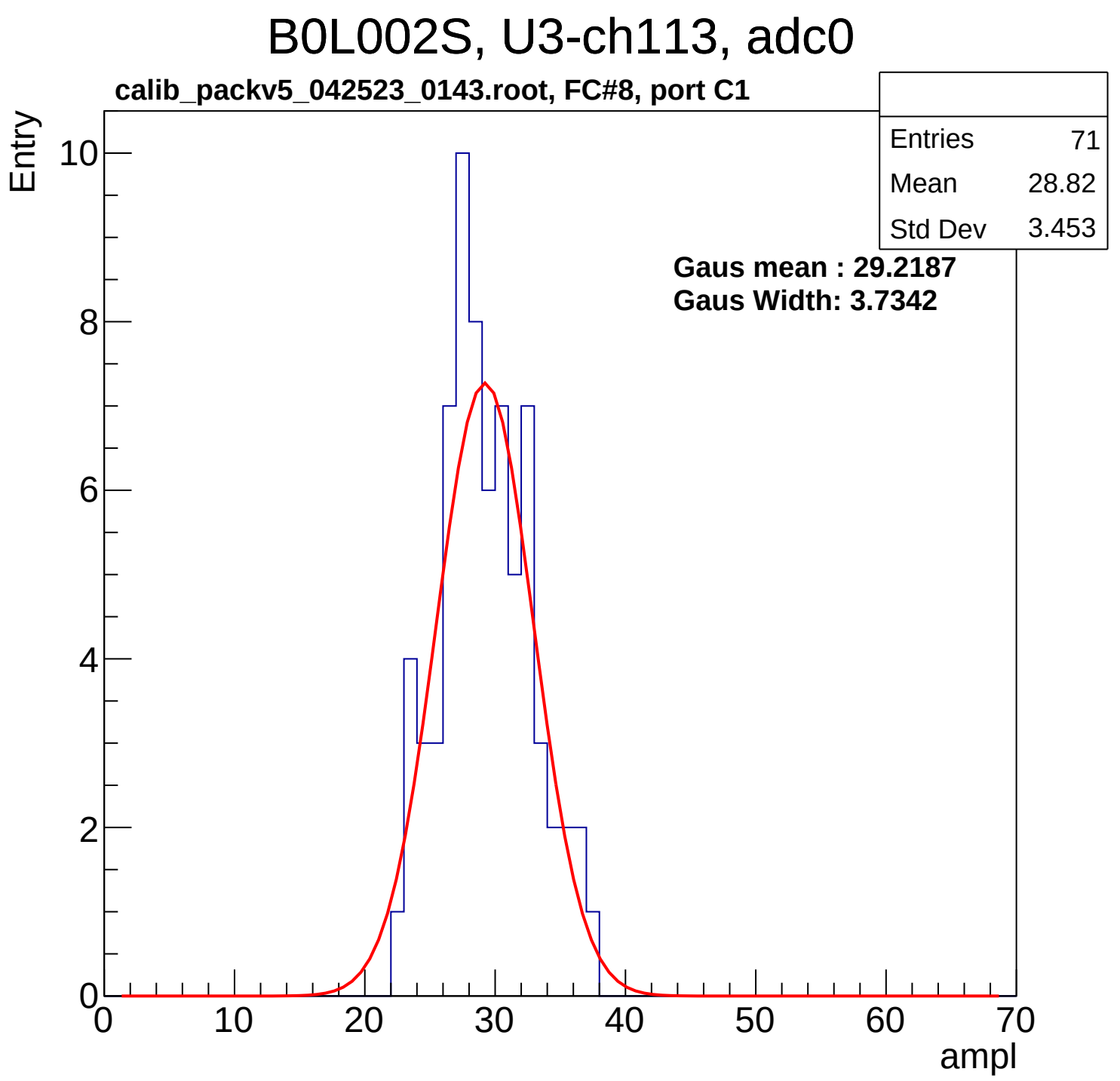
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch113, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 28.82
Std Dev 3.453
Gaus mean : 29.2187
Gaus Width: 3.7342
Entry
ampl
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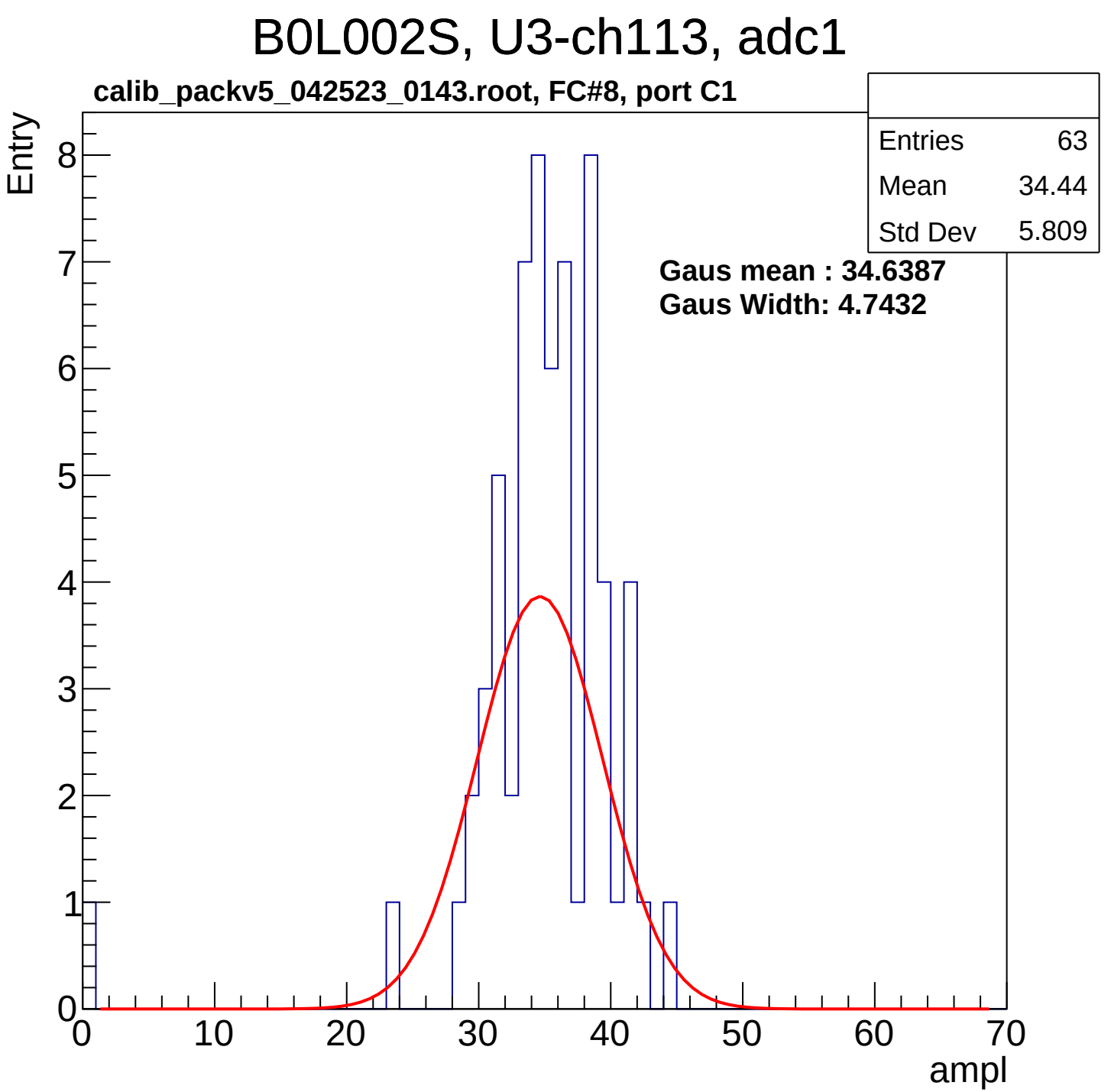
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch113, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 34.44
Std Dev 5.809
Gaus mean : 34.6387
Gaus Width: 4.7432
Entry
ampl
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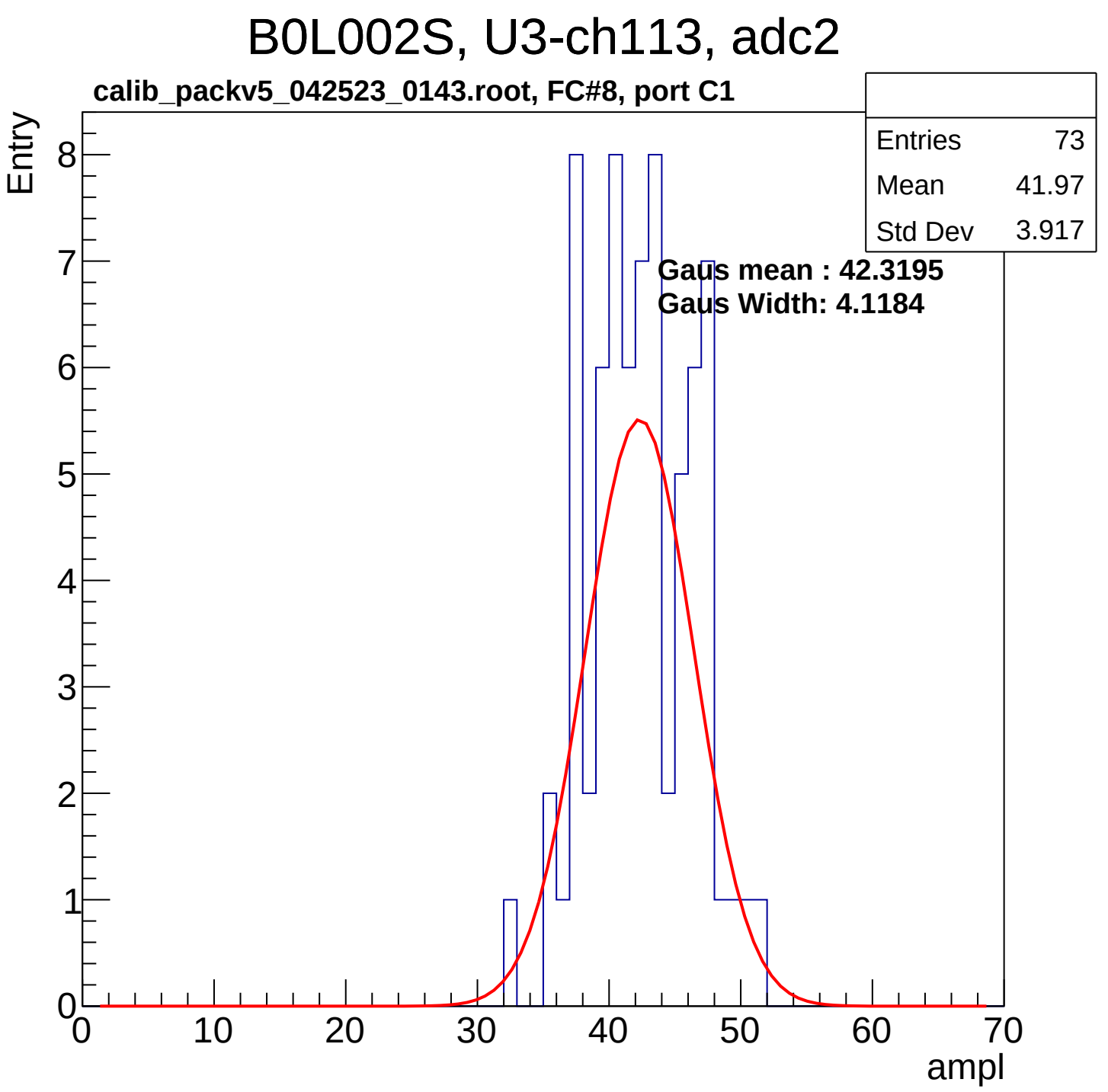
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch113, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 41.97
Std Dev 3.917
Gaus mean : 42.3195
Gaus Width: 4.1184
Entry
ampl
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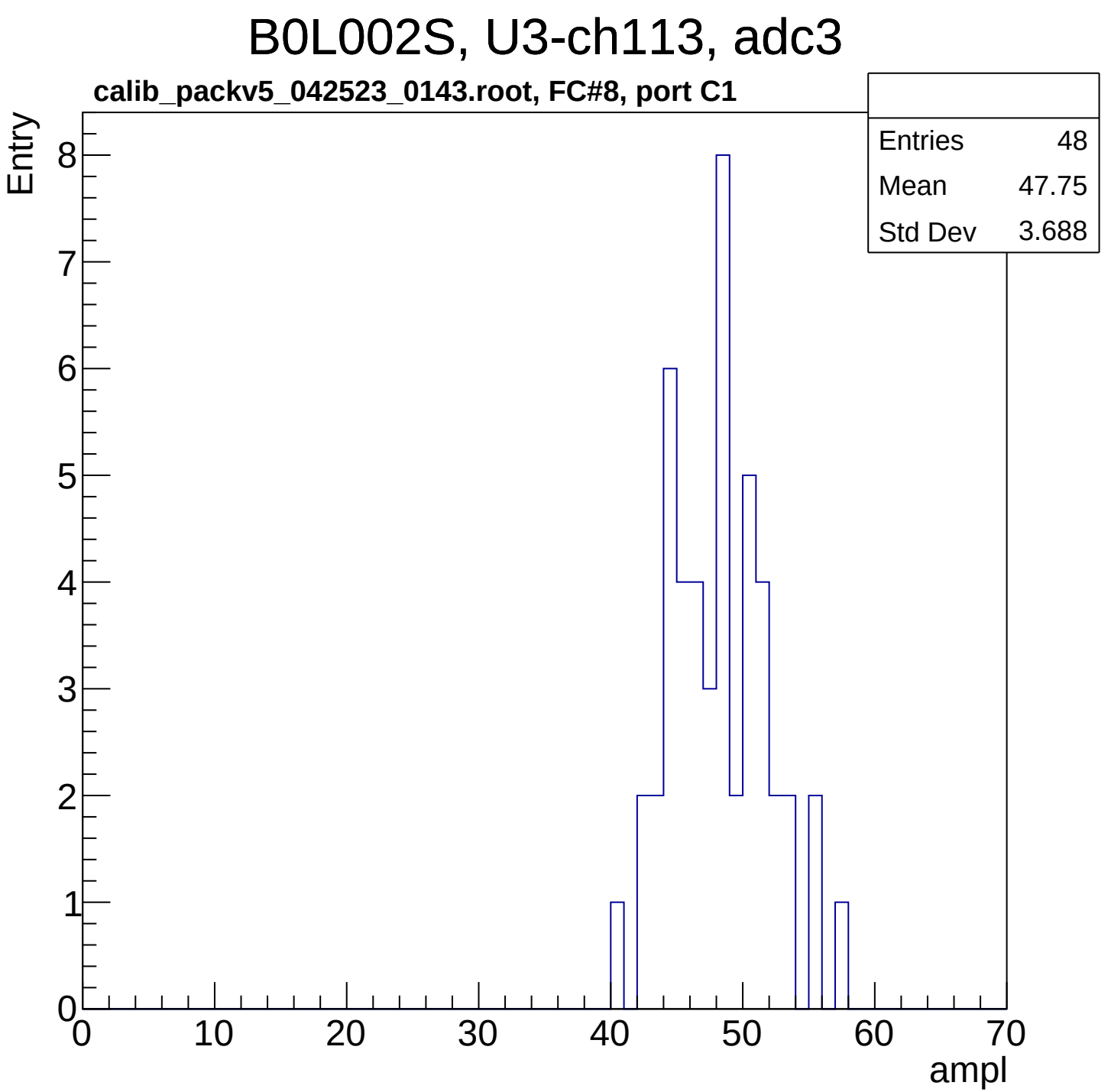
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch113, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 47.75
Std Dev 3.688
Entry
ampl
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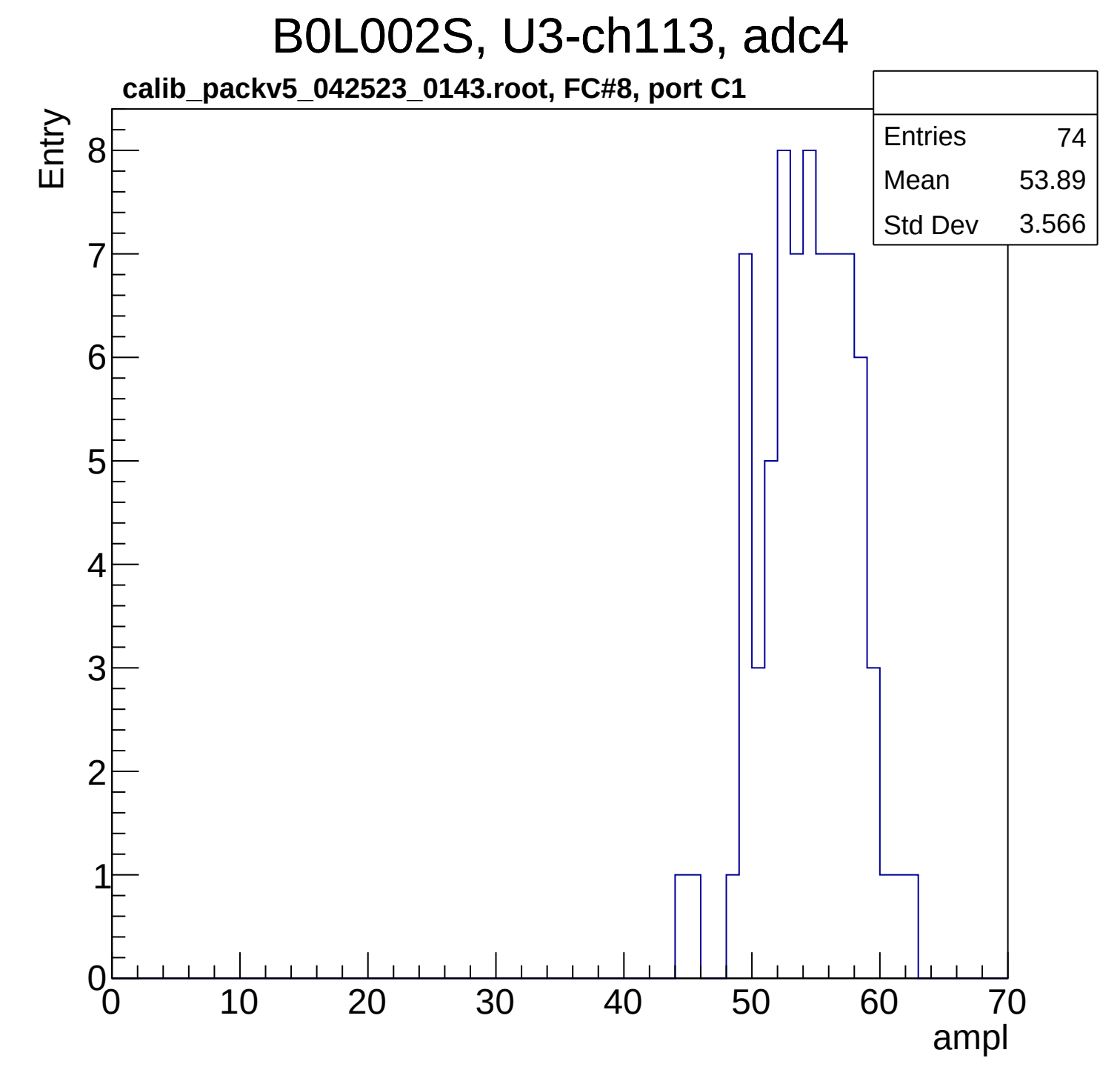
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch113, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 53.89
Std Dev 3.566
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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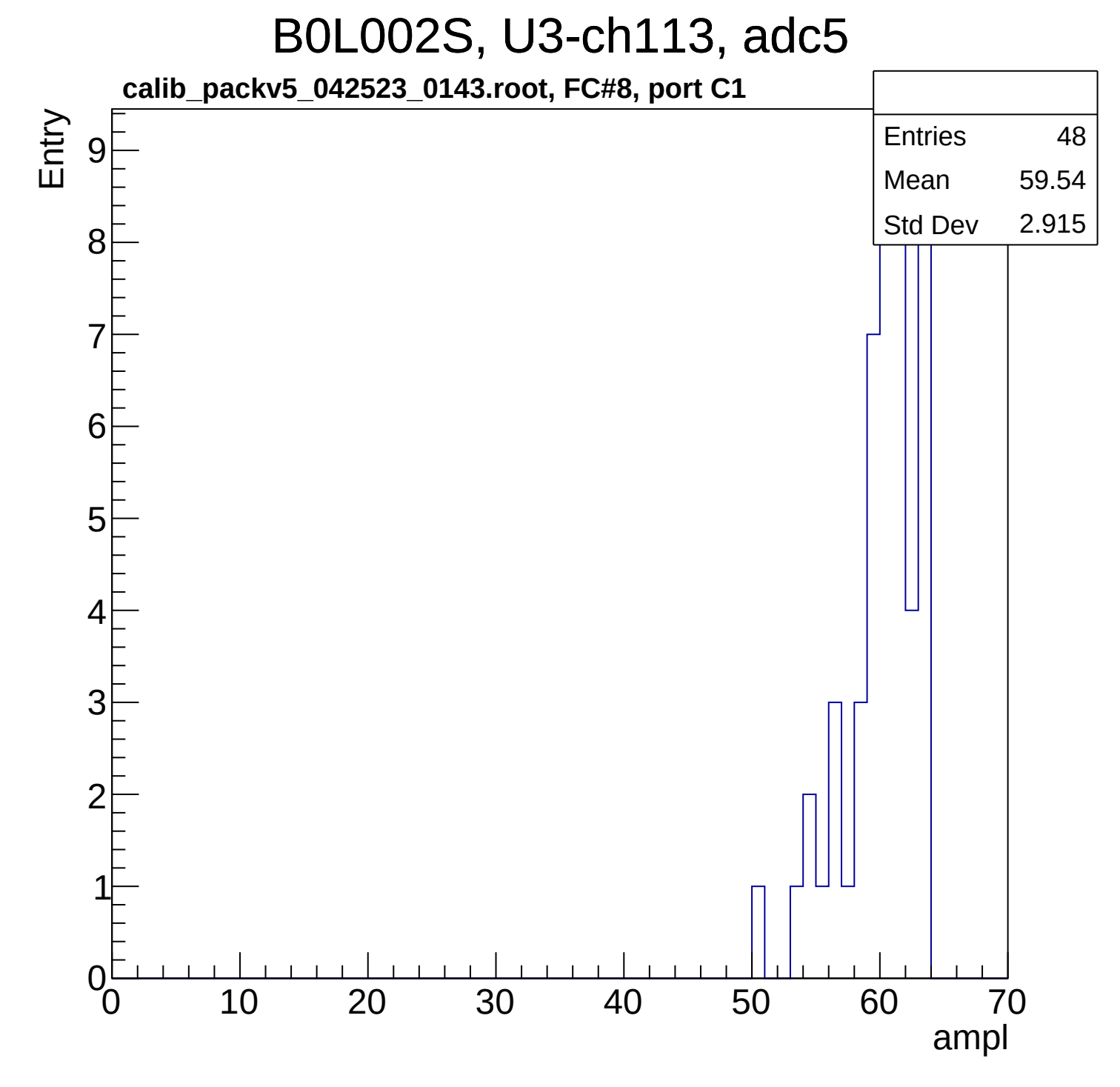
B0L002S, U3-ch113, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 59.54
Std Dev 2.915
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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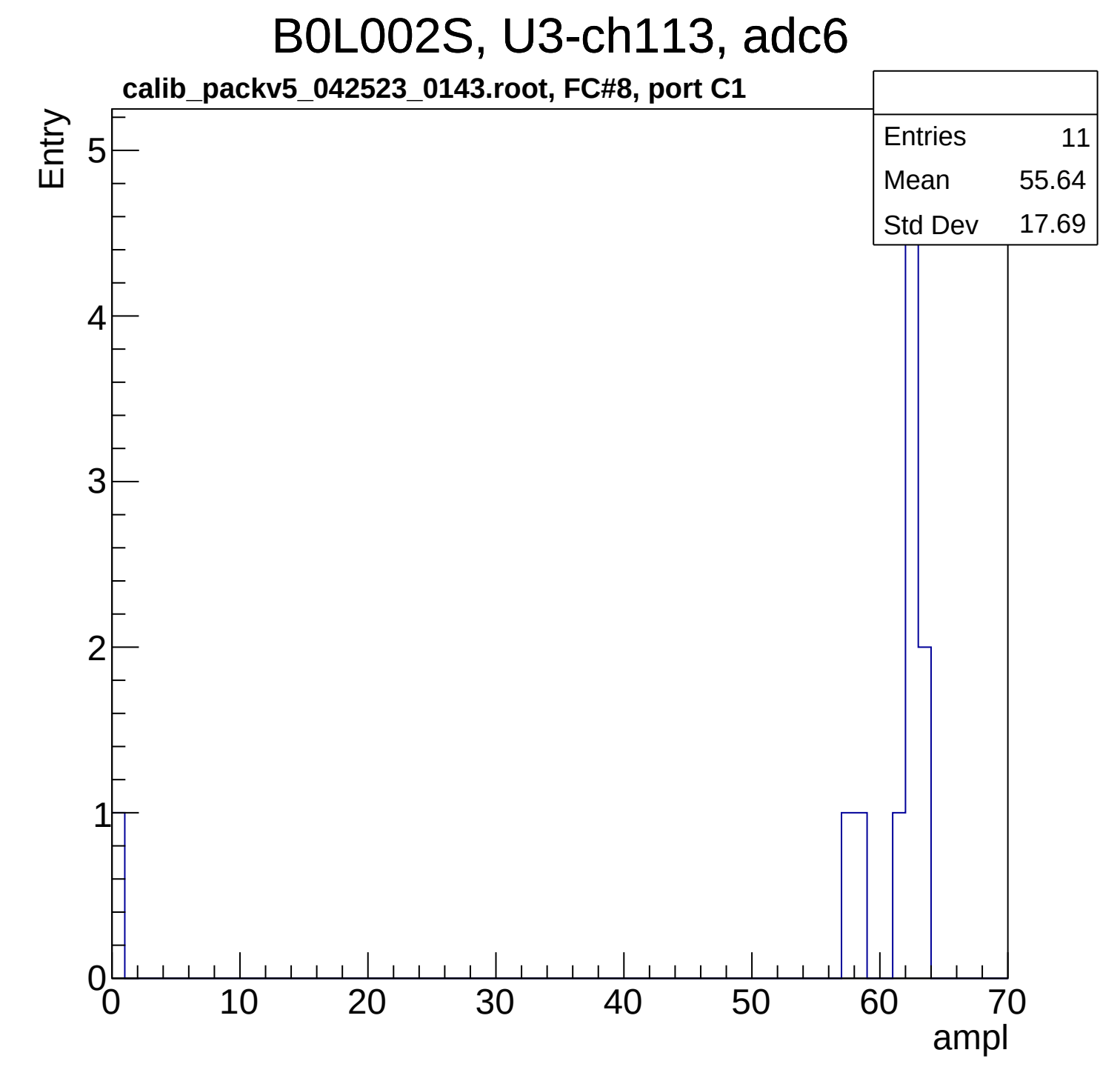
B0L002S, U3-ch113, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 11
Mean 55.64
Std Dev 17.69
Entry
ampl
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1
2
3
4
5
0
10
20
30
40
50
60
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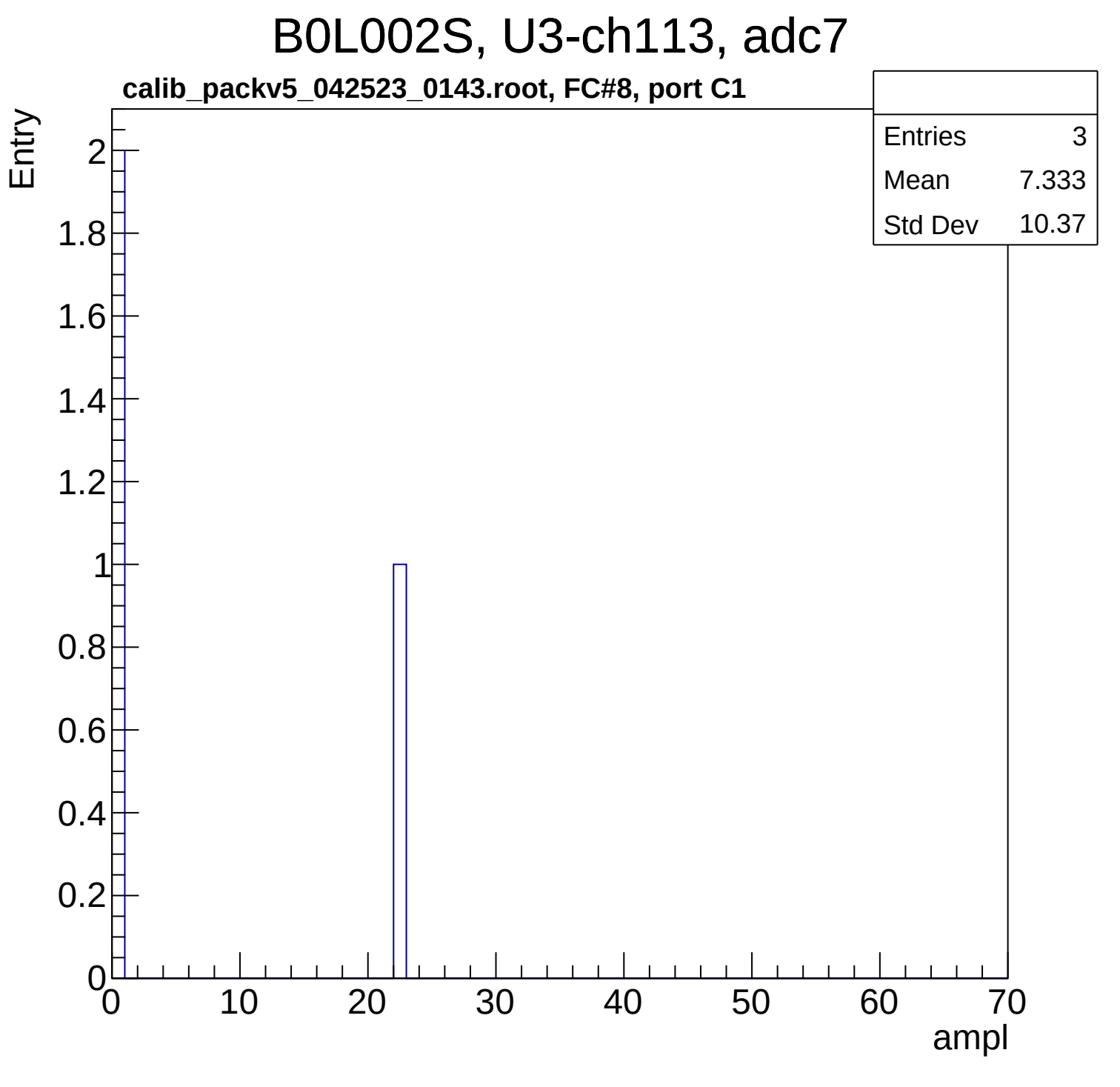
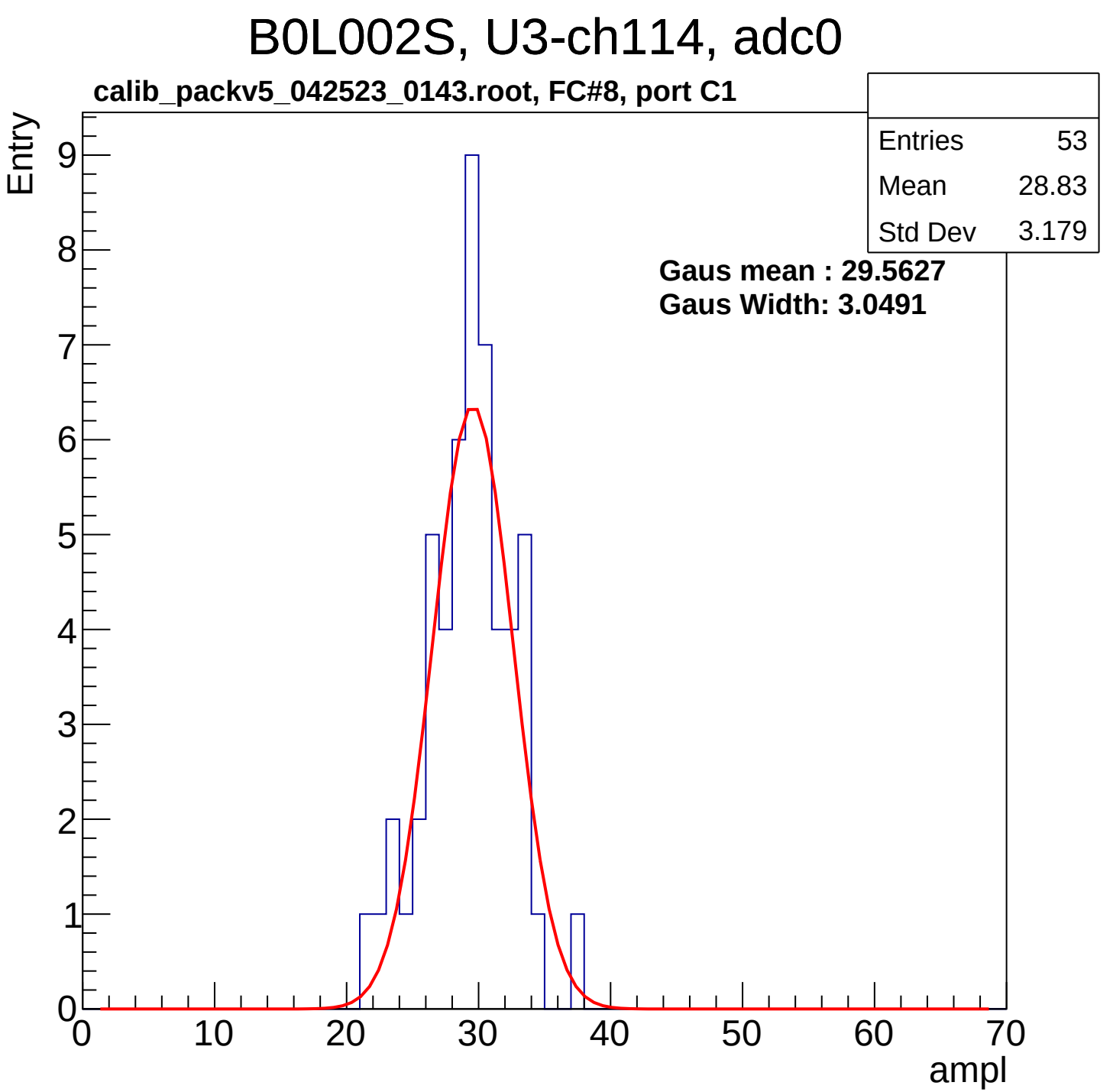

B0L002S, U3-ch113, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L002S, U3-ch114, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 28.83
Std Dev 3.179
Gaus mean : 29.5627
Gaus Width: 3.0491
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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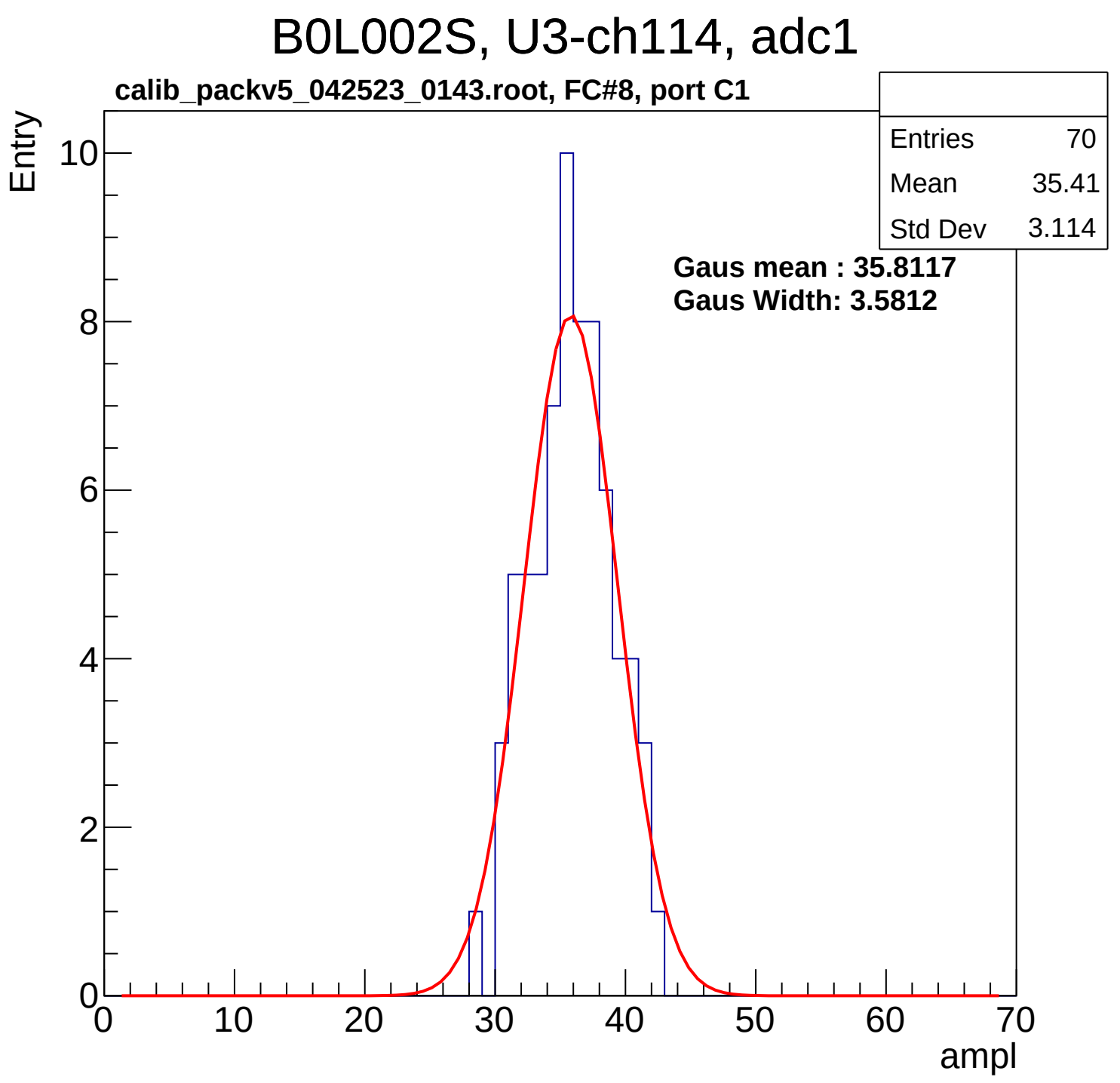
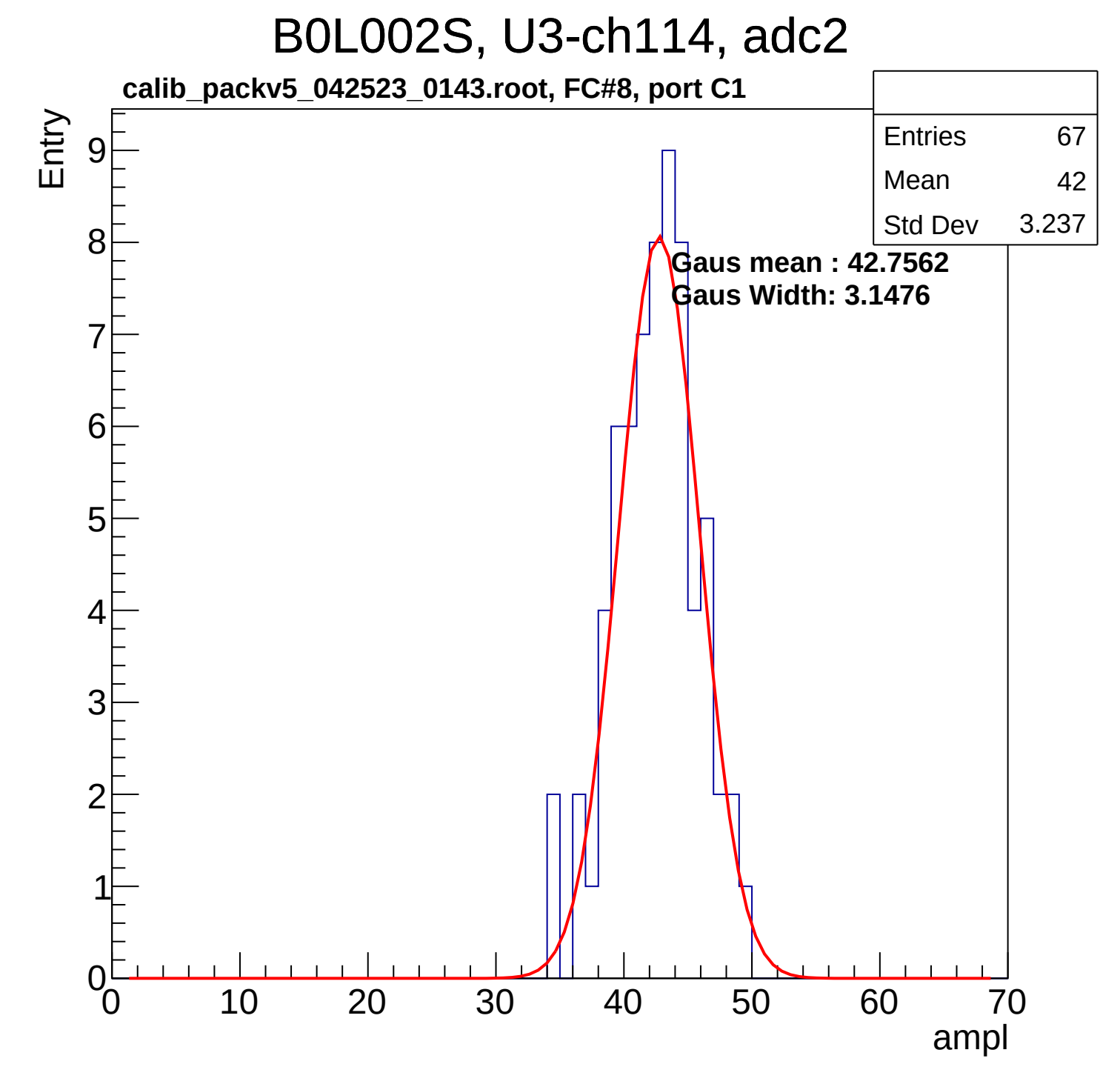

B0L002S, U3-ch114, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 35.41
Std Dev 3.114
Gaus mean : 35.8117
Gaus Width: 3.5812
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch114, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 42
Std Dev 3.237
Gaus mean : 42.7562
Gaus Width: 3.1476
Entry
ampl
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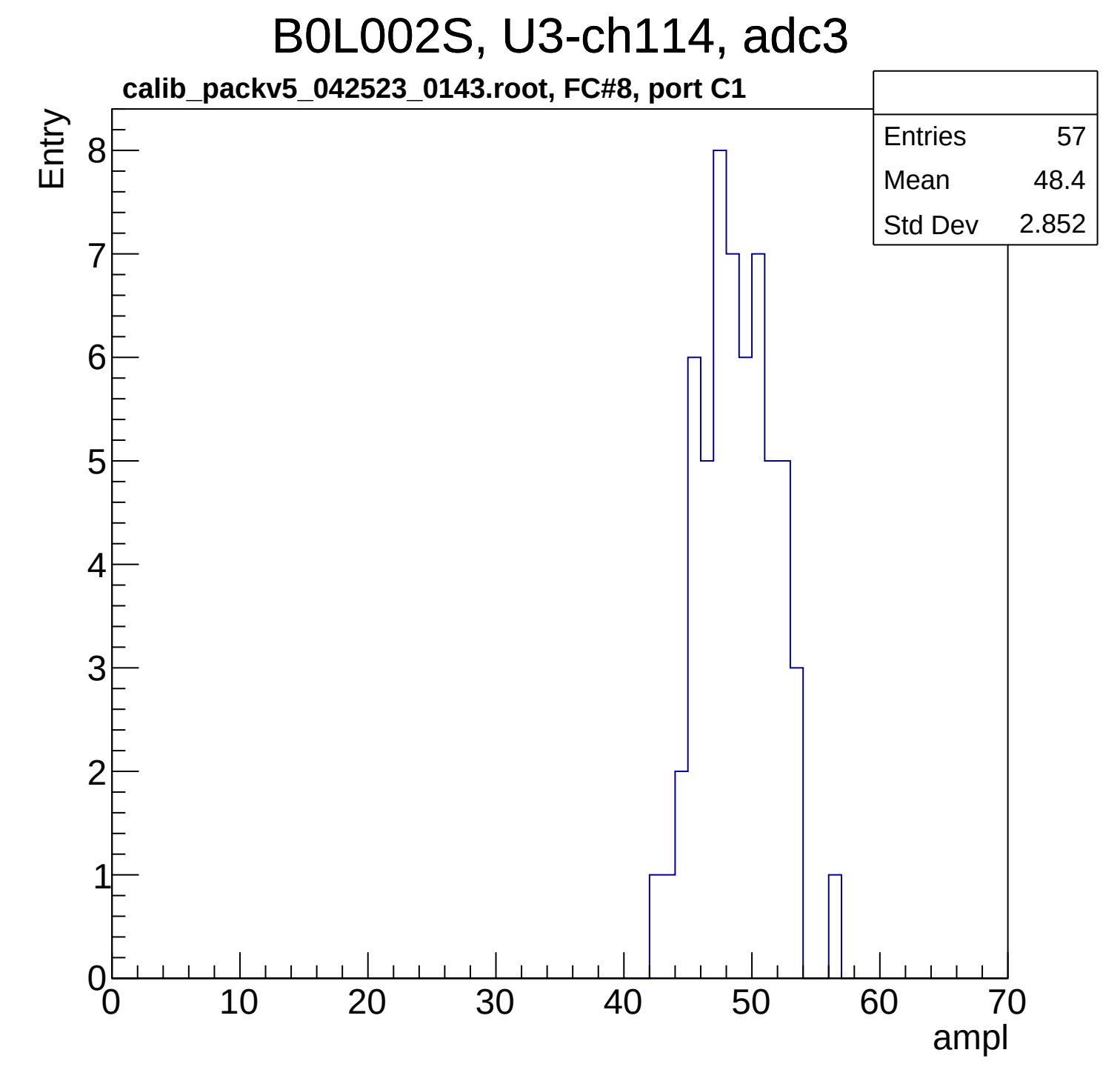
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch114, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 48.4
Std Dev 2.852
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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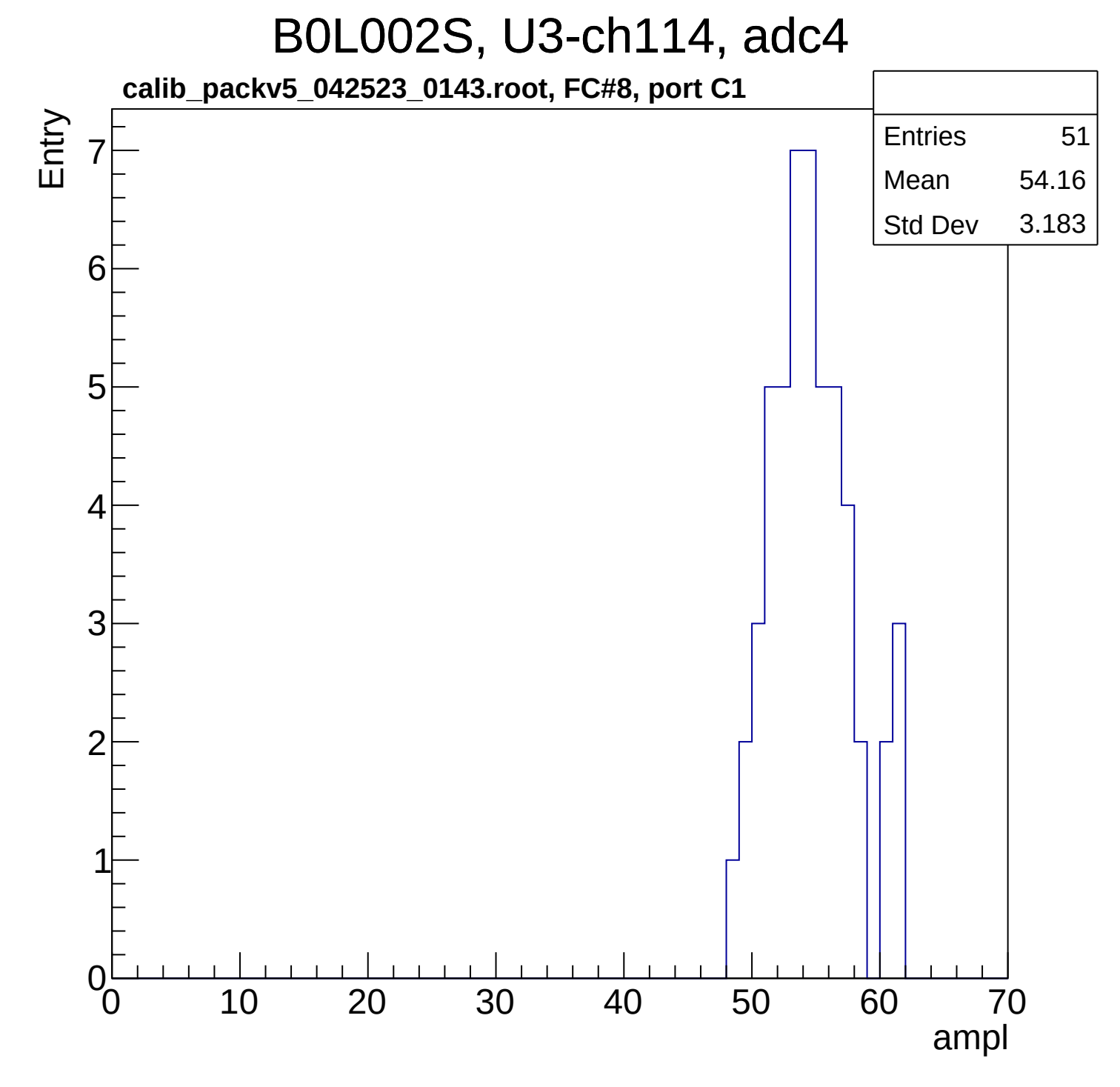
60
70


B0L002S, U3-ch114, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 54.16
Std Dev 3.183
Entry
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2
3
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5
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7
0
10
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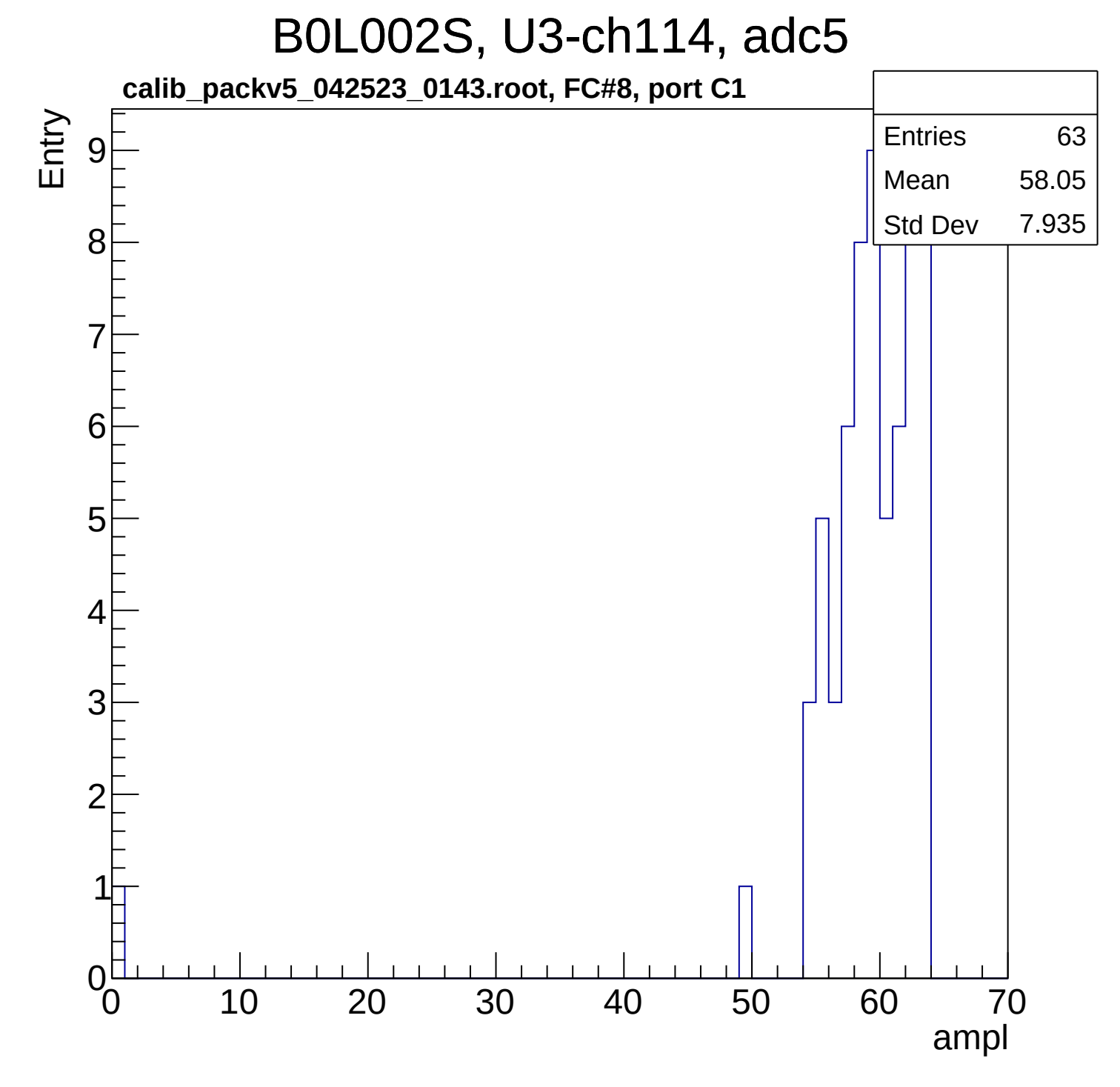
40
50
60
70
ampl


B0L002S, U3-ch114, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 58.05
Std Dev 7.935
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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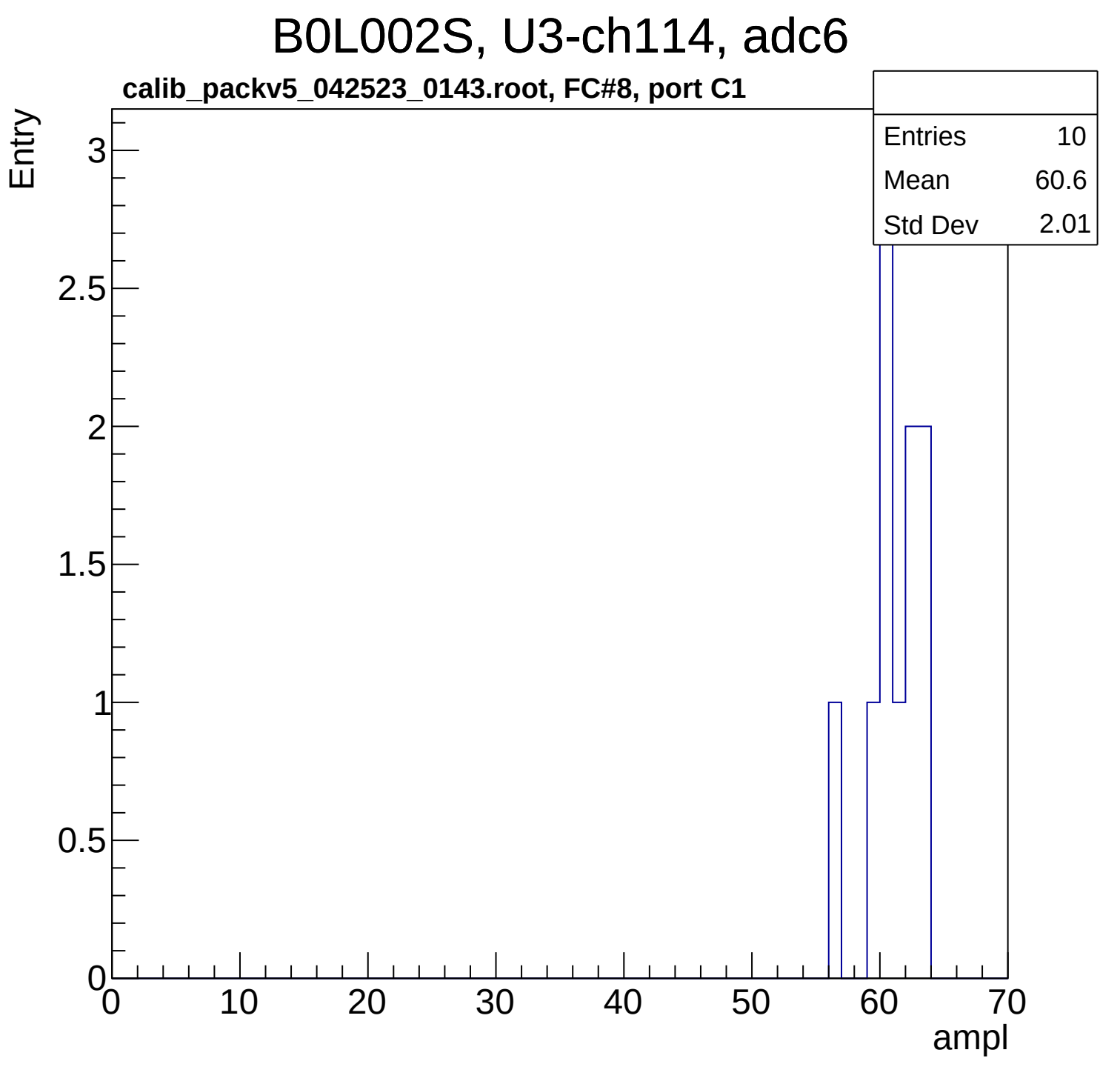
B0L002S, U3-ch114, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 60.6
Std Dev 2.01
Entry
ampl
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1
1.5
2
2.5
3
0
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20
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40
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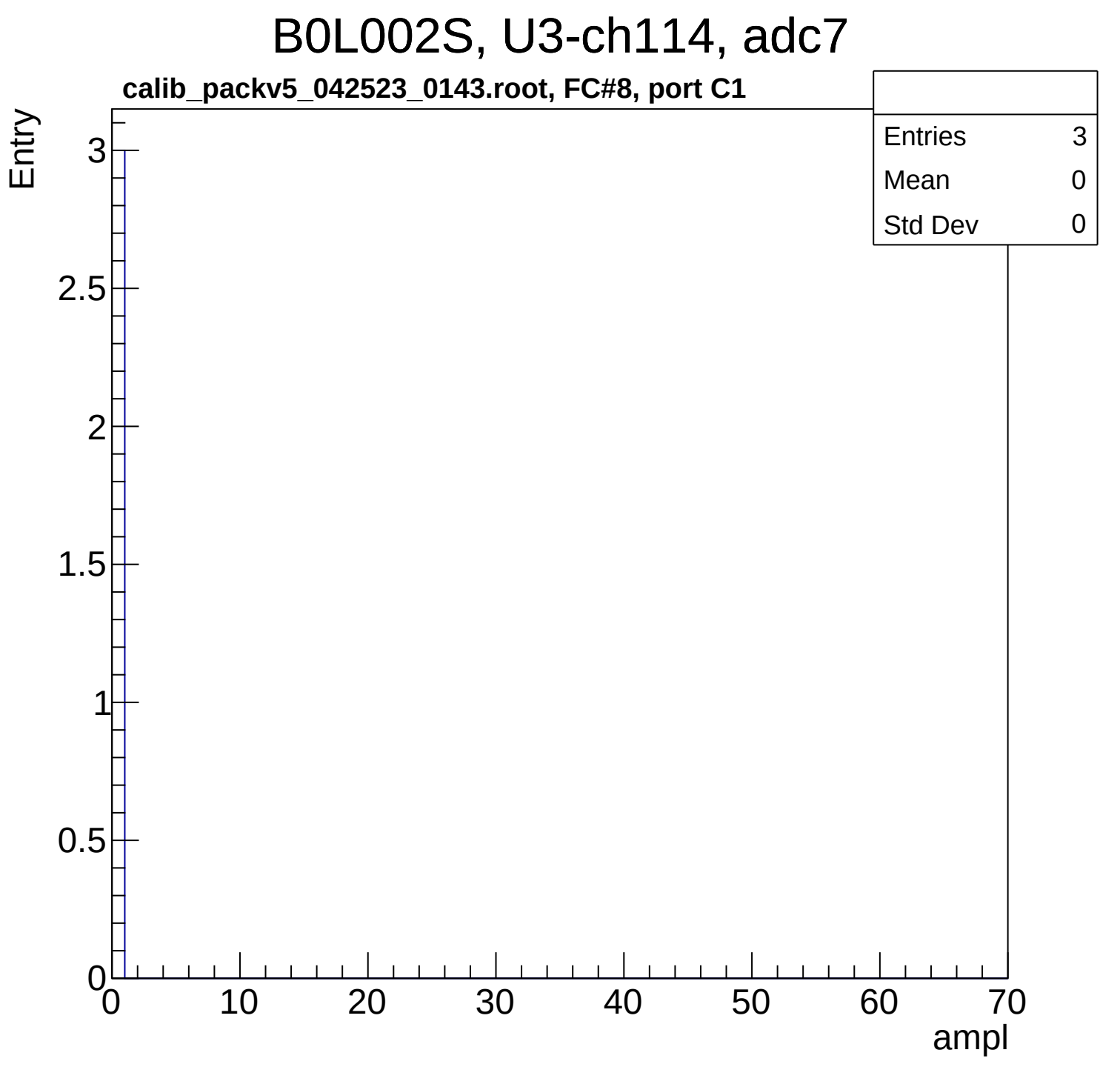
60
70


B0L002S, U3-ch114, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
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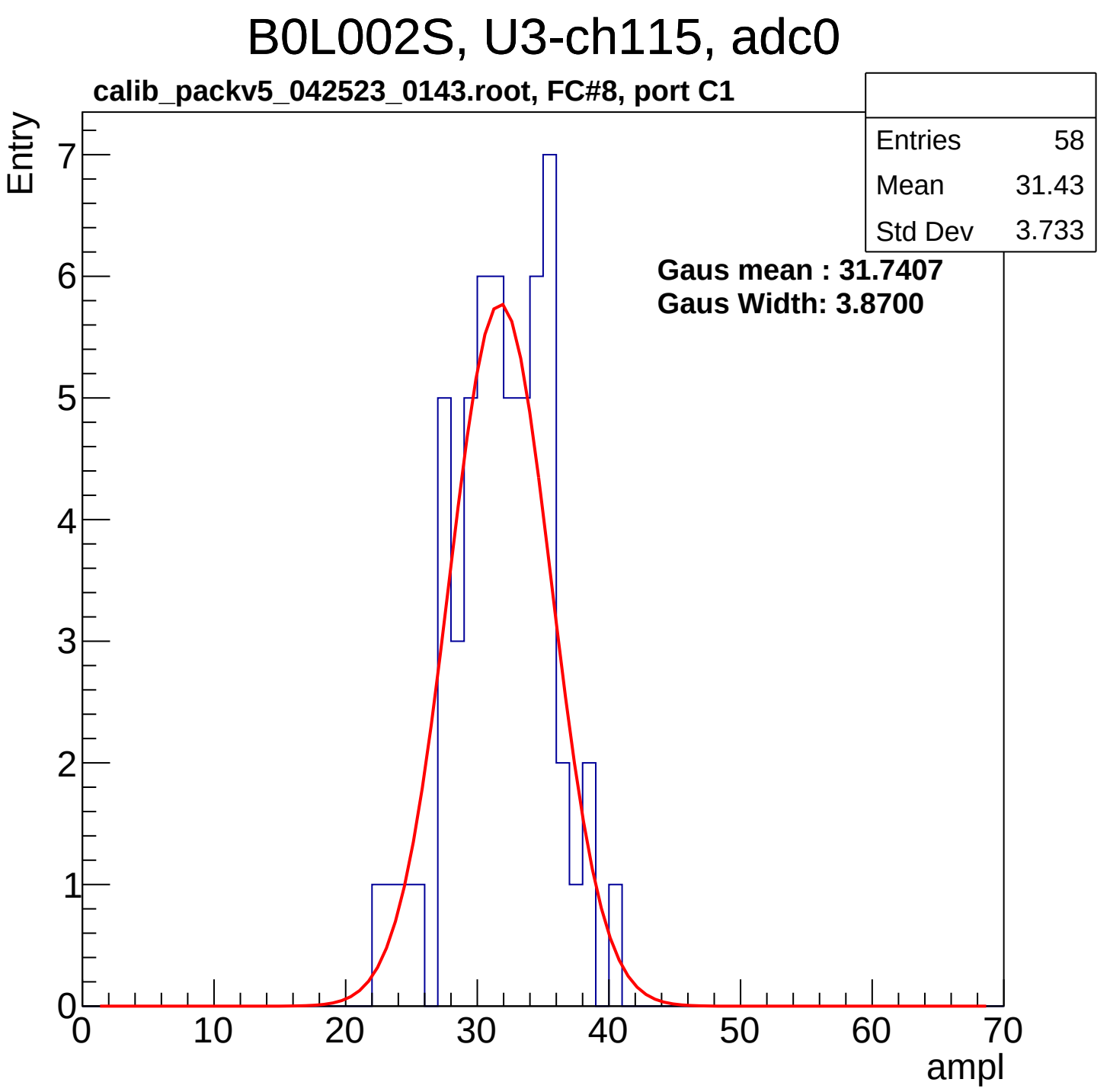
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch115, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 31.43
Std Dev 3.733
Gaus mean : 31.7407
Gaus Width: 3.8700
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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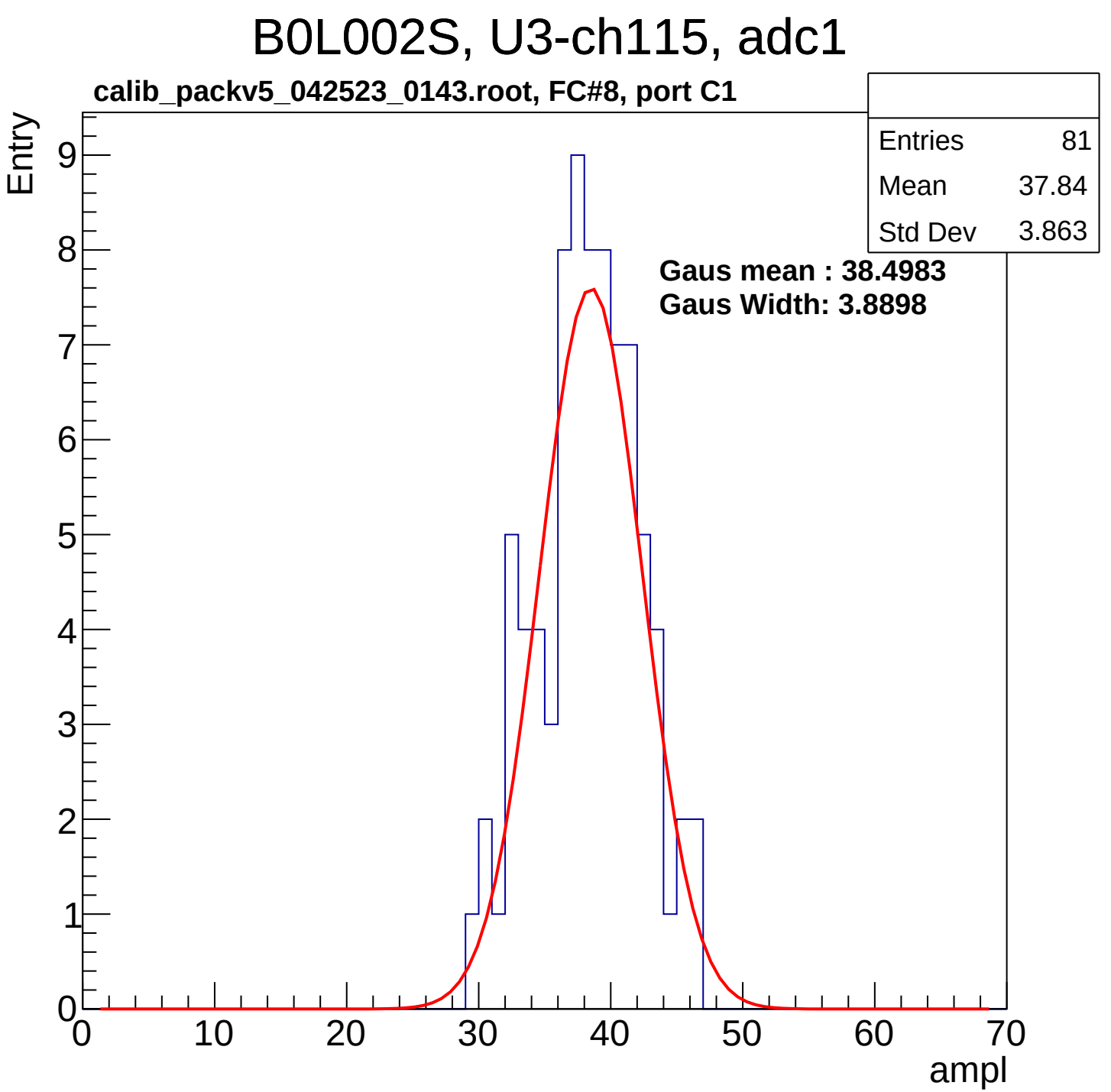
B0L002S, U3-ch115, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 37.84
Std Dev 3.863
Gaus mean : 38.4983
Gaus Width: 3.8898
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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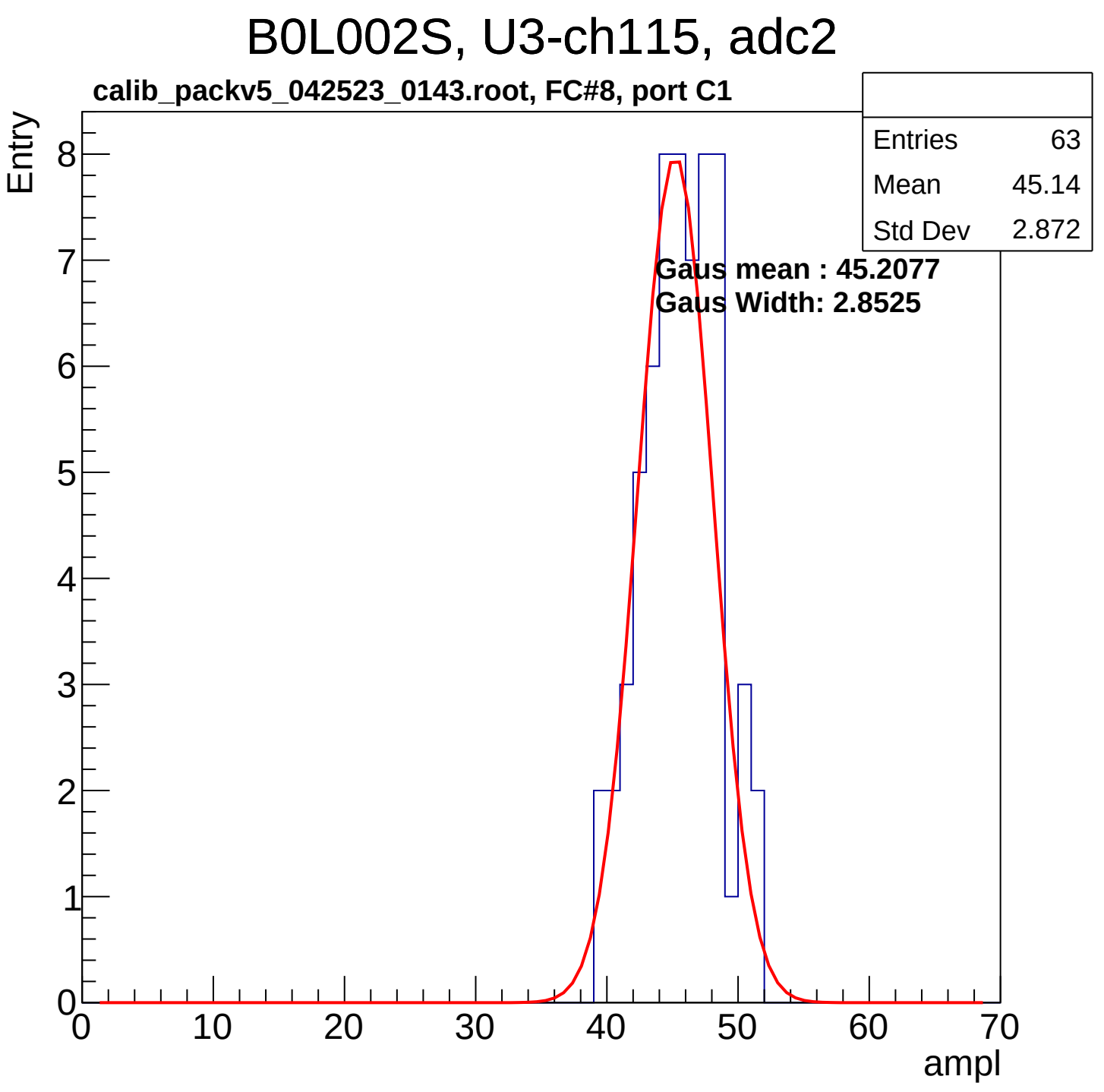
B0L002S, U3-ch115, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 45.14
Std Dev 2.872
Gaus mean : 45.2077
Gaus Width: 2.8525
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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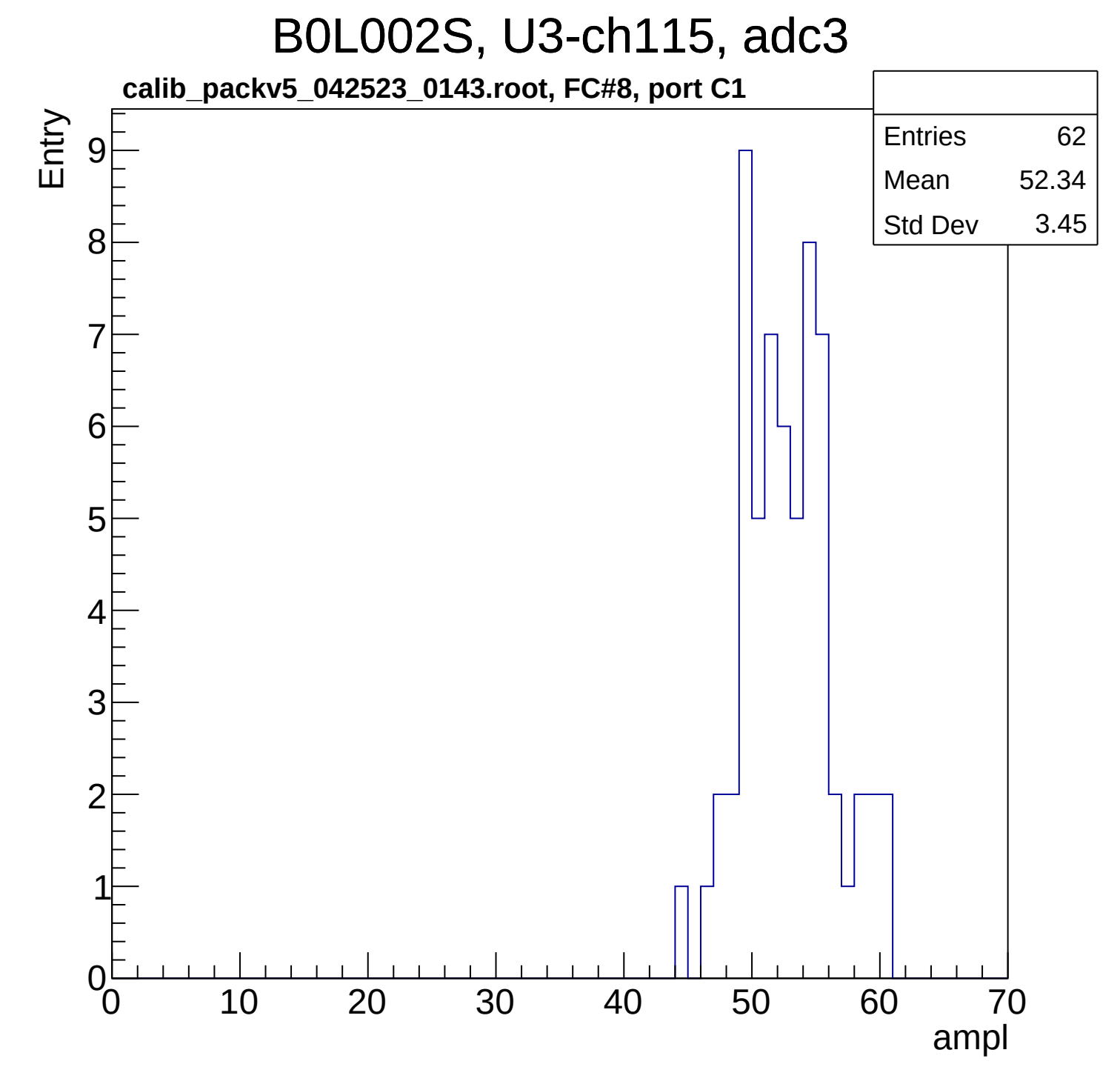
B0L002S, U3-ch115, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 52.34
Std Dev 3.45
Entry
ampl
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8
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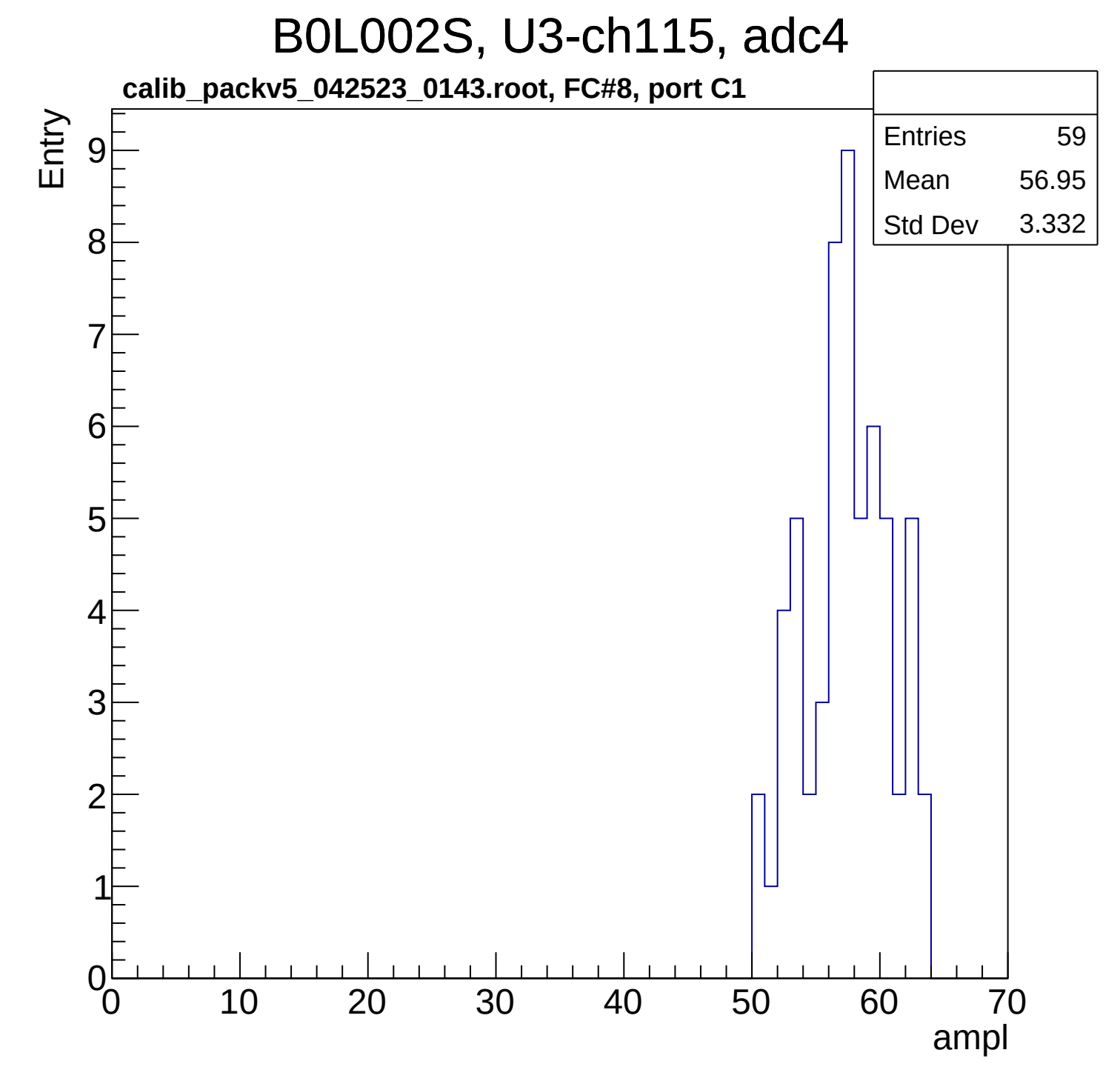
B0L002S, U3-ch115, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 56.95
Std Dev 3.332
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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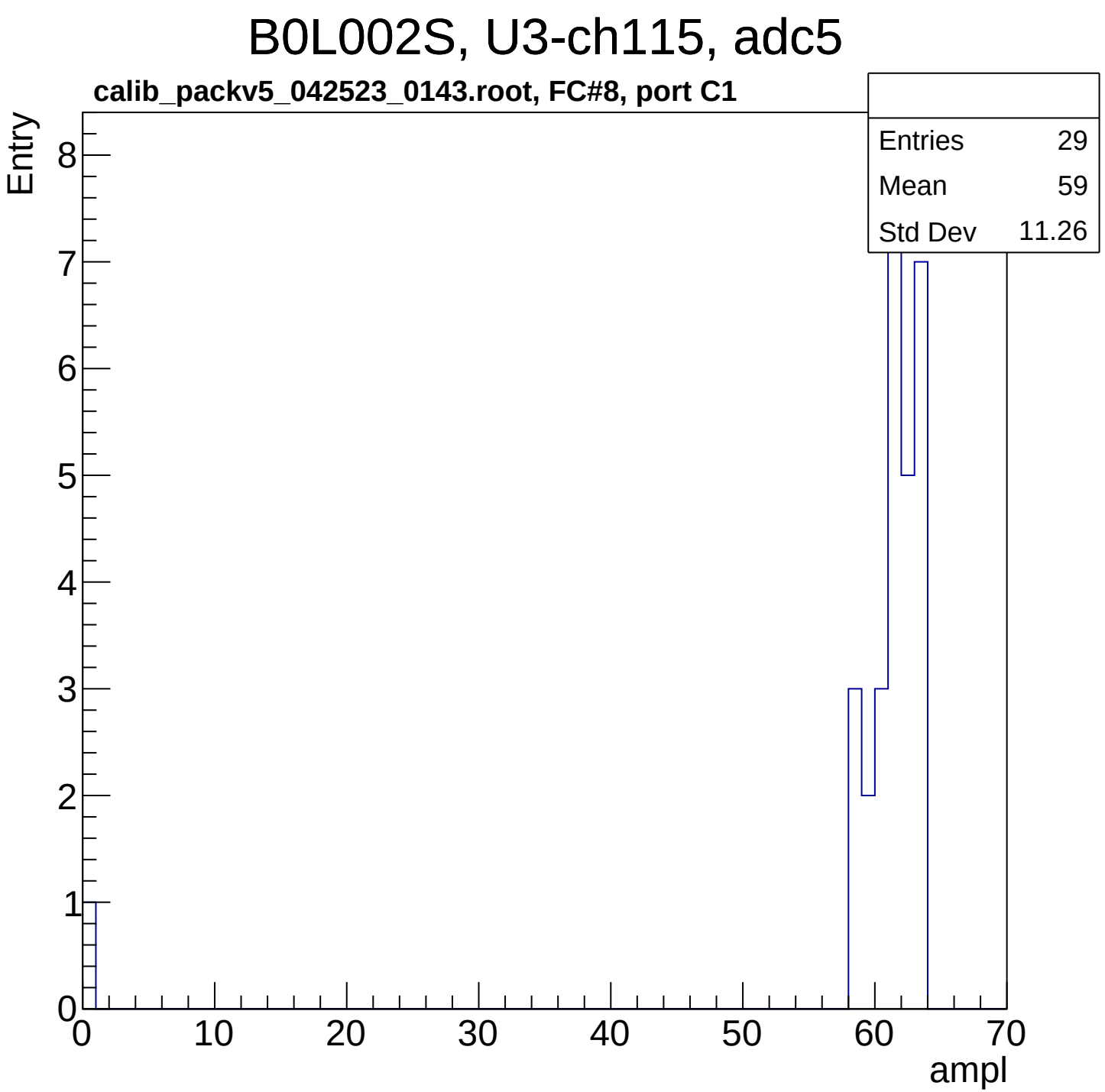
B0L002S, U3-ch115, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59
Std Dev 11.26
Entry
ampl
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2
3
4
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6
7
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0
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20
30
40
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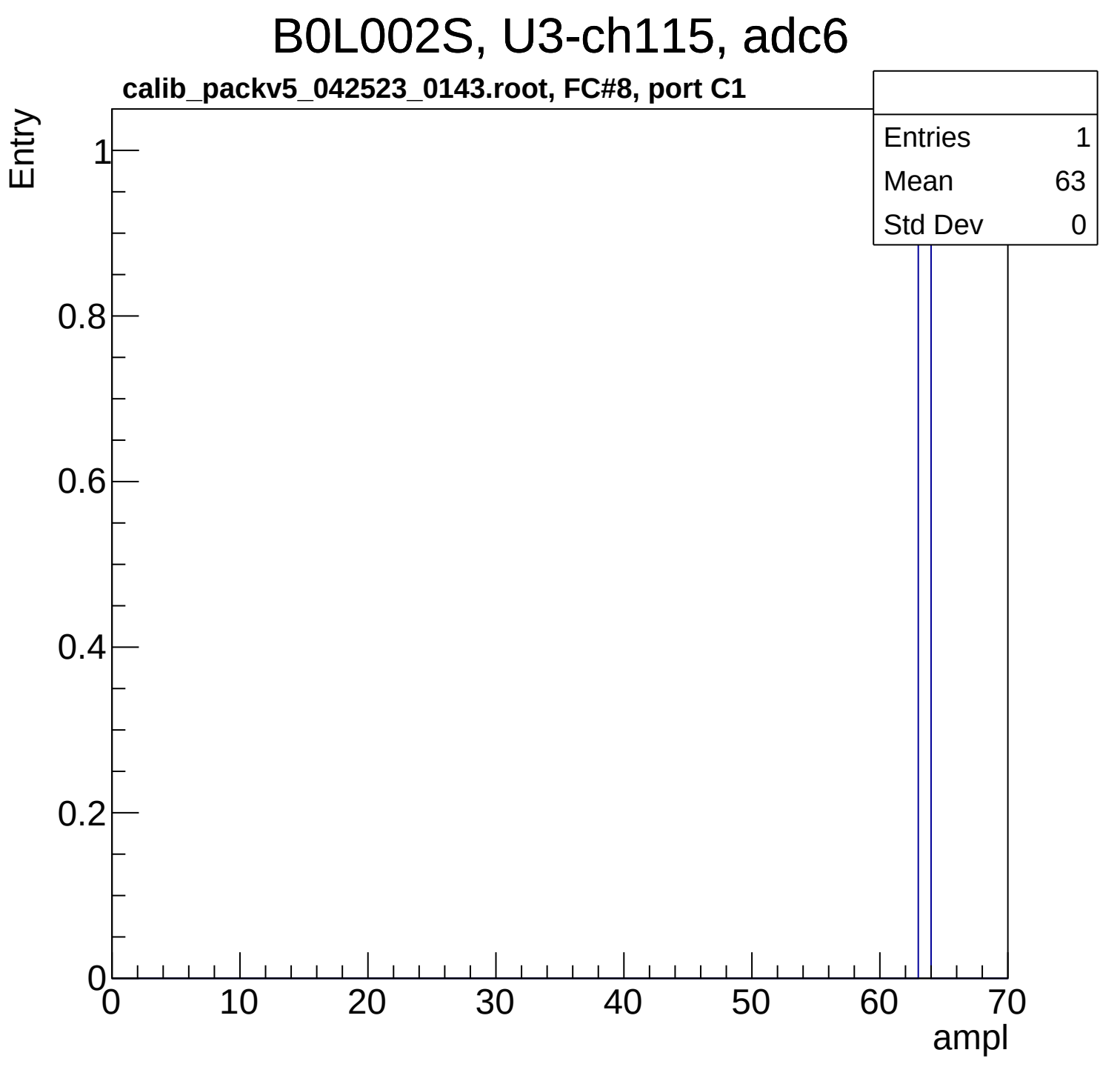
B0L002S, U3-ch115, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
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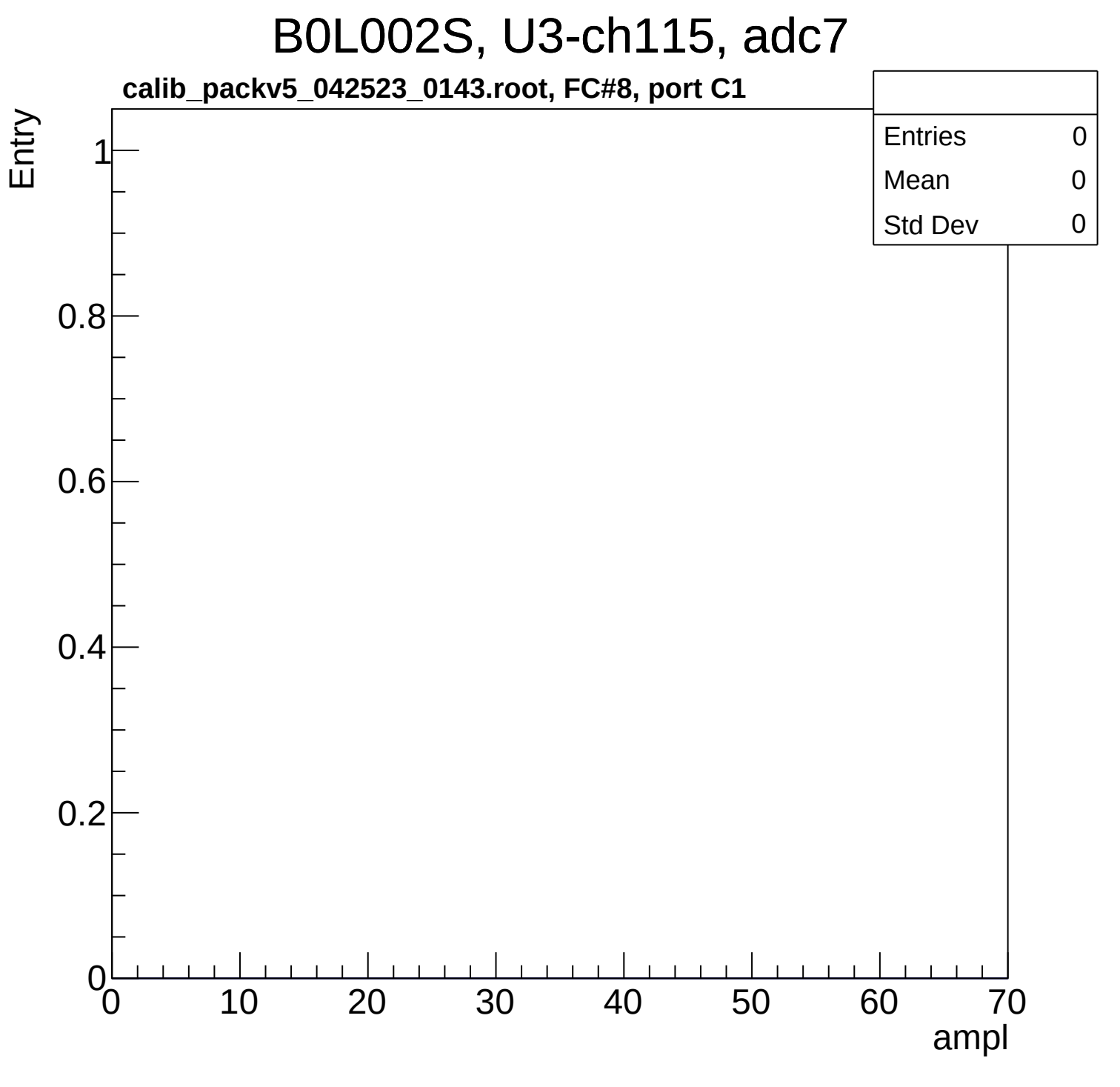
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch115, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
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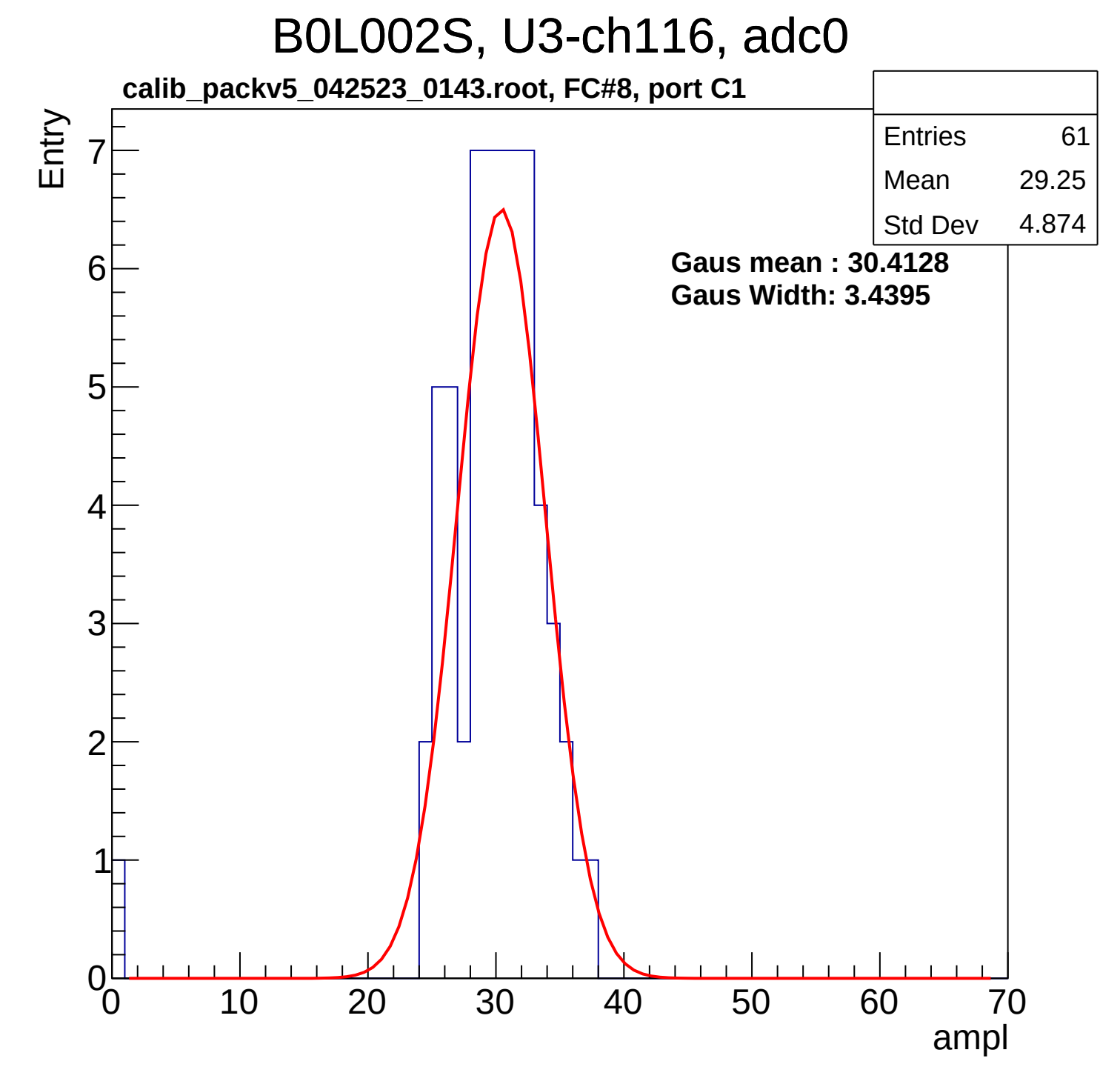
70
ampl


B0L002S, U3-ch116, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 29.25
Std Dev 4.874
Gaus mean : 30.4128
Gaus Width: 3.4395
Entry
ampl
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2
3
4
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0
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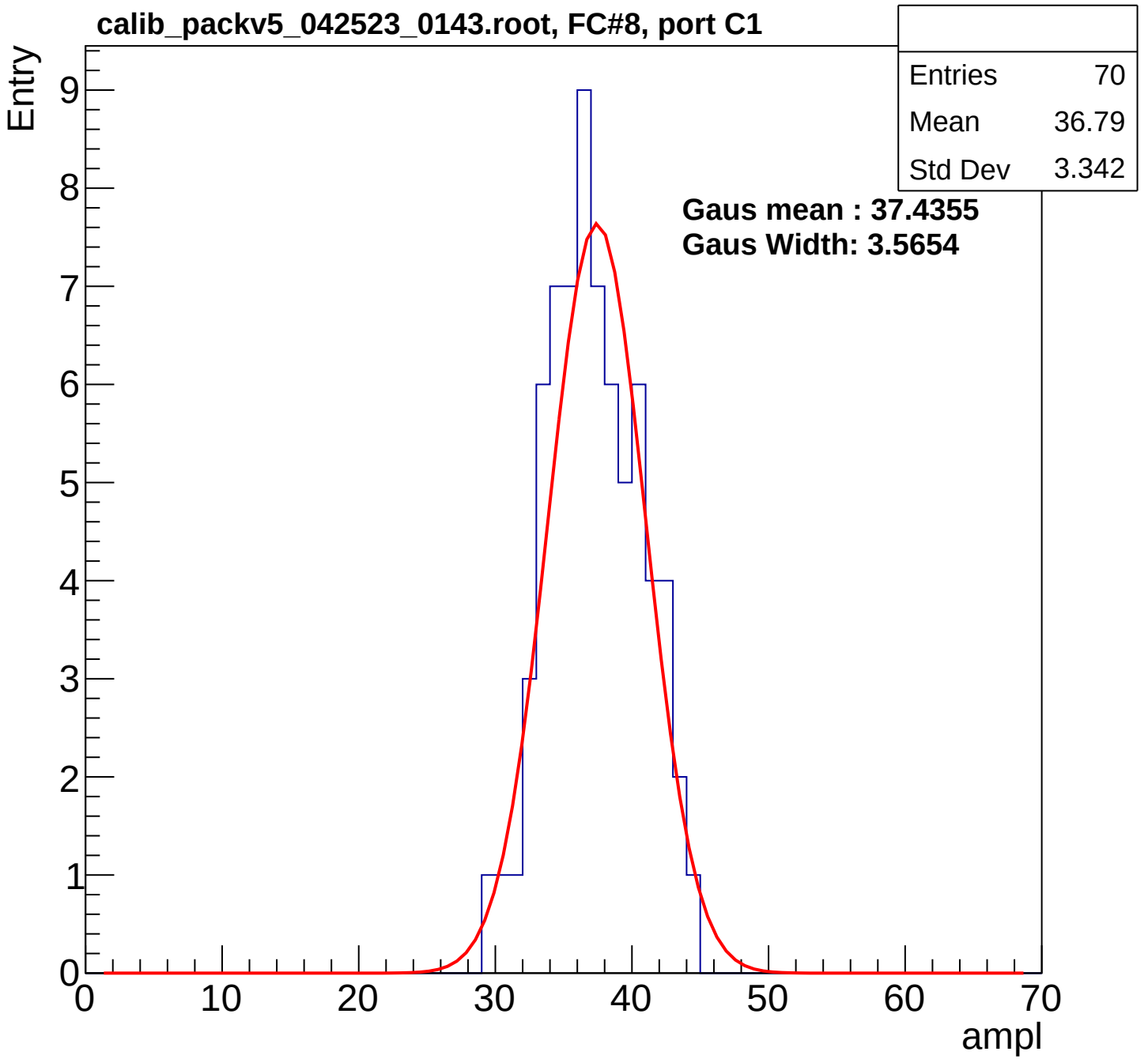
B0L002S, U3-ch116, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.79
Std Dev 3.342
Gaus mean : 37.4355
Gaus Width: 3.5654
Entry
ampl
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3
4
5
6
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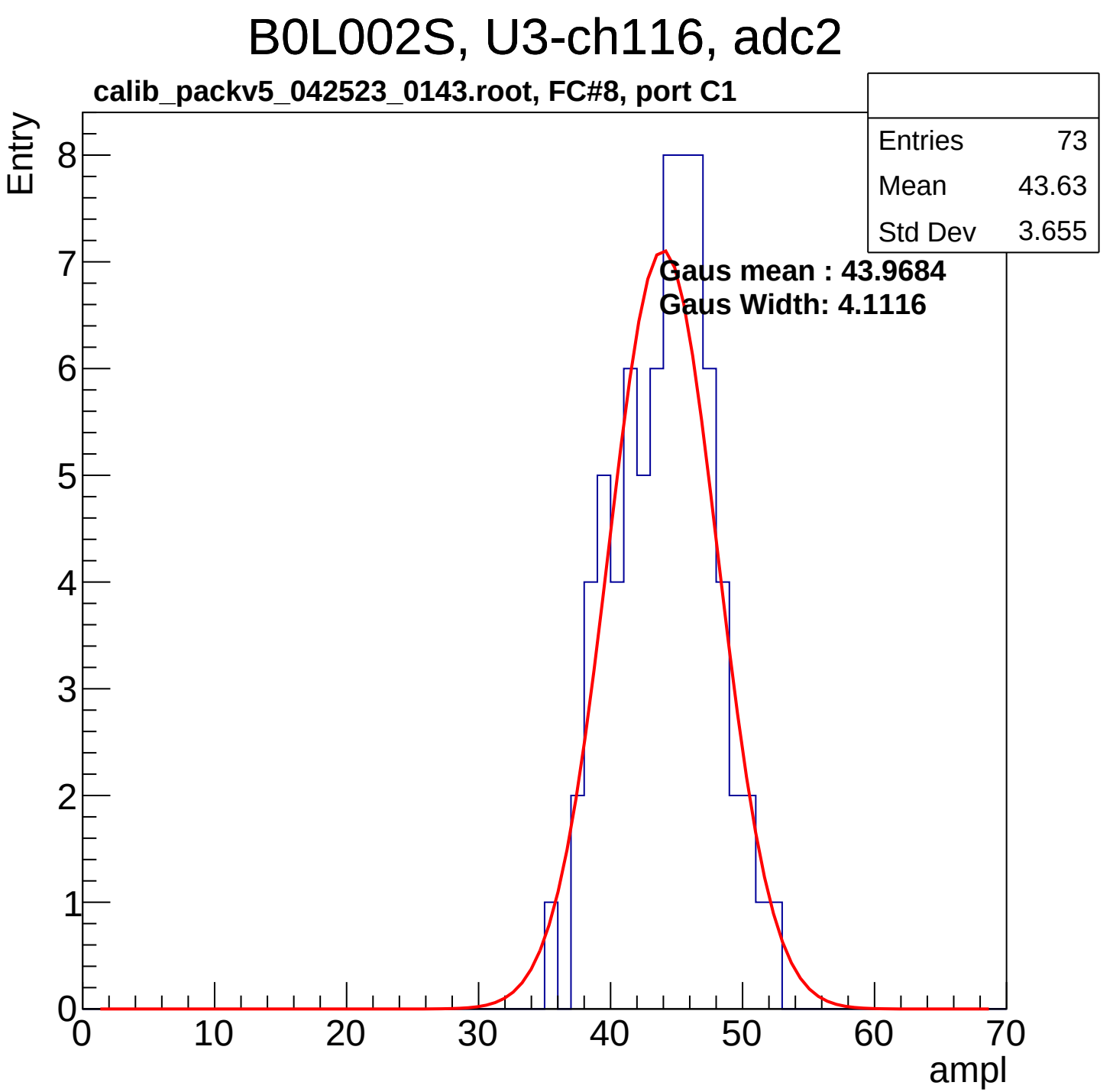
B0L002S, U3-ch116, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 43.63
Std Dev 3.655
Gaus mean : 43.9684
Gaus Width: 4.1116
Entry
ampl
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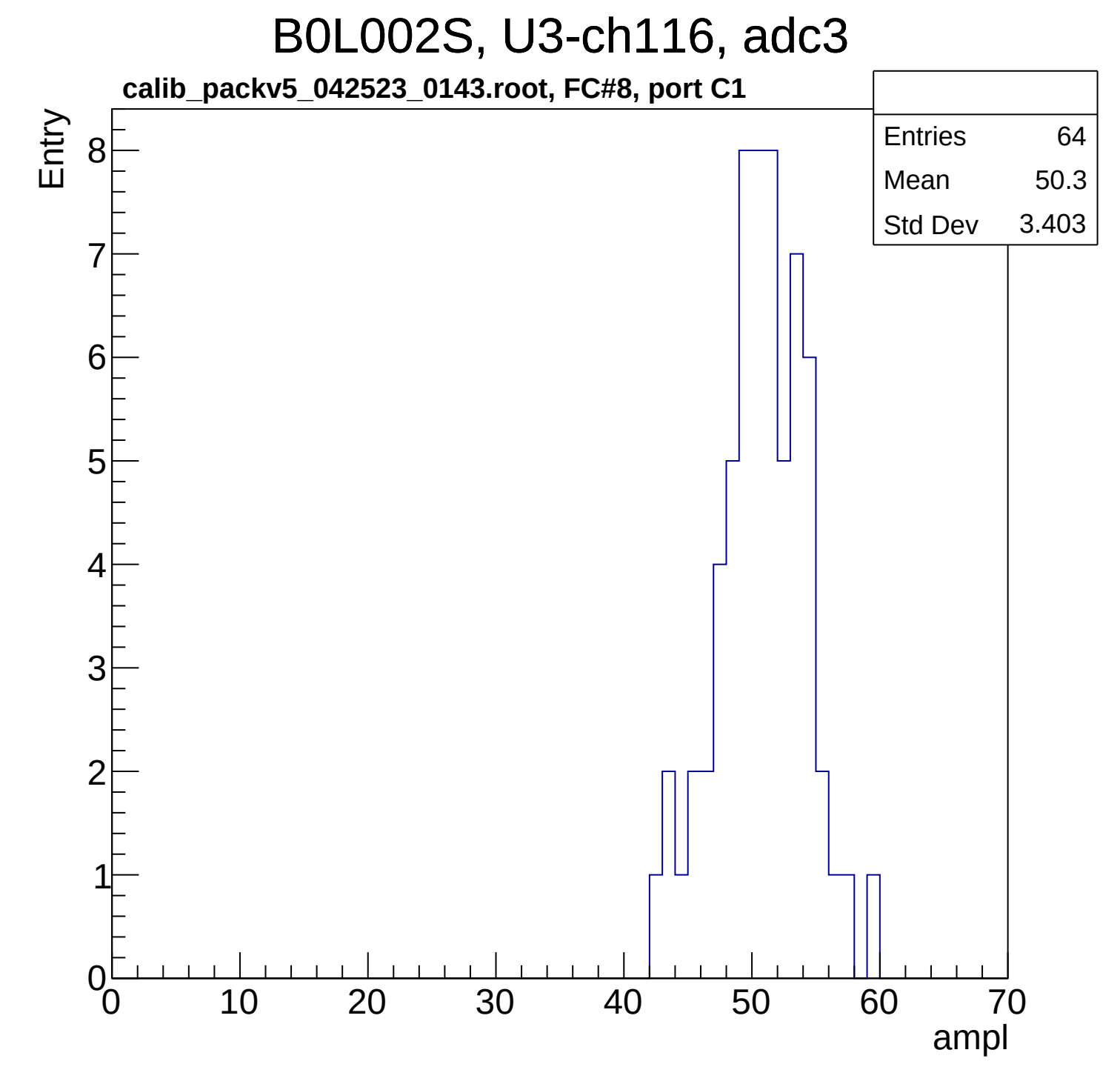
B0L002S, U3-ch116, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.3
Std Dev 3.403
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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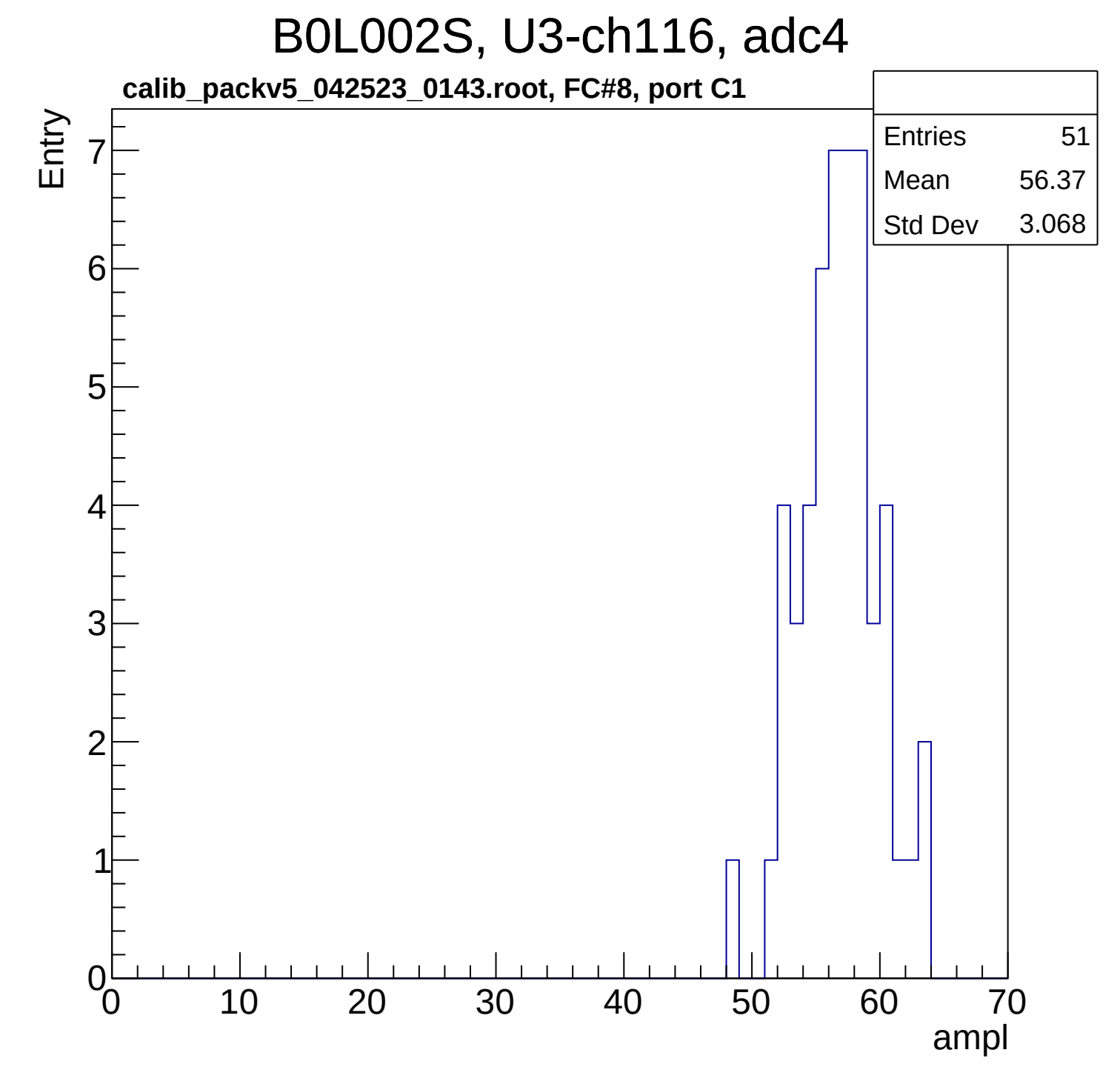
B0L002S, U3-ch116, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.37
Std Dev 3.068
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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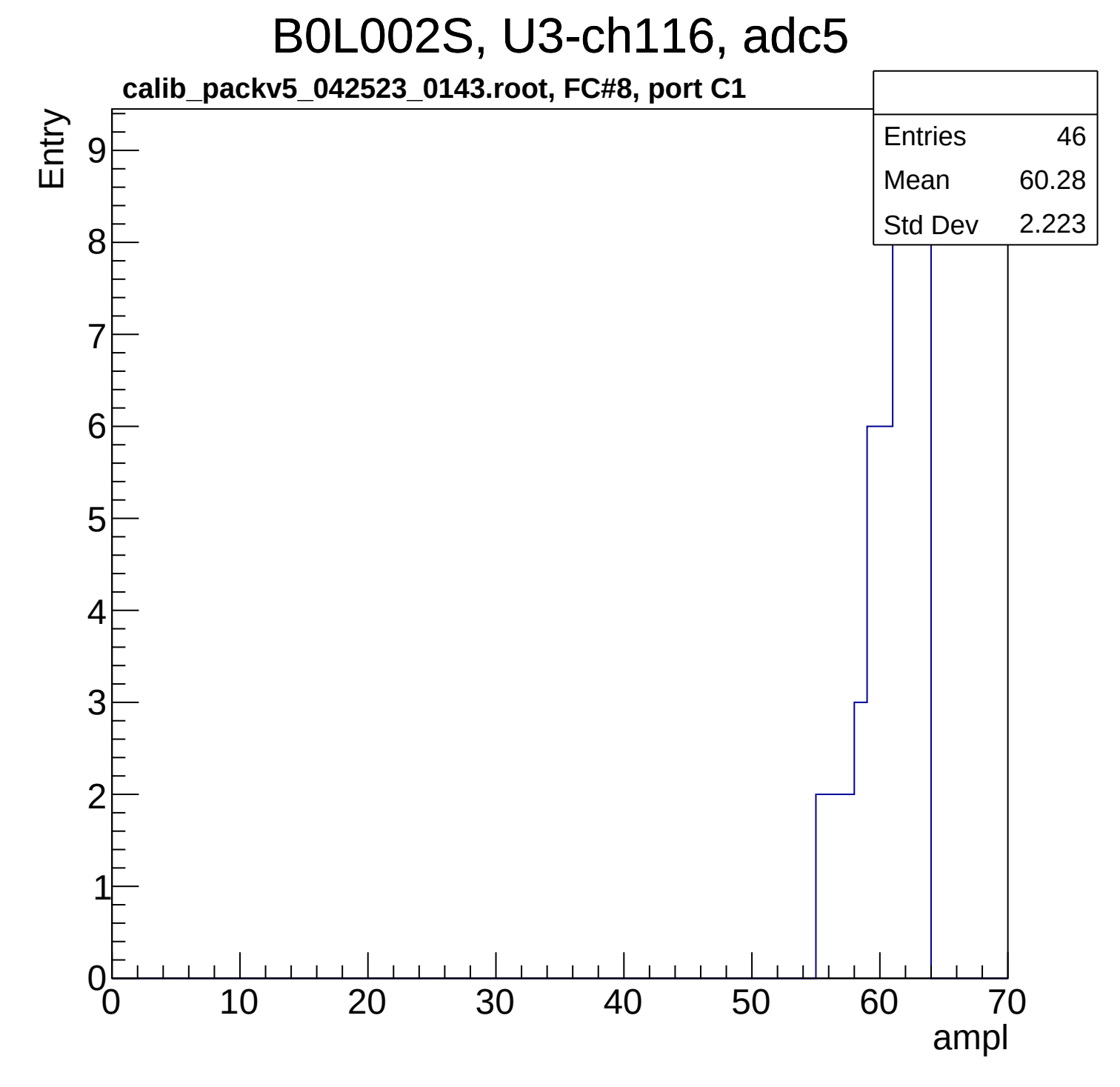
B0L002S, U3-ch116, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 60.28
Std Dev 2.223
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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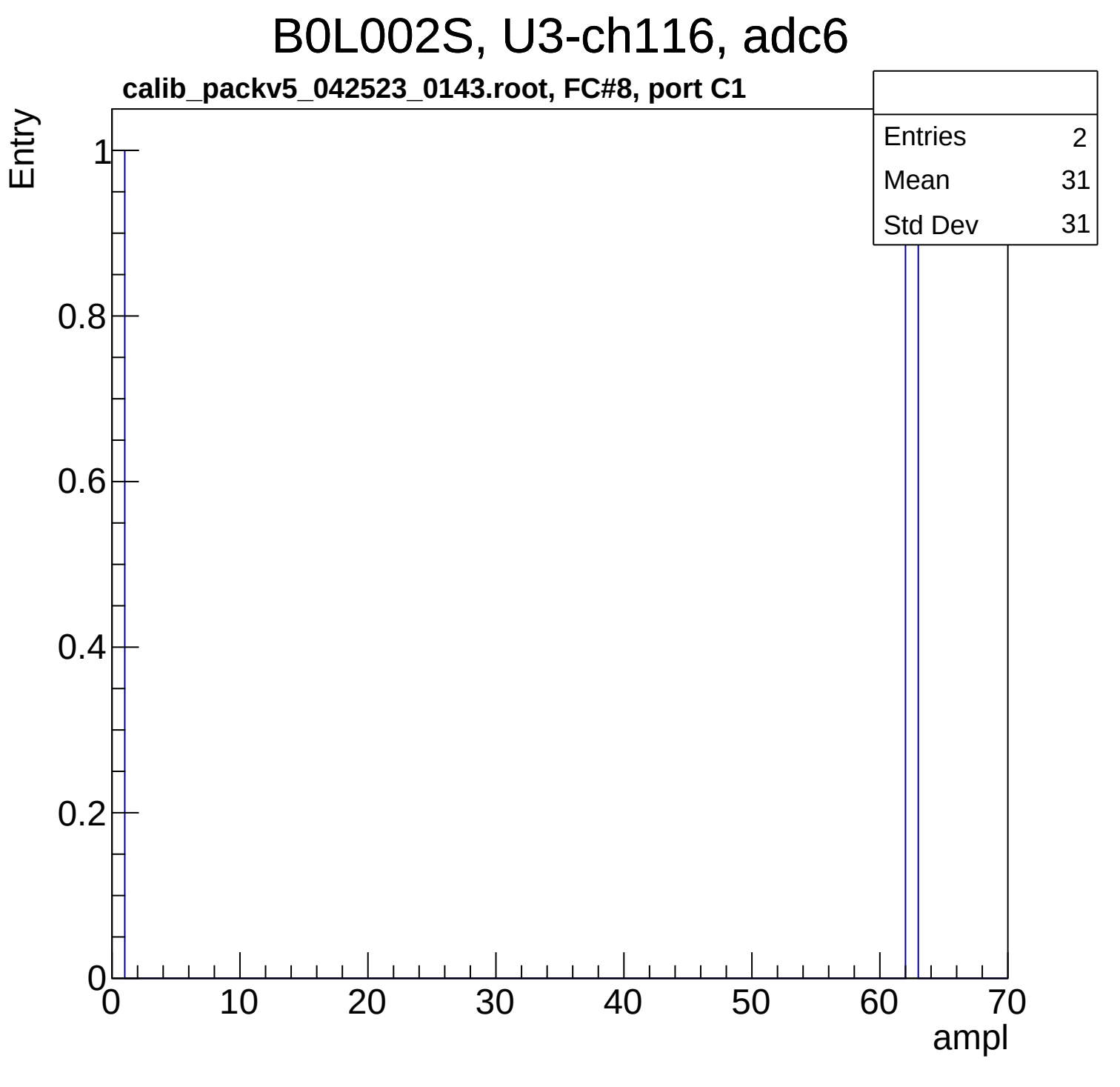
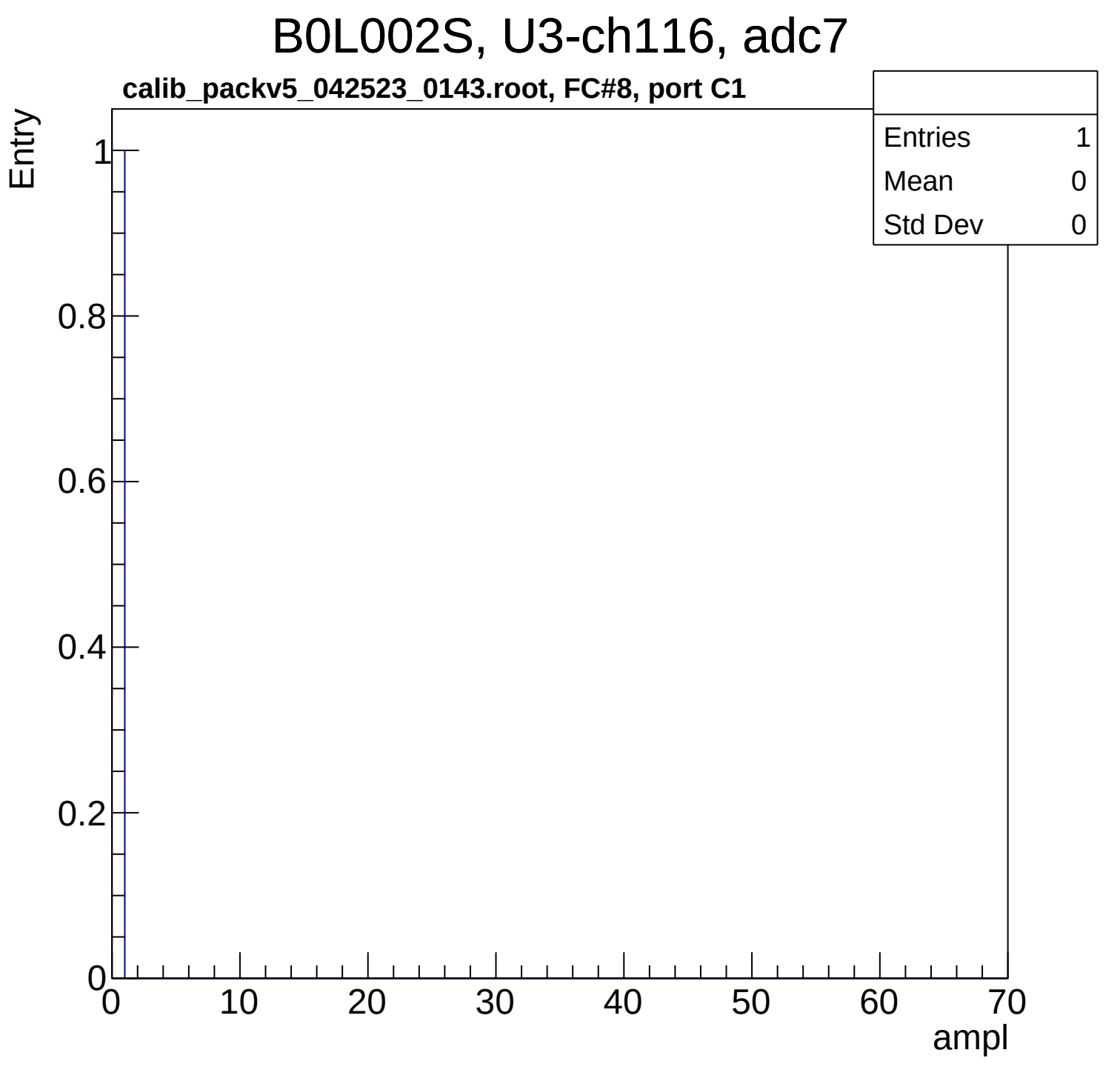

B0L002S, U3-ch116, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch116, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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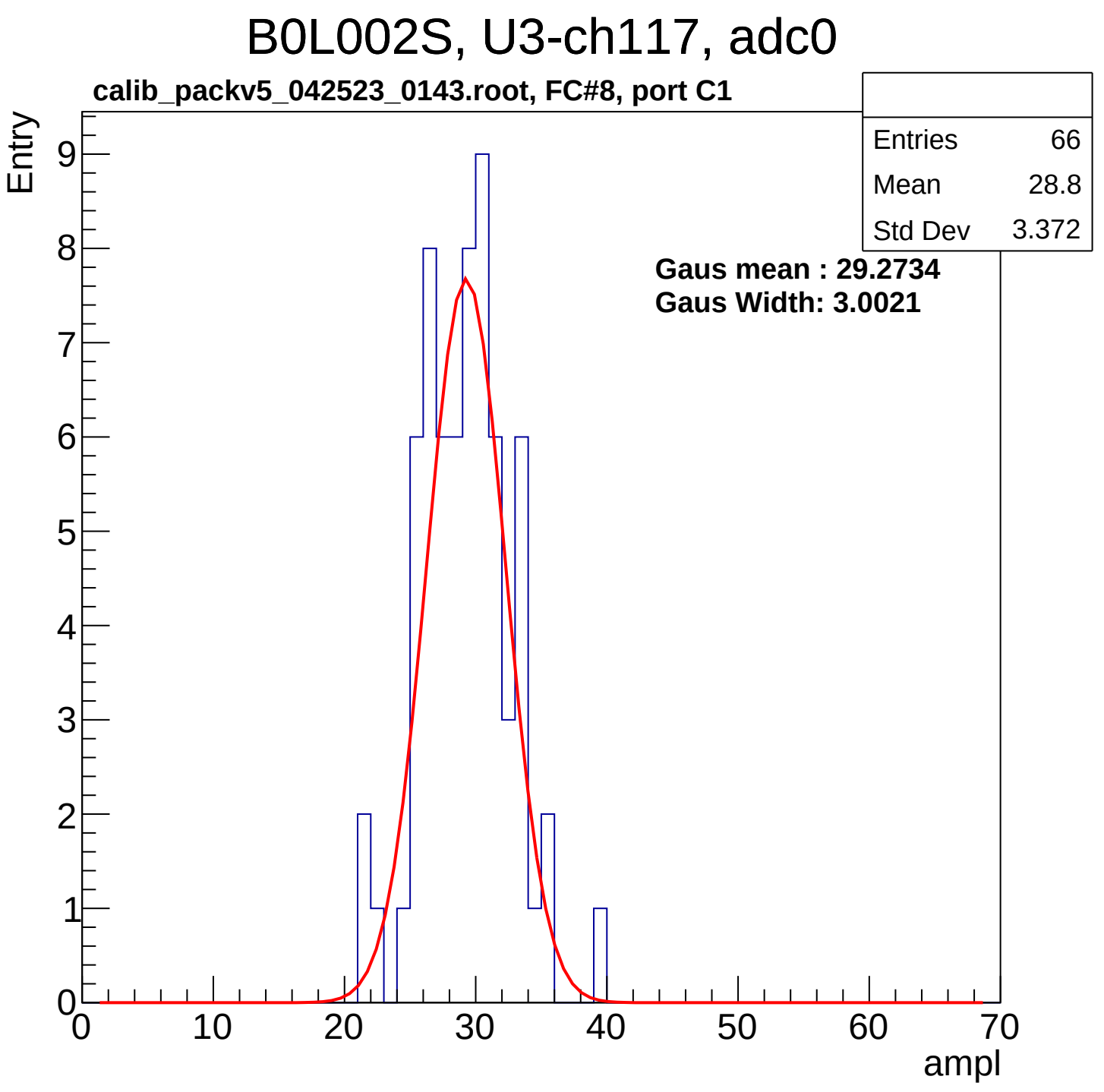
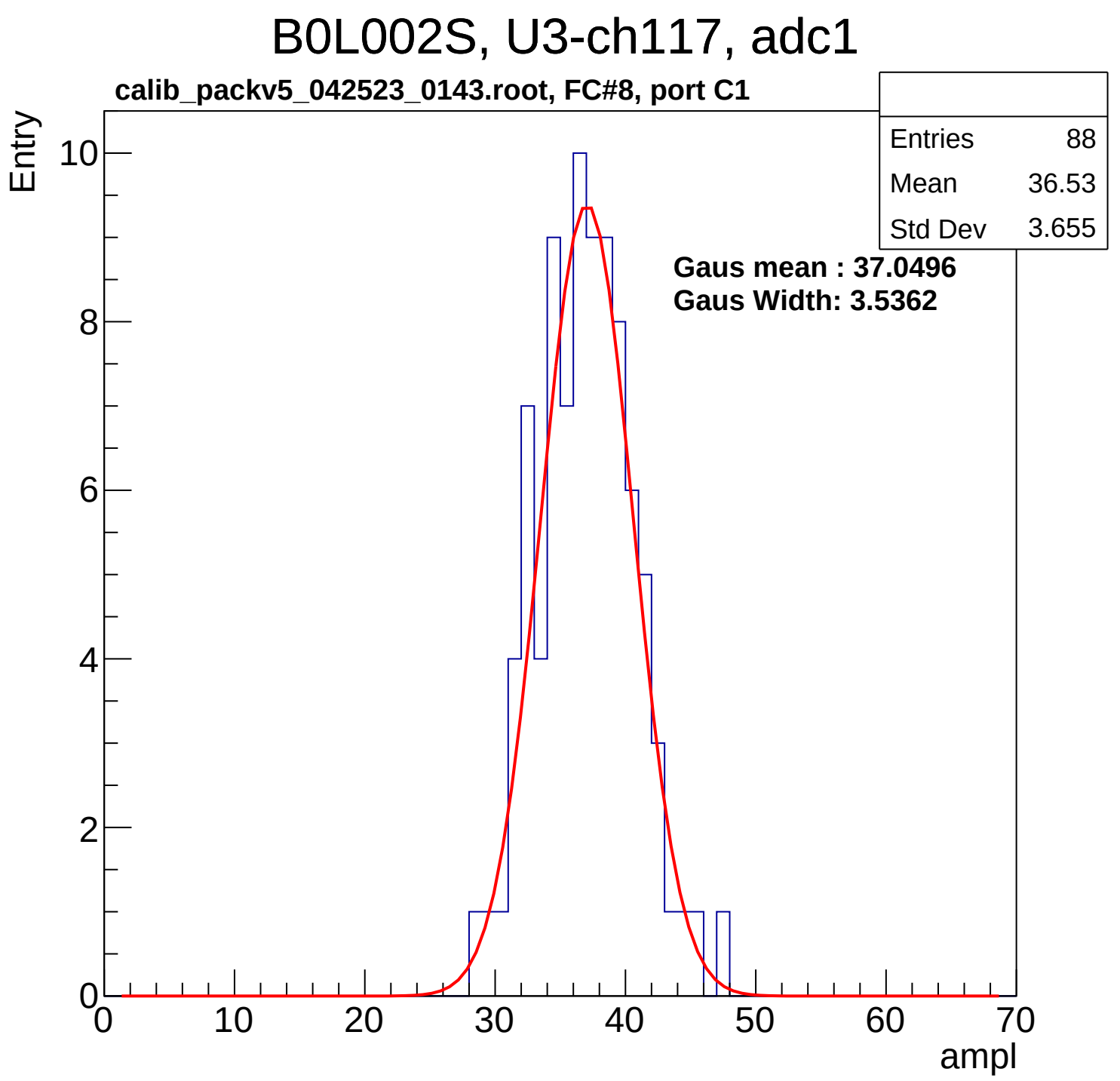
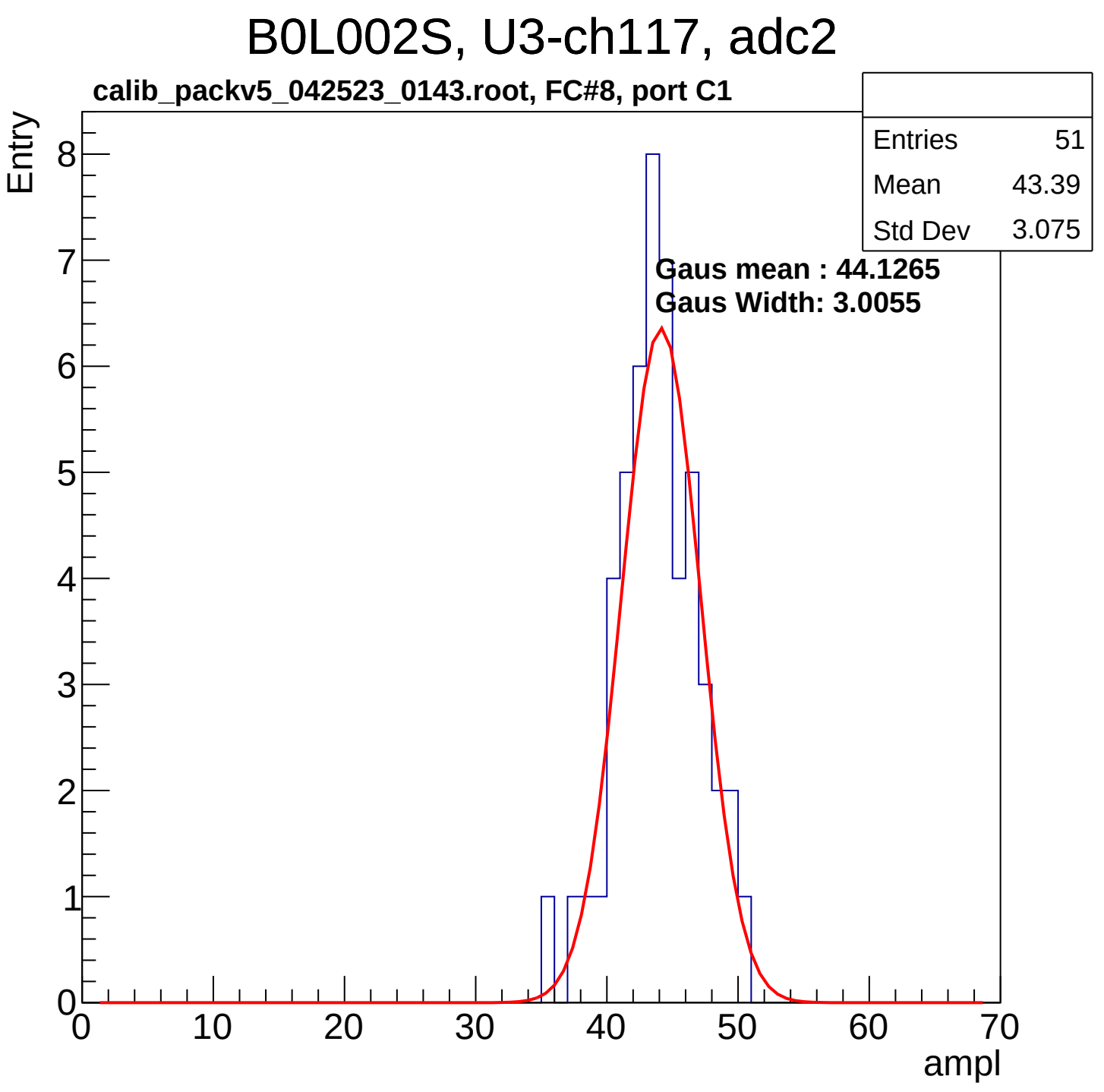
40
50
60
70
ampl


B0L002S, U3-ch117, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 28.8
Std Dev 3.372
Gaus mean : 29.2734
Gaus Width: 3.0021
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch117, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 36.53
Std Dev 3.655
Gaus mean : 37.0496
Gaus Width: 3.5362
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch117, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 43.39
Std Dev 3.075
Gaus mean : 44.1265
Gaus Width: 3.0055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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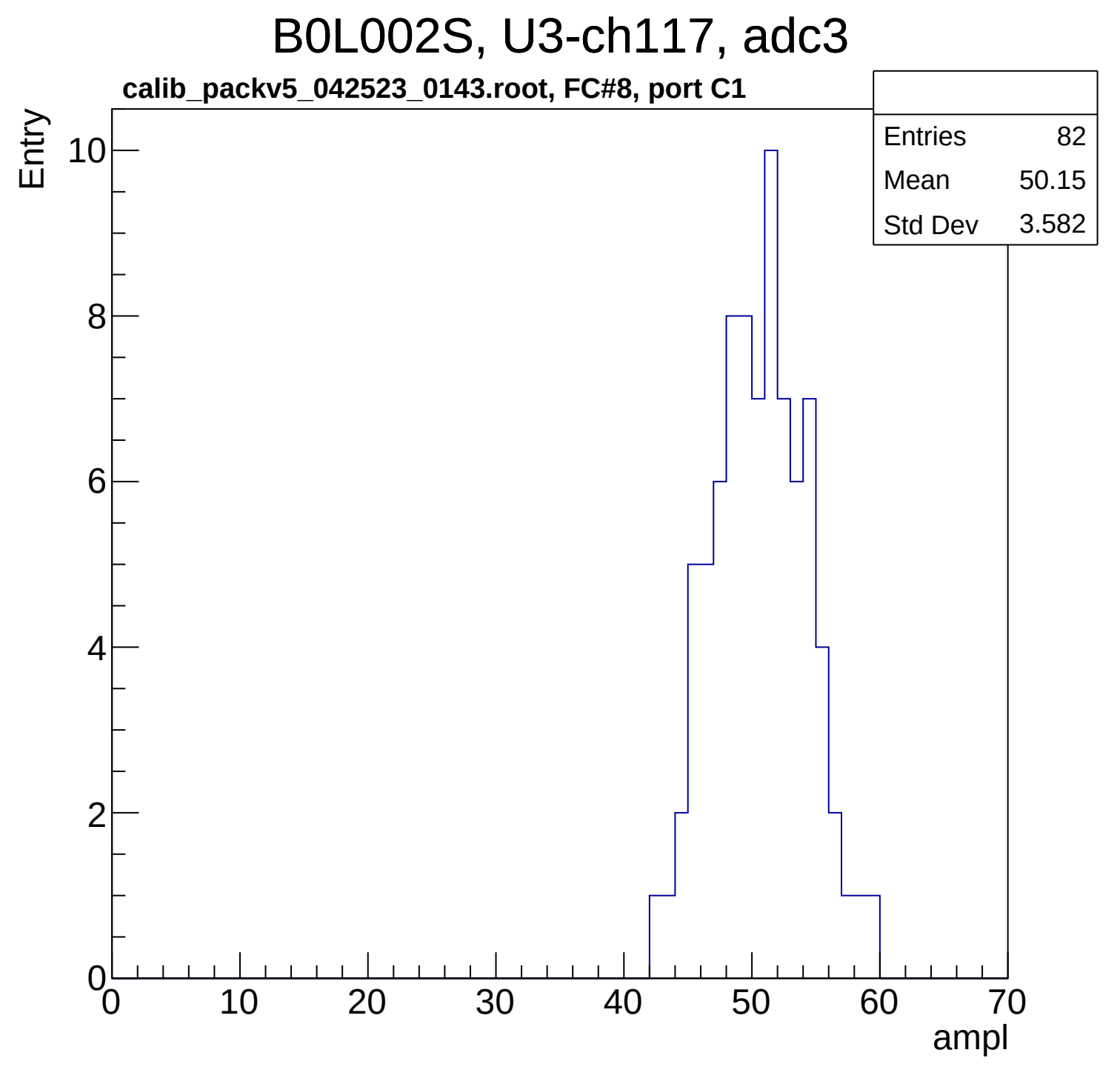
B0L002S, U3-ch117, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 50.15
Std Dev 3.582
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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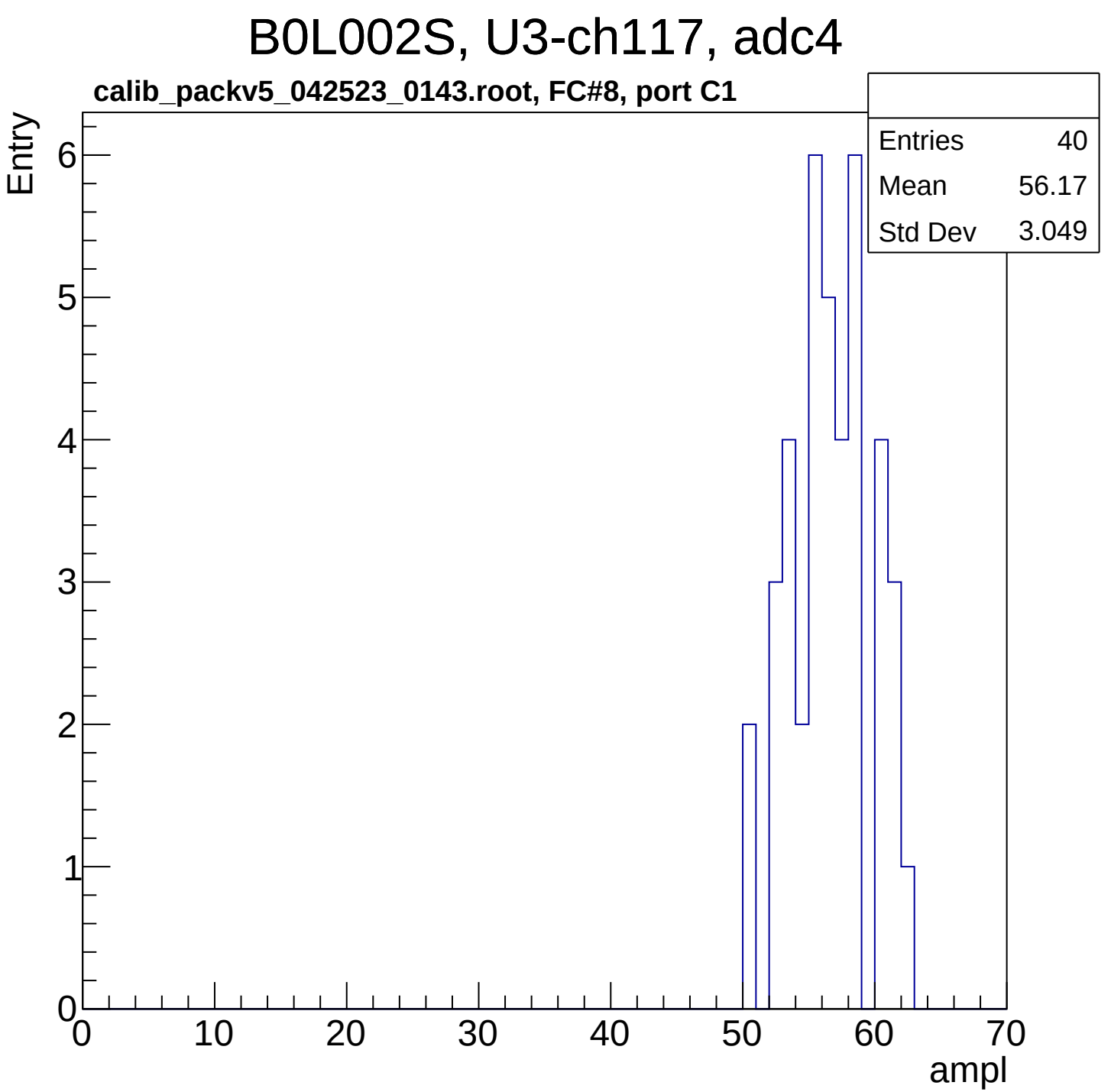
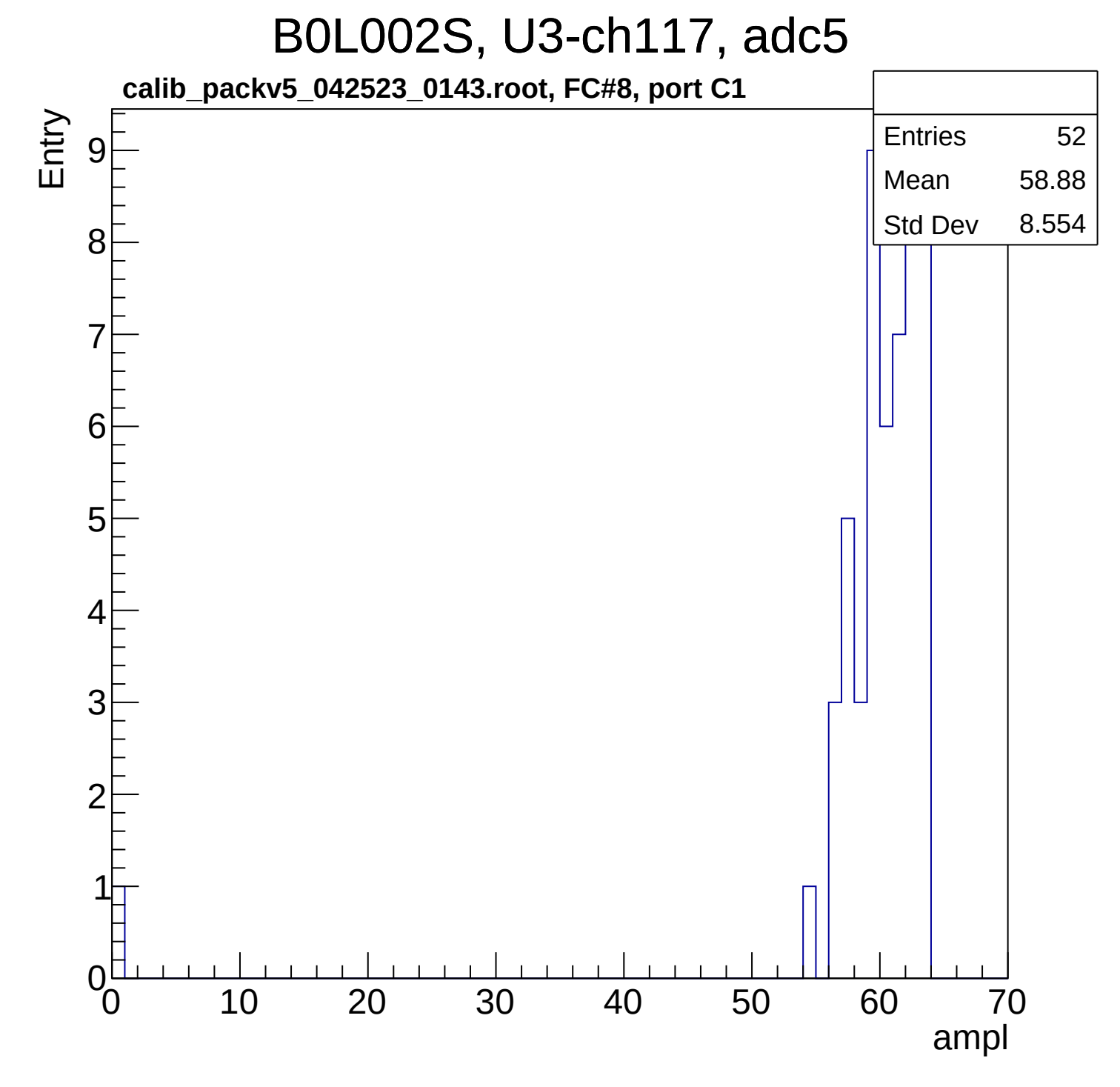

B0L002S, U3-ch117, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 56.17
Std Dev 3.049
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch117, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.88
Std Dev 8.554
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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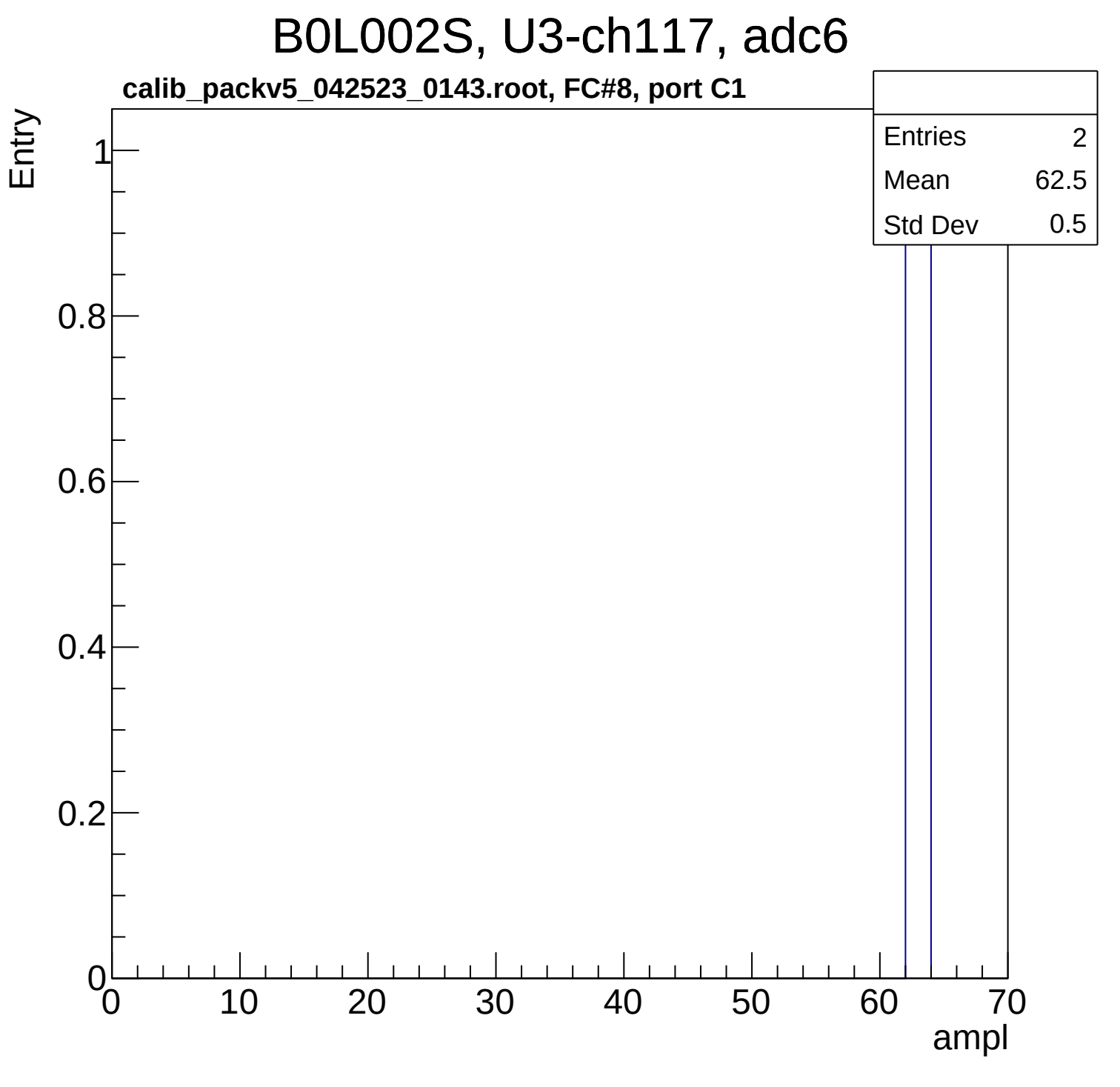
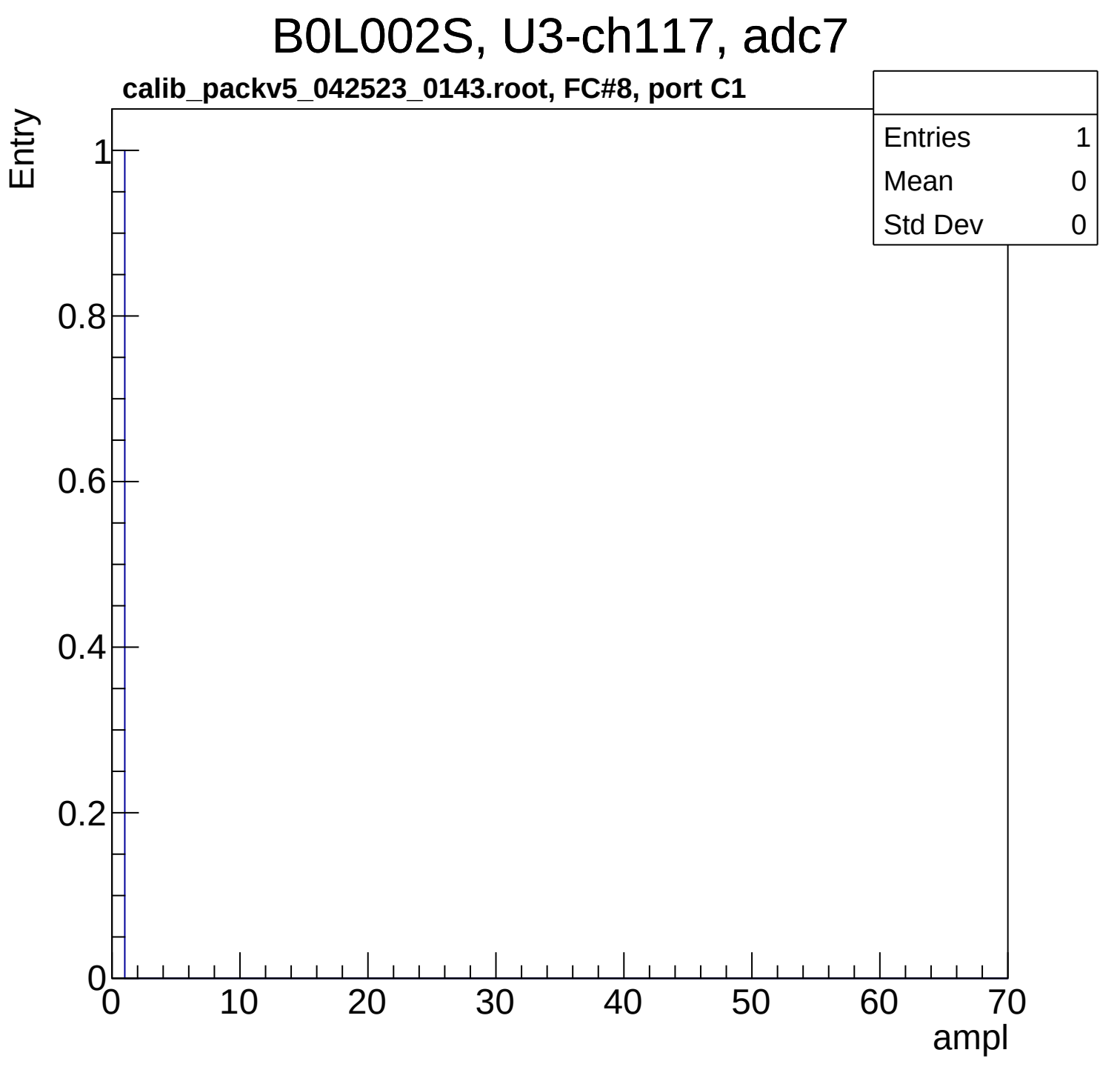
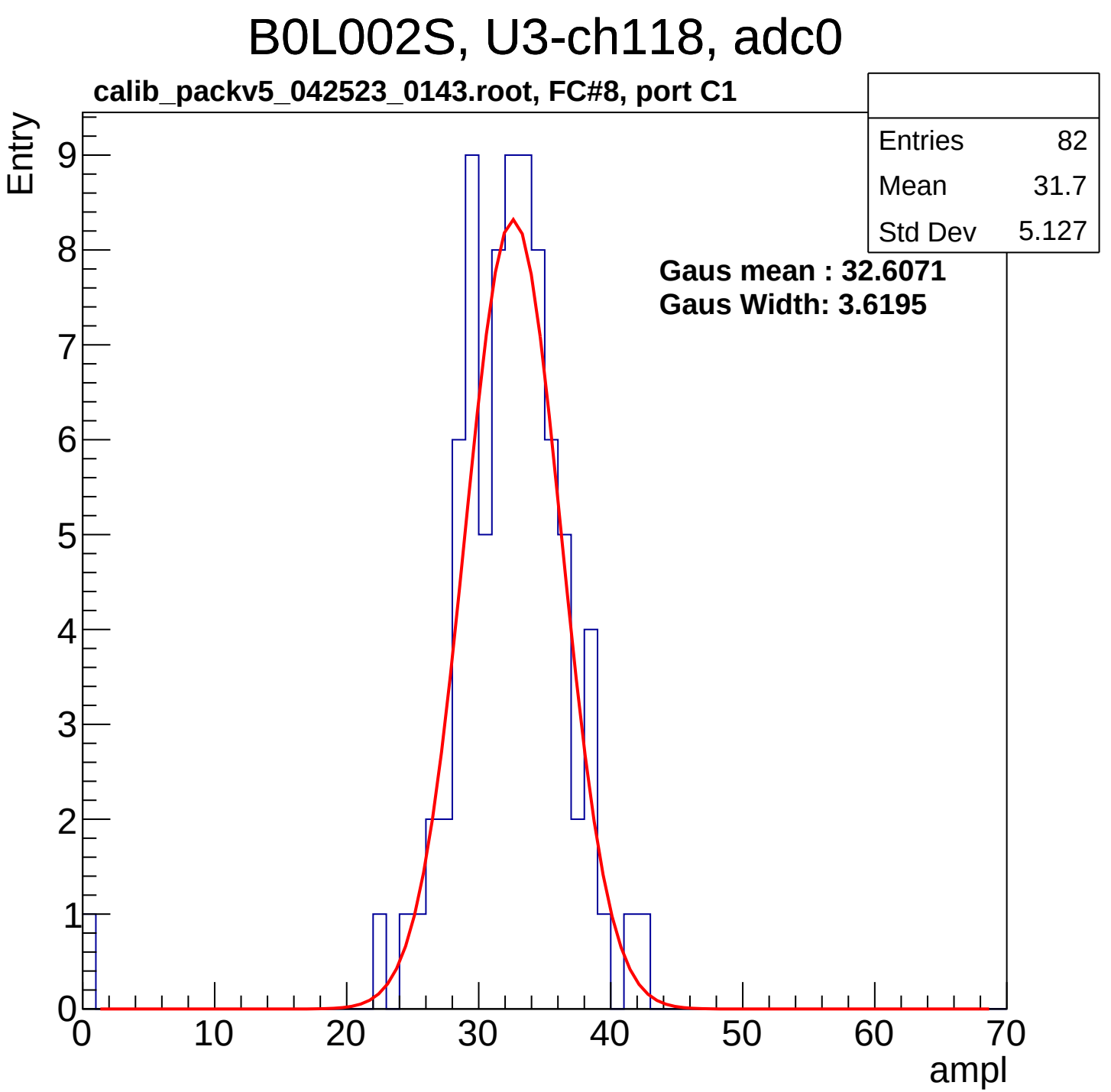

B0L002S, U3-ch117, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch117, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch118, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 31.7
Std Dev 5.127
Gaus mean : 32.6071
Gaus Width: 3.6195
Entry
ampl
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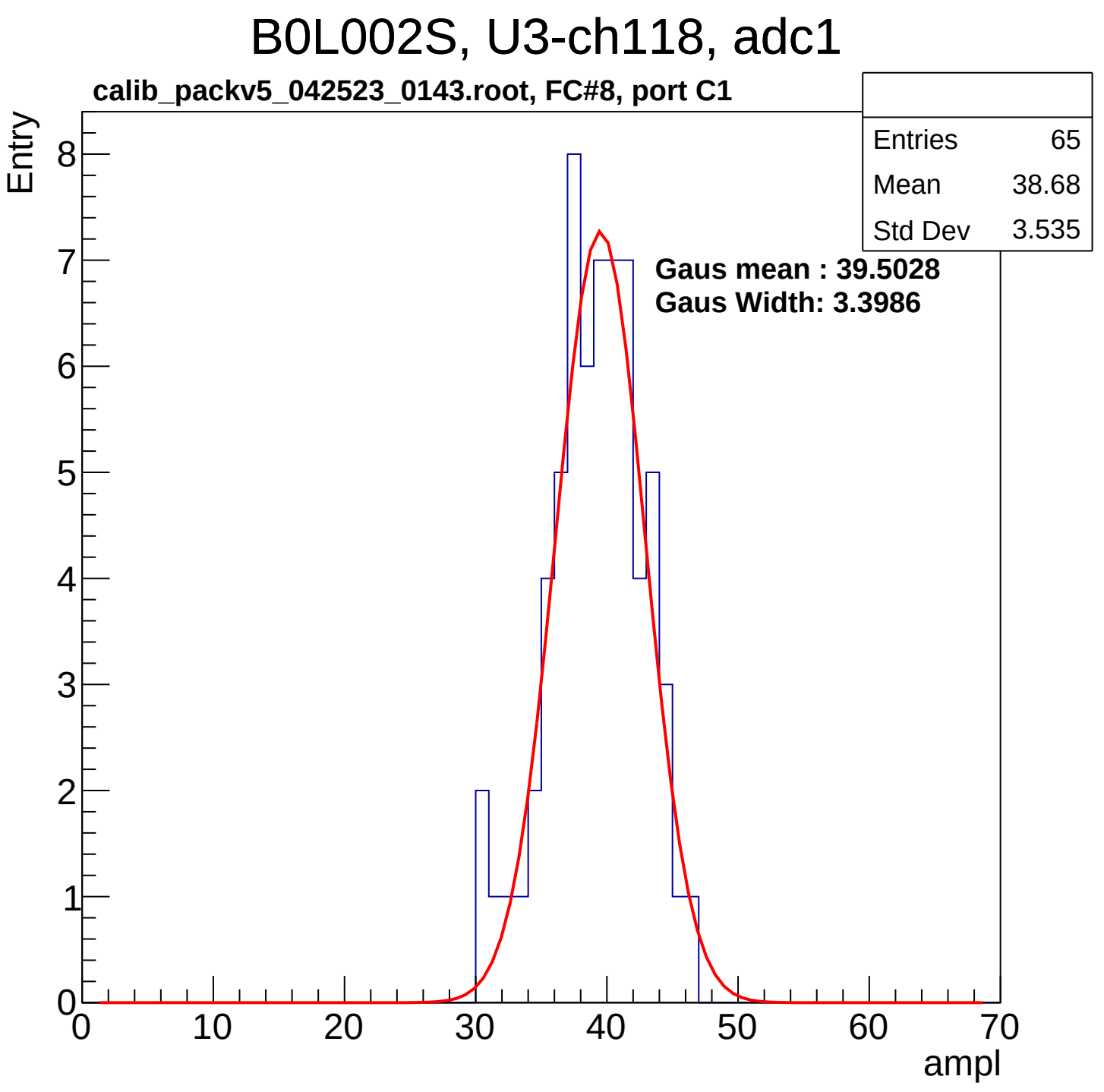
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch118, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 38.68
Std Dev 3.535
Gaus mean : 39.5028
Gaus Width: 3.3986
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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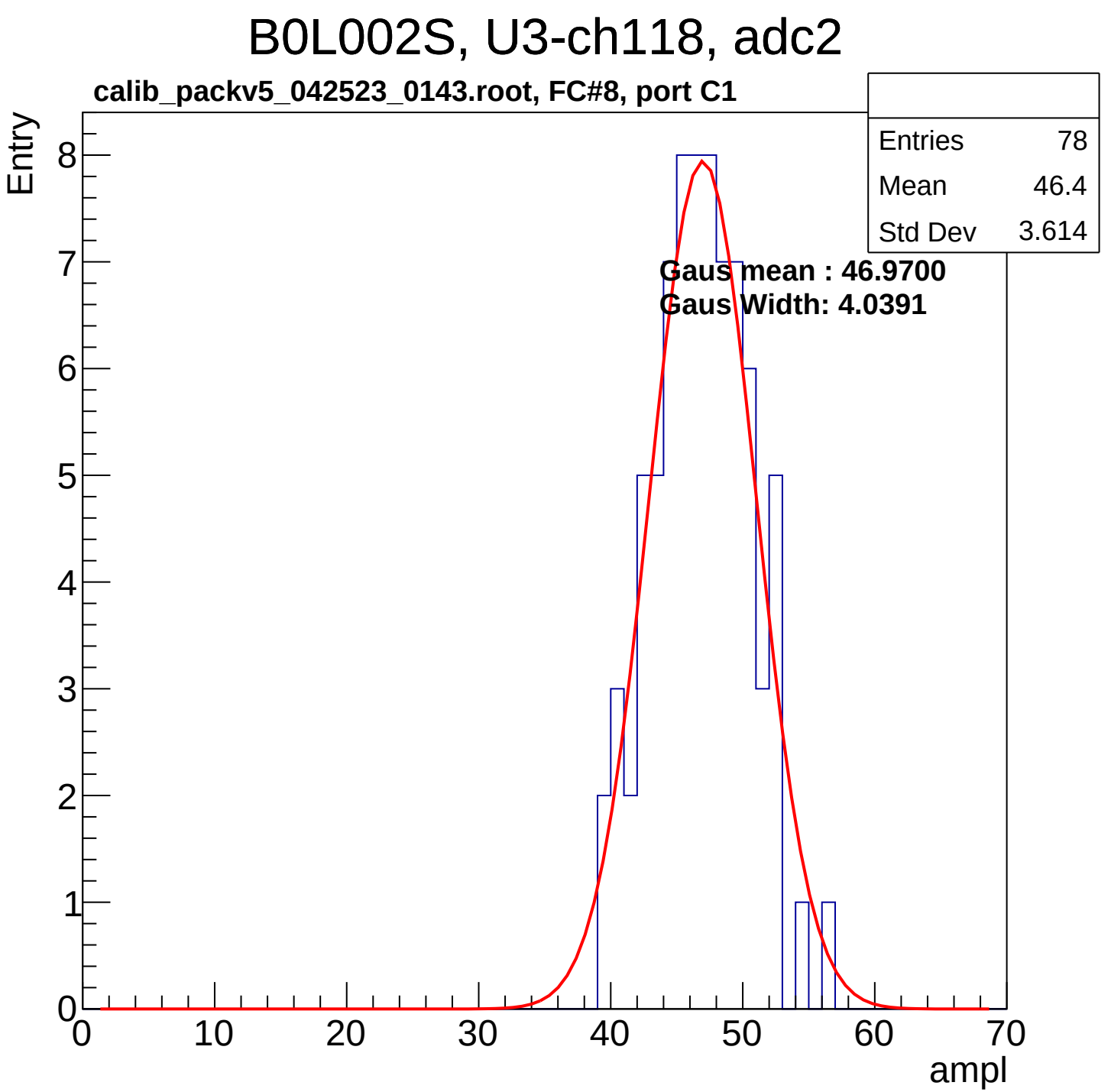
B0L002S, U3-ch118, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 46.4
Std Dev 3.614
Gaus mean : 46.9700
Gaus Width: 4.0391
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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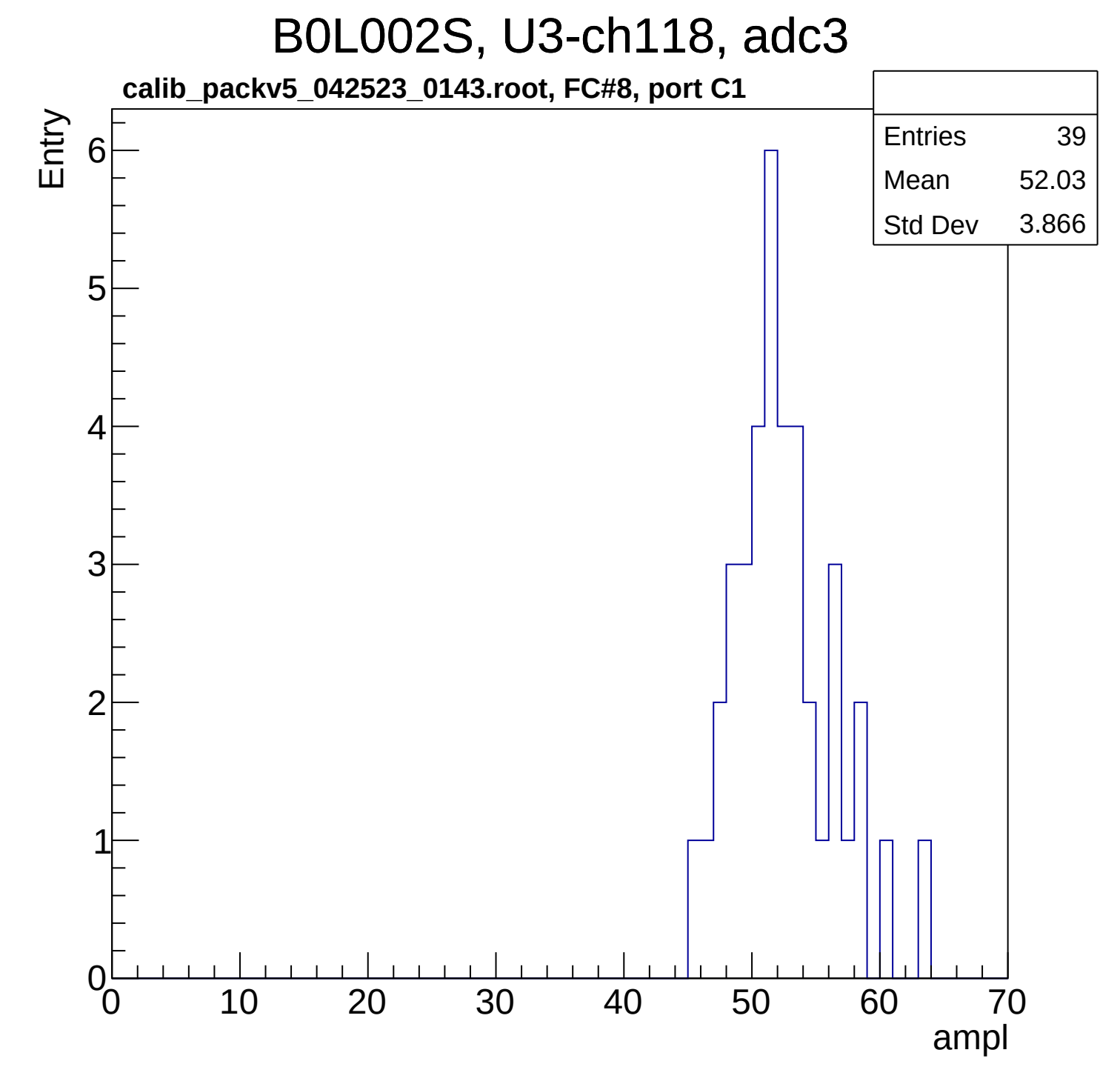
B0L002S, U3-ch118, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 52.03
Std Dev 3.866
Entry
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10
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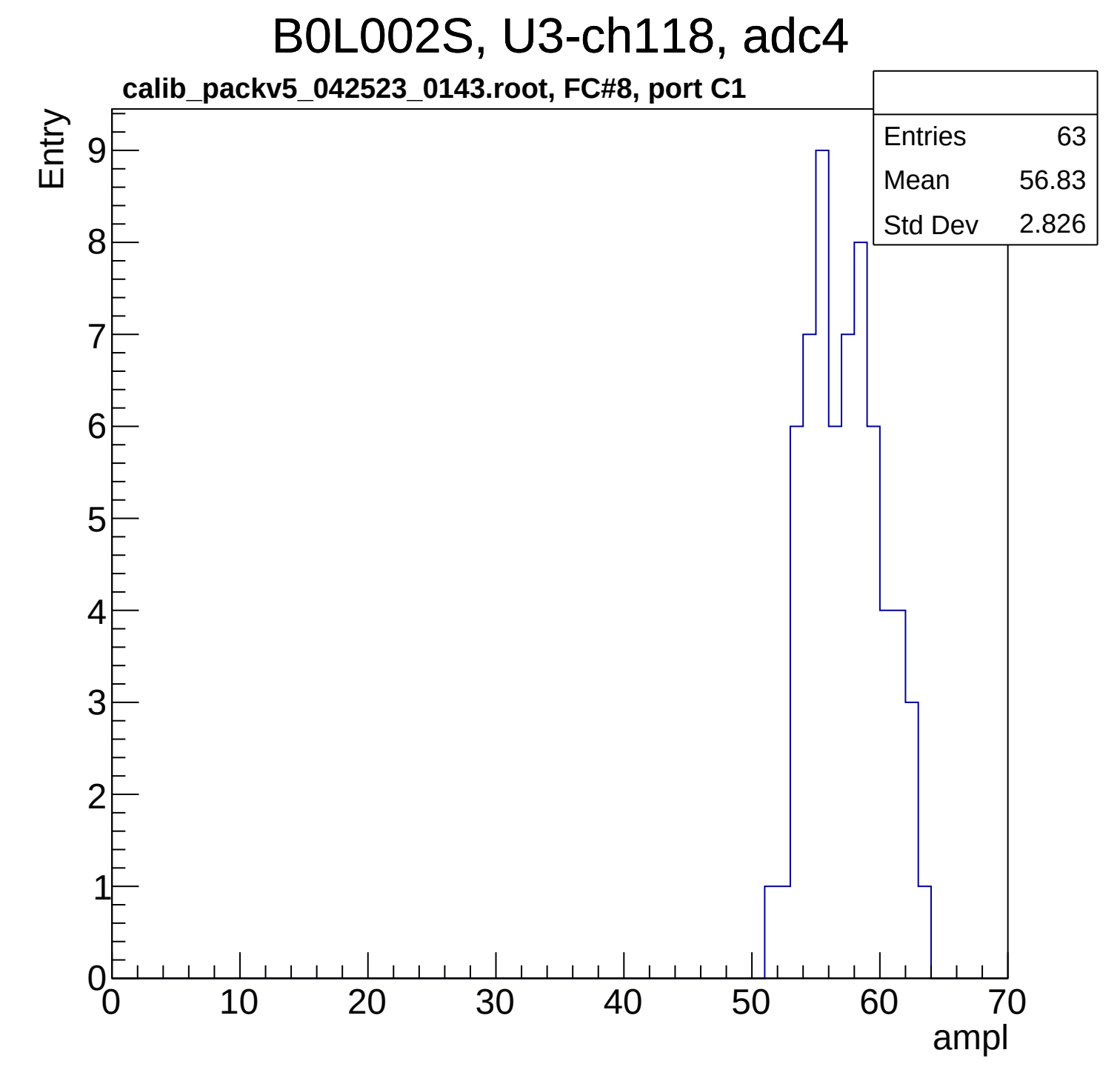
50
60
70
ampl


B0L002S, U3-ch118, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 56.83
Std Dev 2.826
Entry
ampl
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0
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60
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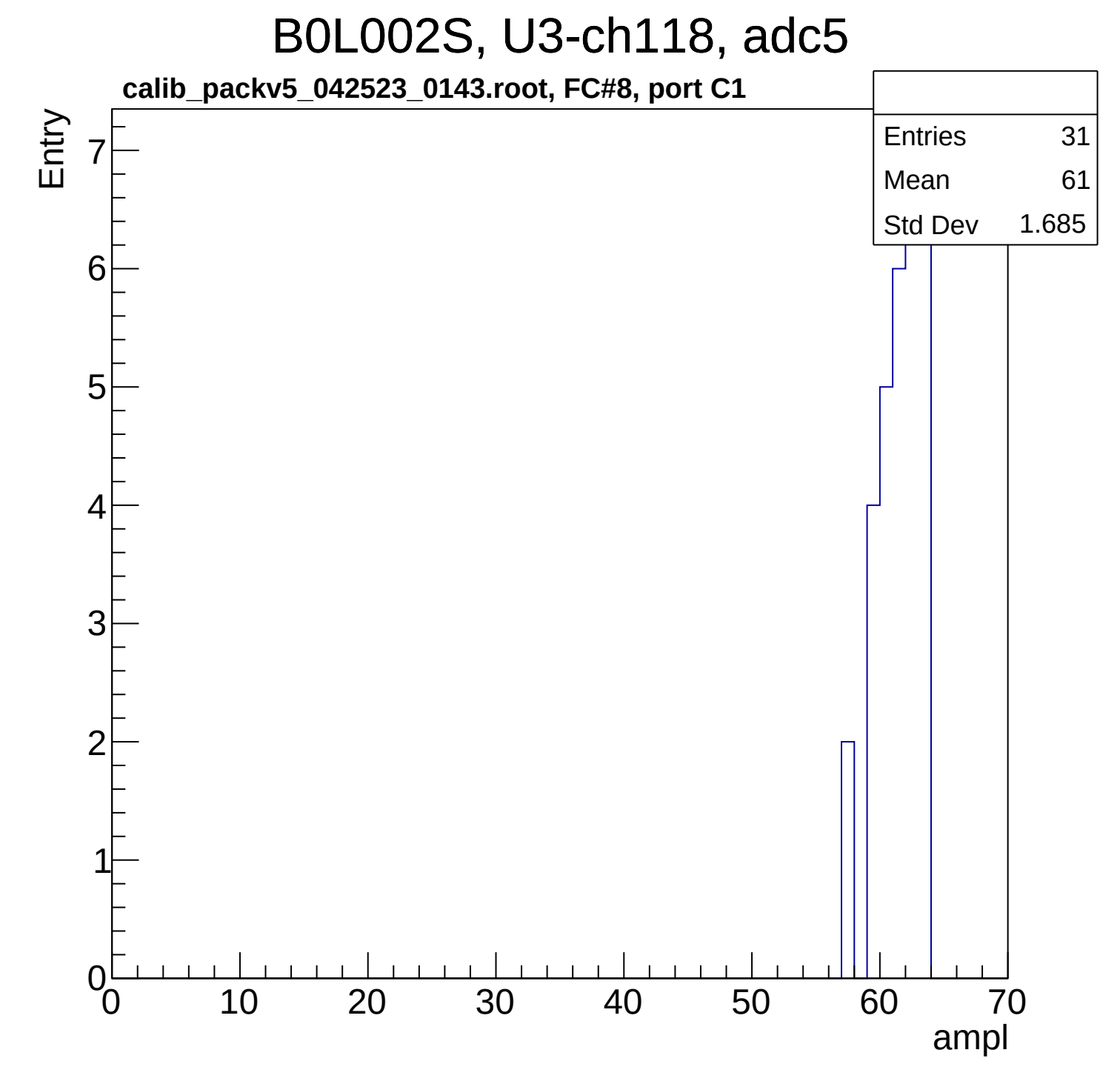
B0L002S, U3-ch118, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 61
Std Dev 1.685
Entry
ampl
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4
5
6
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0
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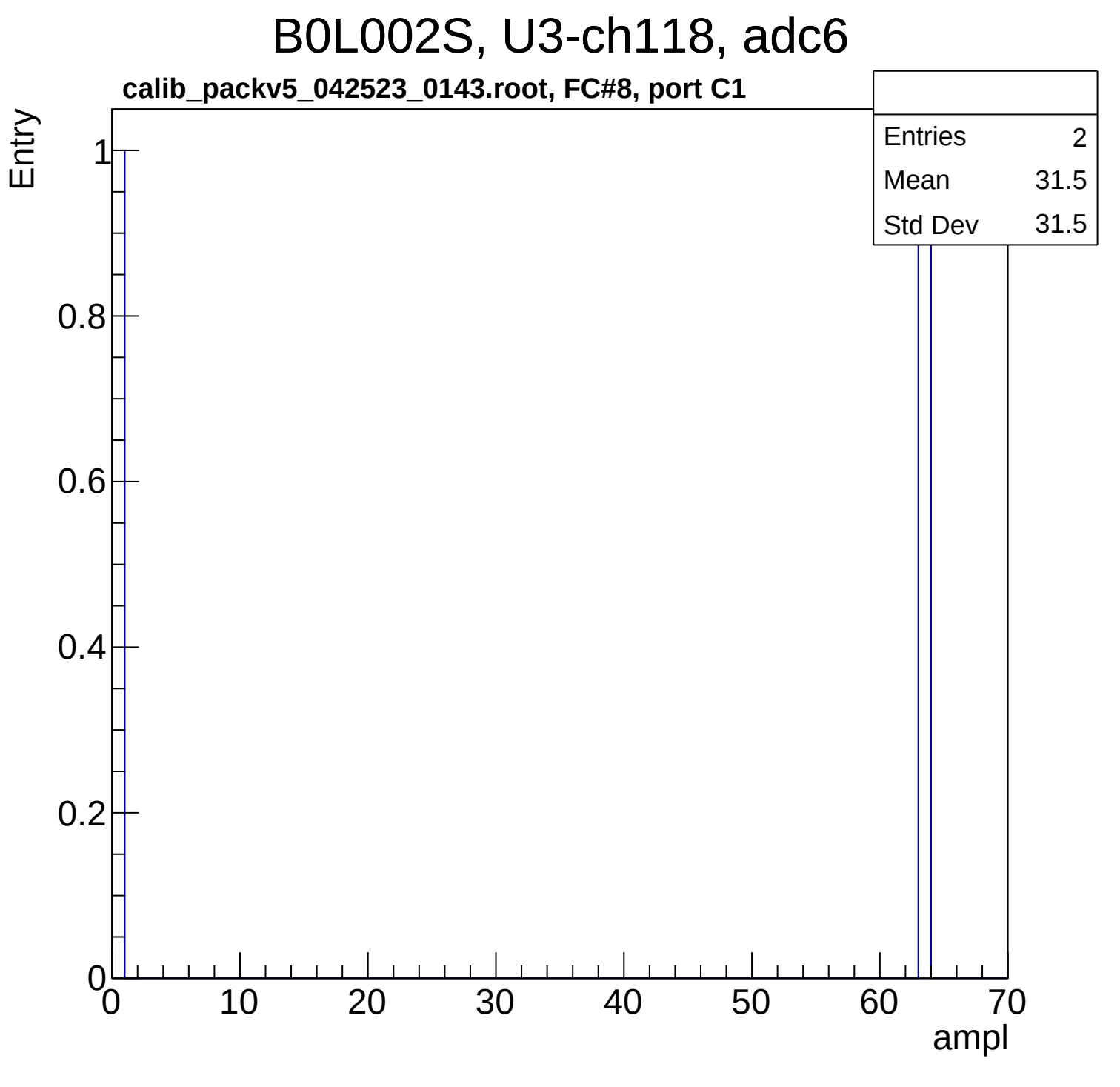
40
50
60
70


B0L002S, U3-ch118, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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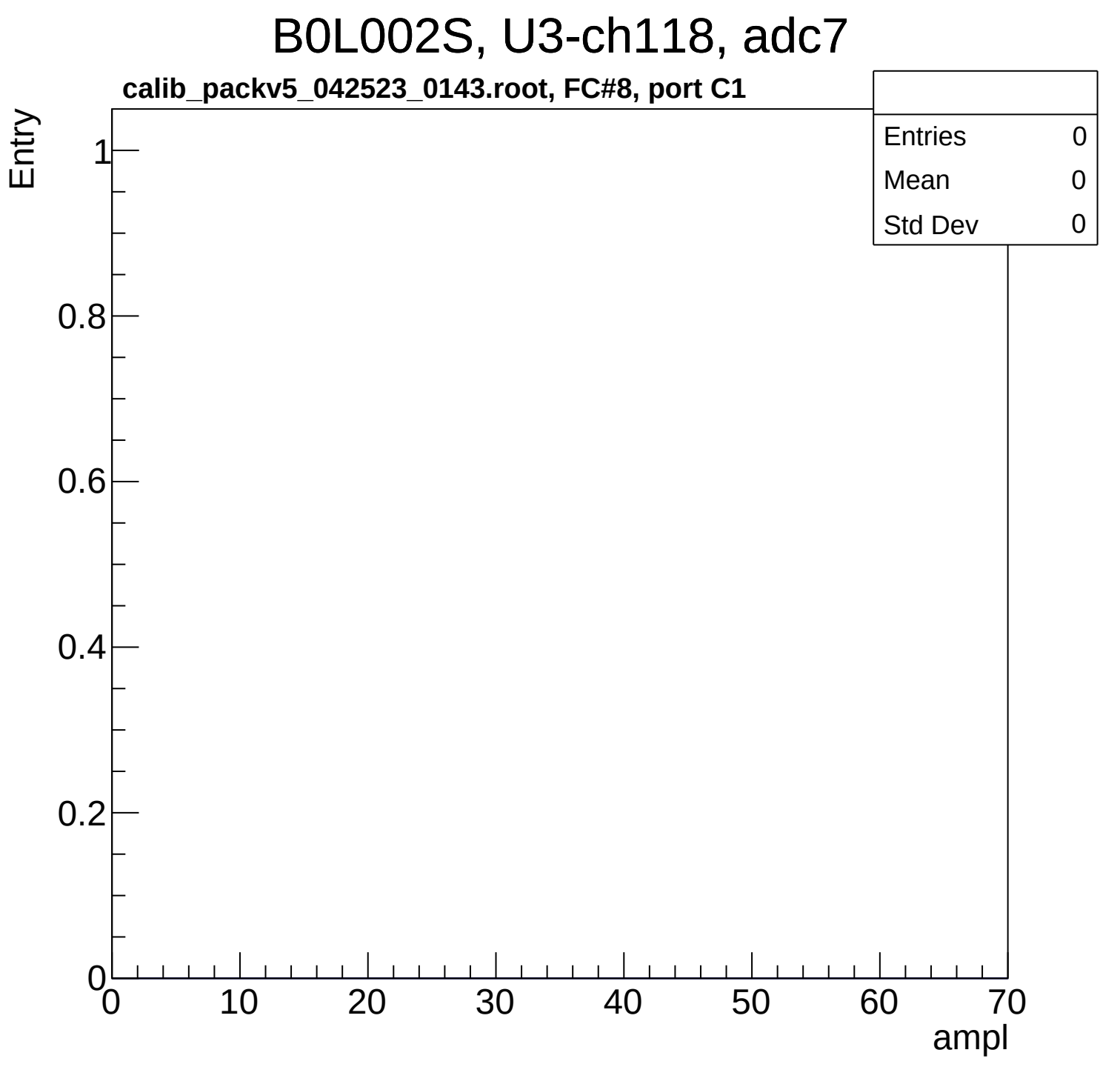
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch118, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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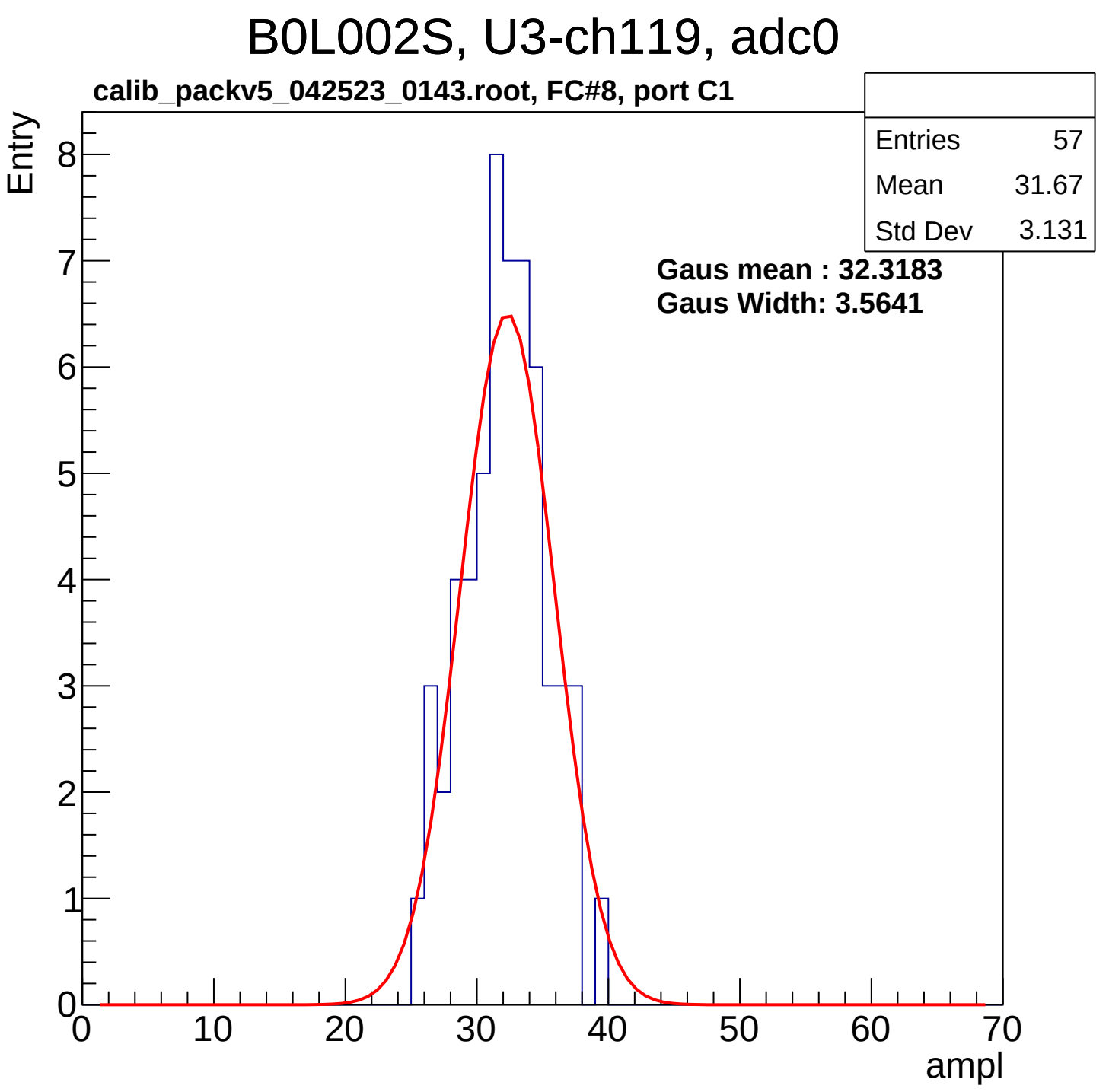
50
60
70
ampl


B0L002S, U3-ch119, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 31.67
Std Dev 3.131
Gaus mean : 32.3183
Gaus Width: 3.5641
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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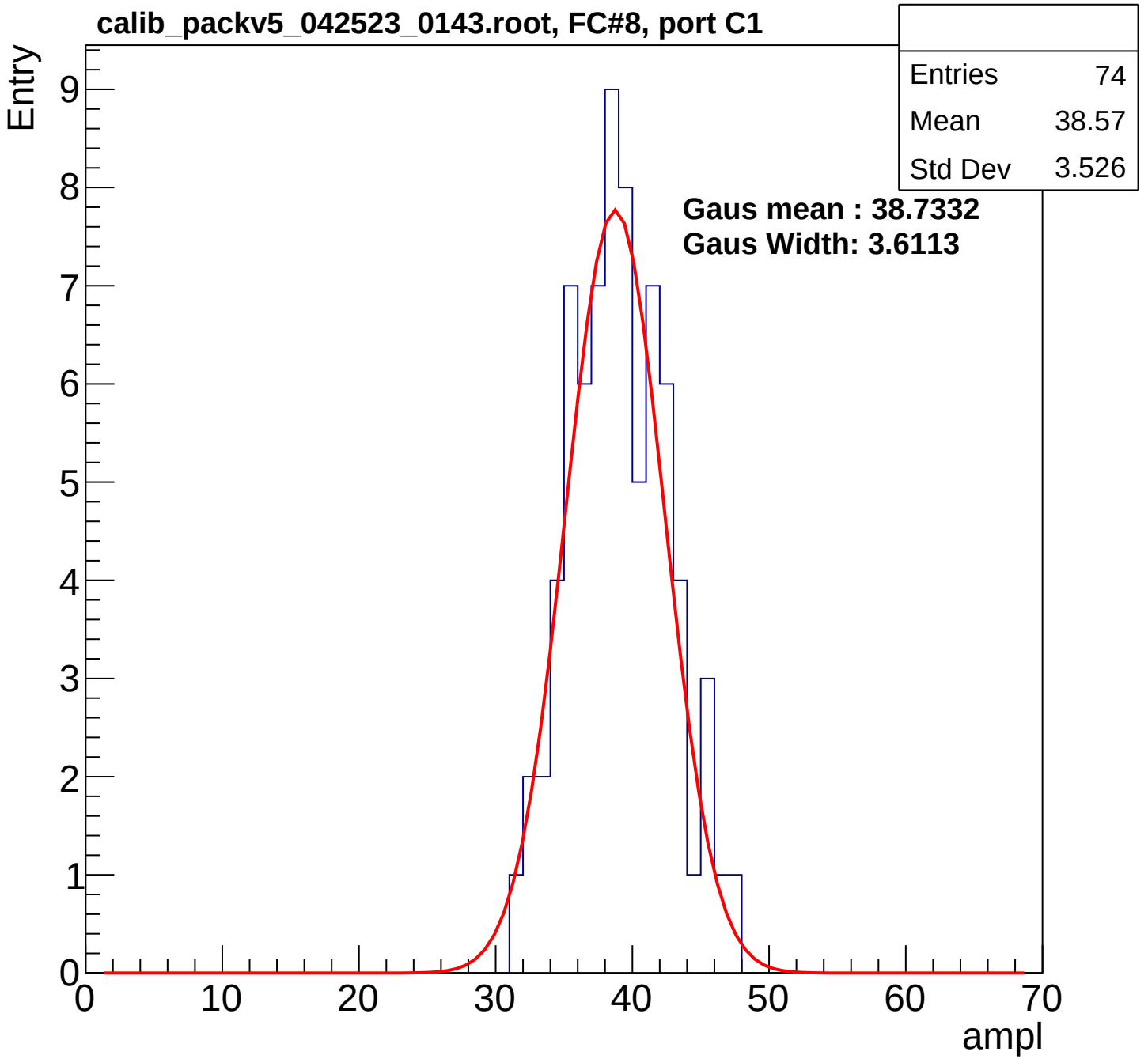
B0L002S, U3-ch119, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 38.57
Std Dev 3.526
Gaus mean : 38.7332
Gaus Width: 3.6113
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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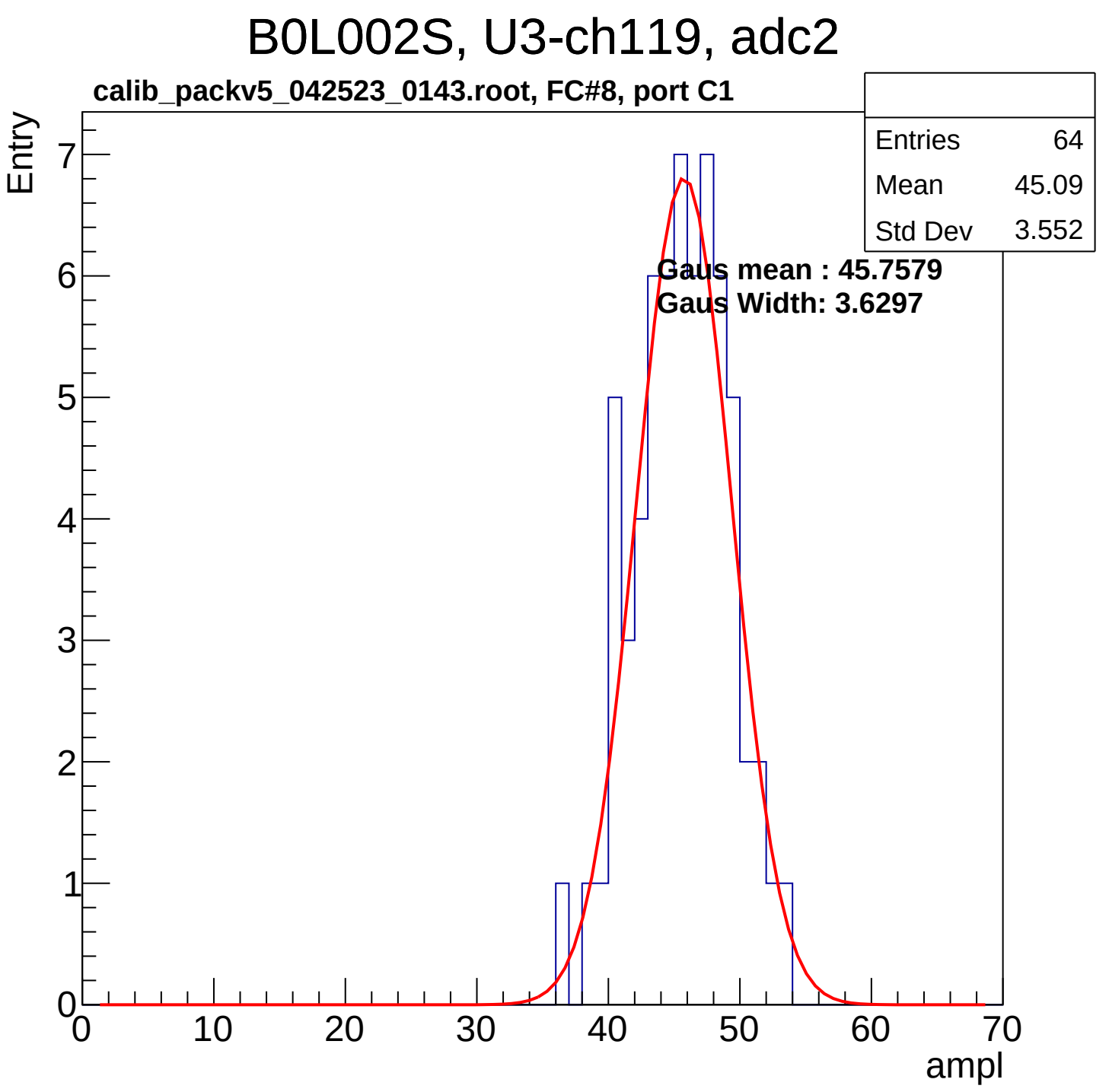
B0L002S, U3-ch119, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 45.09
Std Dev 3.552
Gaus mean : 45.7579
Gaus Width: 3.6297
Entry
ampl
0
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2
3
4
5
6
7
0
10
20
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60
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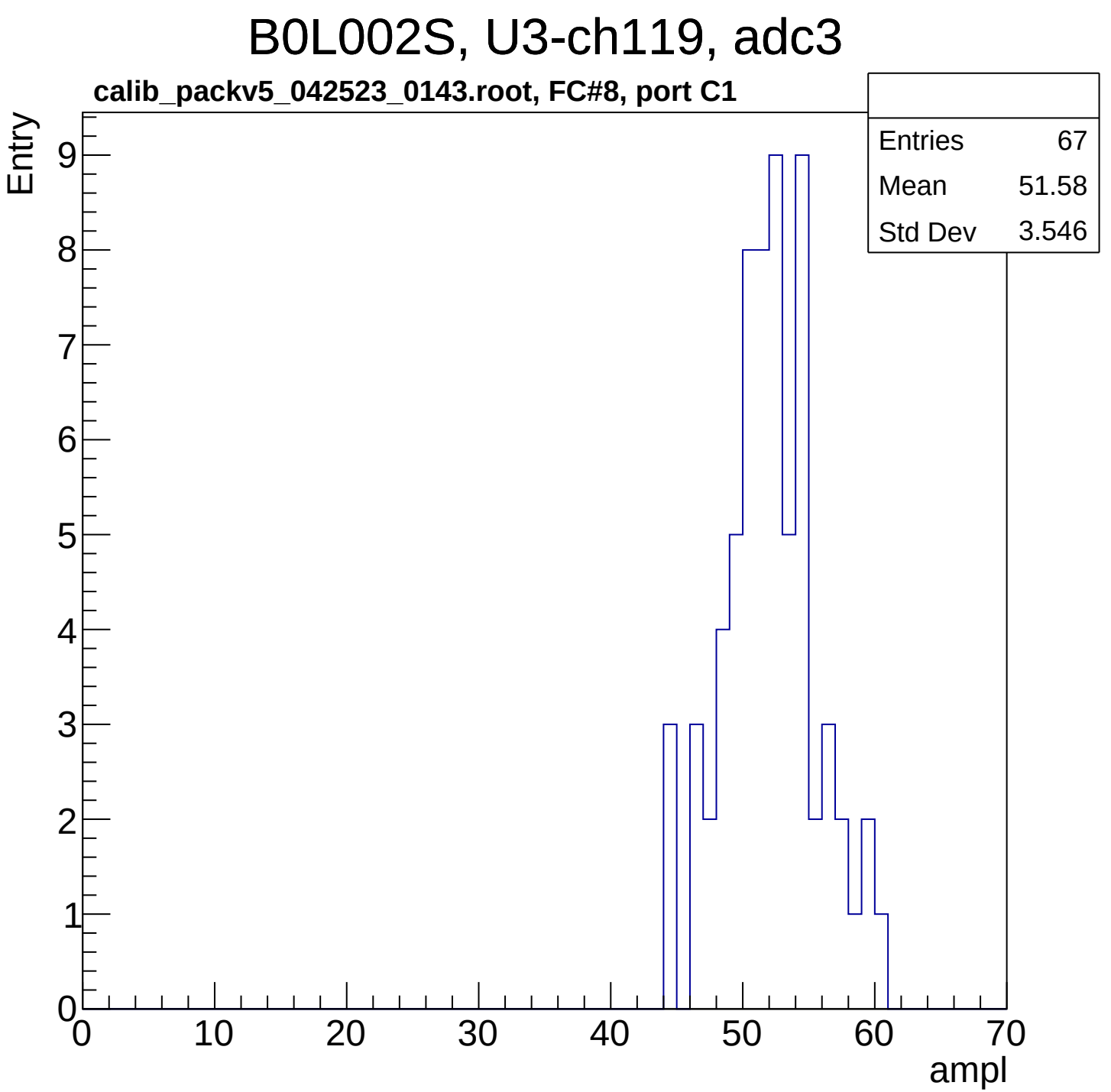
B0L002S, U3-ch119, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 51.58
Std Dev 3.546
Entry
ampl
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3
4
5
6
7
8
9
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10
20
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60
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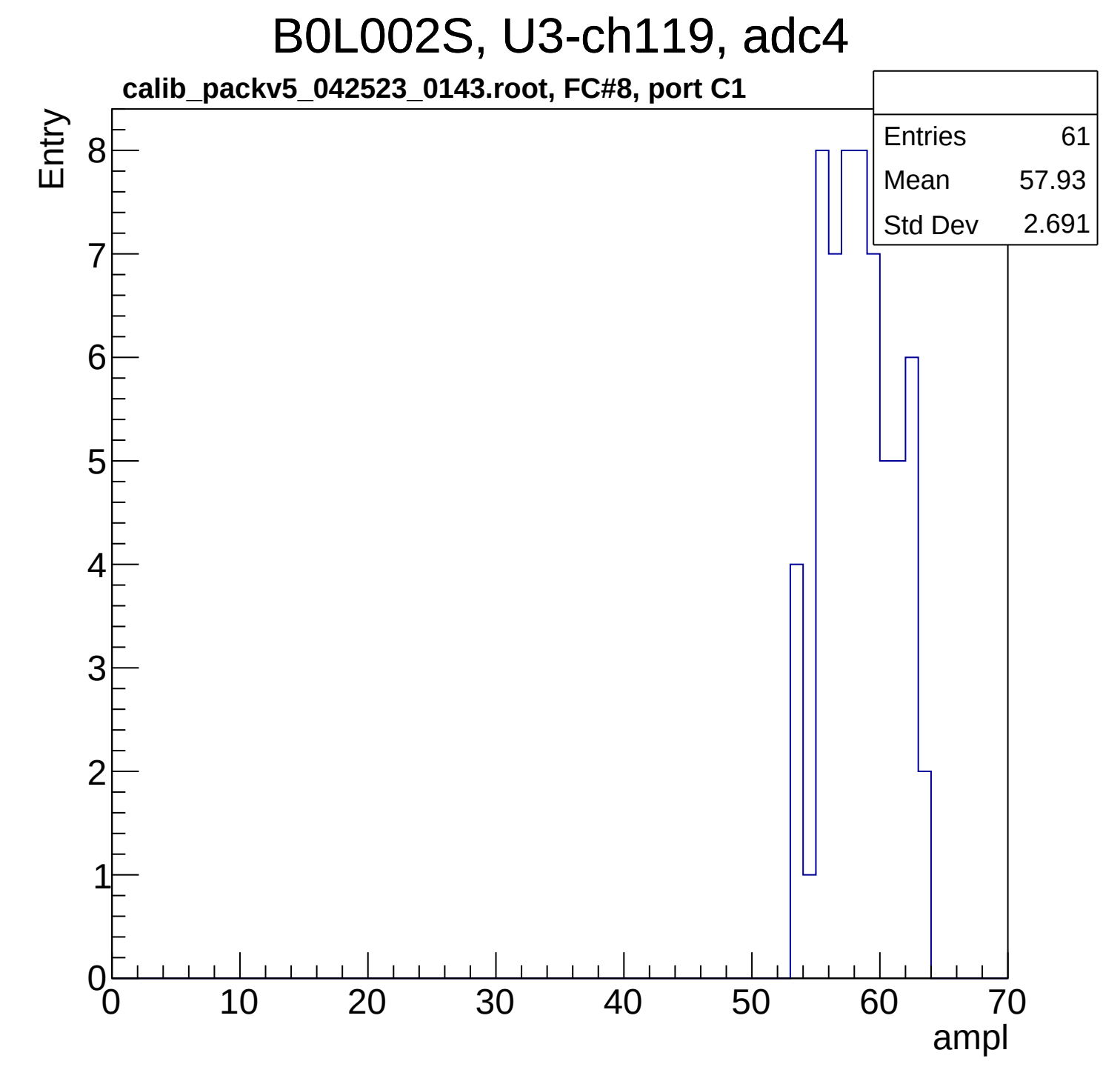
B0L002S, U3-ch119, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 57.93
Std Dev 2.691
Entry
ampl
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2
3
4
5
6
7
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10
20
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40
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60
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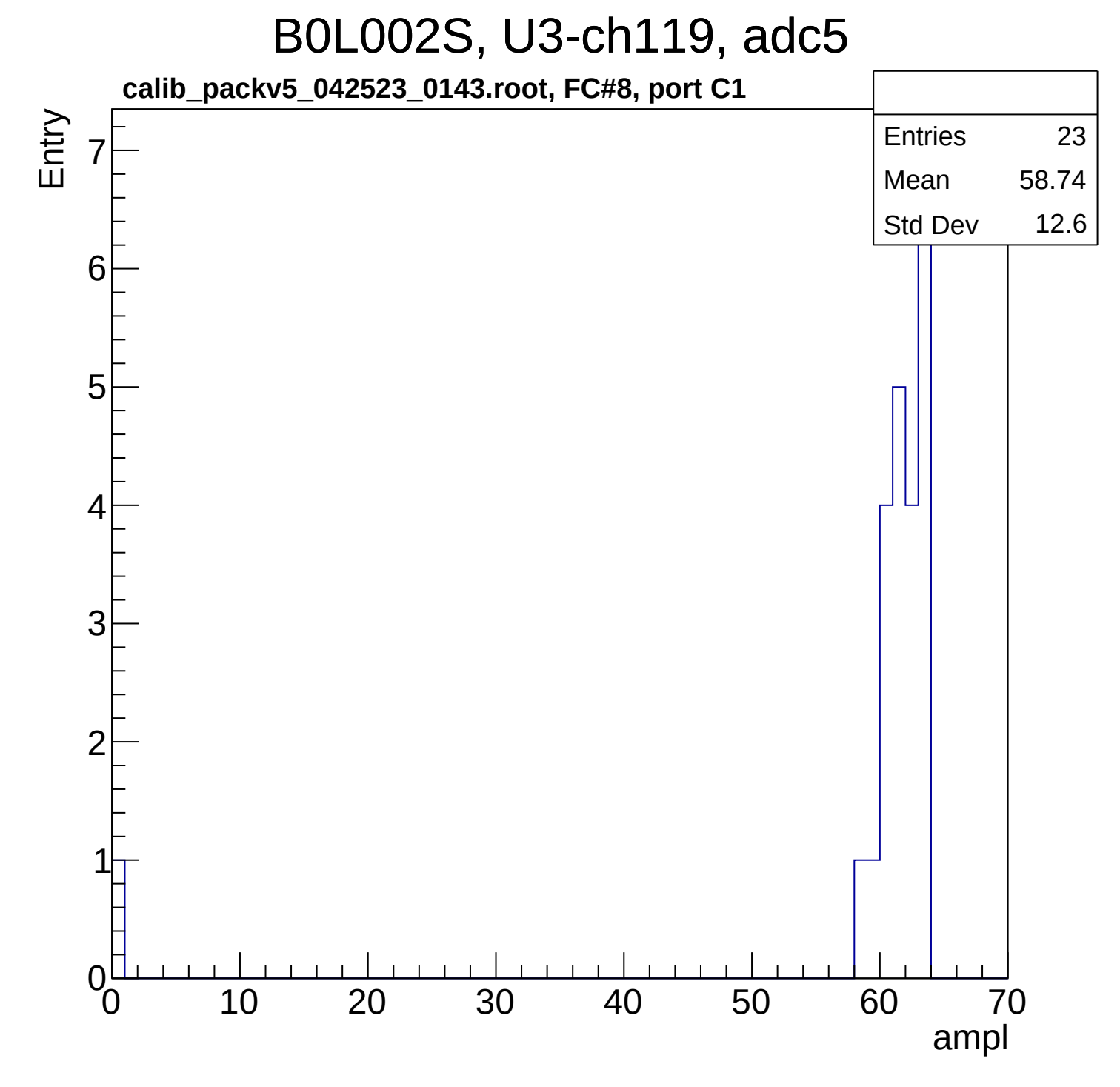
B0L002S, U3-ch119, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 58.74
Std Dev 12.6
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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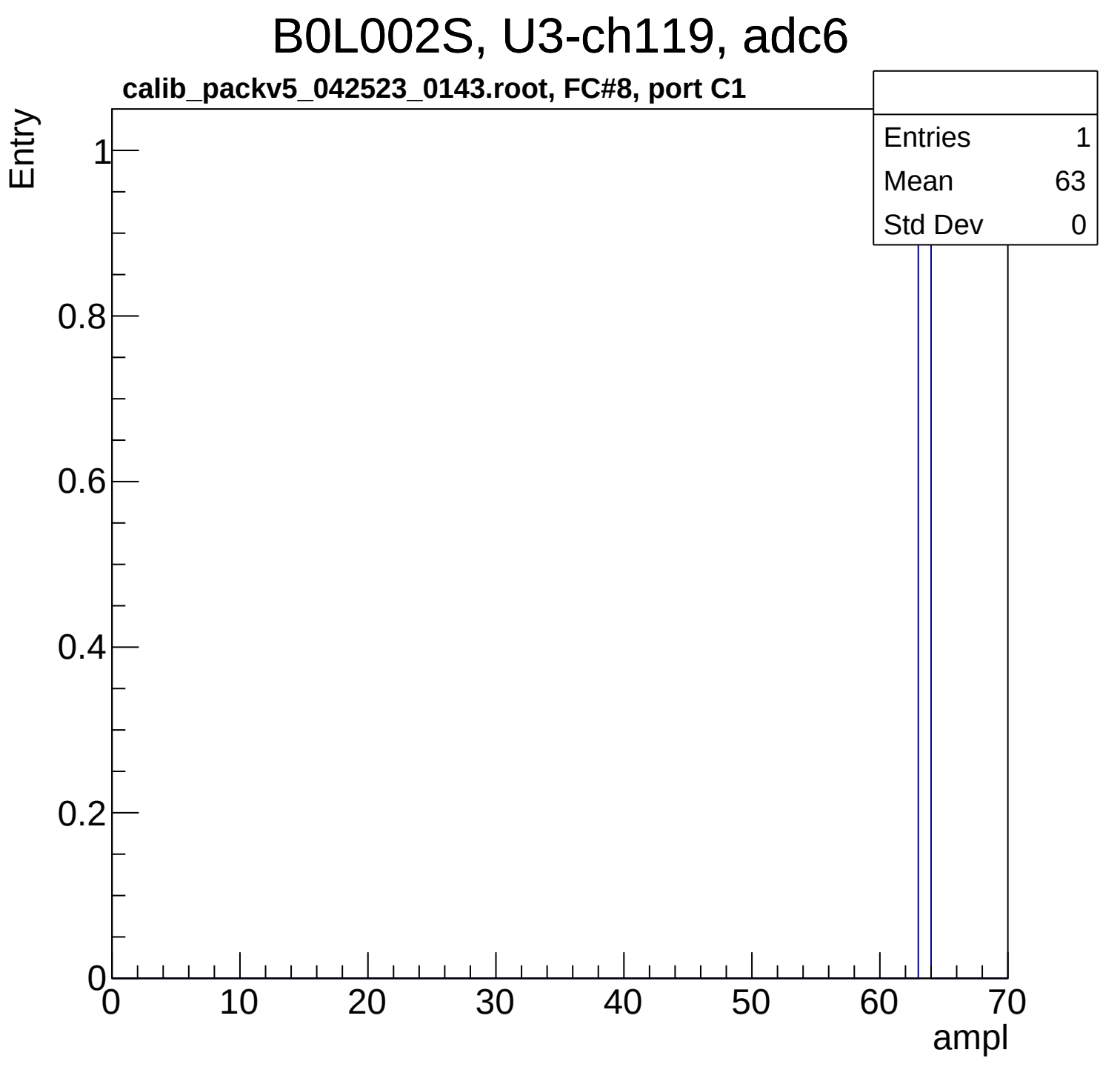
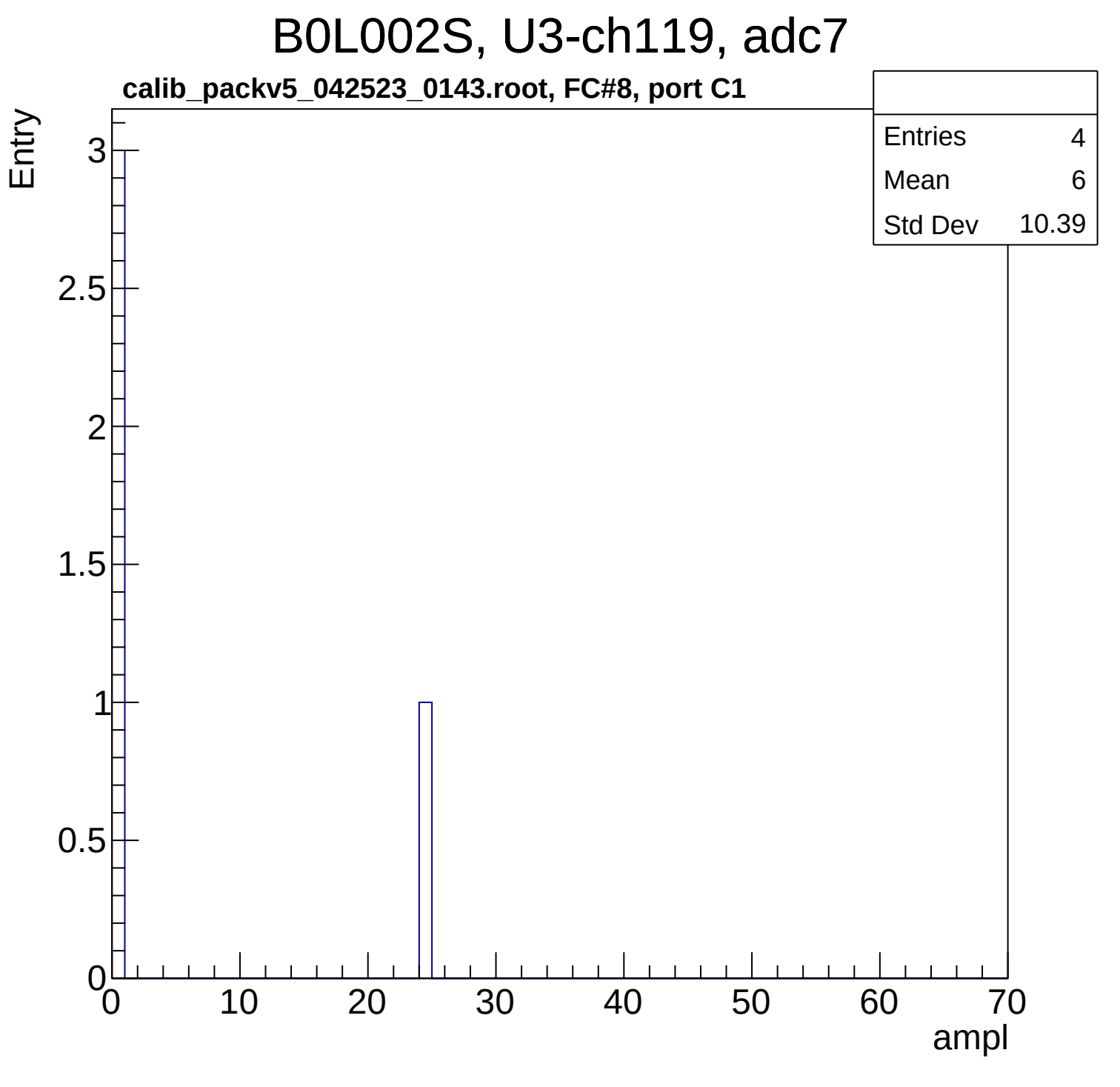

B0L002S, U3-ch119, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch119, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 6
Std Dev 10.39
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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40
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60
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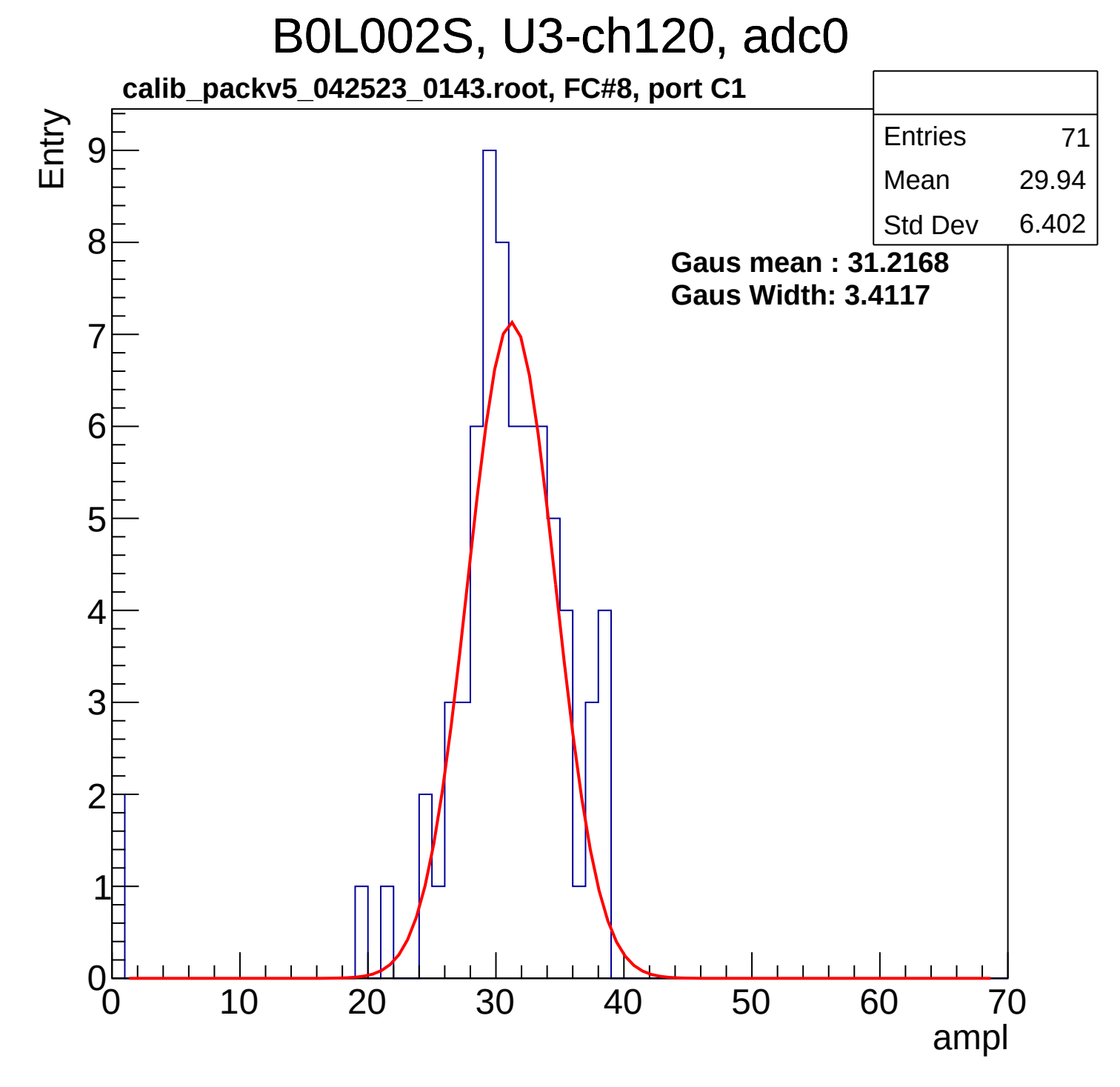
B0L002S, U3-ch120, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 29.94
Std Dev 6.402
Gaus mean : 31.2168
Gaus Width: 3.4117
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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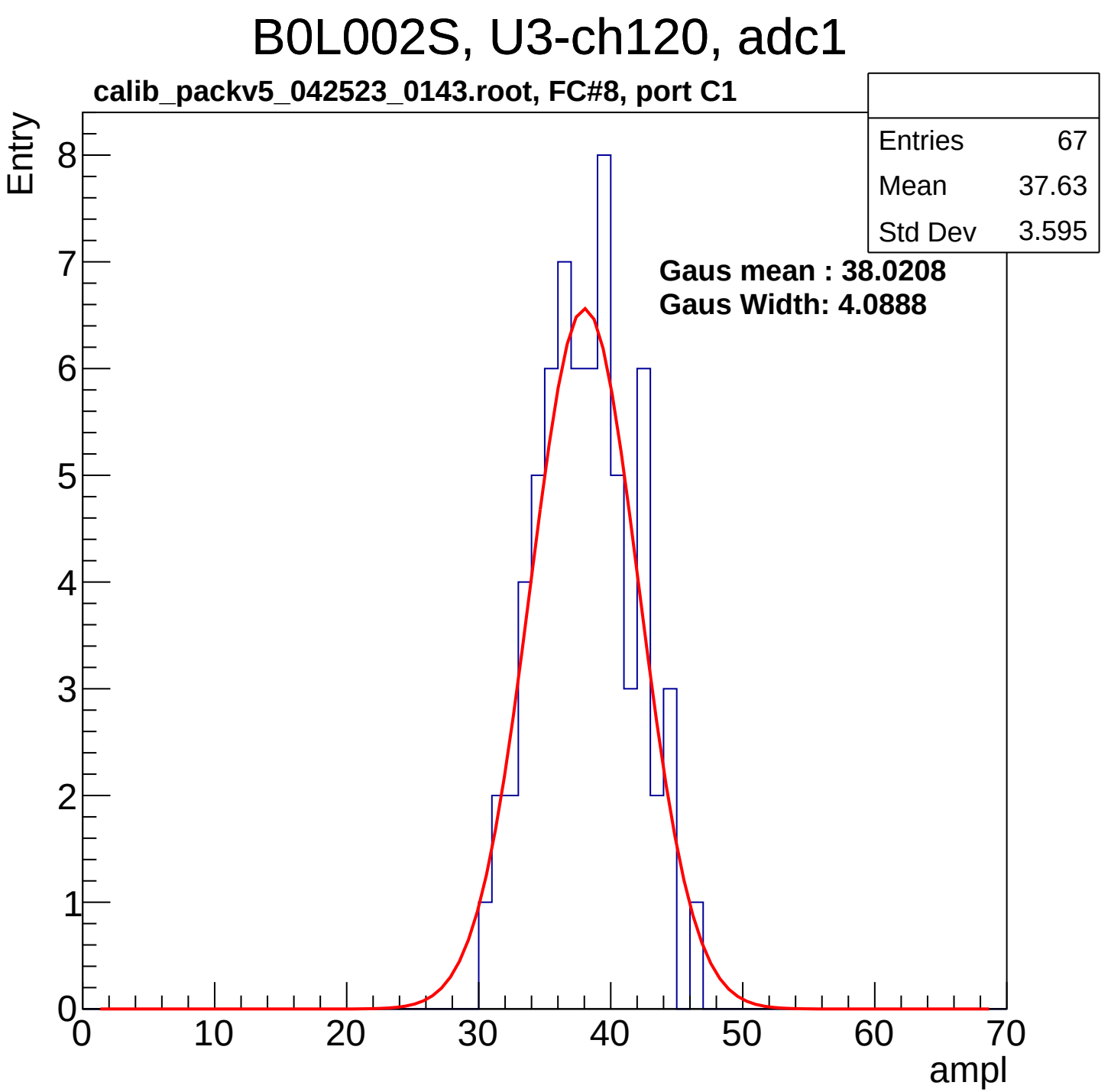
B0L002S, U3-ch120, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.63
Std Dev 3.595
Gaus mean : 38.0208
Gaus Width: 4.0888
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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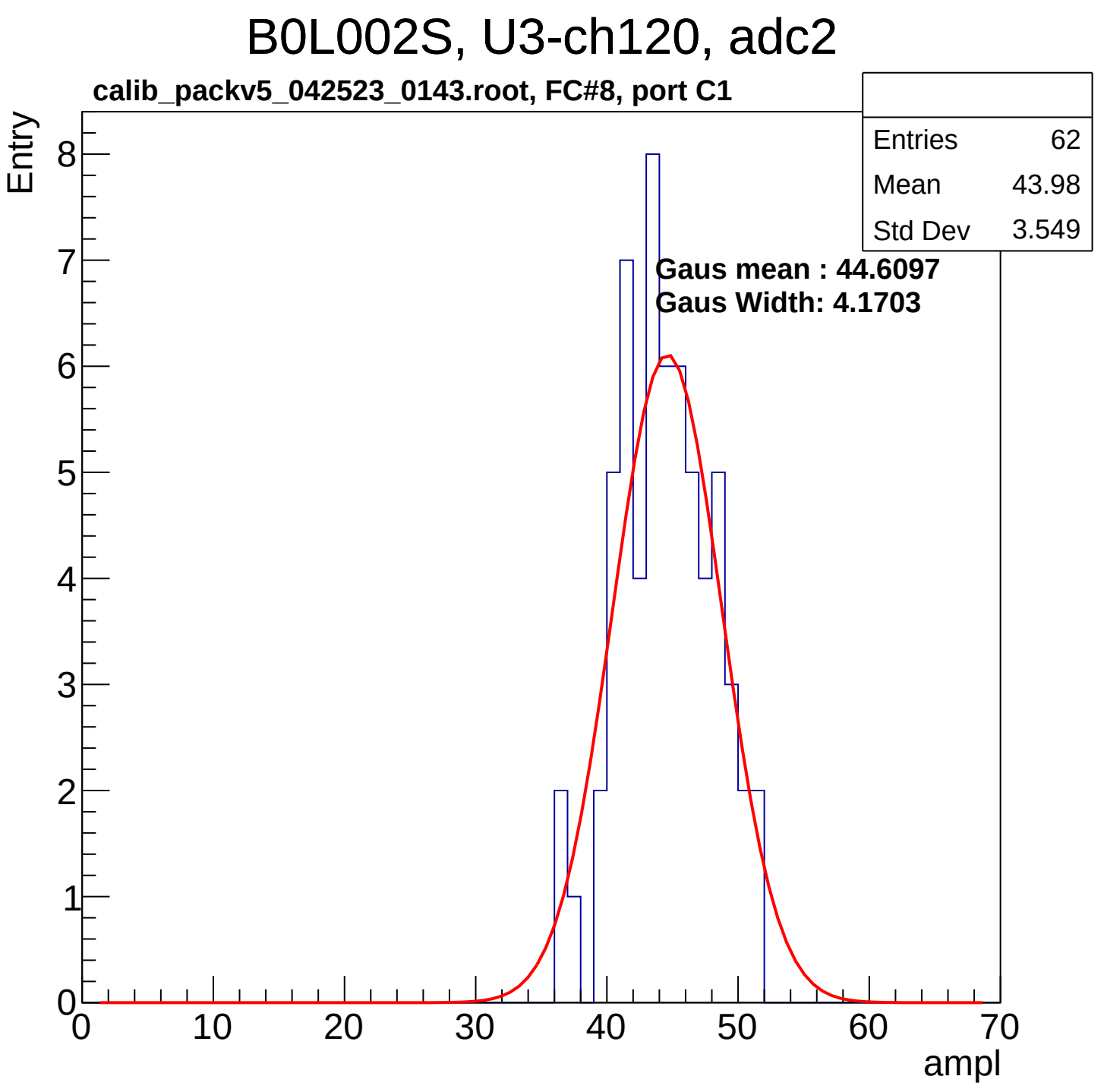
B0L002S, U3-ch120, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 43.98
Std Dev 3.549
Gaus mean : 44.6097
Gaus Width: 4.1703
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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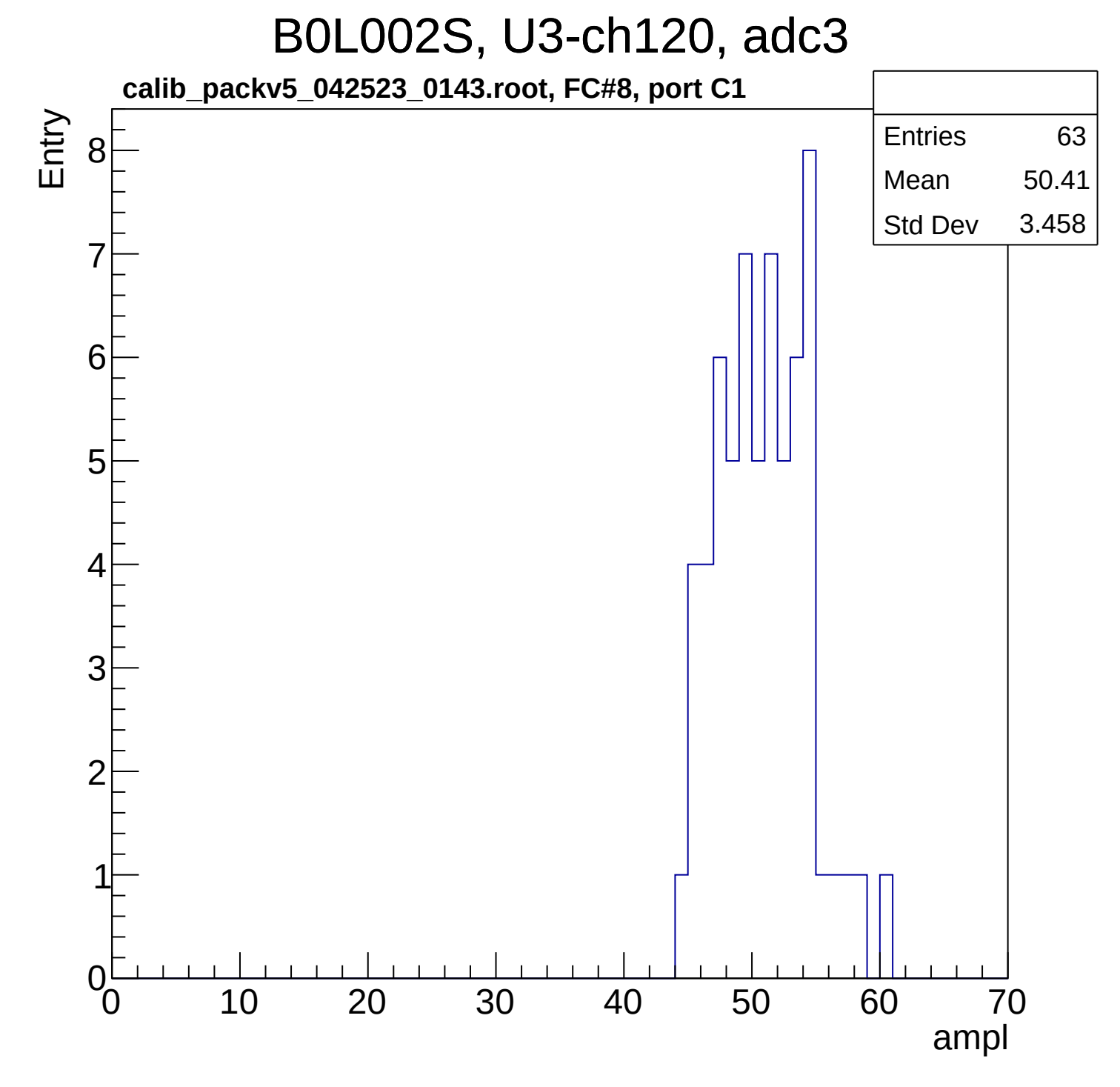
B0L002S, U3-ch120, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.41
Std Dev 3.458
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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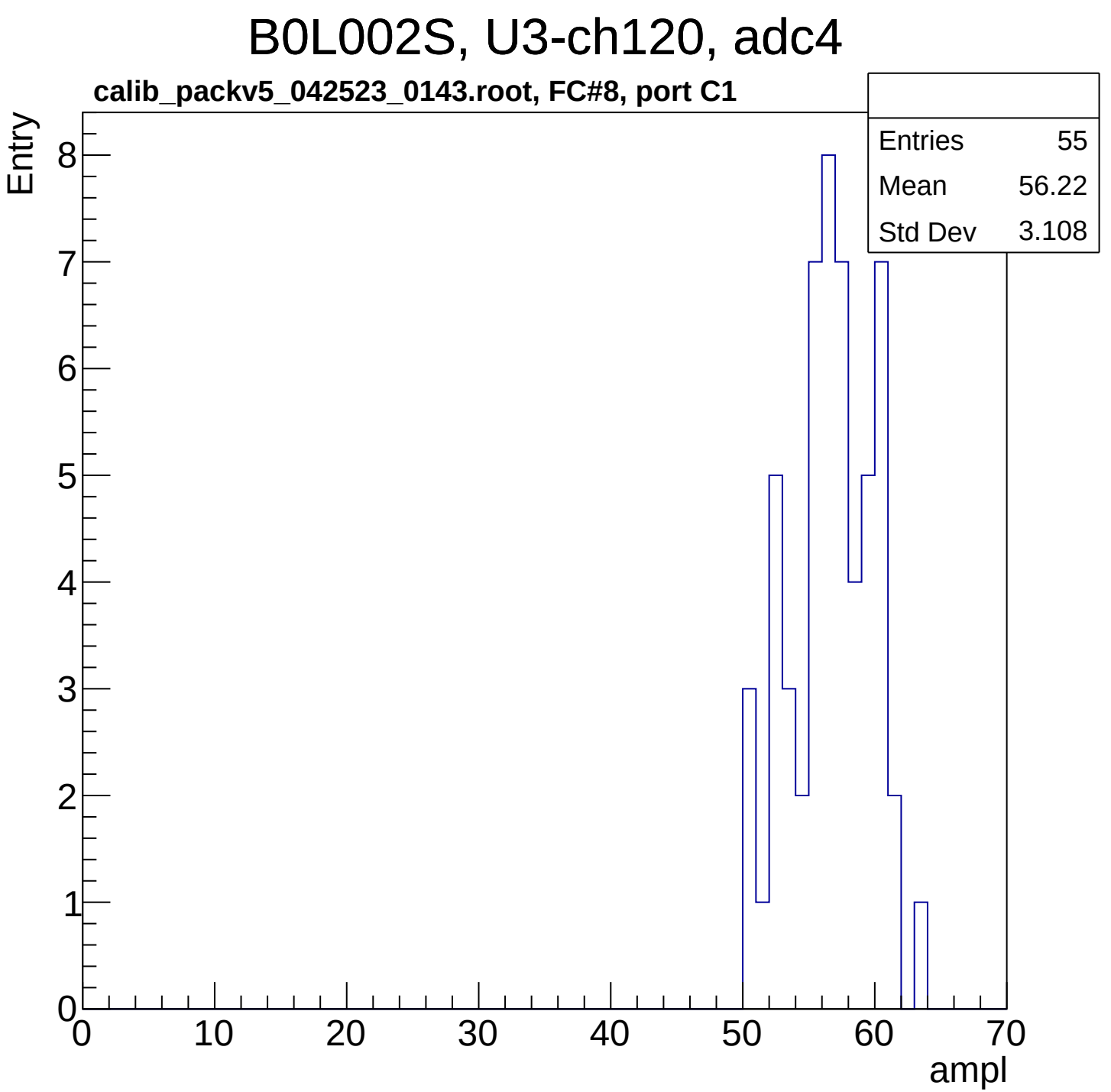
B0L002S, U3-ch120, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.22
Std Dev 3.108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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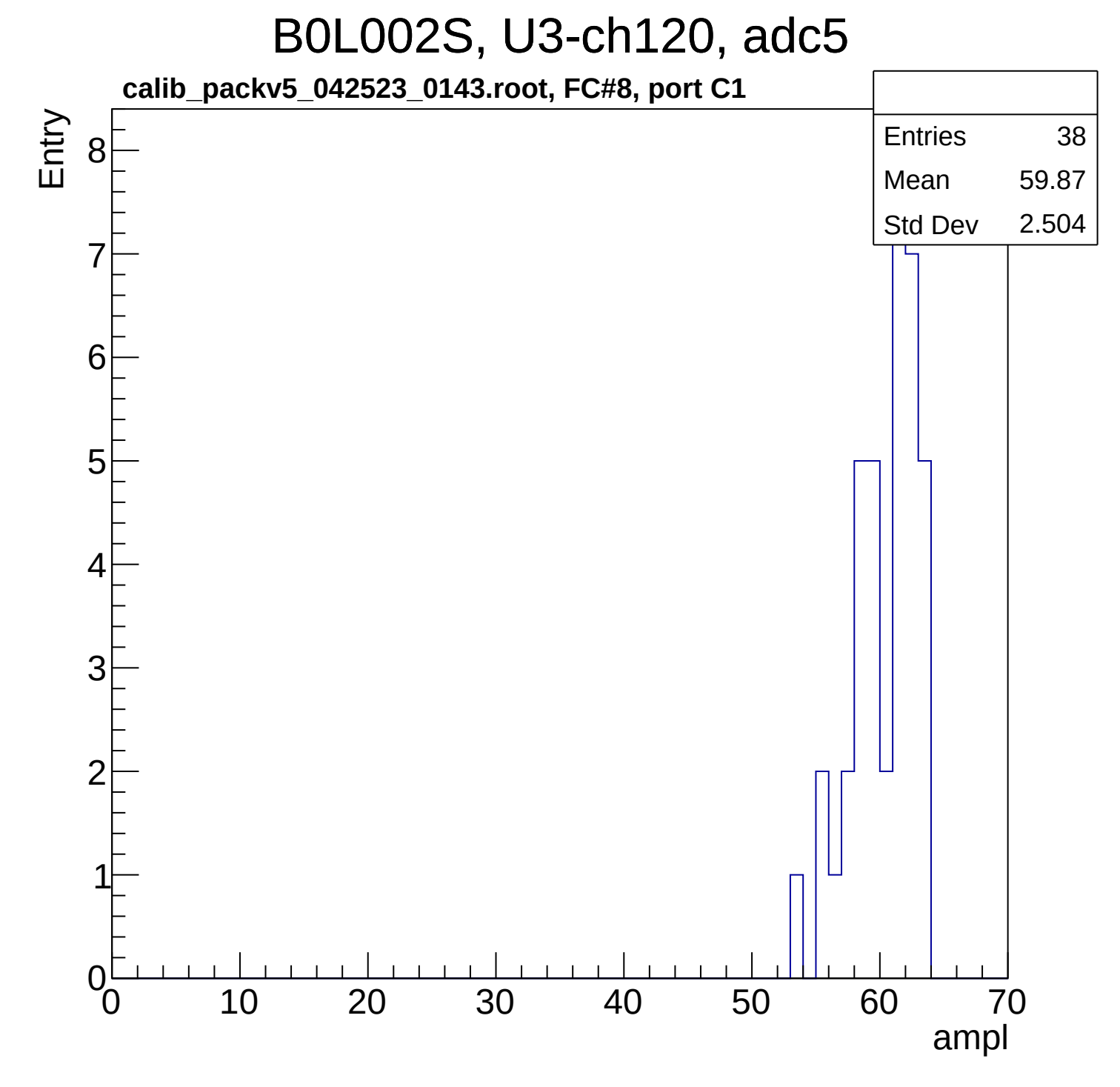
B0L002S, U3-ch120, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59.87
Std Dev 2.504
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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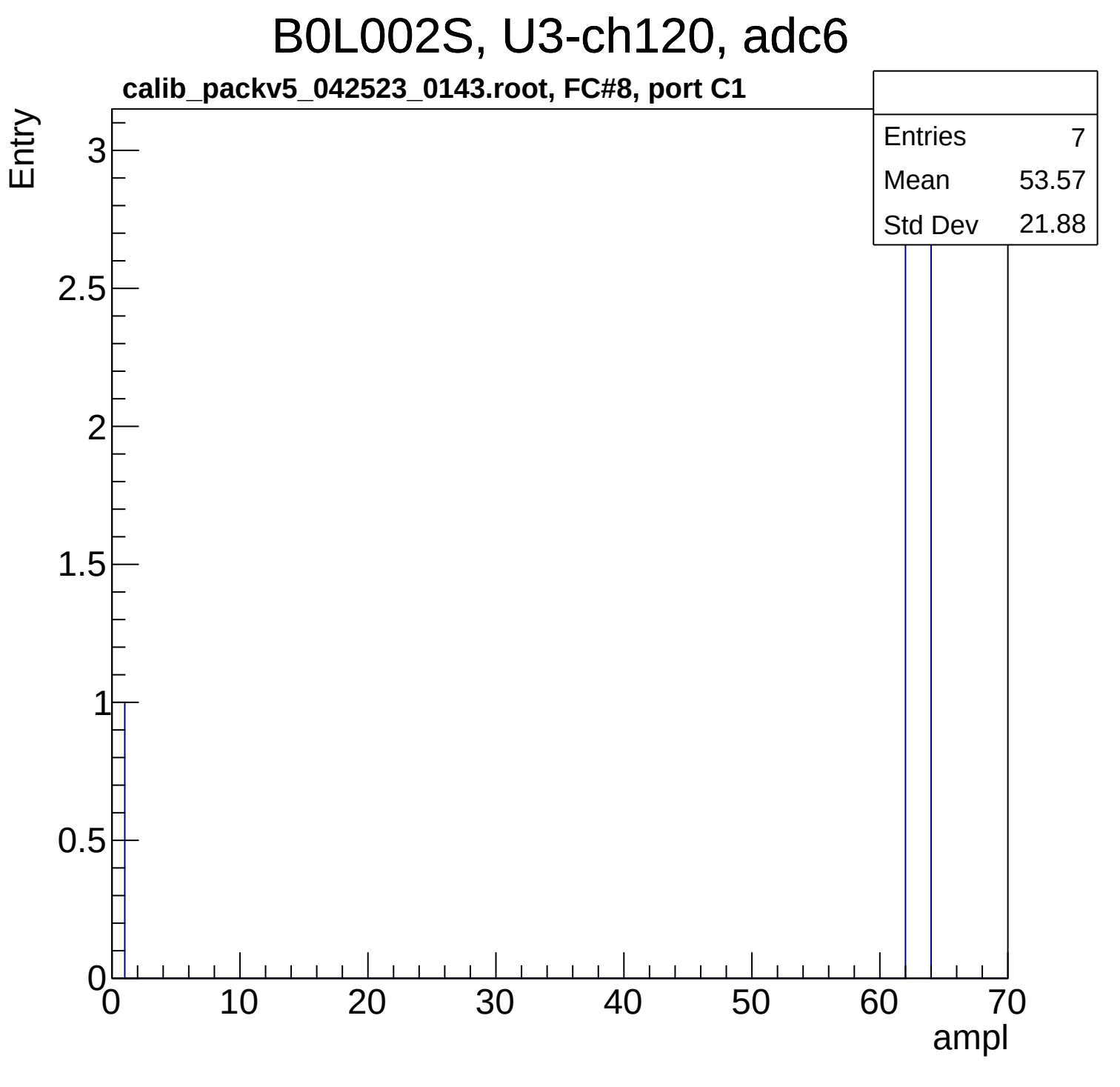
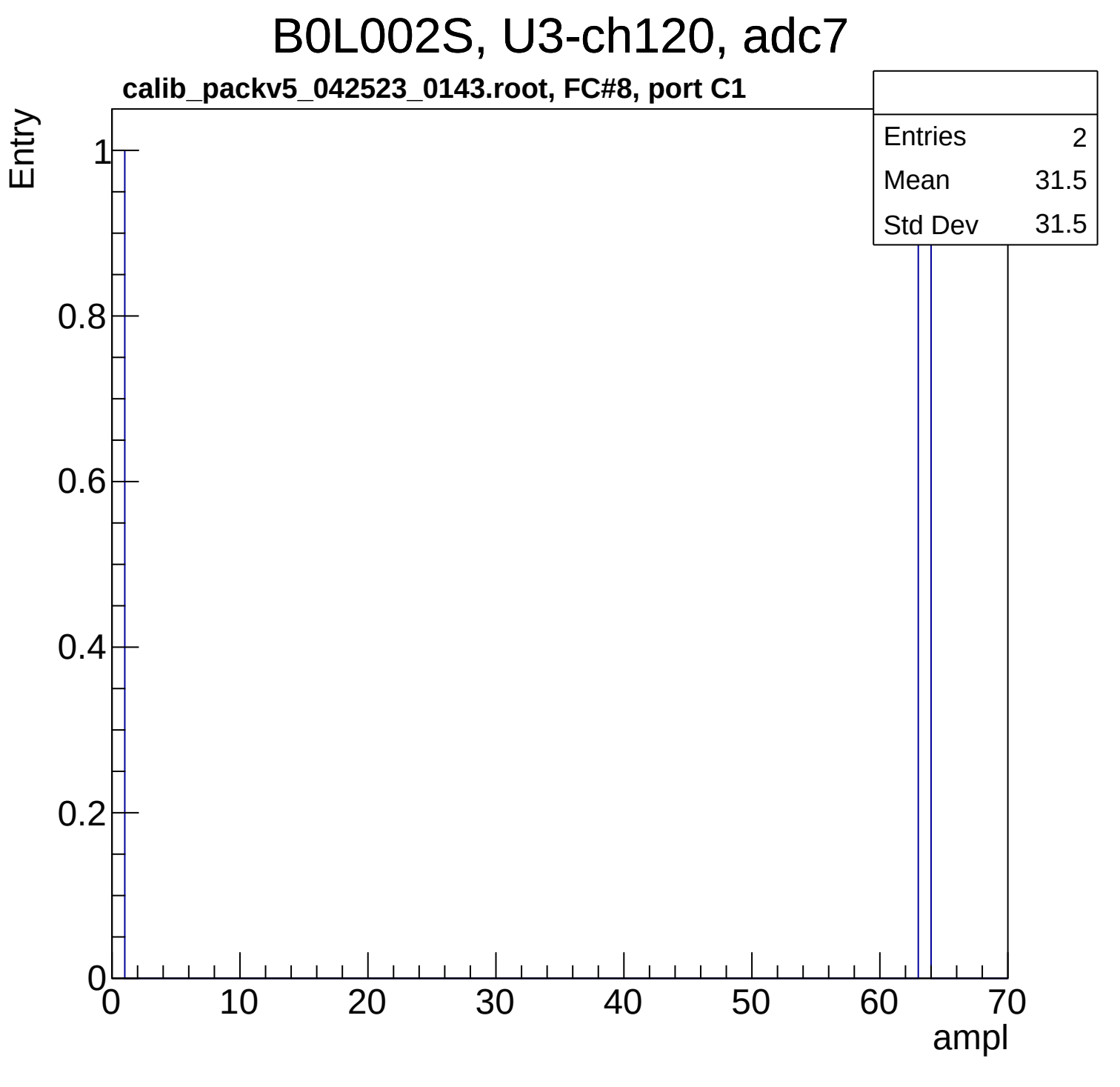
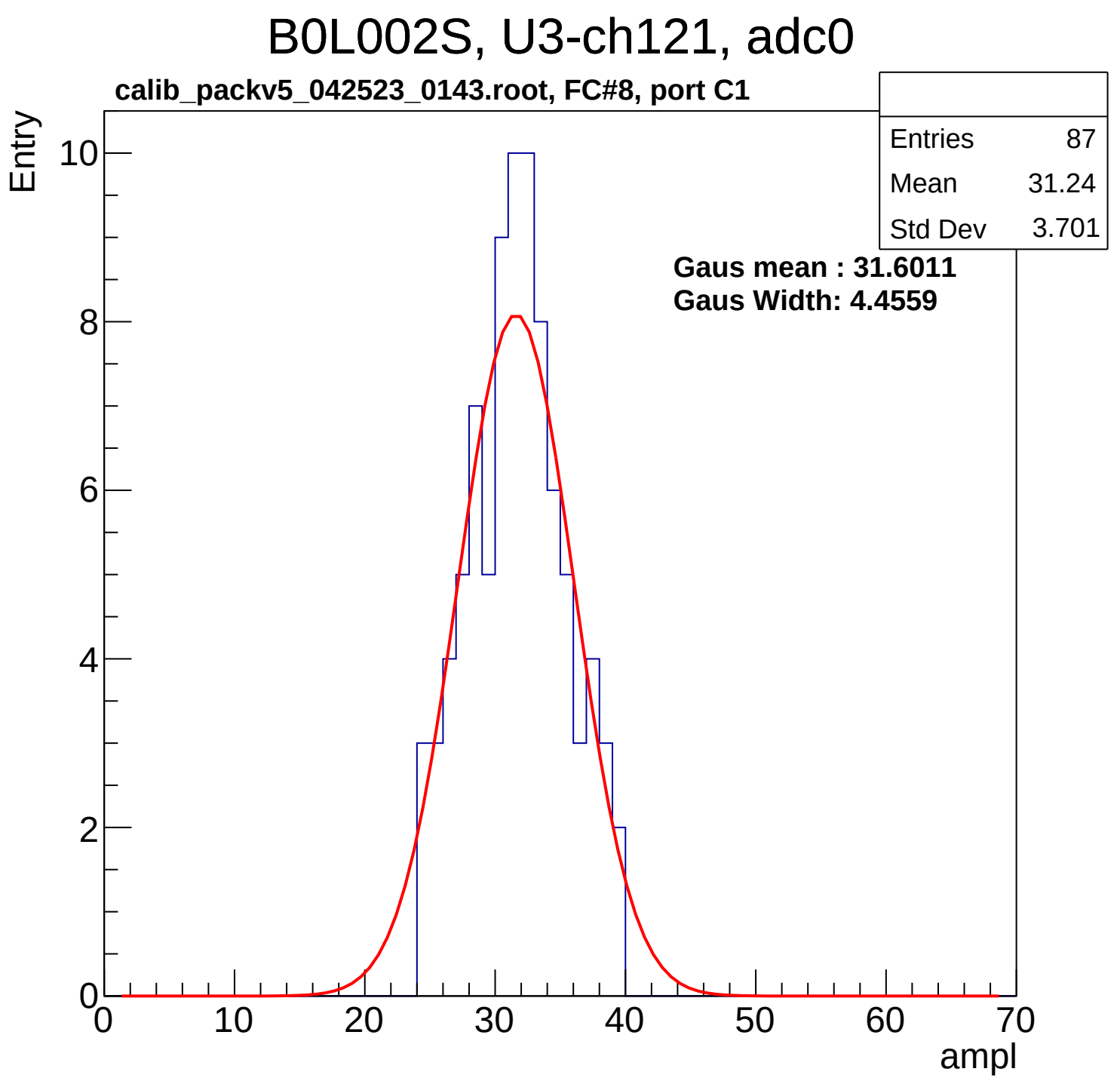
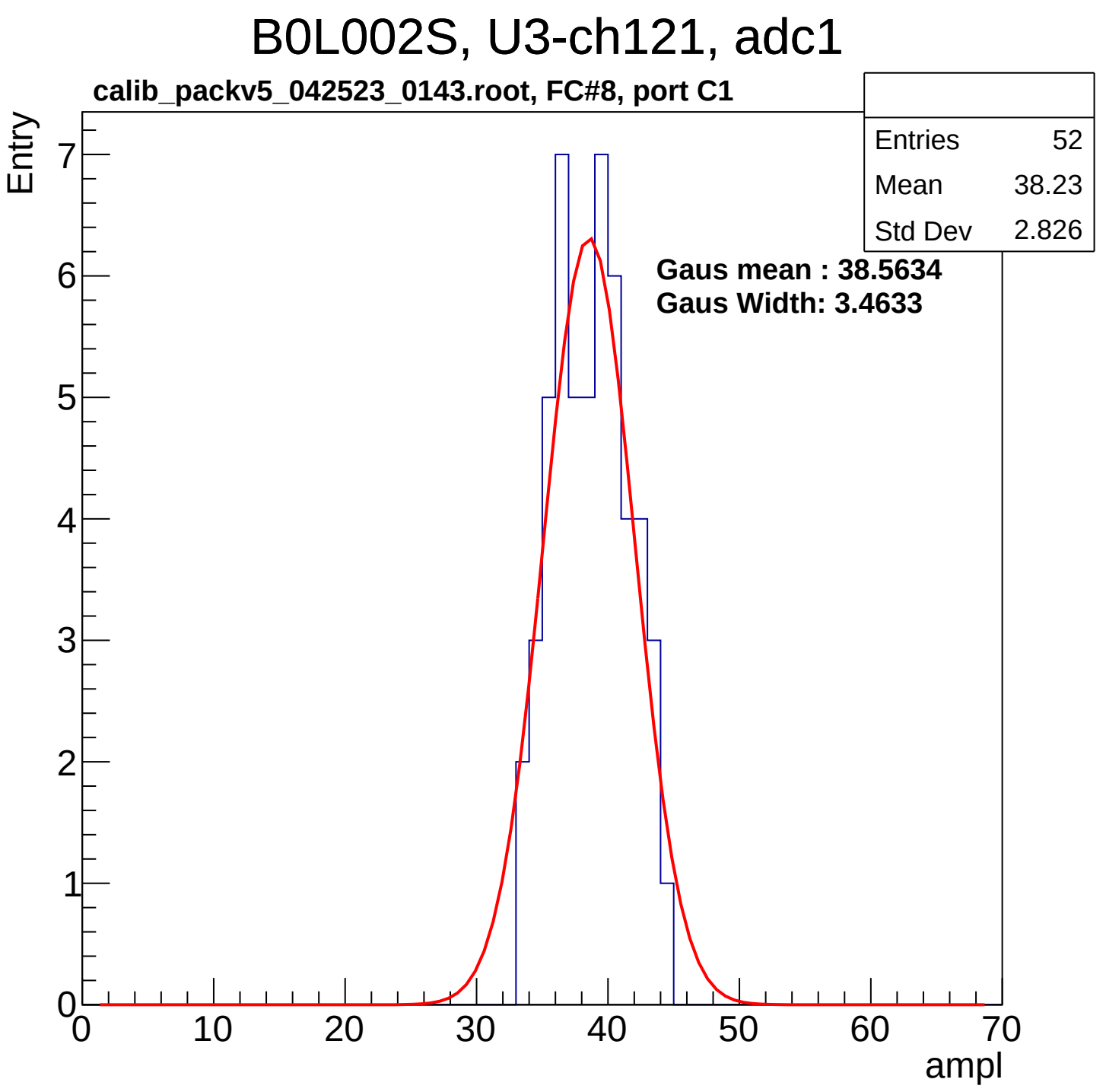

B0L002S, U3-ch120, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch120, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch121, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 31.24
Std Dev 3.701
Gaus mean : 31.6011
Gaus Width: 4.4559
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch121, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 38.23
Std Dev 2.826
Gaus mean : 38.5634
Gaus Width: 3.4633
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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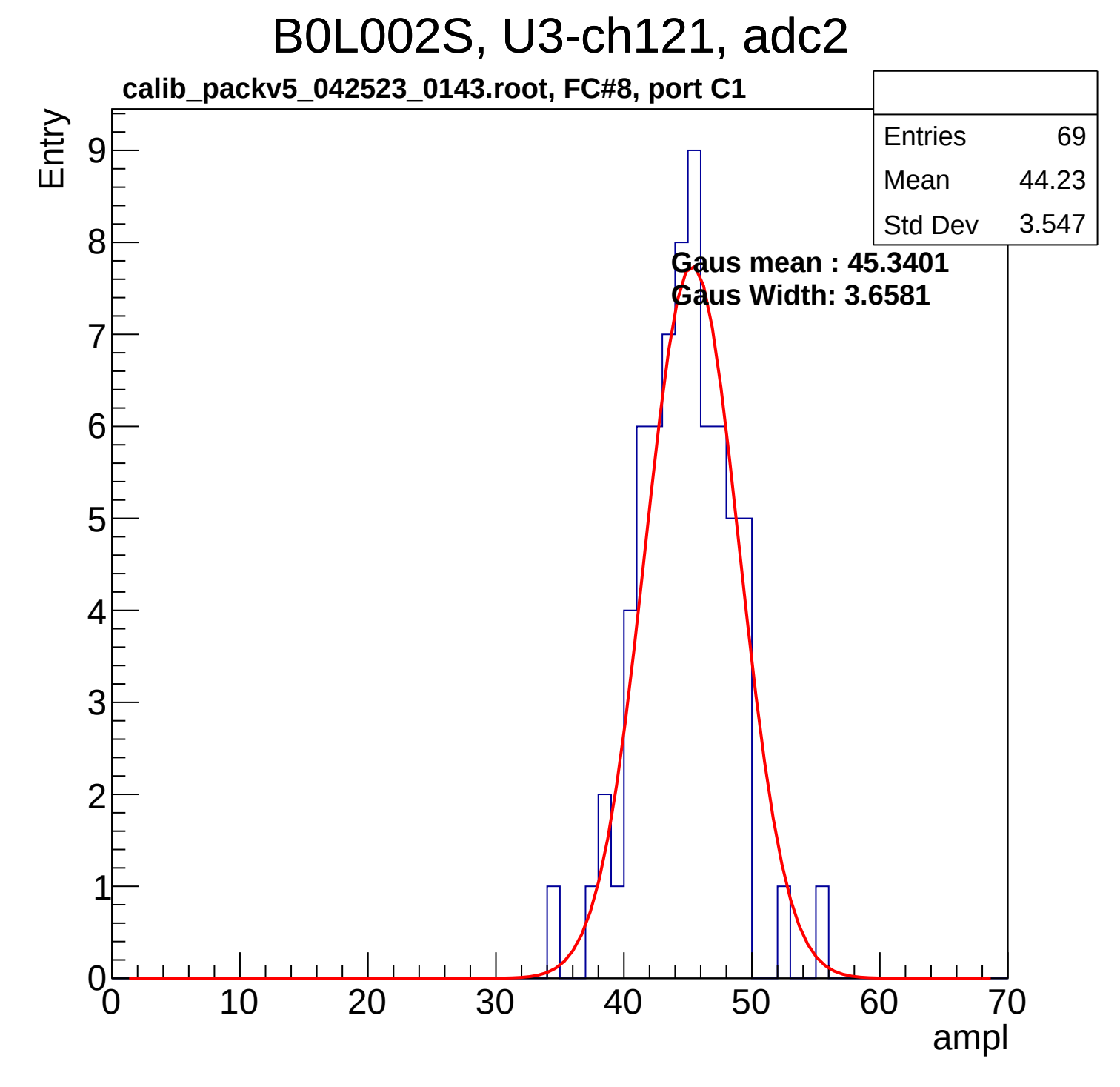
B0L002S, U3-ch121, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.23
Std Dev 3.547
Gaus mean : 45.3401
Gaus Width: 3.6581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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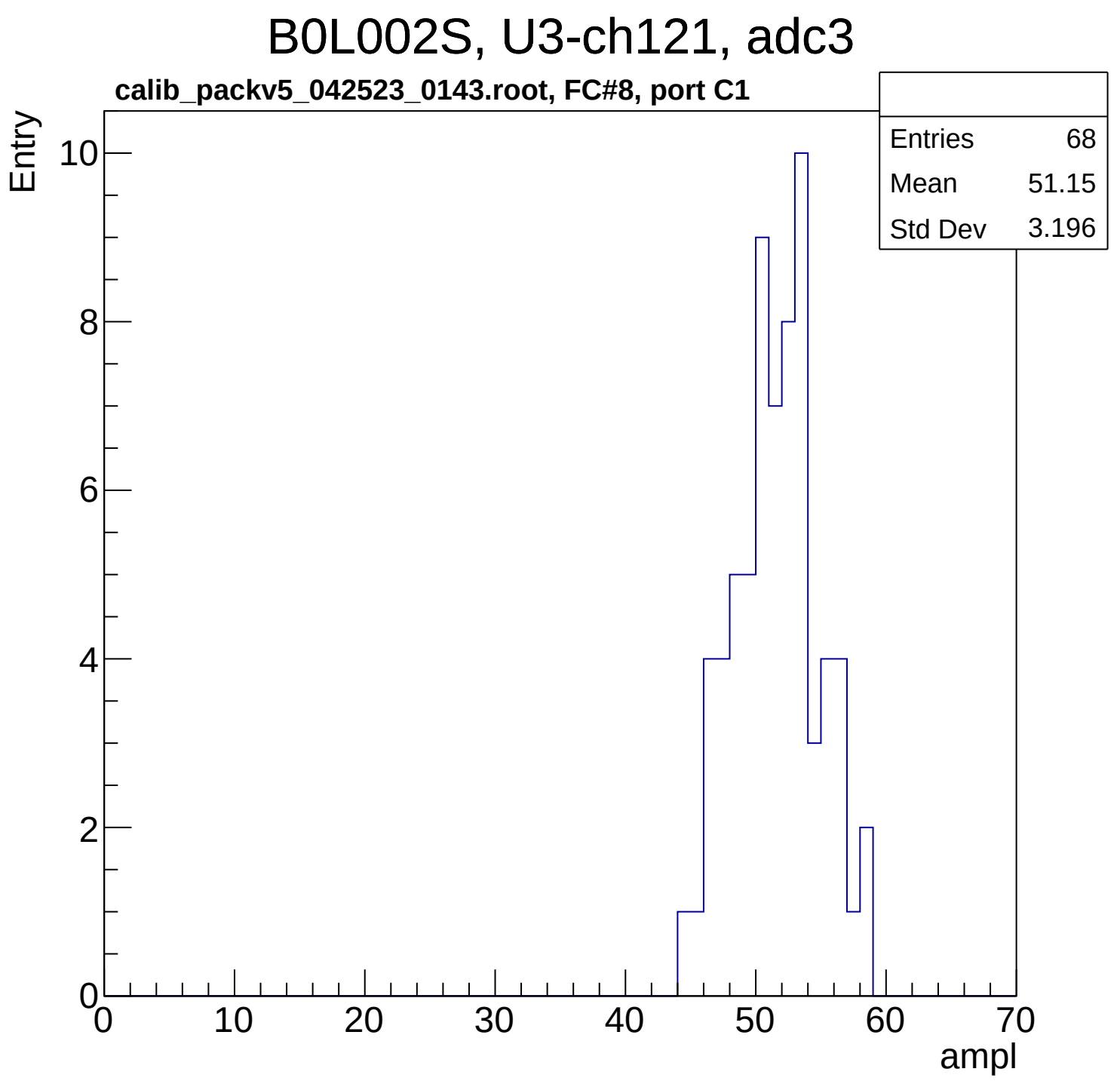
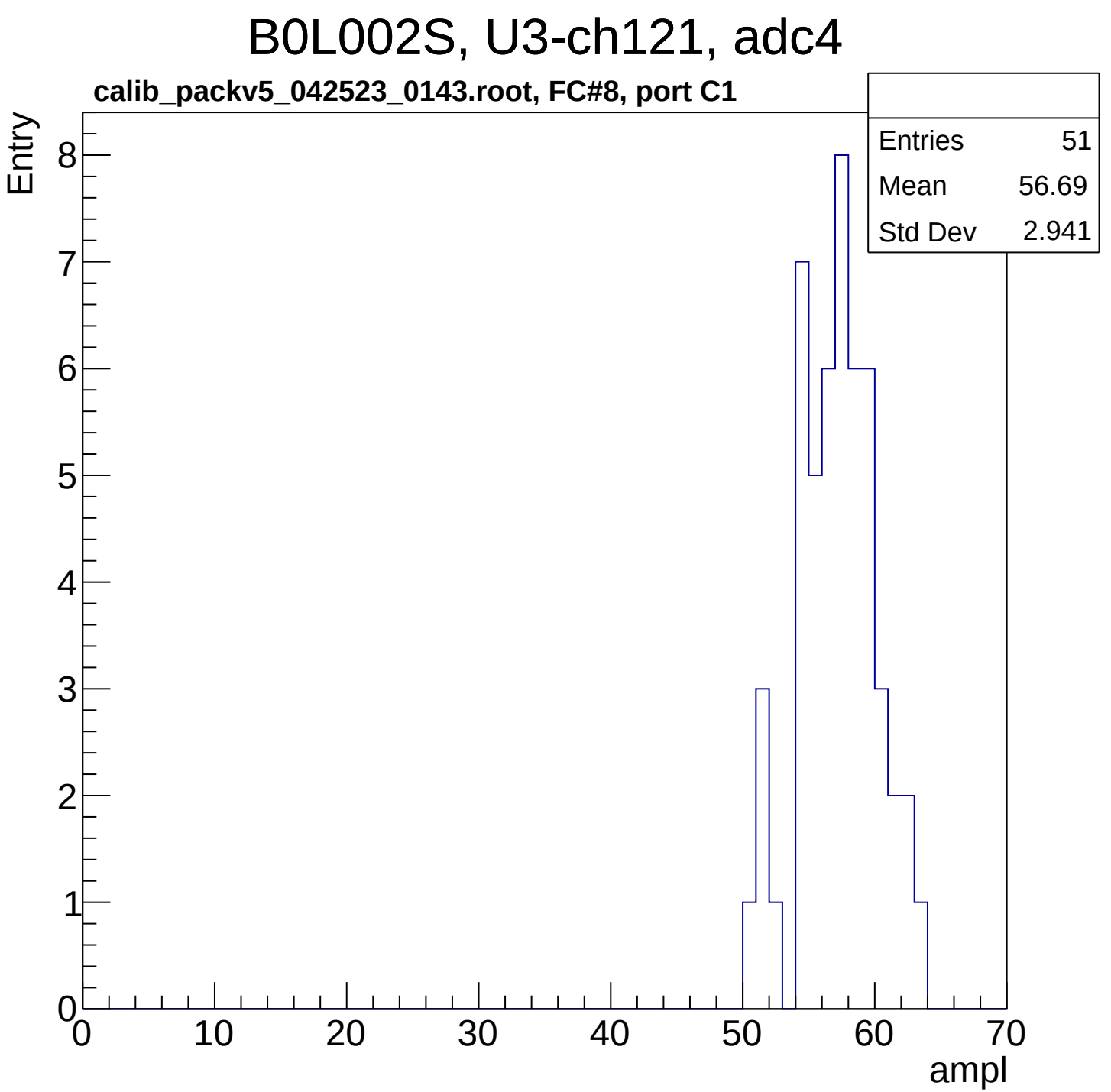

B0L002S, U3-ch121, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.15
Std Dev 3.196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch121, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.69
Std Dev 2.941
Entry
ampl
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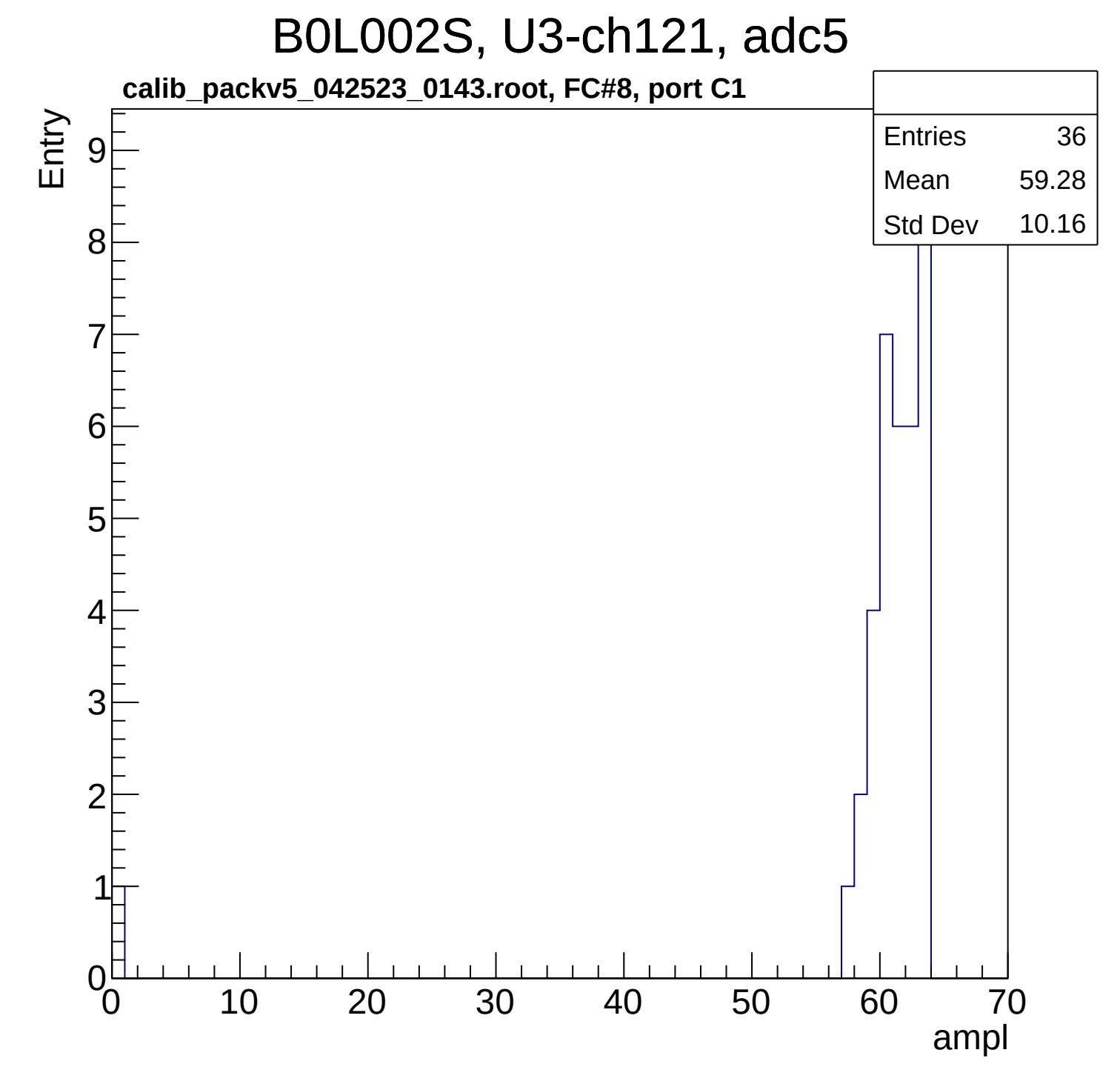
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch121, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.28
Std Dev 10.16
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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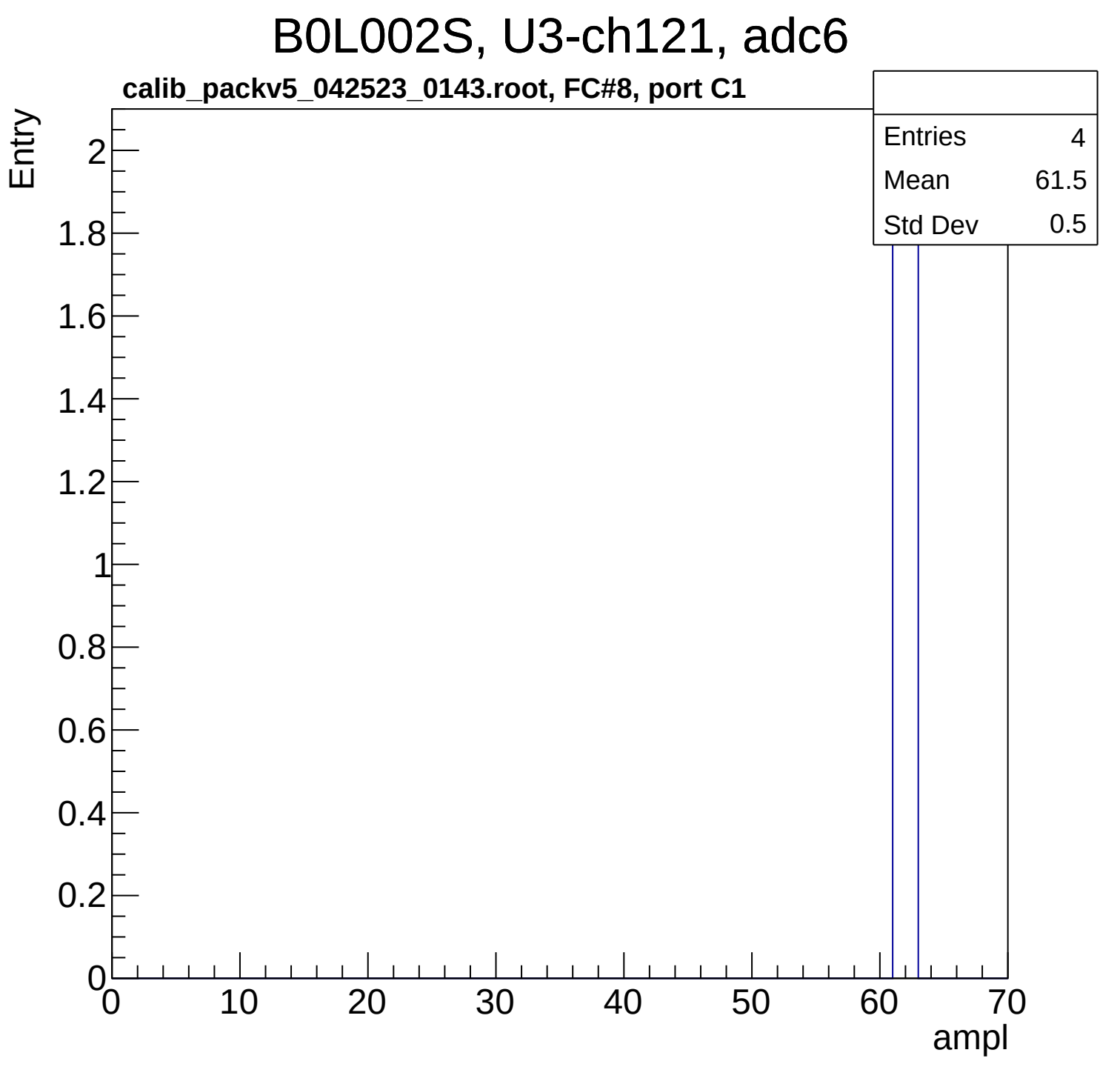
B0L002S, U3-ch121, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 61.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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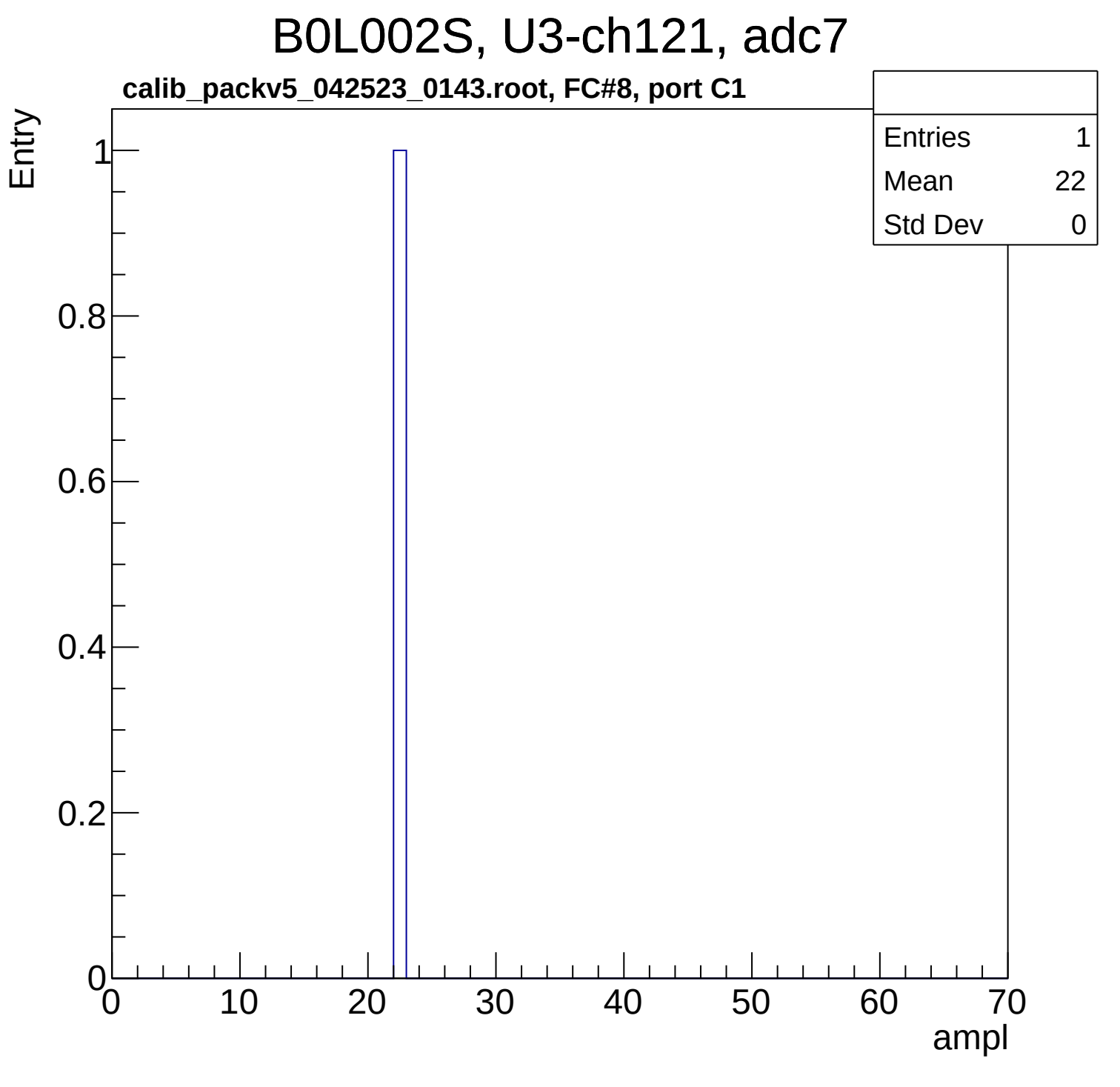
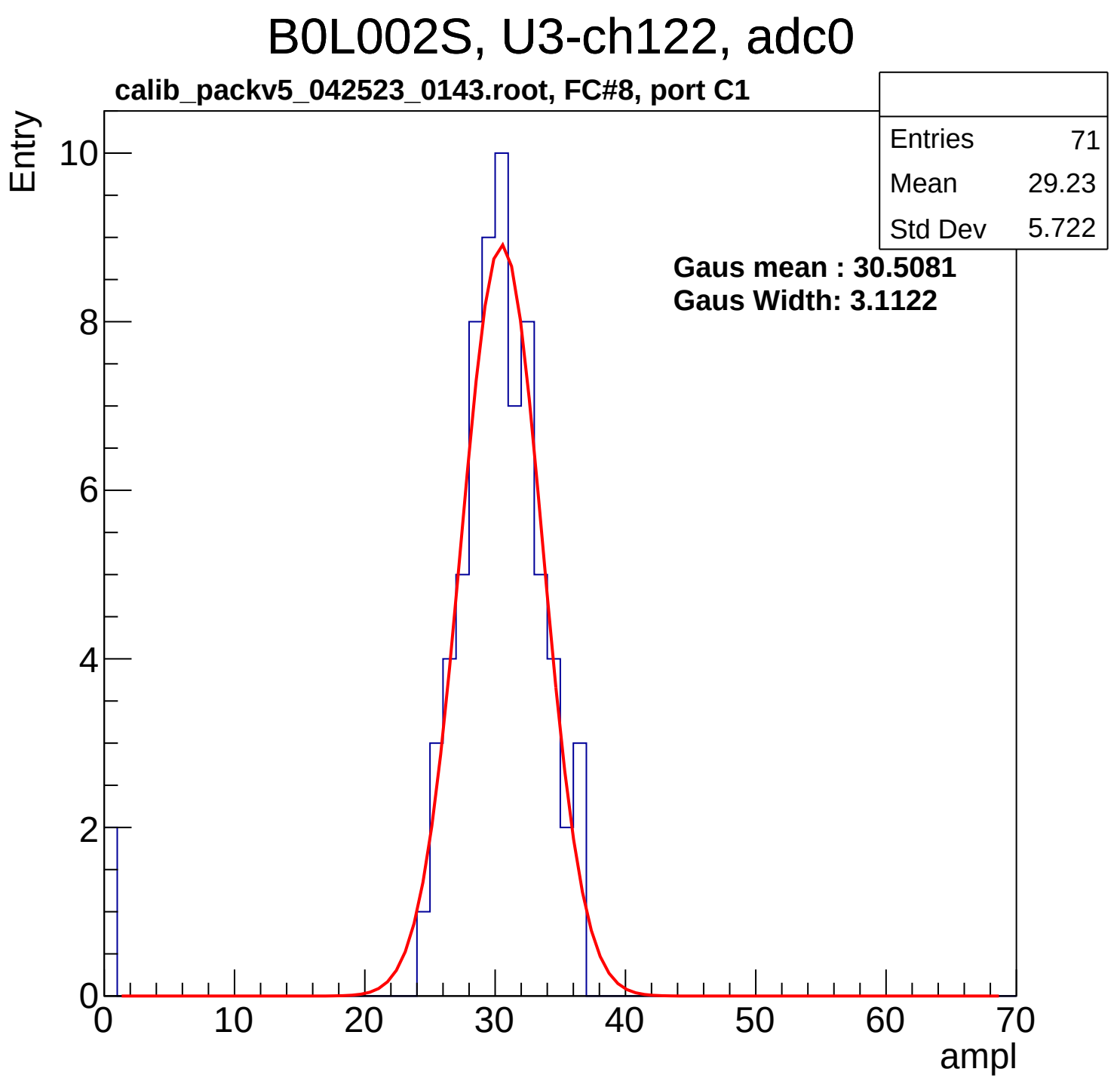
40
50
60
70


B0L002S, U3-ch121, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch122, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 29.23
Std Dev 5.722
Gaus mean : 30.5081
Gaus Width: 3.1122
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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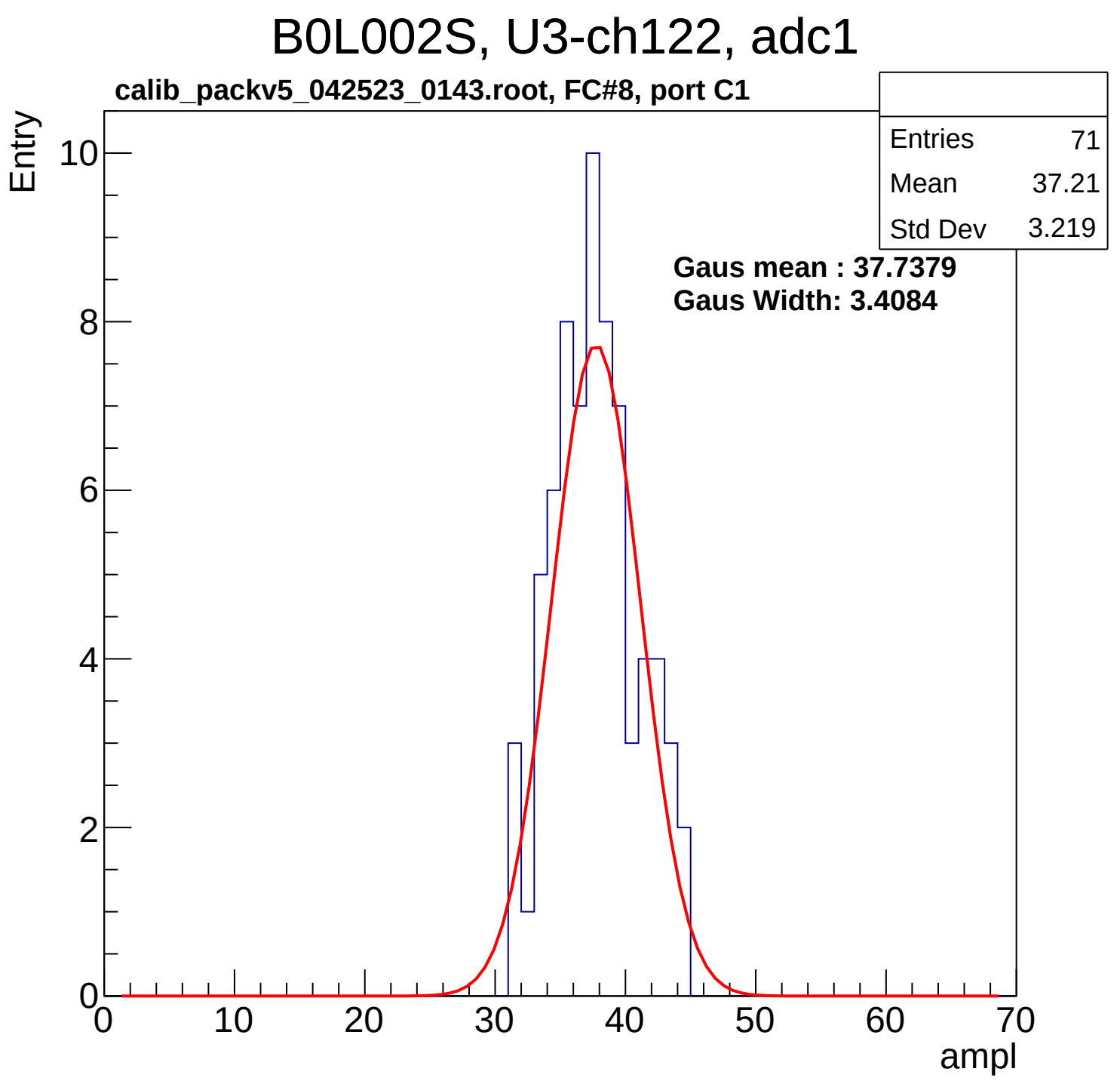
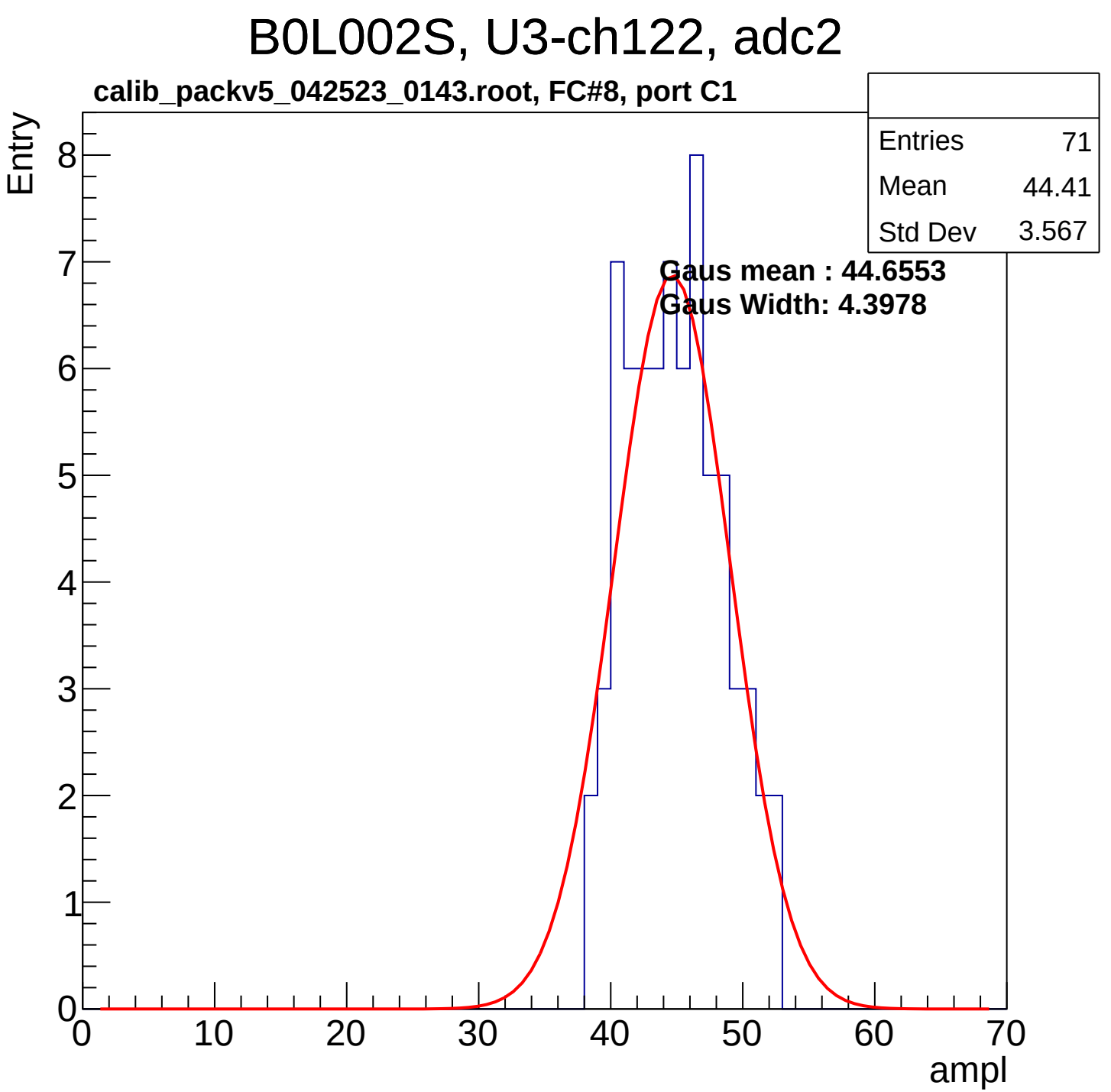

B0L002S, U3-ch122, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37.21
Std Dev 3.219
Gaus mean : 37.7379
Gaus Width: 3.4084
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch122, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 44.41
Std Dev 3.567
Gaus mean : 44.6553
Gaus Width: 4.3978
Entry
ampl
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1
2
3
4
5
6
7
8
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60
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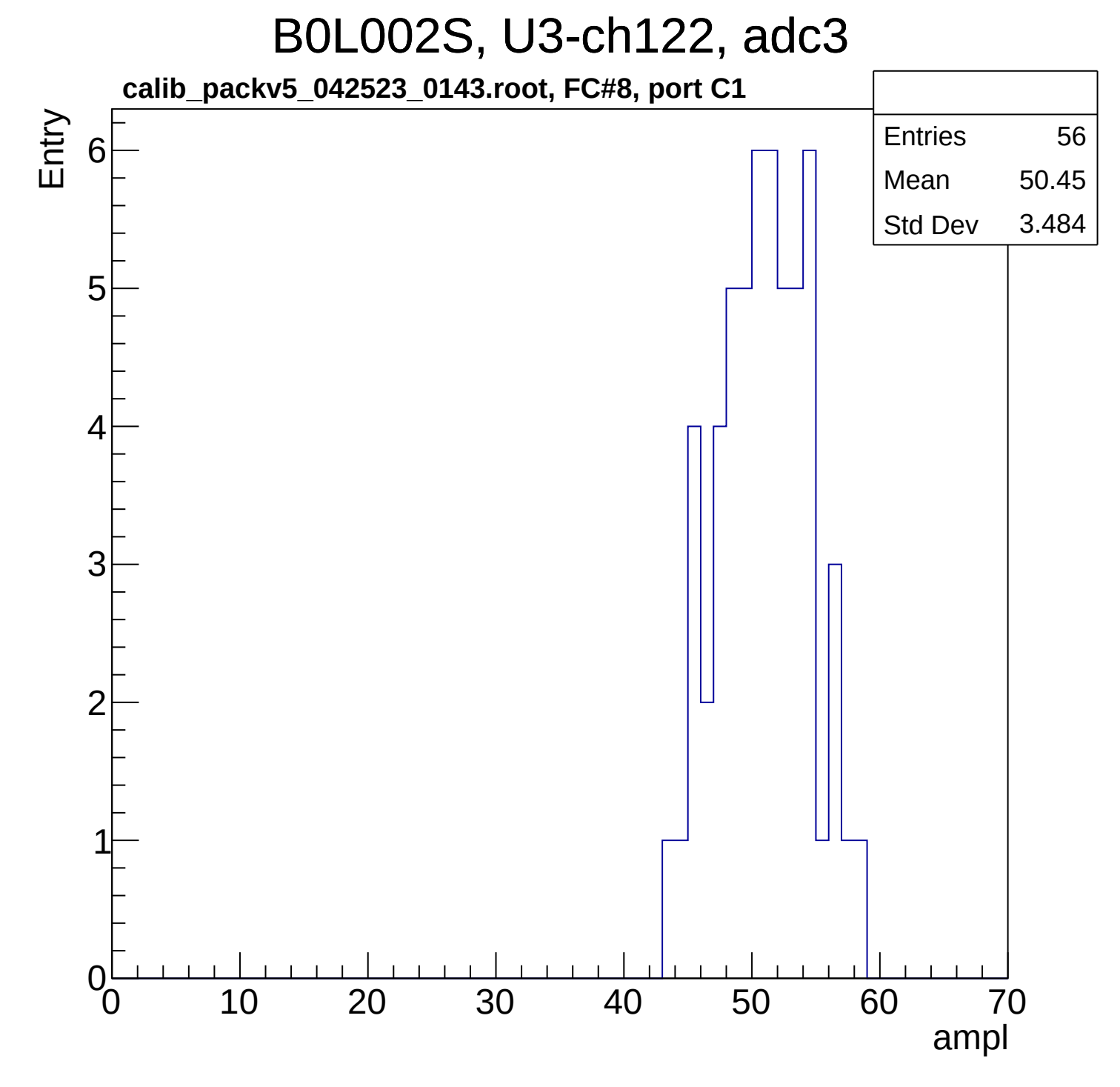
B0L002S, U3-ch122, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 50.45
Std Dev 3.484
Entry
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1
2
3
4
5
6
0
10
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30
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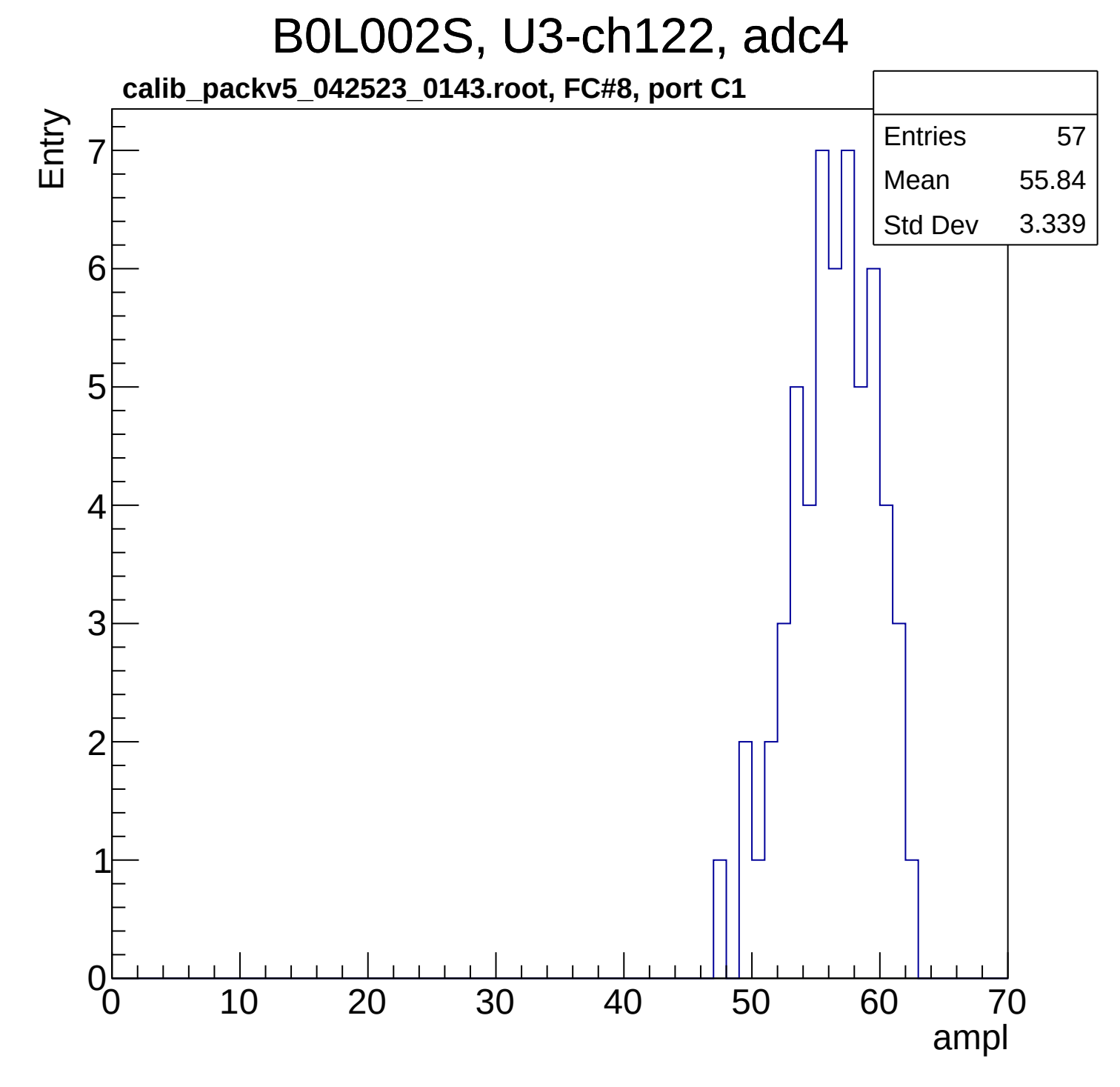
50
60
70
ampl


B0L002S, U3-ch122, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.84
Std Dev 3.339
Entry
ampl
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3
4
5
6
7
0
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60
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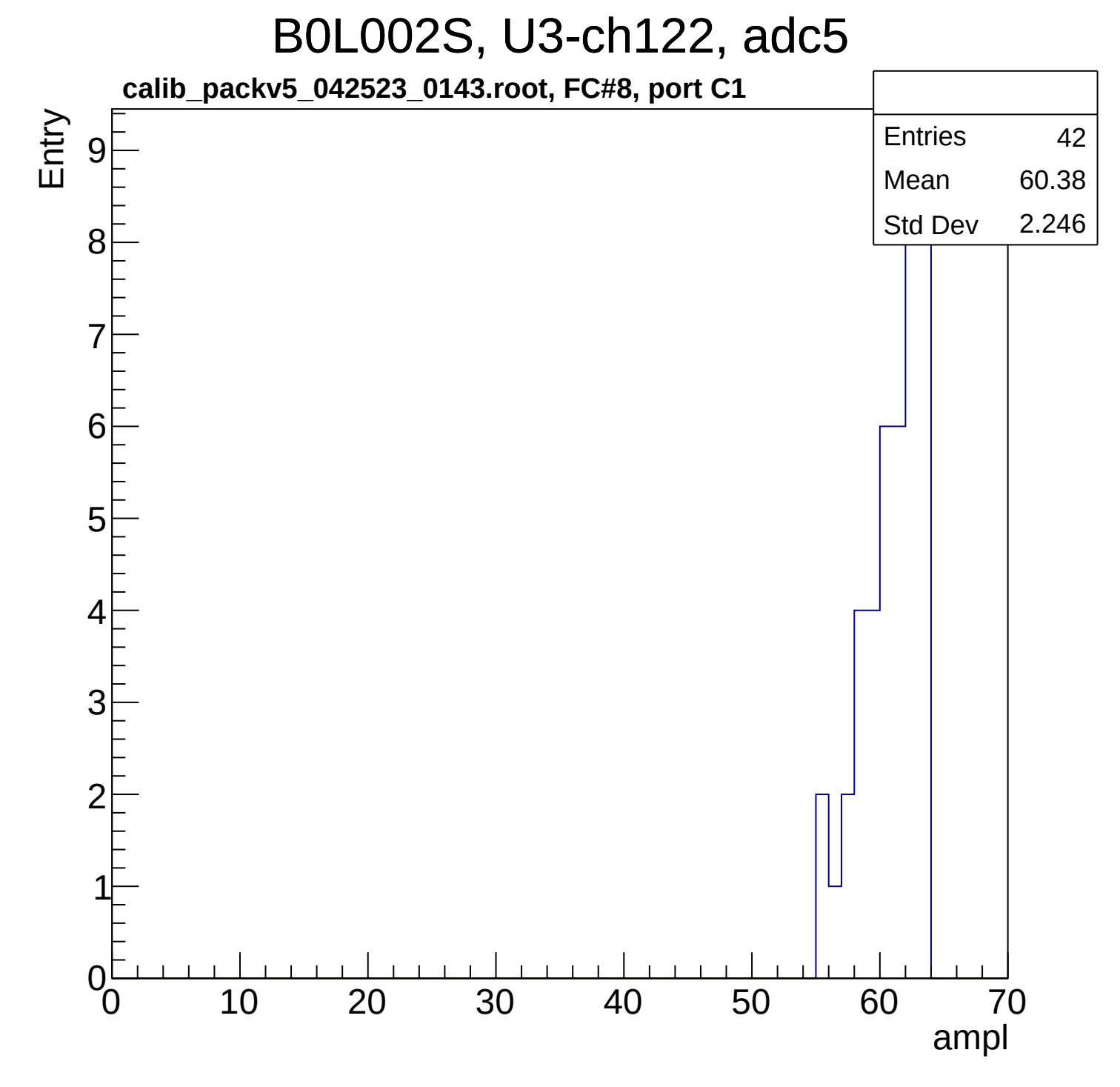
B0L002S, U3-ch122, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 60.38
Std Dev 2.246
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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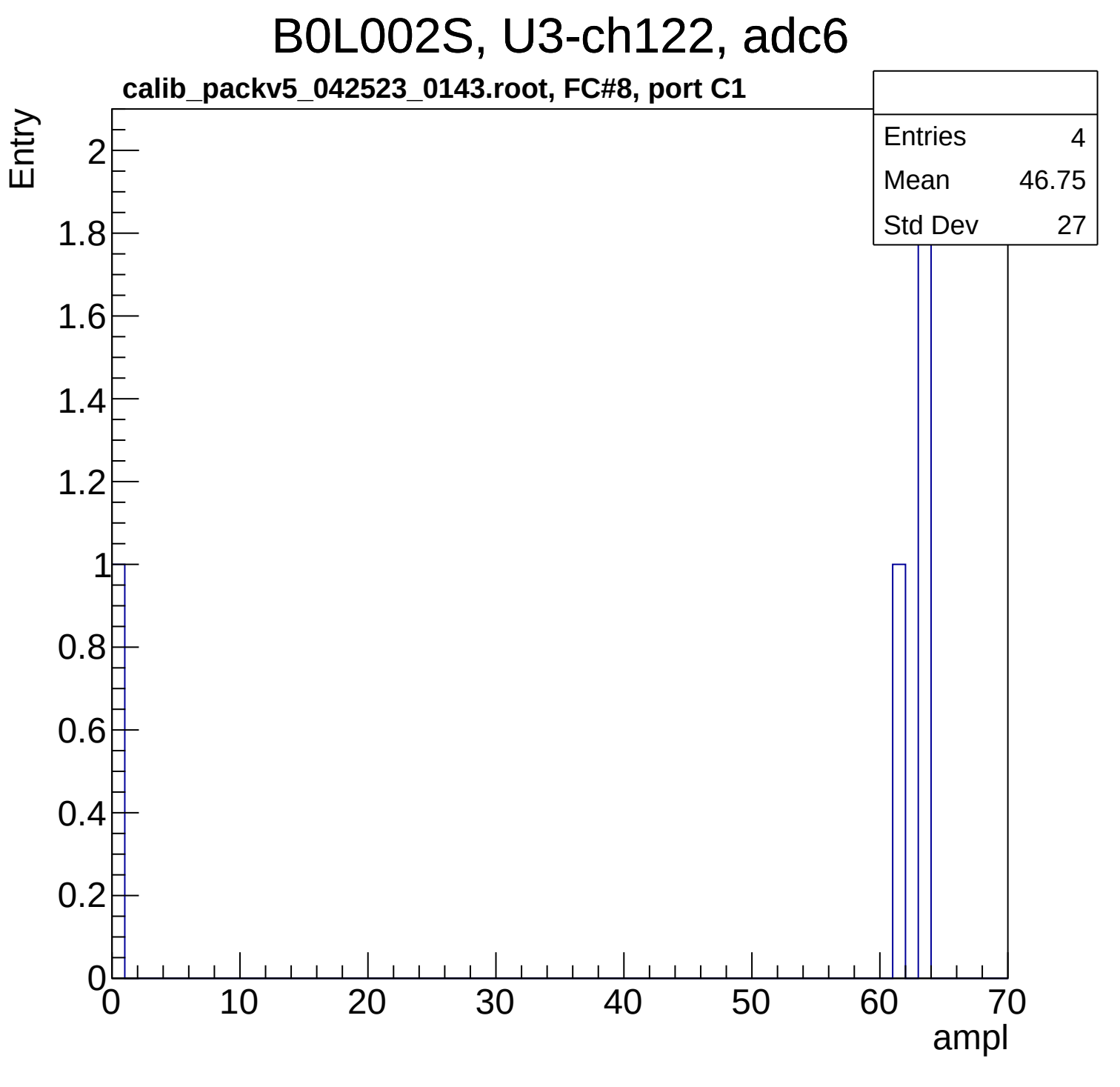
B0L002S, U3-ch122, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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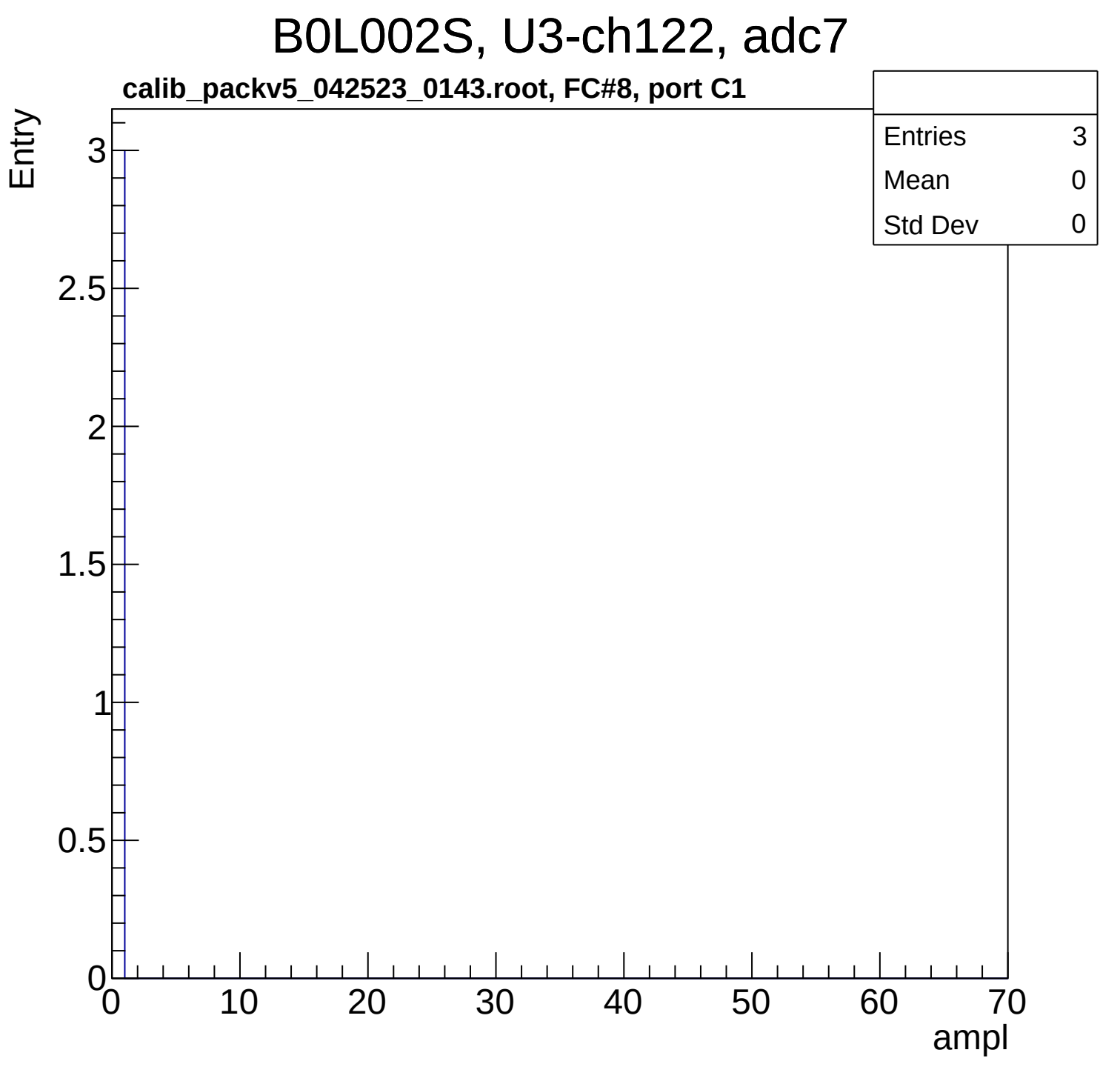
B0L002S, U3-ch122, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
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2.5
2
1.5
1
0.5
0
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10
20
30
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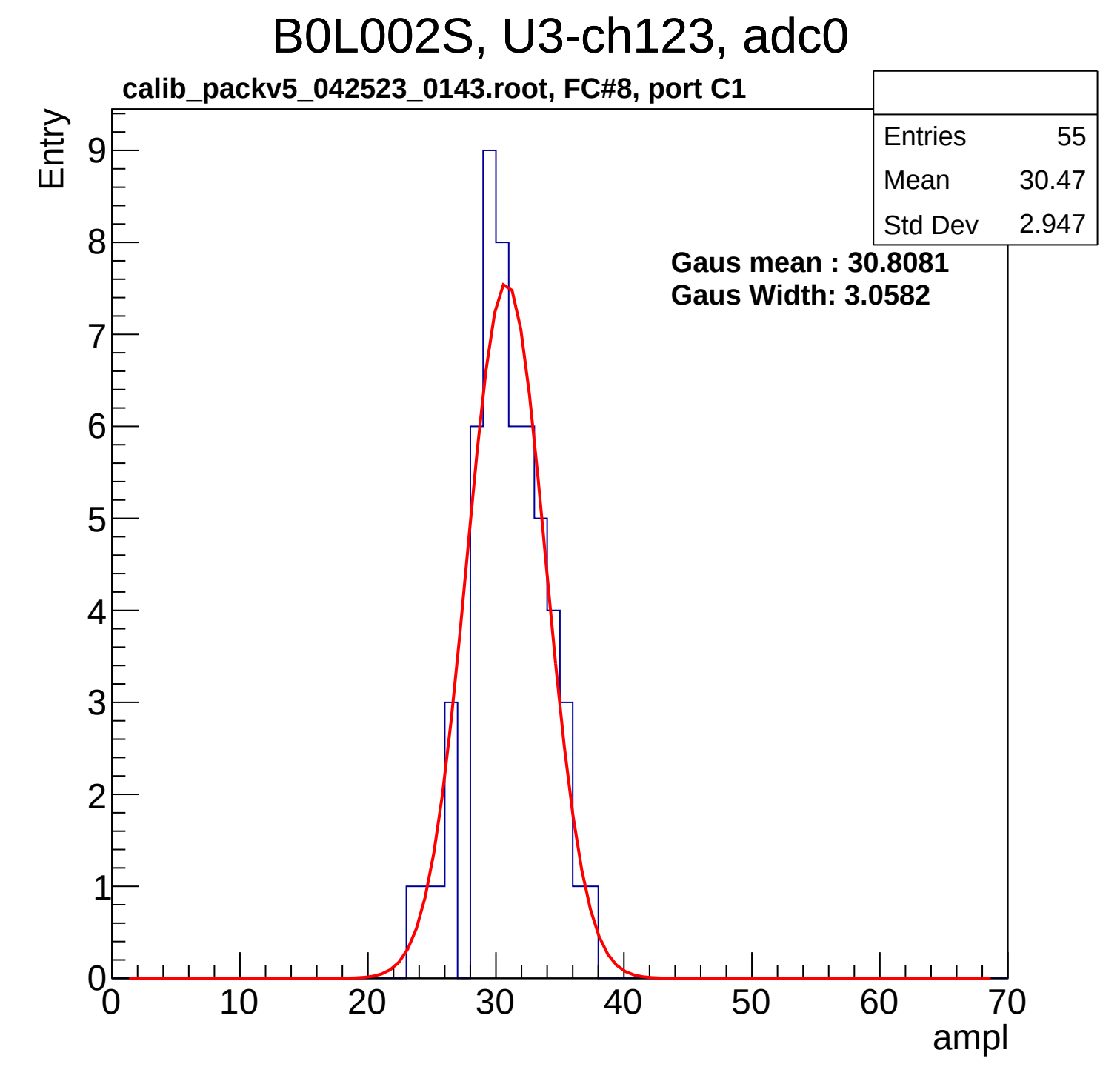
50
60
70
ampl


B0L002S, U3-ch123, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 30.47
Std Dev 2.947
Gaus mean : 30.8081
Gaus Width: 3.0582
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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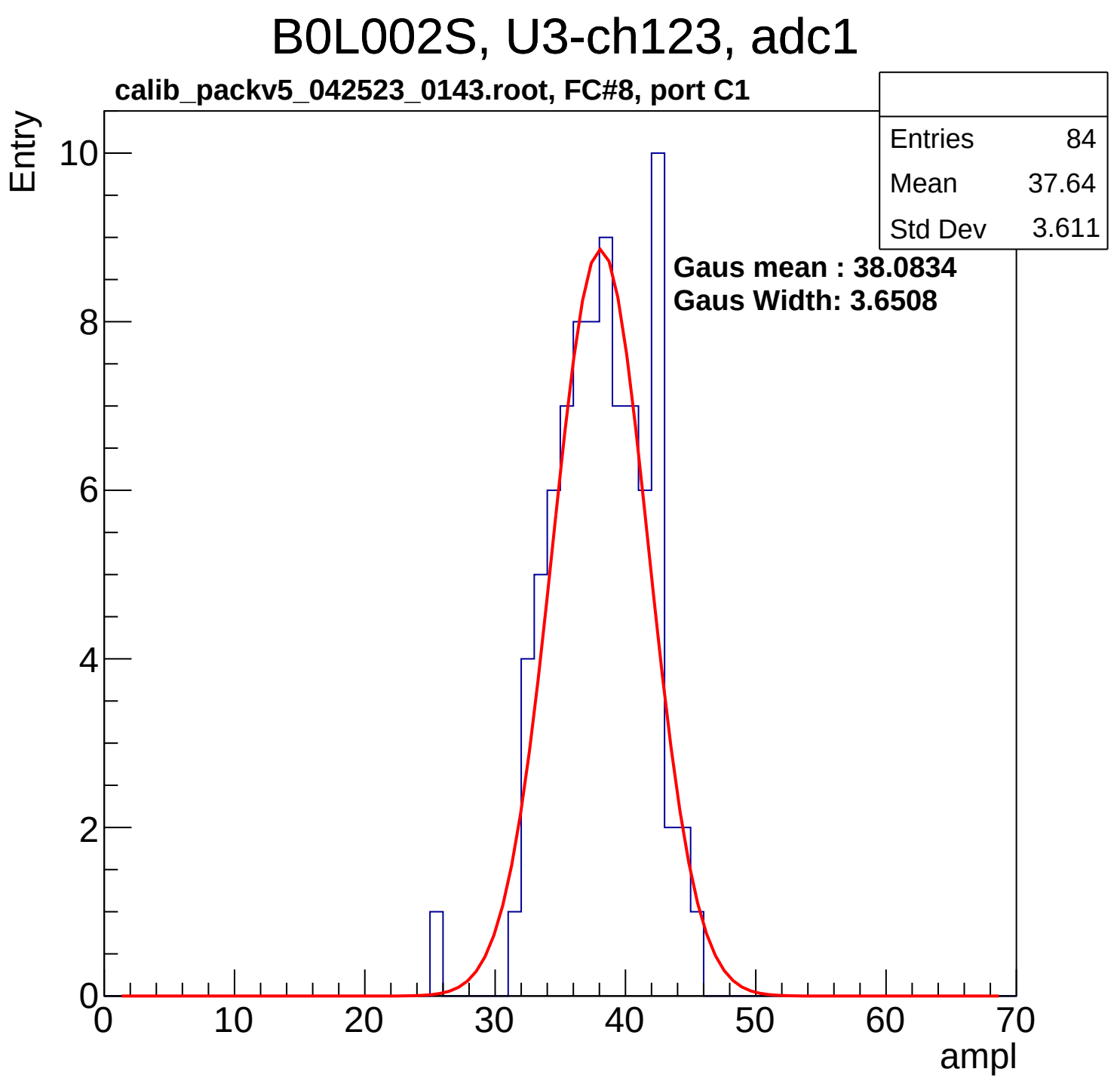
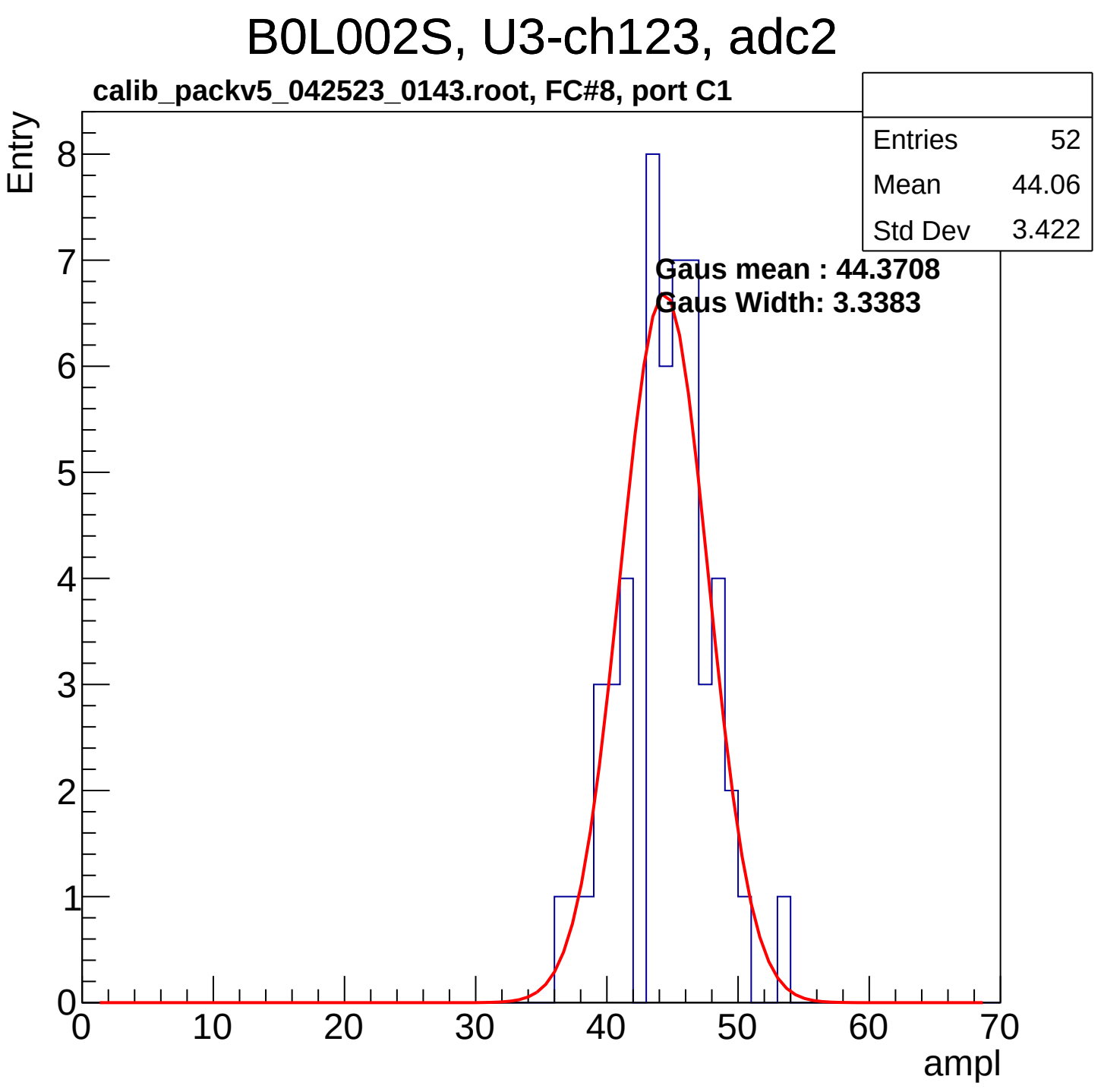

B0L002S, U3-ch123, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 37.64
Std Dev 3.611
Gaus mean : 38.0834
Gaus Width: 3.6508
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch123, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 44.06
Std Dev 3.422
Gaus mean : 44.3708
Gaus Width: 3.3383
Entry
ampl
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2
3
4
5
6
7
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0
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60
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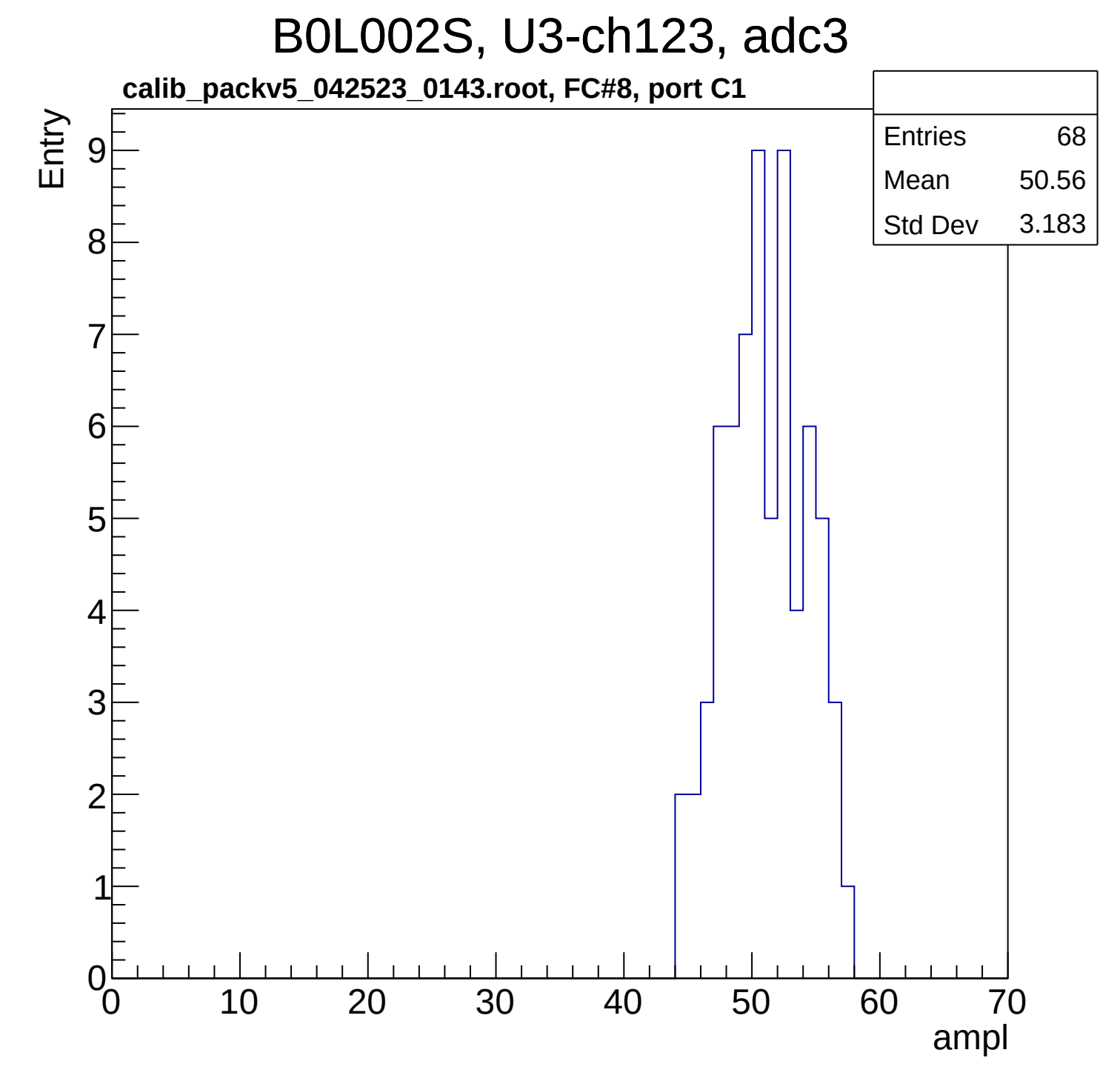
B0L002S, U3-ch123, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.56
Std Dev 3.183
Entry
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3
4
5
6
7
8
9
0
10
20
30
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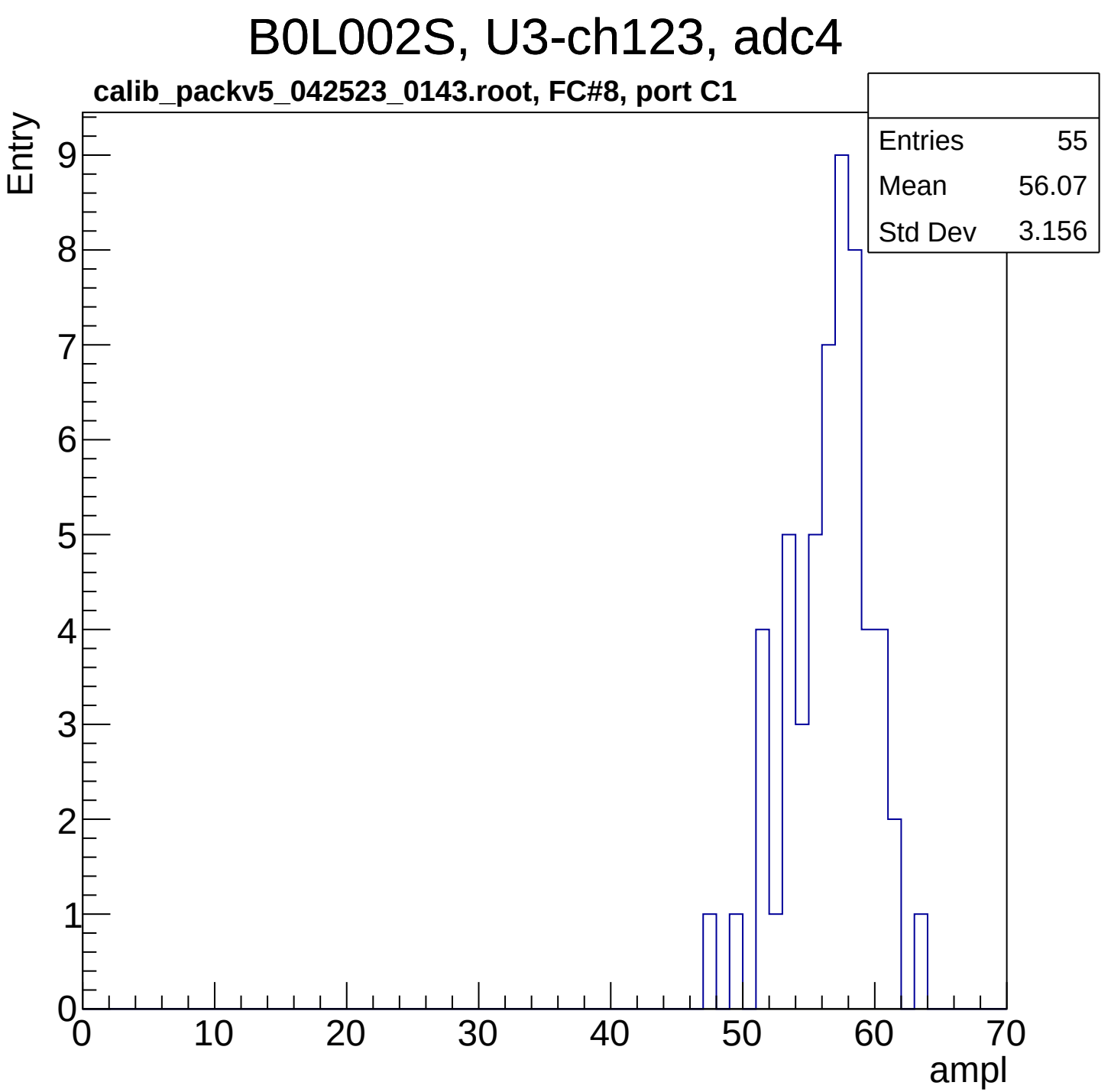
50
60
70
ampl


B0L002S, U3-ch123, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.07
Std Dev 3.156
Entry
ampl
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6
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0
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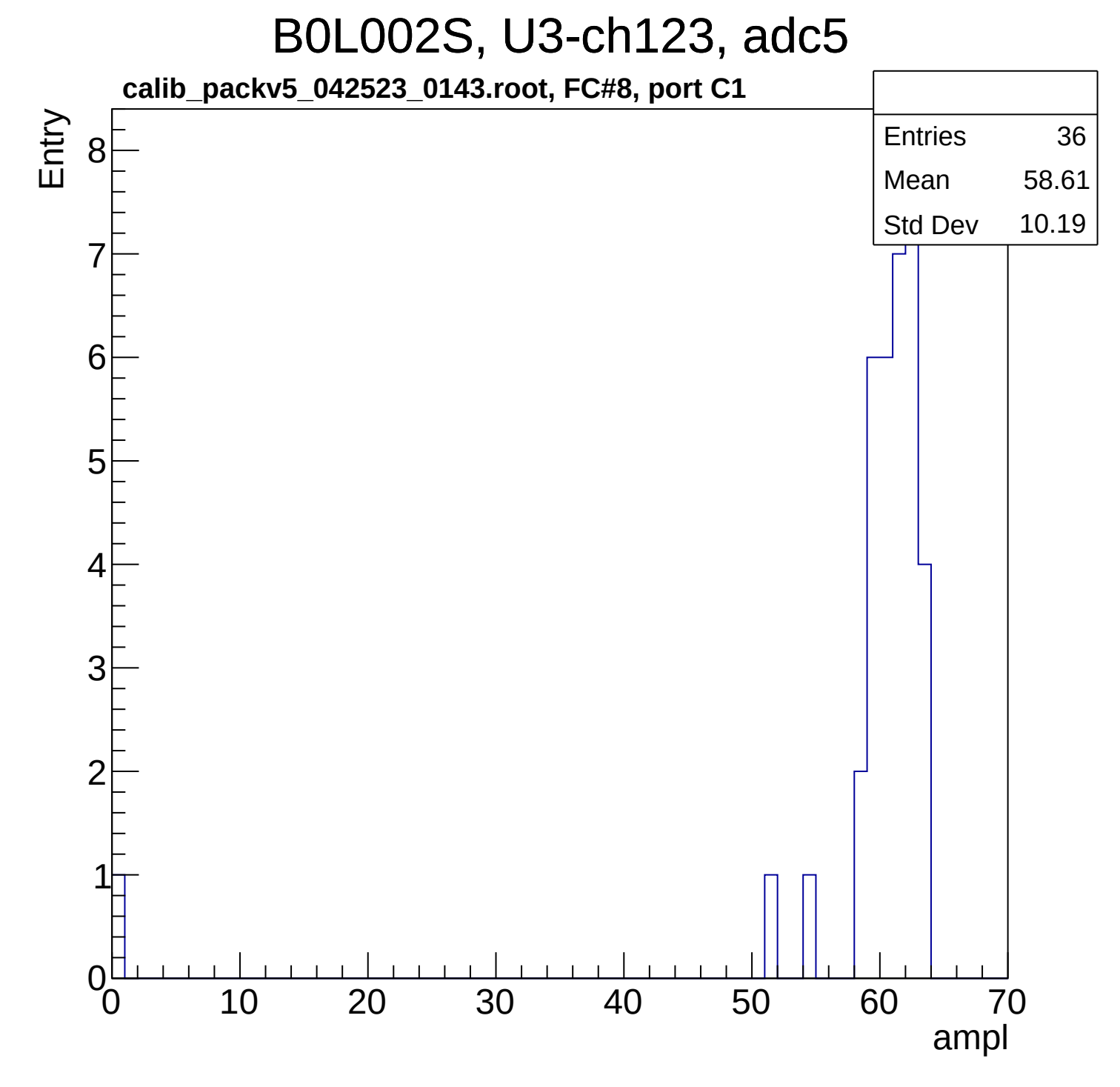
B0L002S, U3-ch123, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.61
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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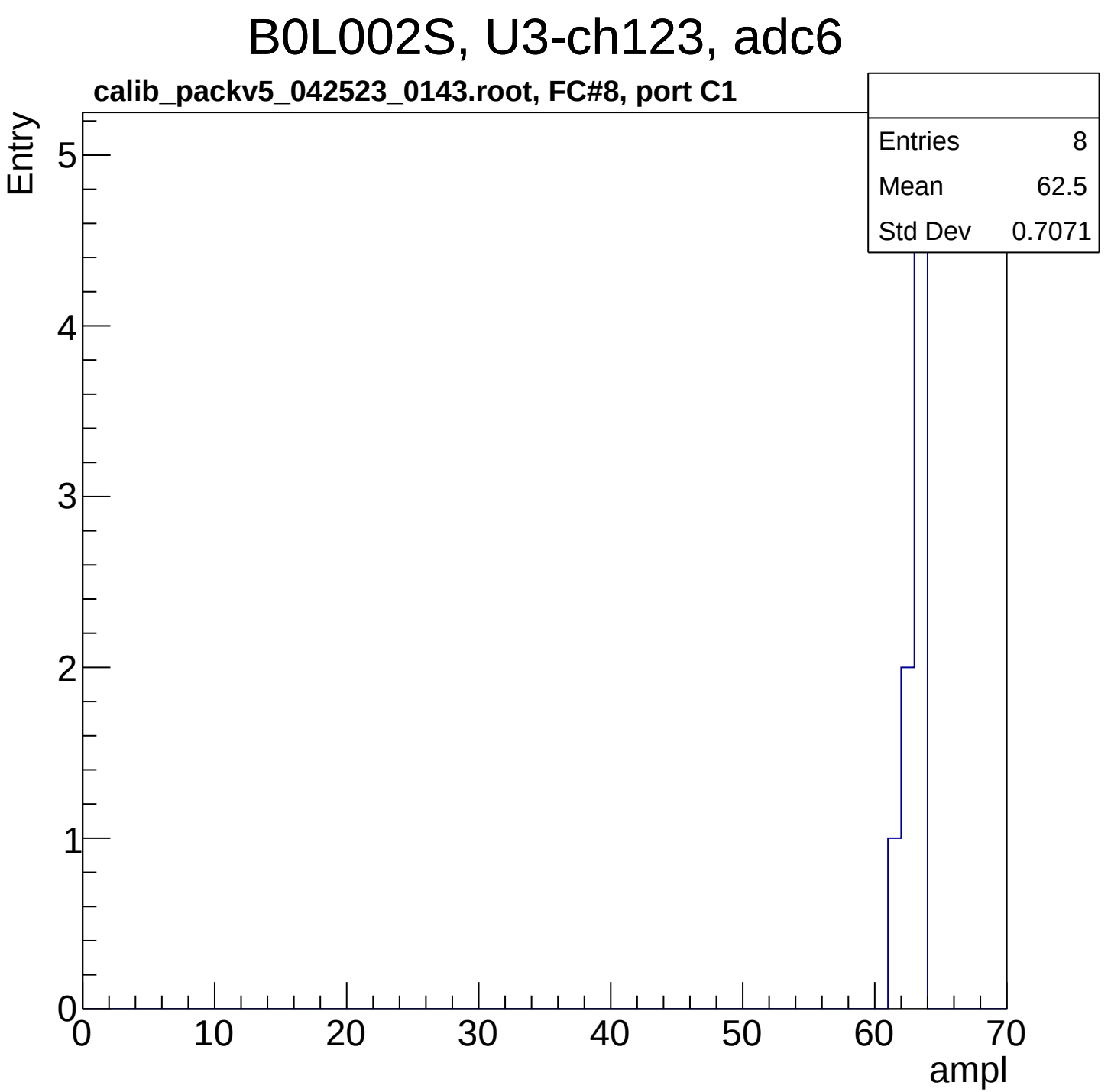
B0L002S, U3-ch123, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 8
Mean 62.5
Std Dev 0.7071
Entry
0
1
2
3
4
5
0
10
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50
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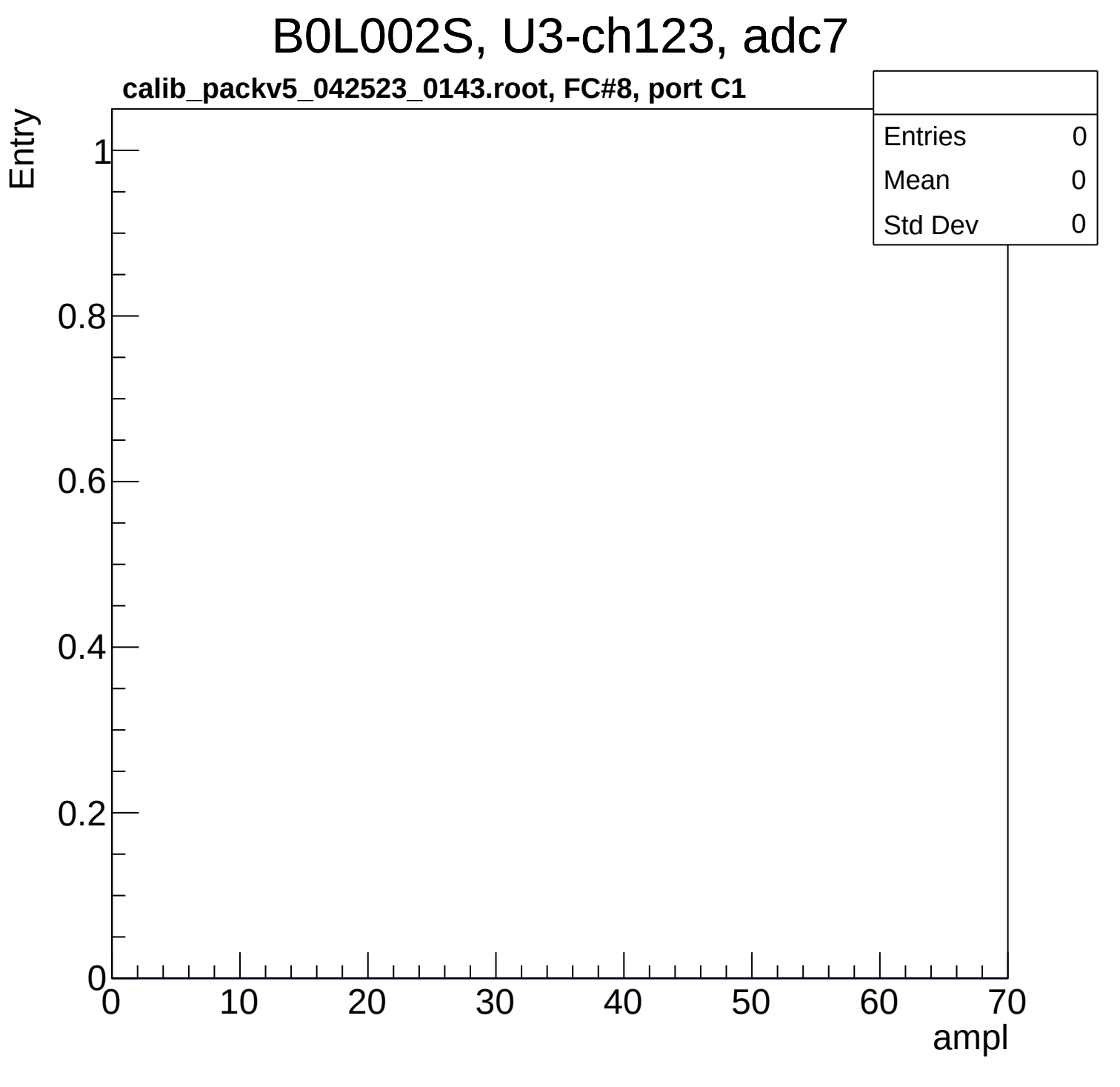
70
ampl


B0L002S, U3-ch123, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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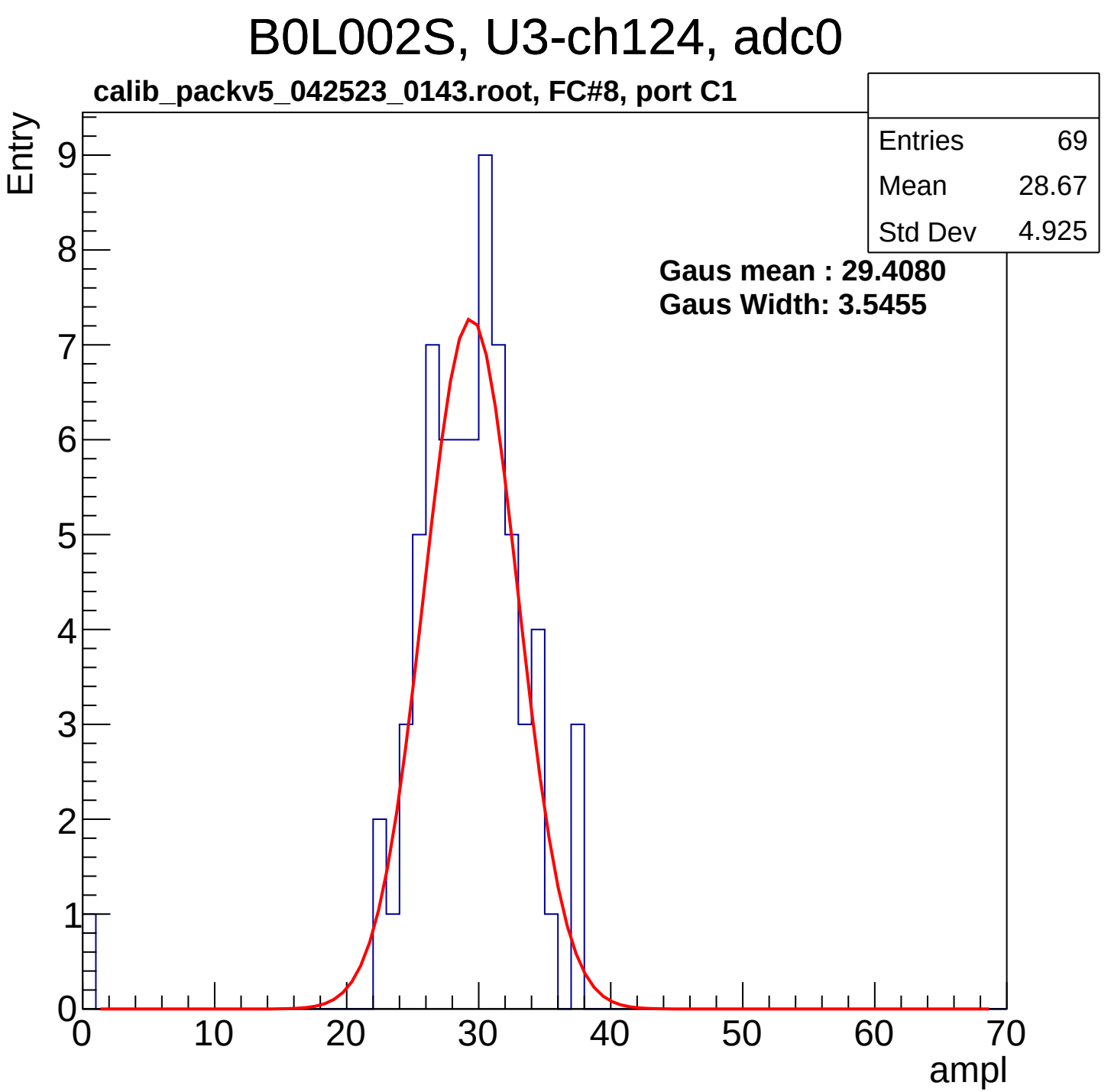
40
50
60
70
ampl


B0L002S, U3-ch124, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 28.67
Std Dev 4.925
Gaus mean : 29.4080
Gaus Width: 3.5455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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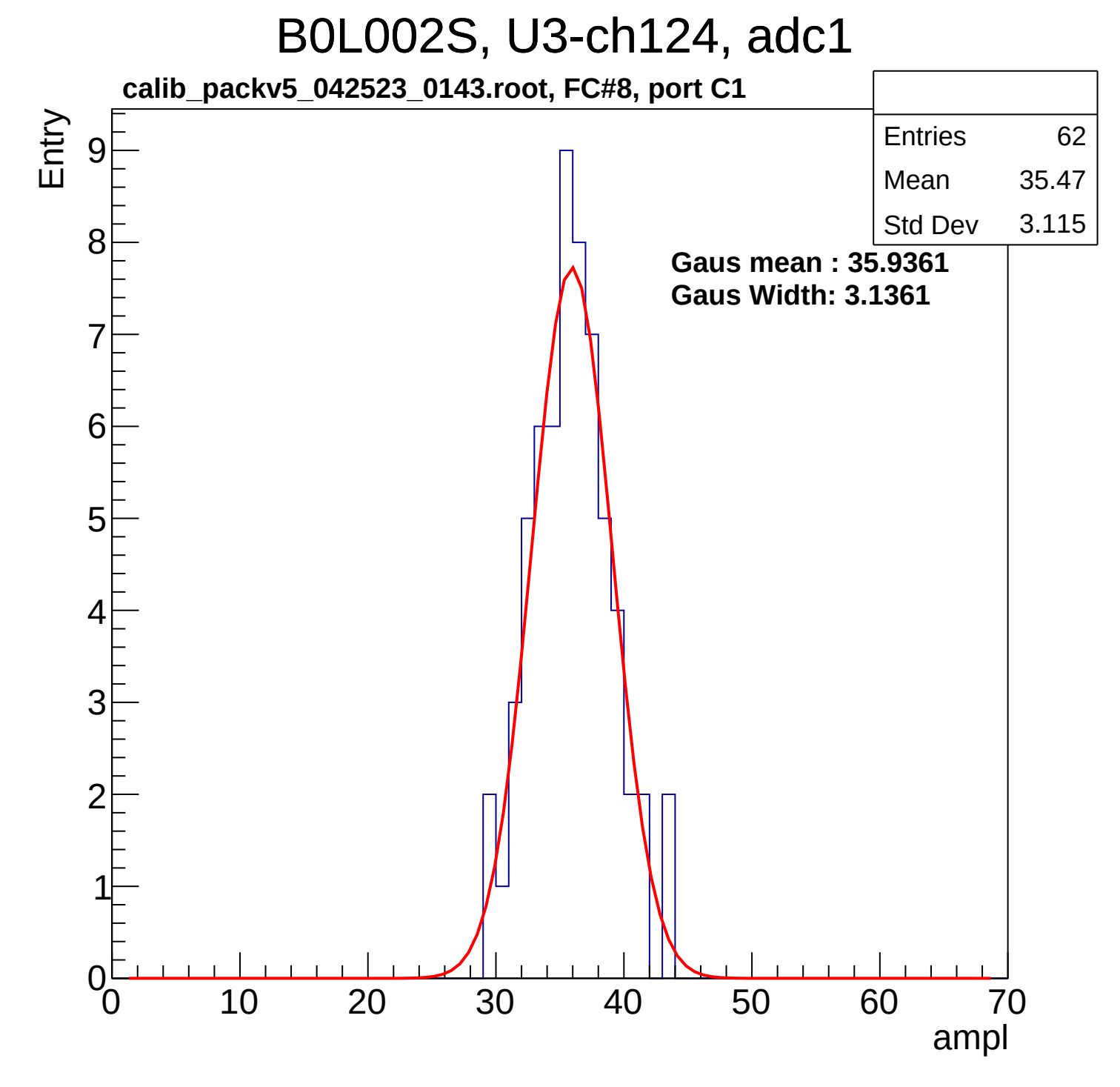
B0L002S, U3-ch124, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 35.47
Std Dev 3.115
Gaus mean : 35.9361
Gaus Width: 3.1361
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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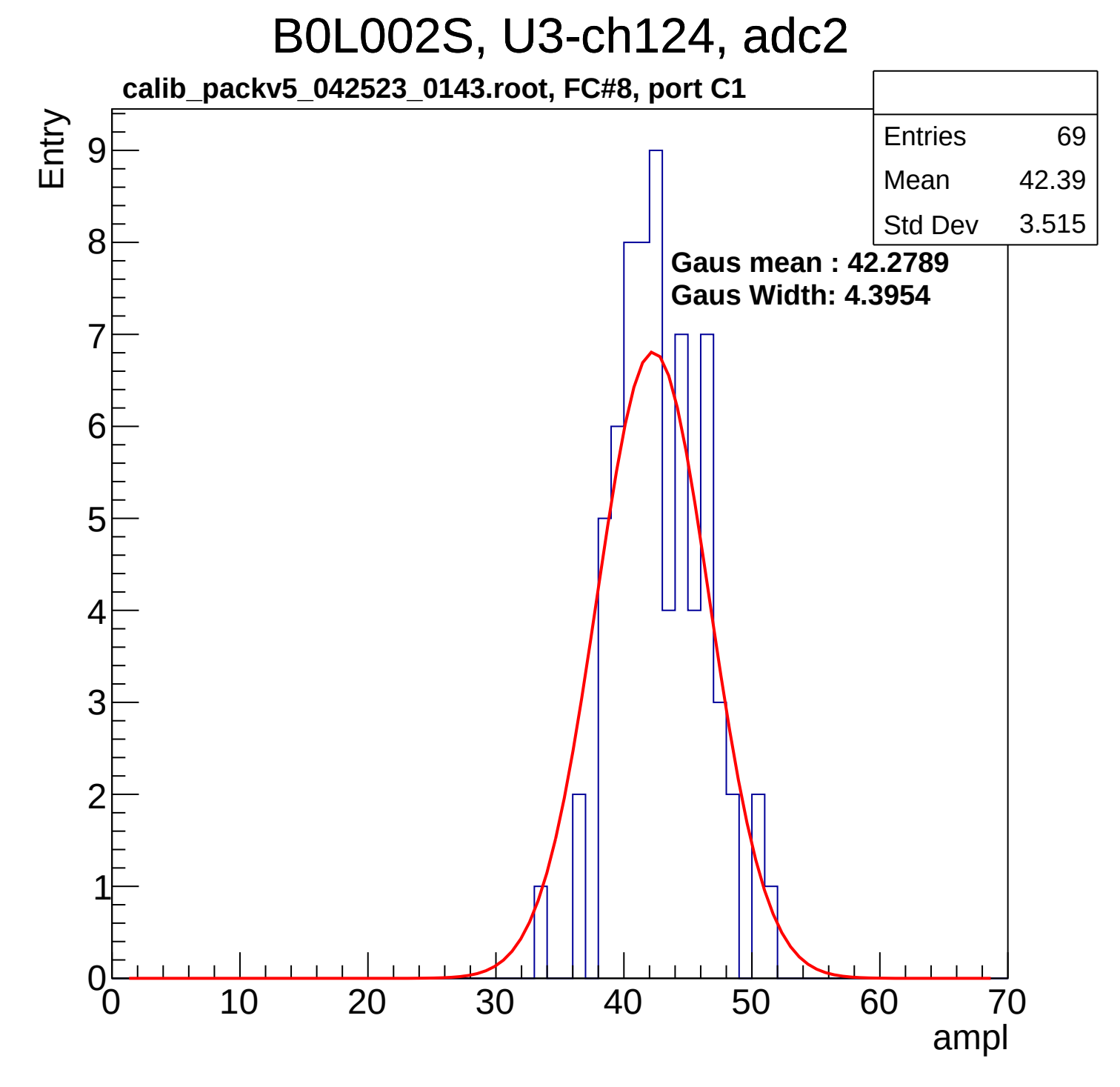
B0L002S, U3-ch124, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 42.39
Std Dev 3.515
Gaus mean : 42.2789
Gaus Width: 4.3954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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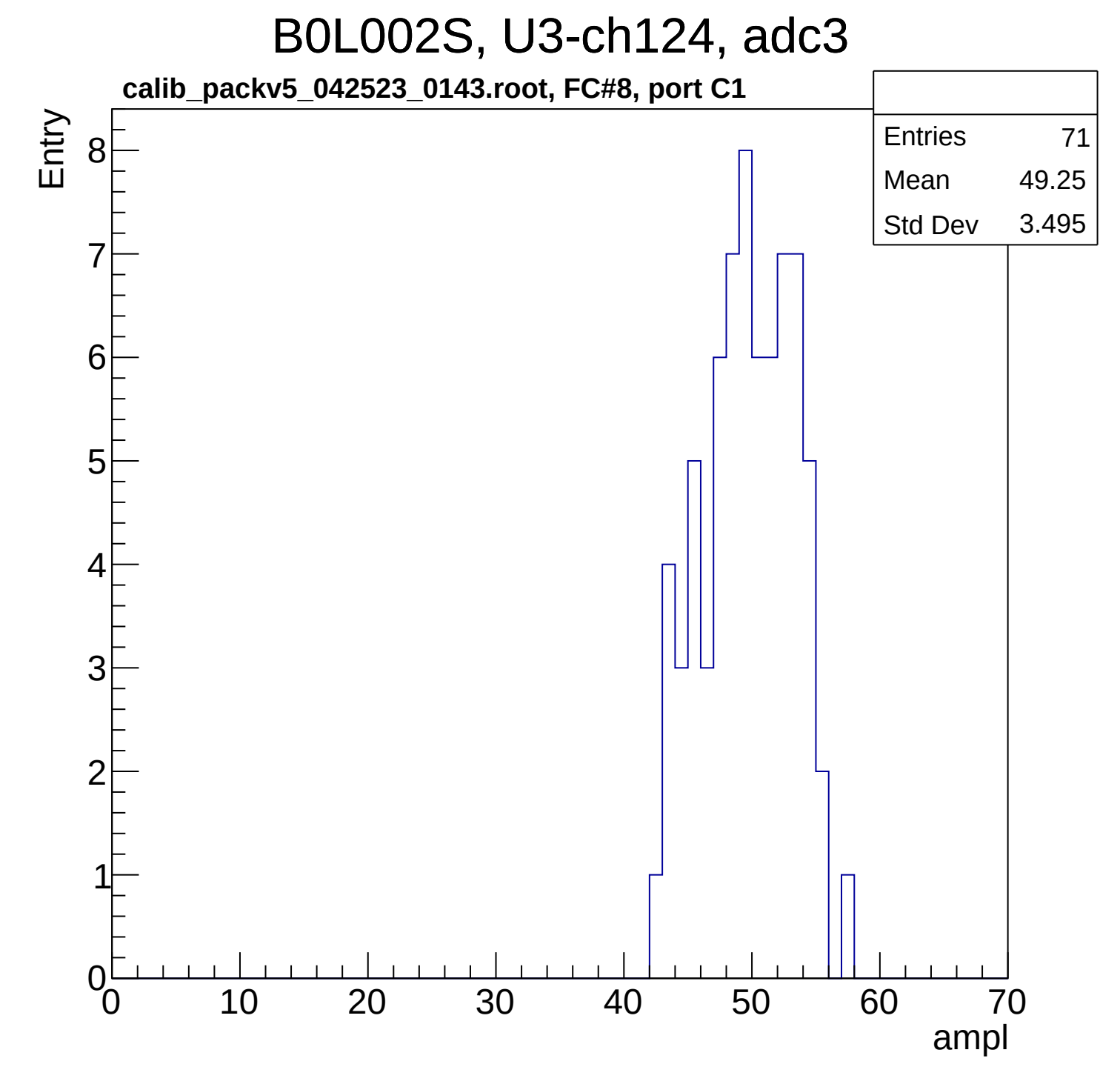
B0L002S, U3-ch124, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 49.25
Std Dev 3.495
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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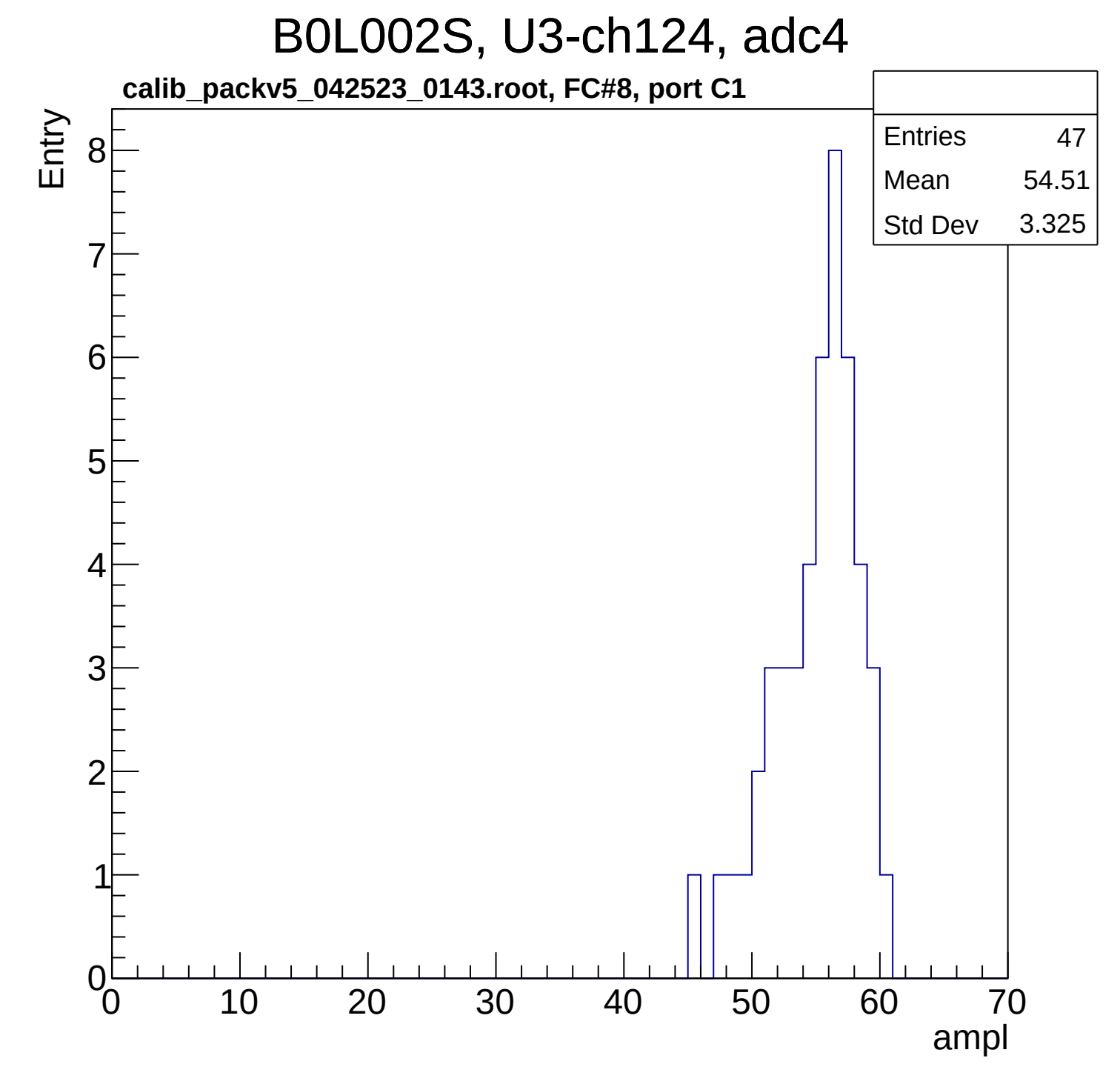
B0L002S, U3-ch124, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 54.51
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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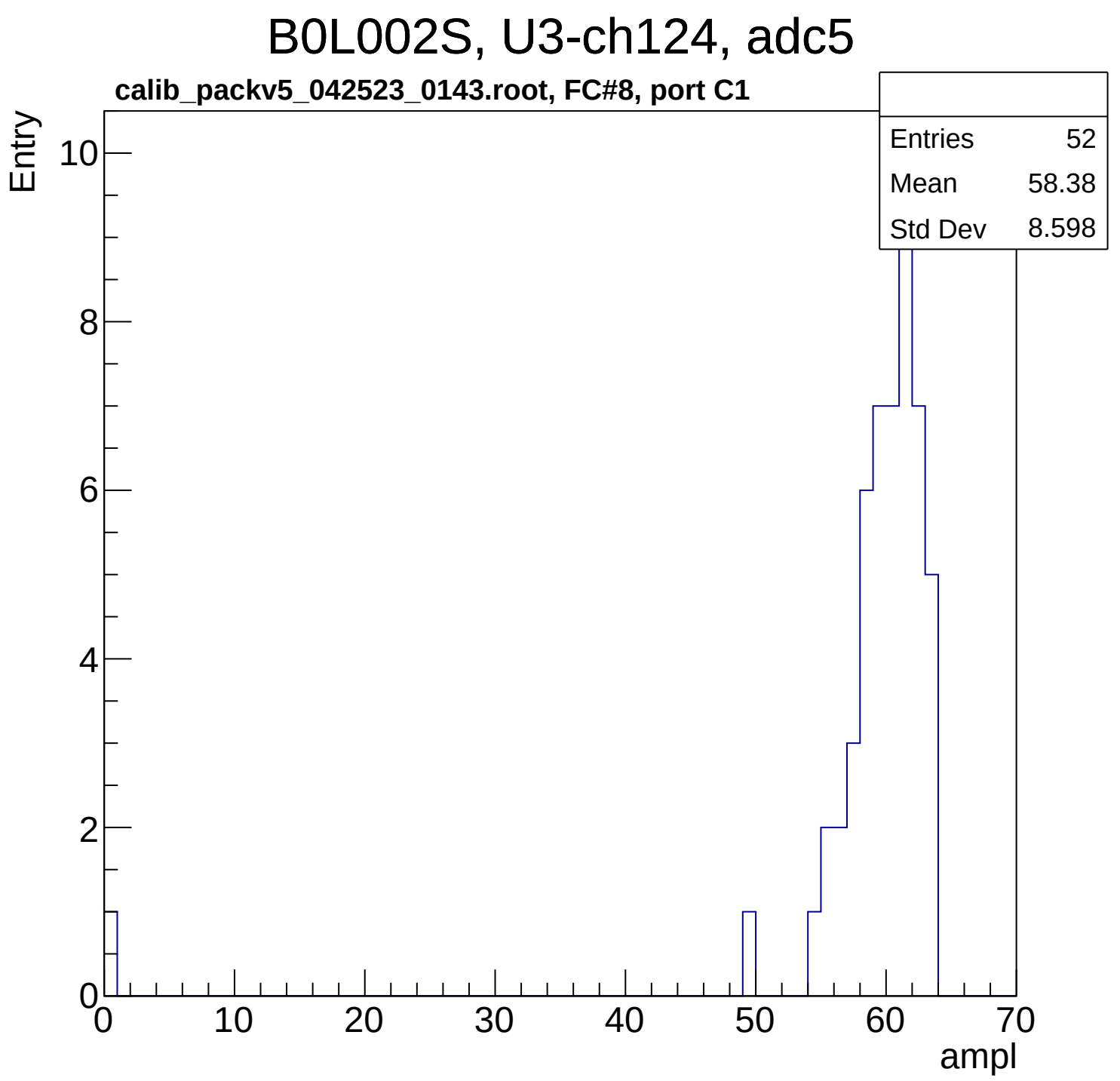
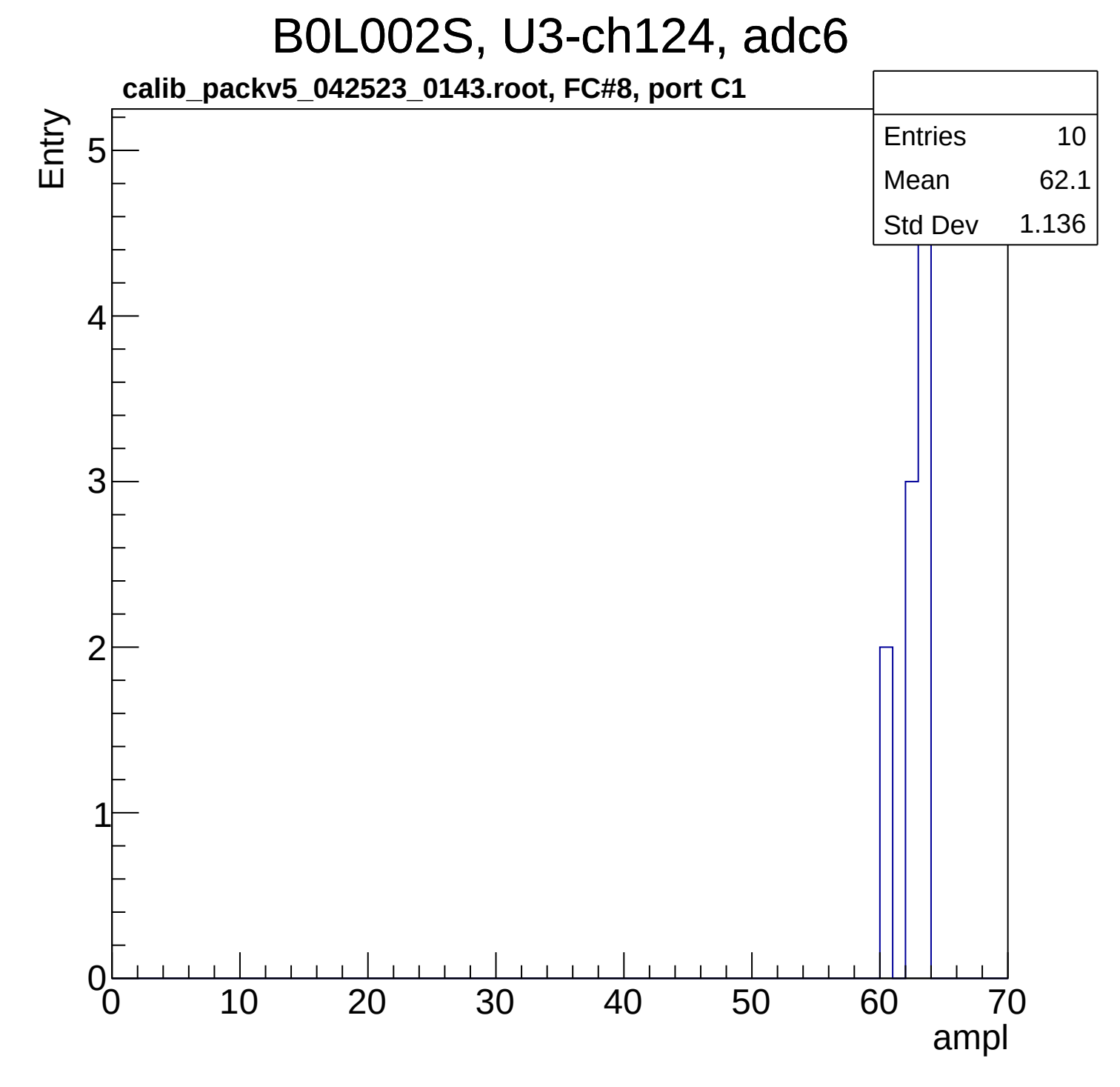
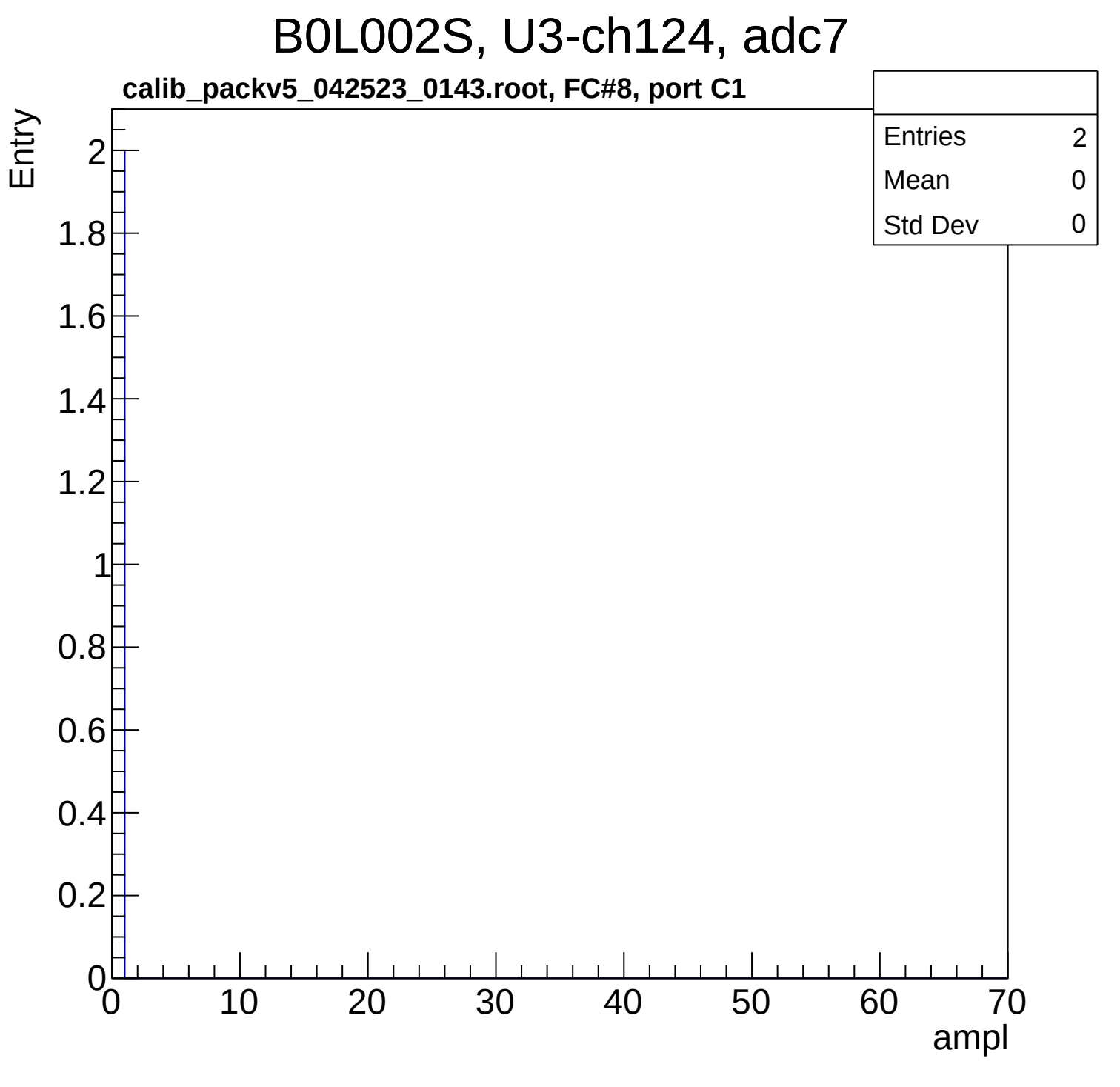
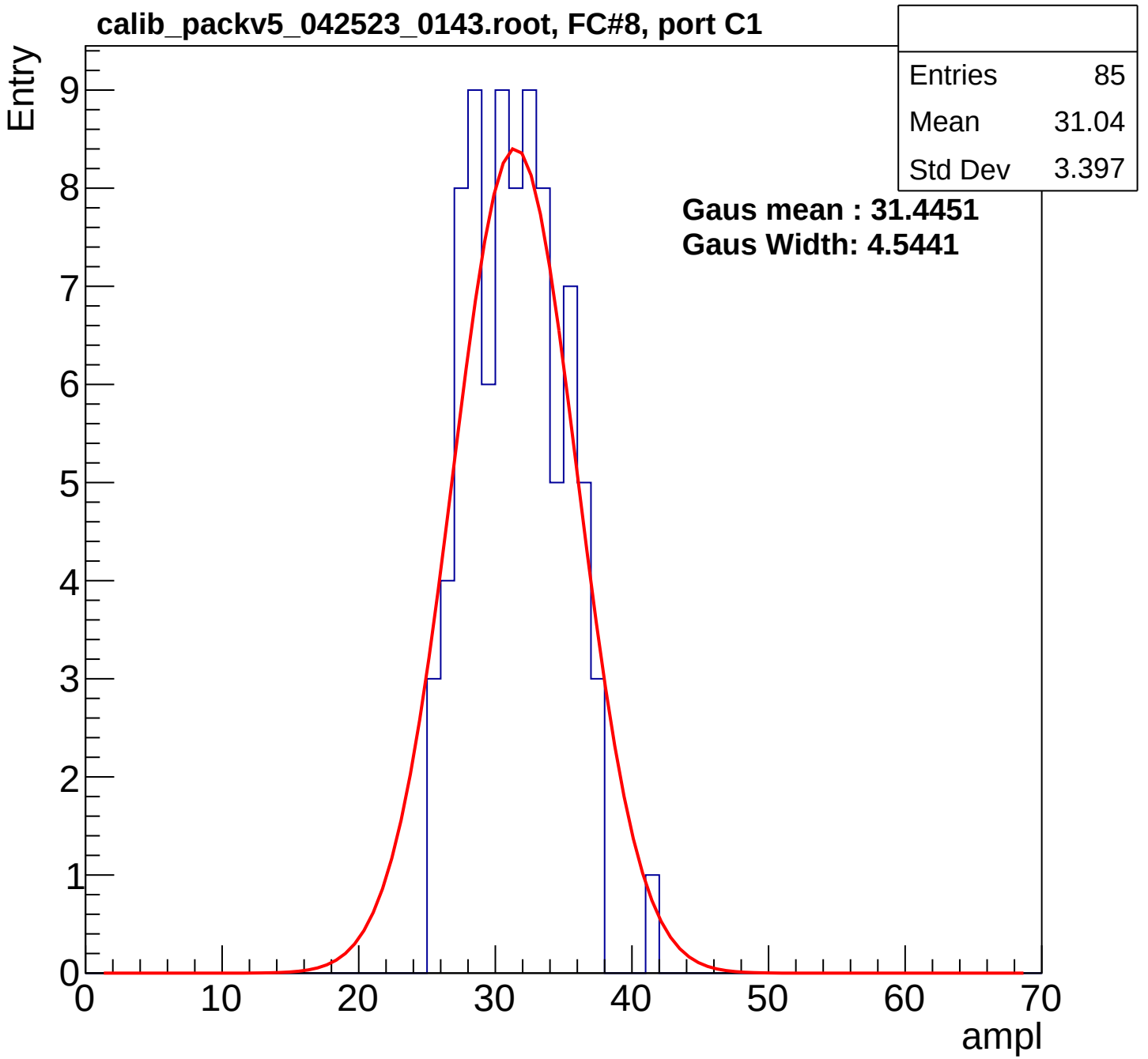
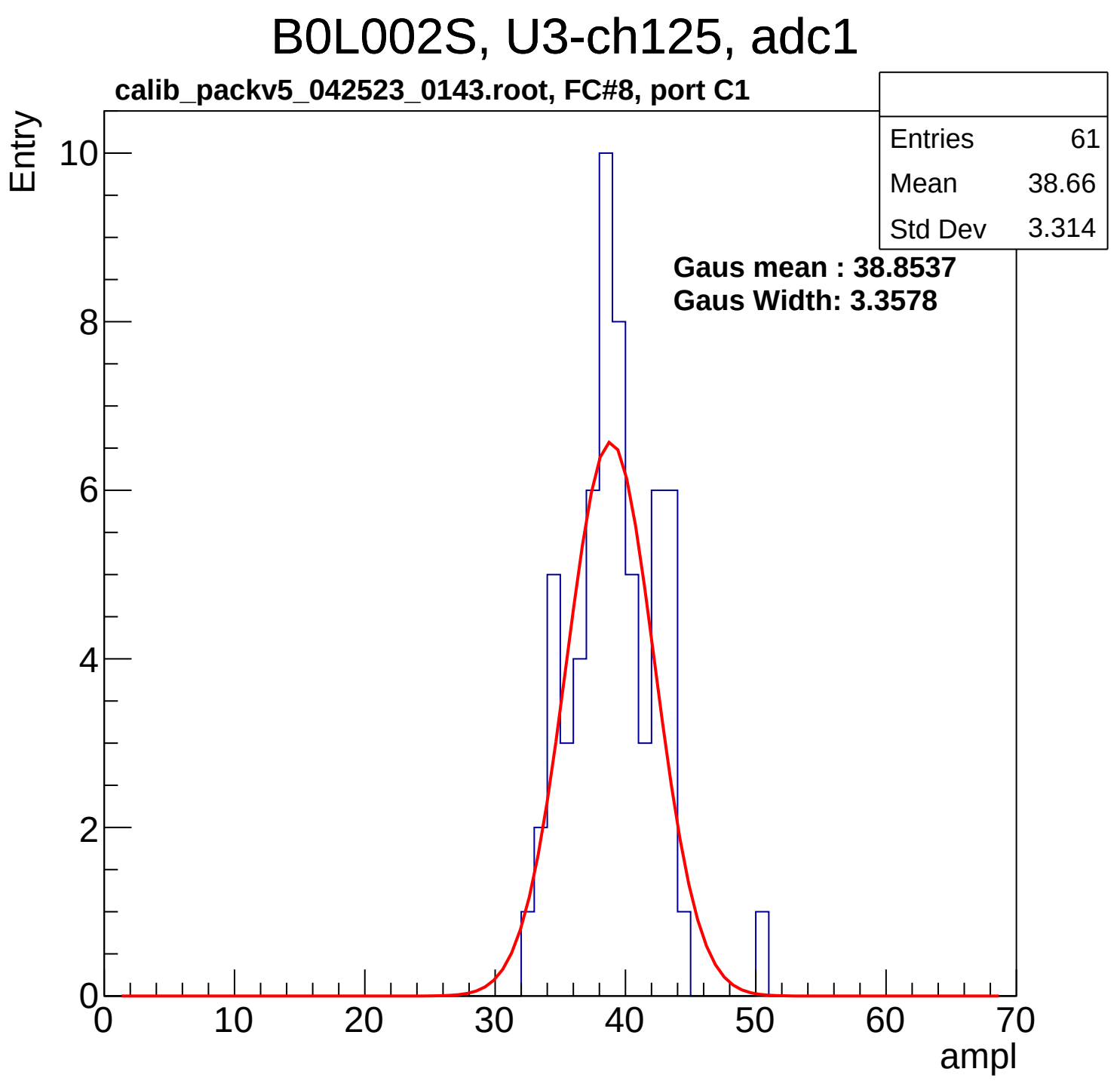
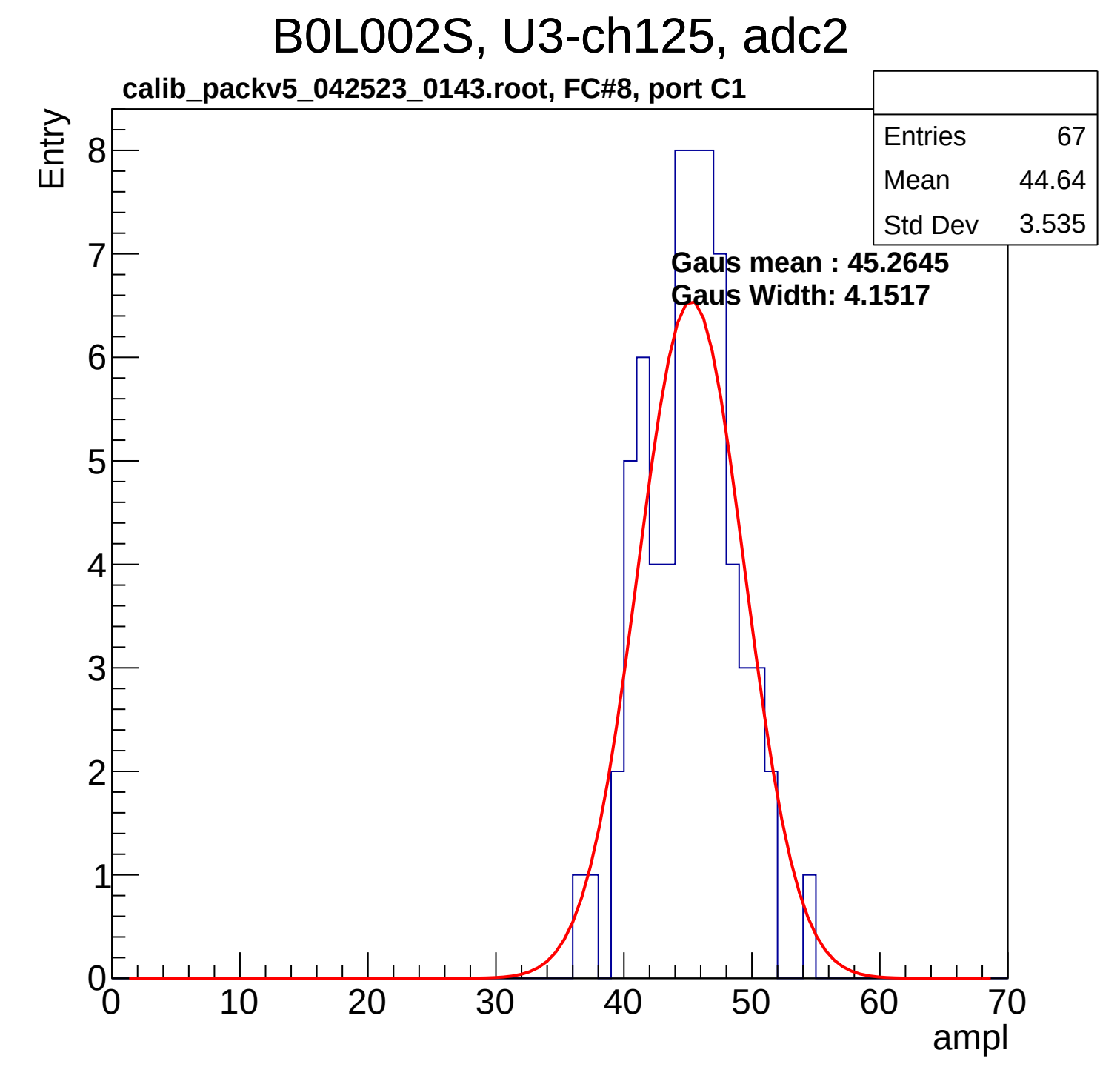

B0L002S, U3-ch124, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.38
Std Dev 8.598
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

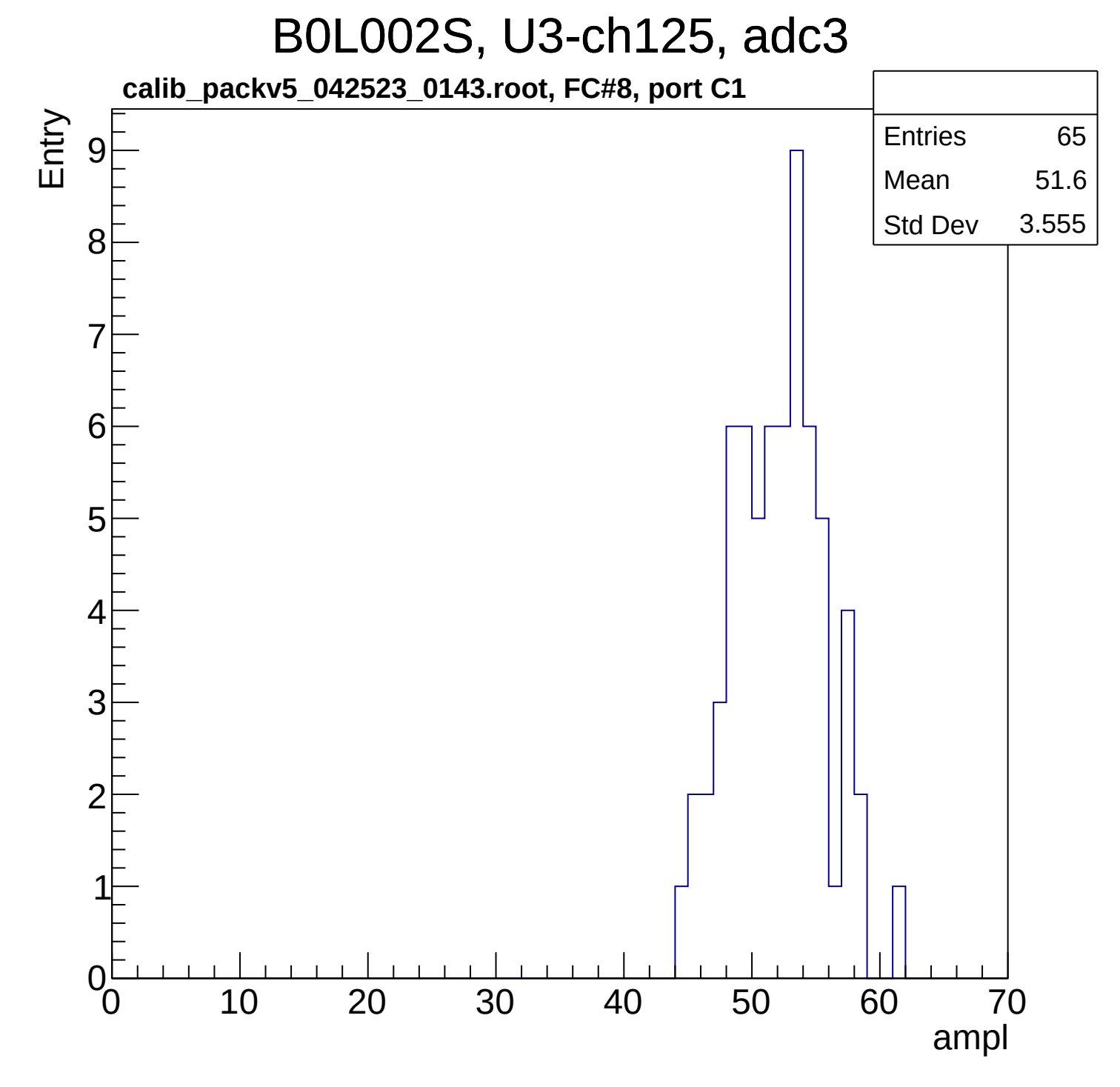

B0L002S, U3-ch124, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 62.1
Std Dev 1.136
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch124, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch125, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 31.04
Std Dev 3.397
Gaus mean : 31.4451
Gaus Width: 4.5441
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch125, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 38.66
Std Dev 3.314
Gaus mean : 38.8537
Gaus Width: 3.3578
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L002S, U3-ch125, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 44.64
Std Dev 3.535
Gaus mean : 45.2645
Gaus Width: 4.1517
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch125, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.6
Std Dev 3.555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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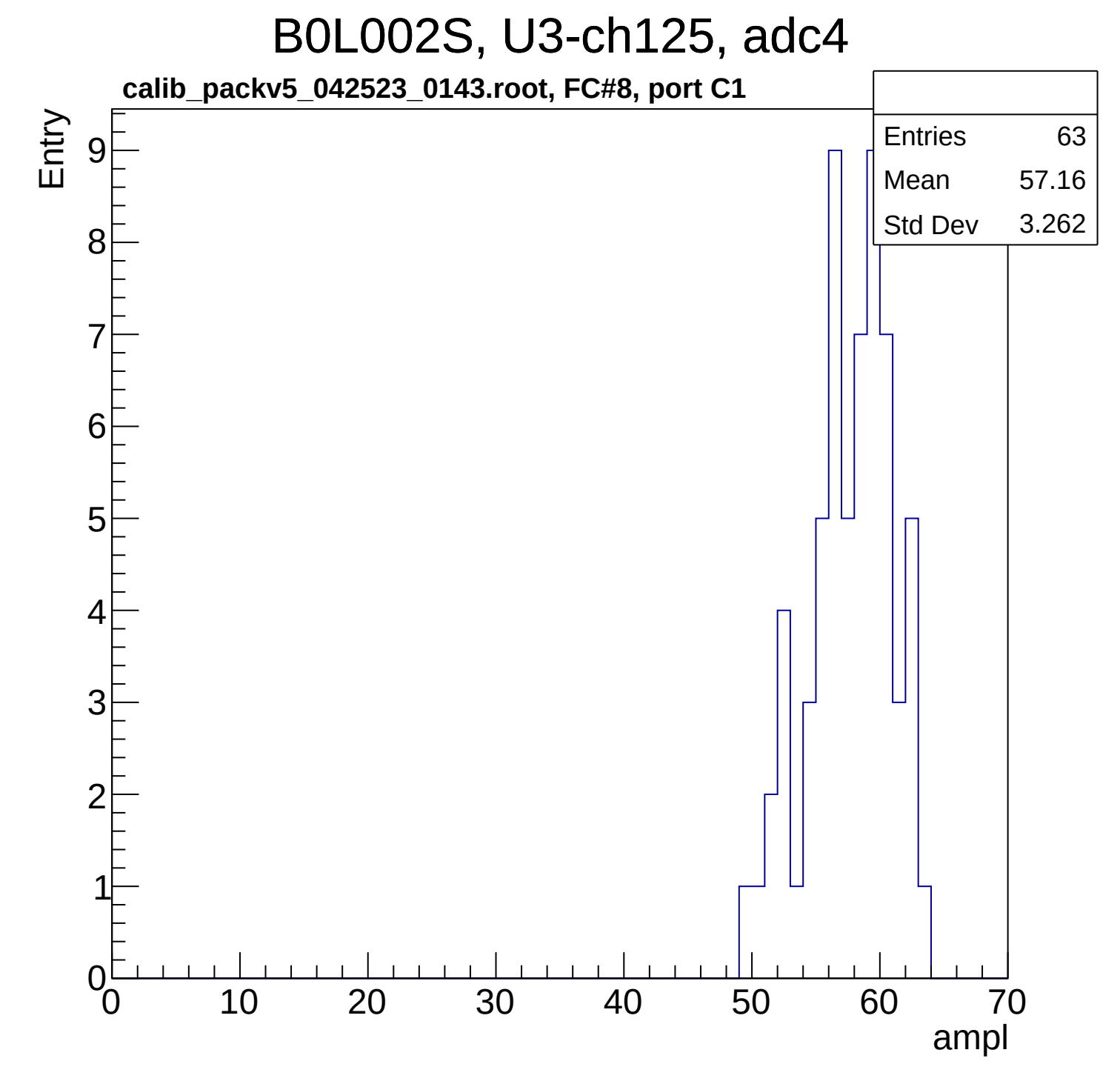
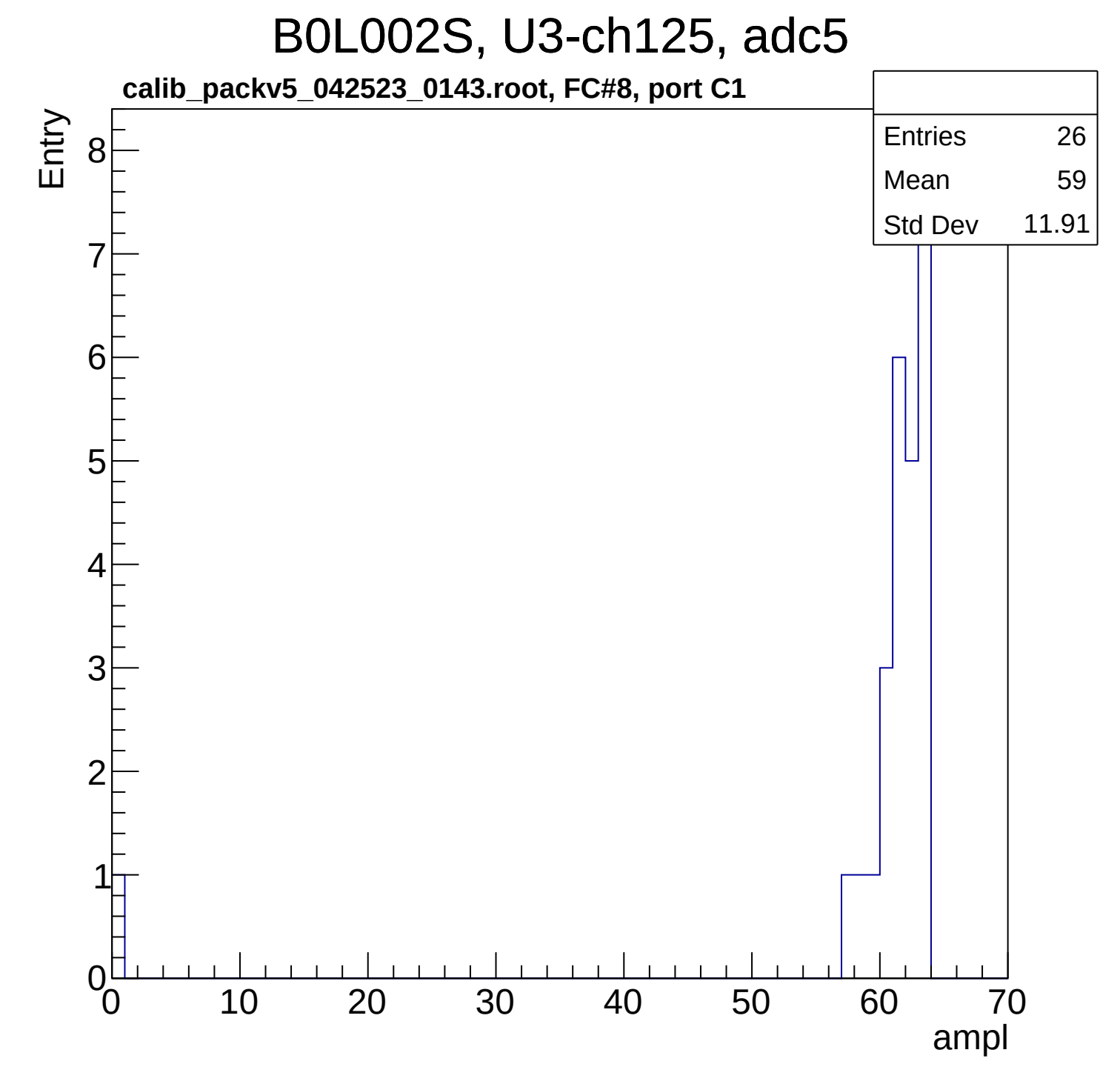
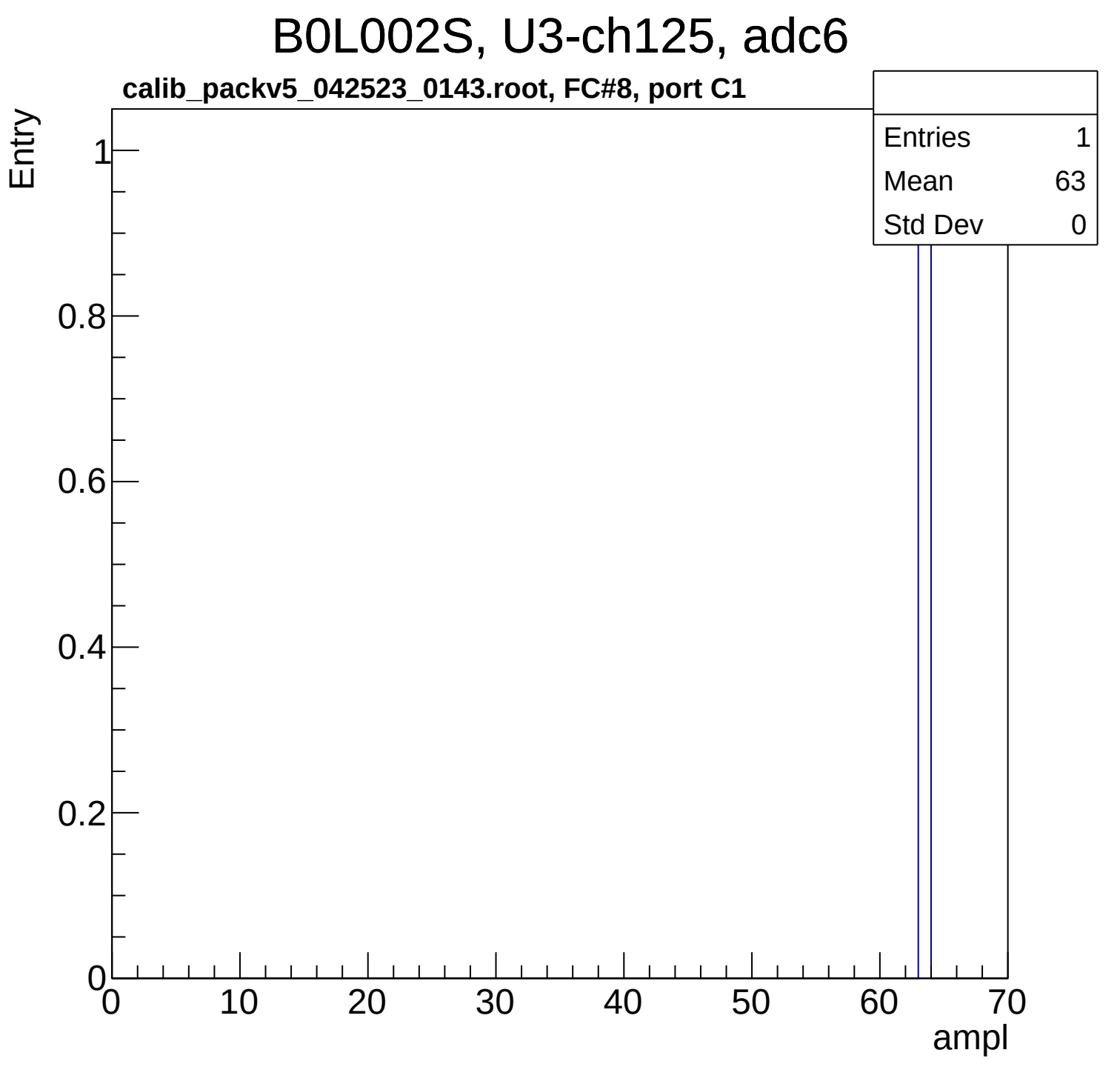
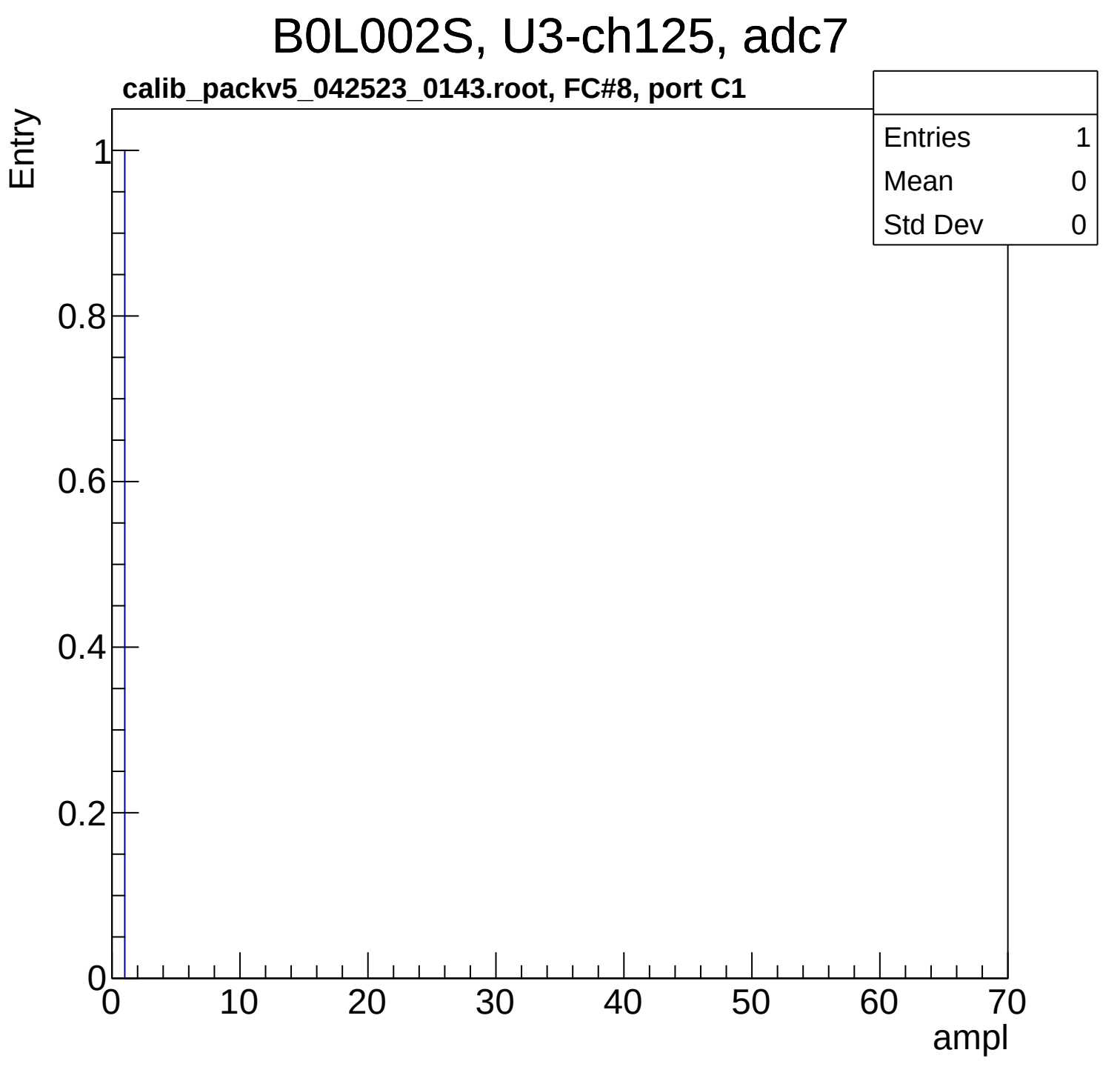
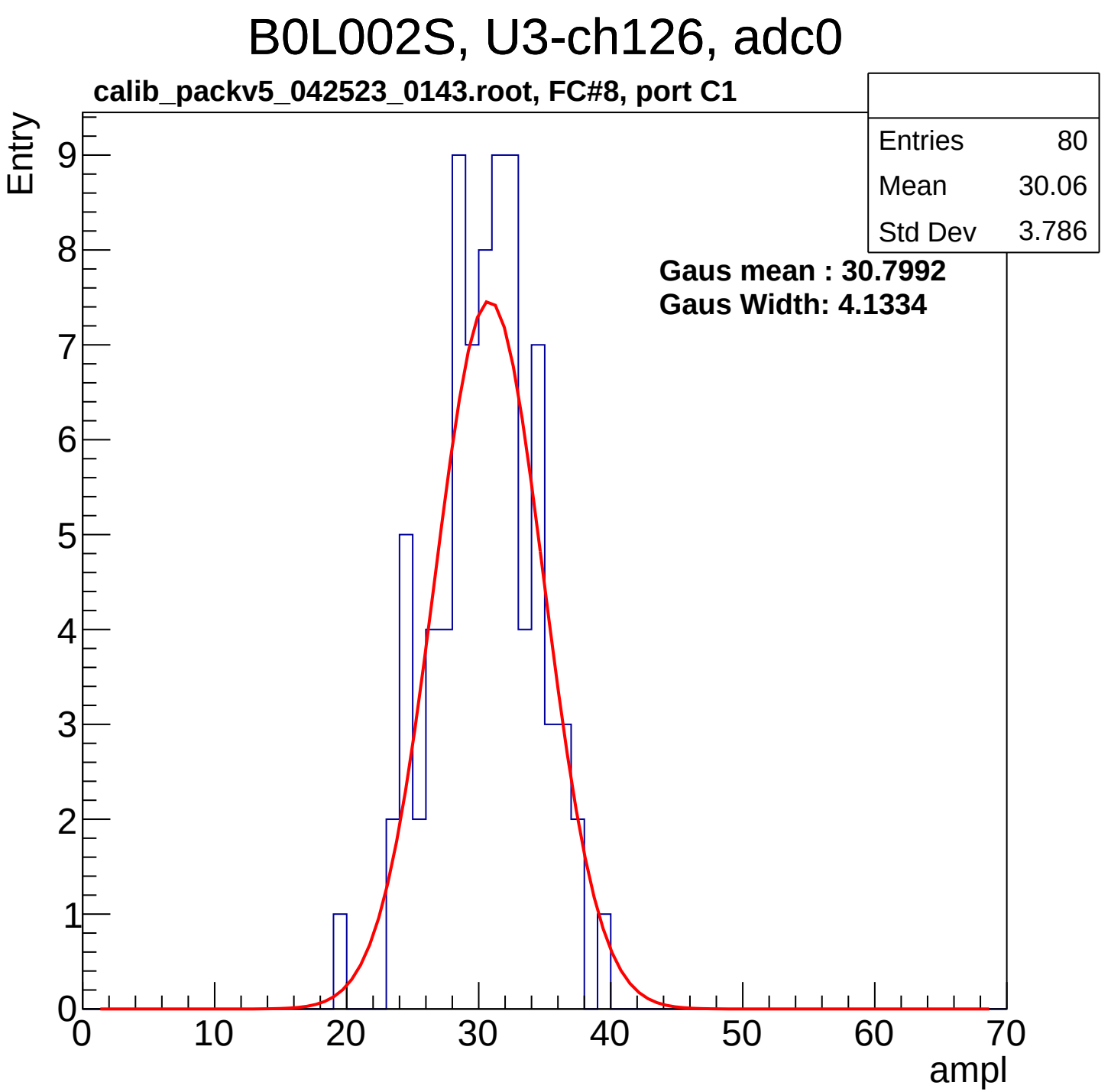
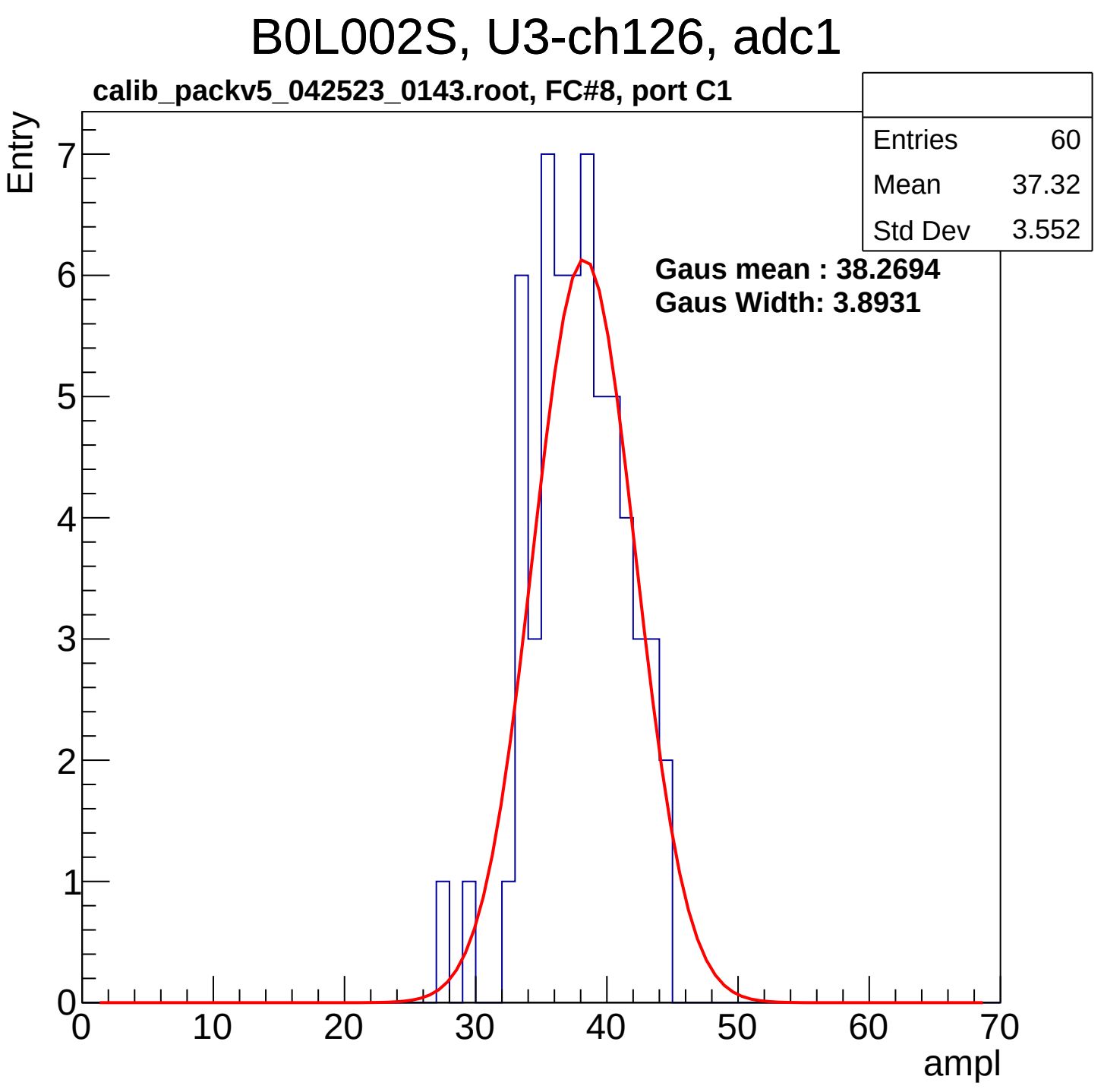

B0L002S, U3-ch125, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.16
Std Dev 3.262
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

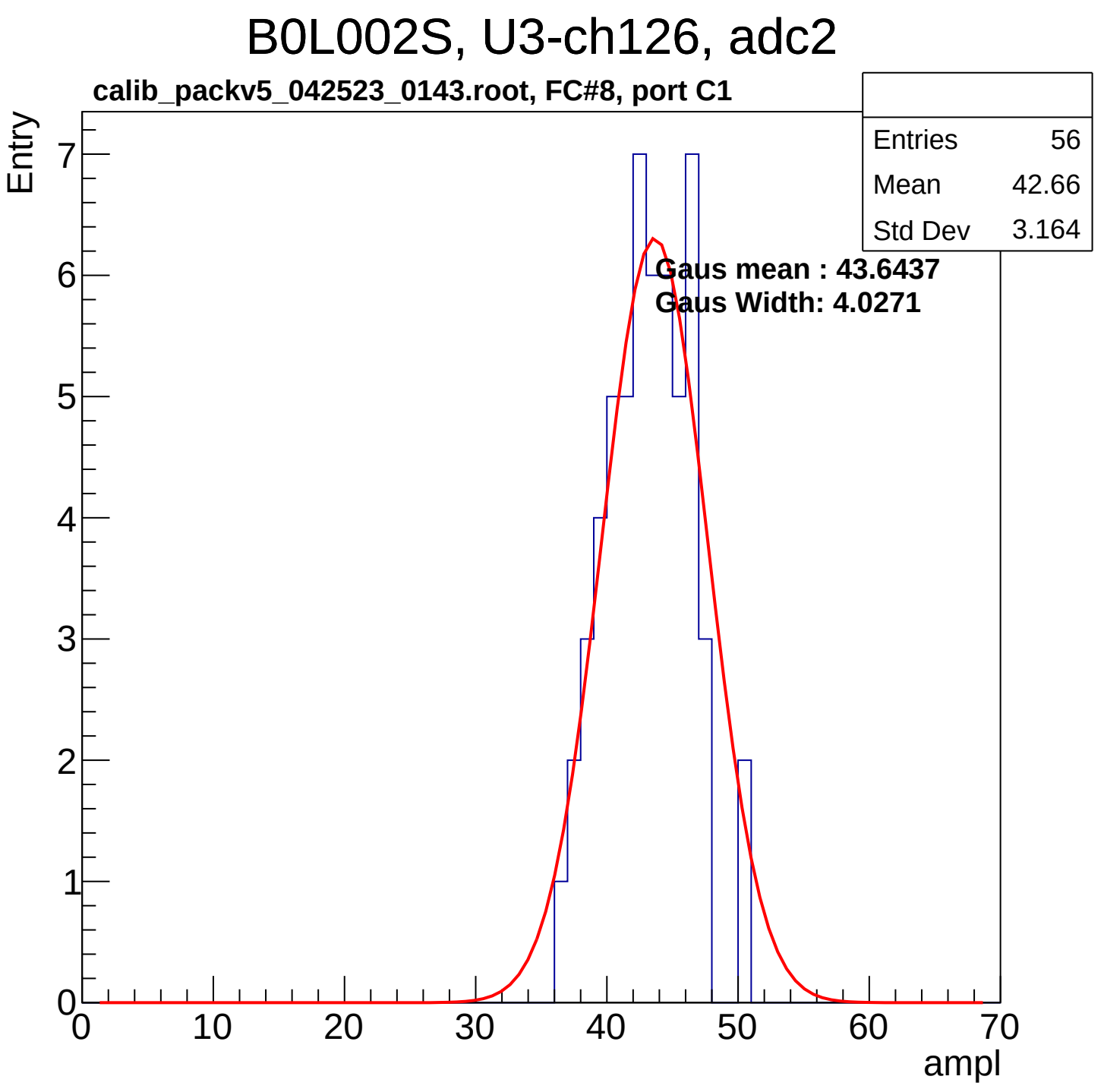

B0L002S, U3-ch125, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 59
Std Dev 11.91
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

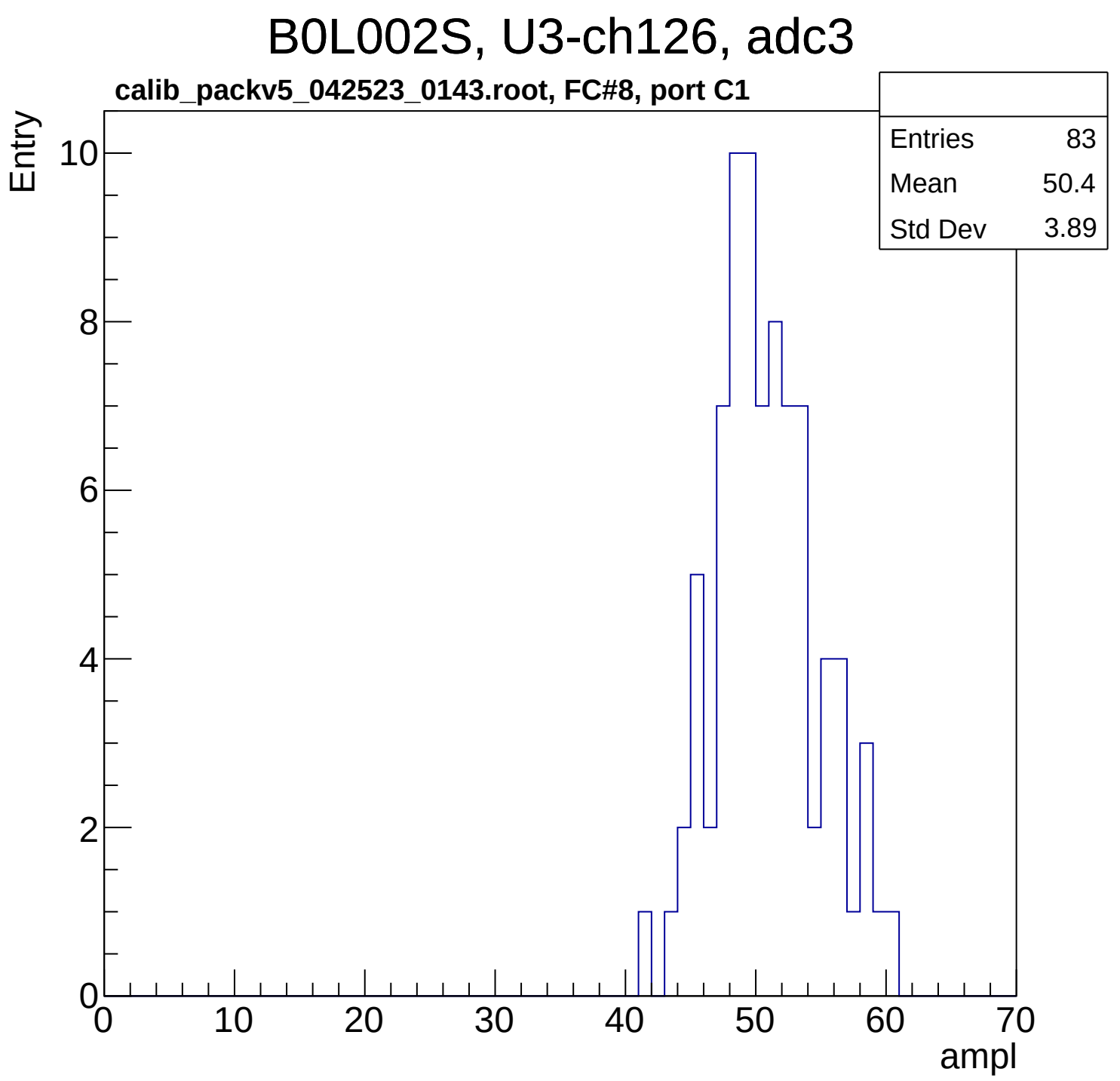

B0L002S, U3-ch125, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

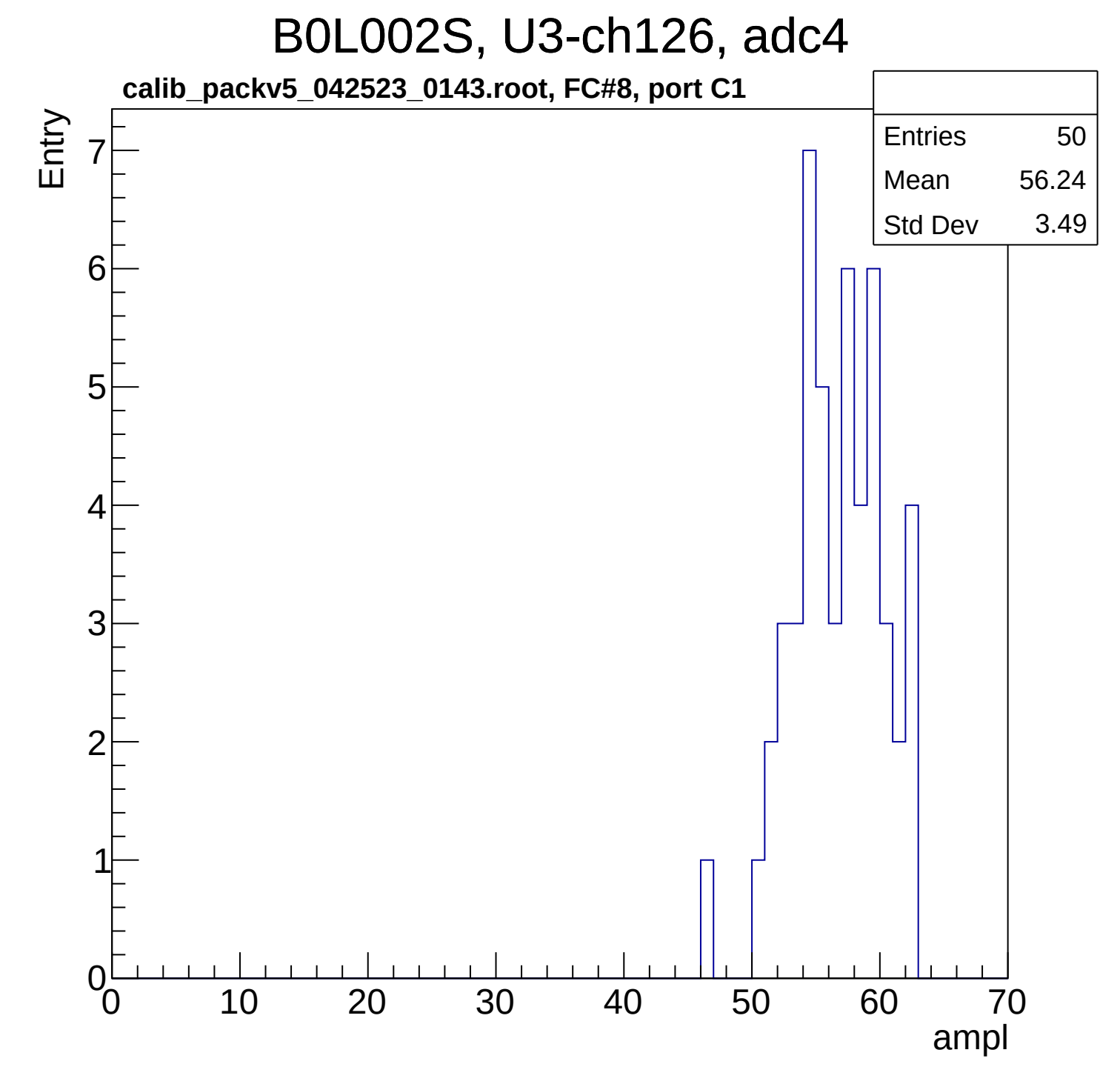

B0L002S, U3-ch125, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch126, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.06
Std Dev 3.786
Gaus mean : 30.7992
Gaus Width: 4.1334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch126, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.32
Std Dev 3.552
Gaus mean : 38.2694
Gaus Width: 3.8931
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch126, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 42.66
Std Dev 3.164
Gaus mean : 43.6437
Gaus Width: 4.0271
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch126, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 50.4
Std Dev 3.89
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch126, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 56.24
Std Dev 3.49
Entry
ampl
0
1
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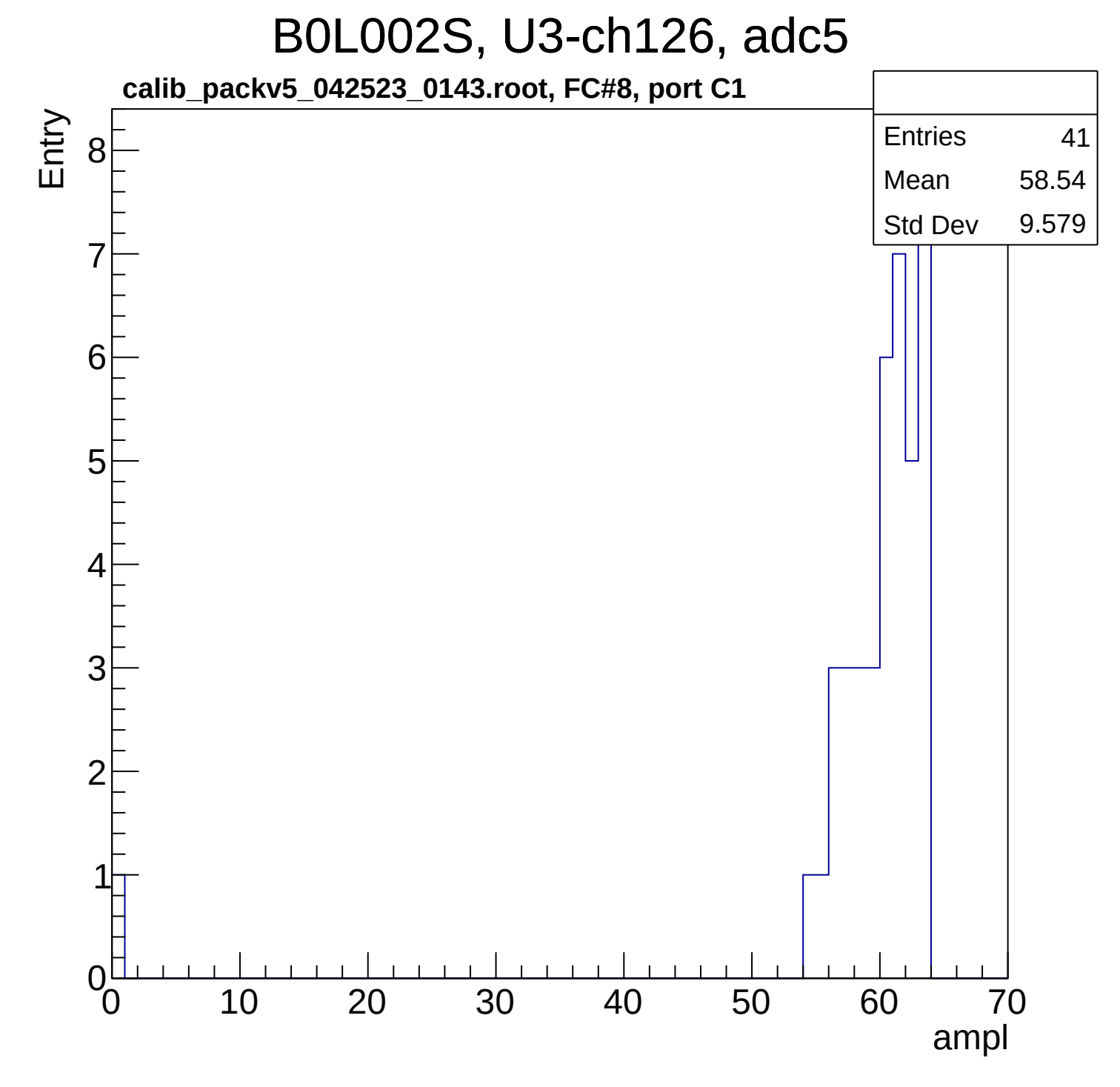
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U3-ch126, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.54
Std Dev 9.579
Entry
ampl
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3
4
5
6
7
8
0
10
20
30
40
50
60
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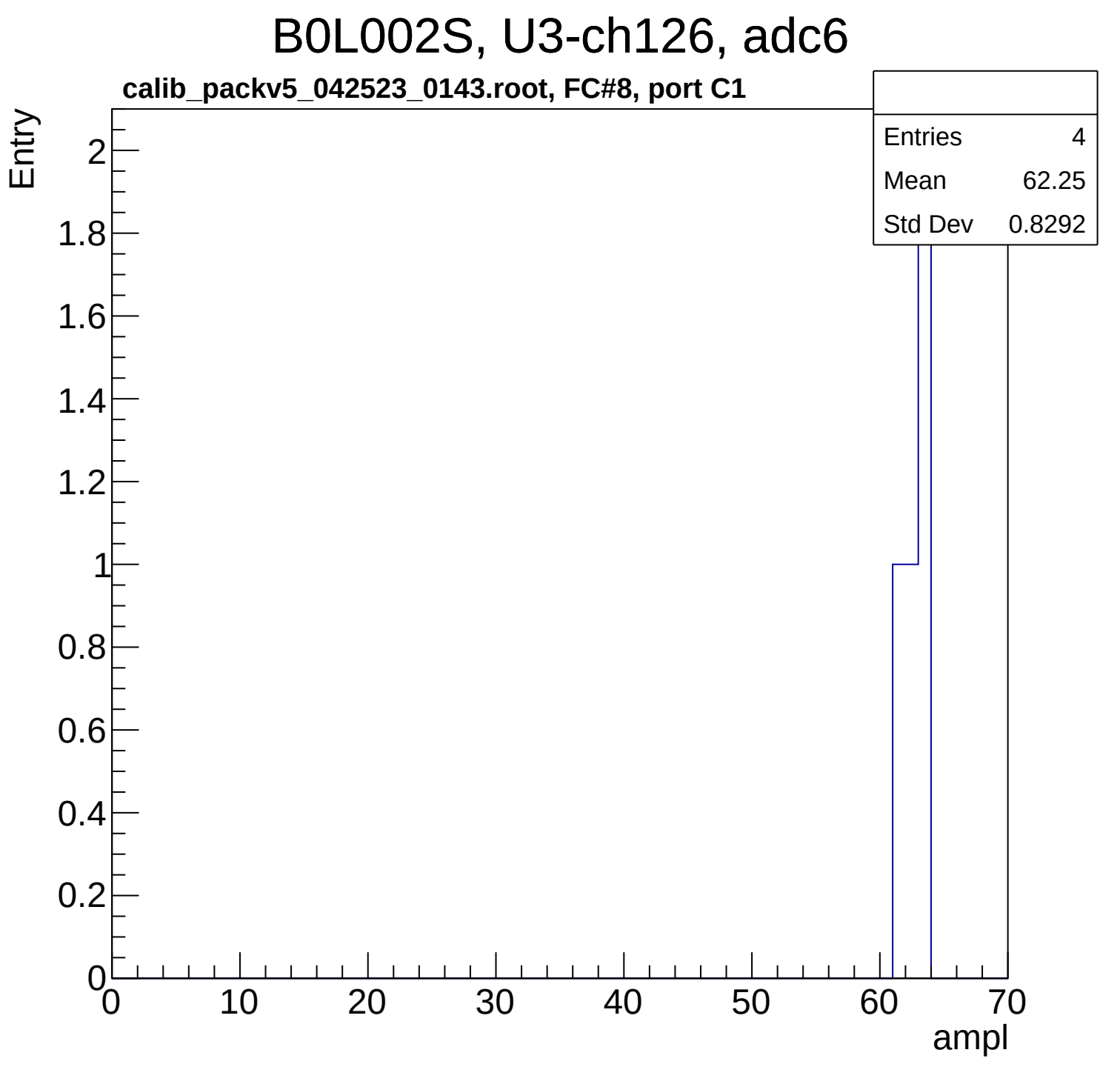
B0L002S, U3-ch126, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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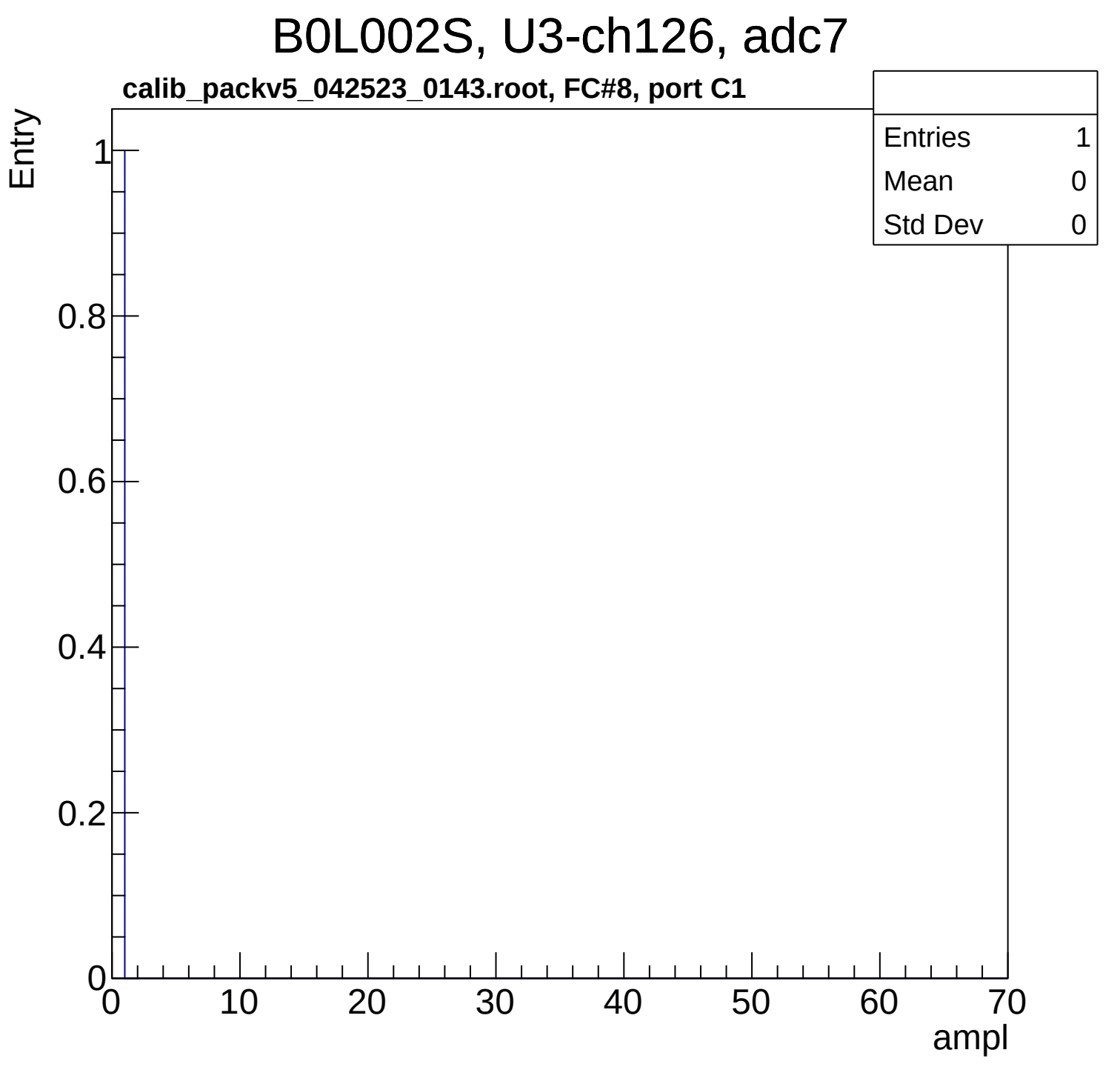
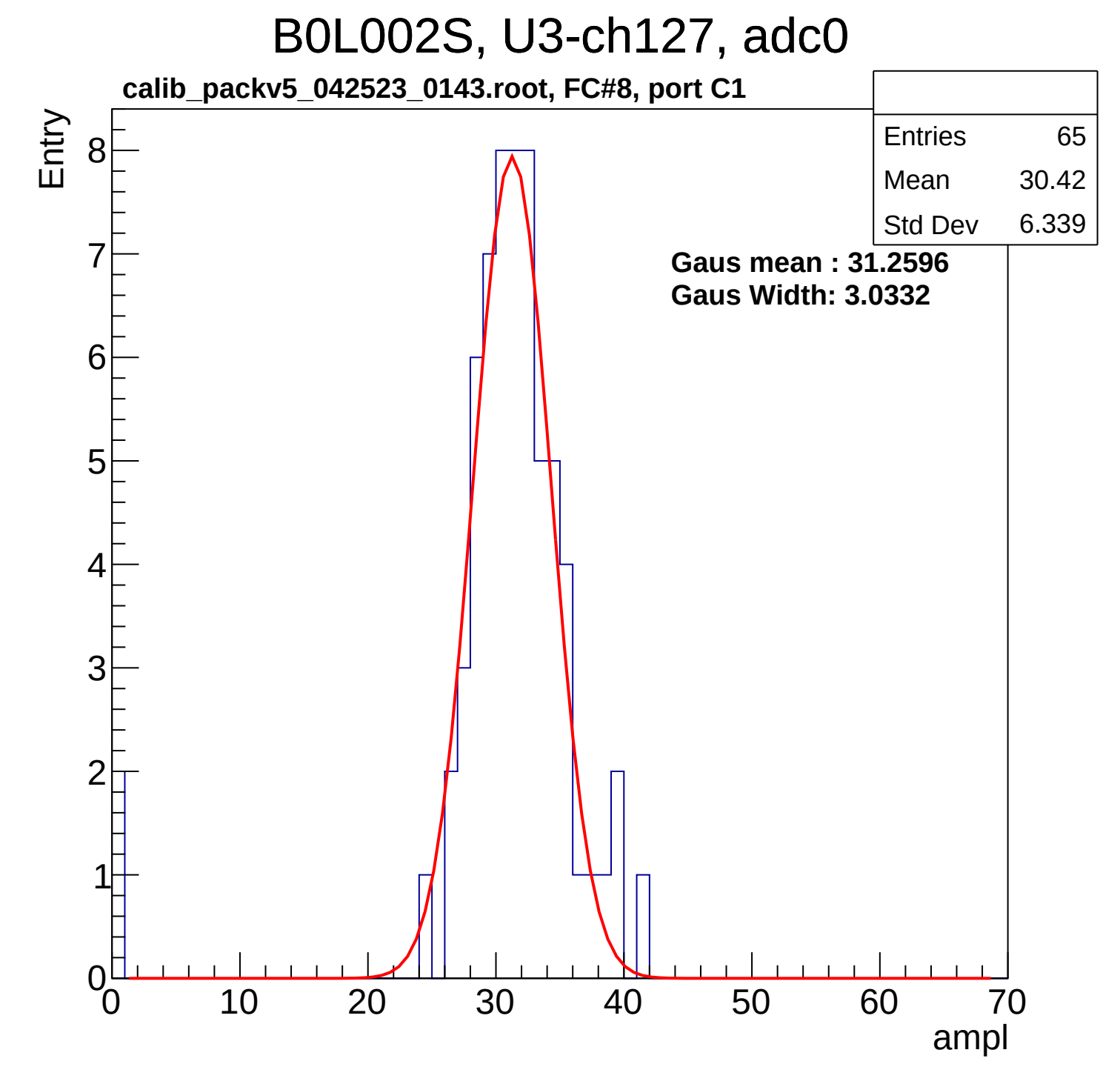

B0L002S, U3-ch126, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch127, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 30.42
Std Dev 6.339
Gaus mean : 31.2596
Gaus Width: 3.0332
Entry
ampl
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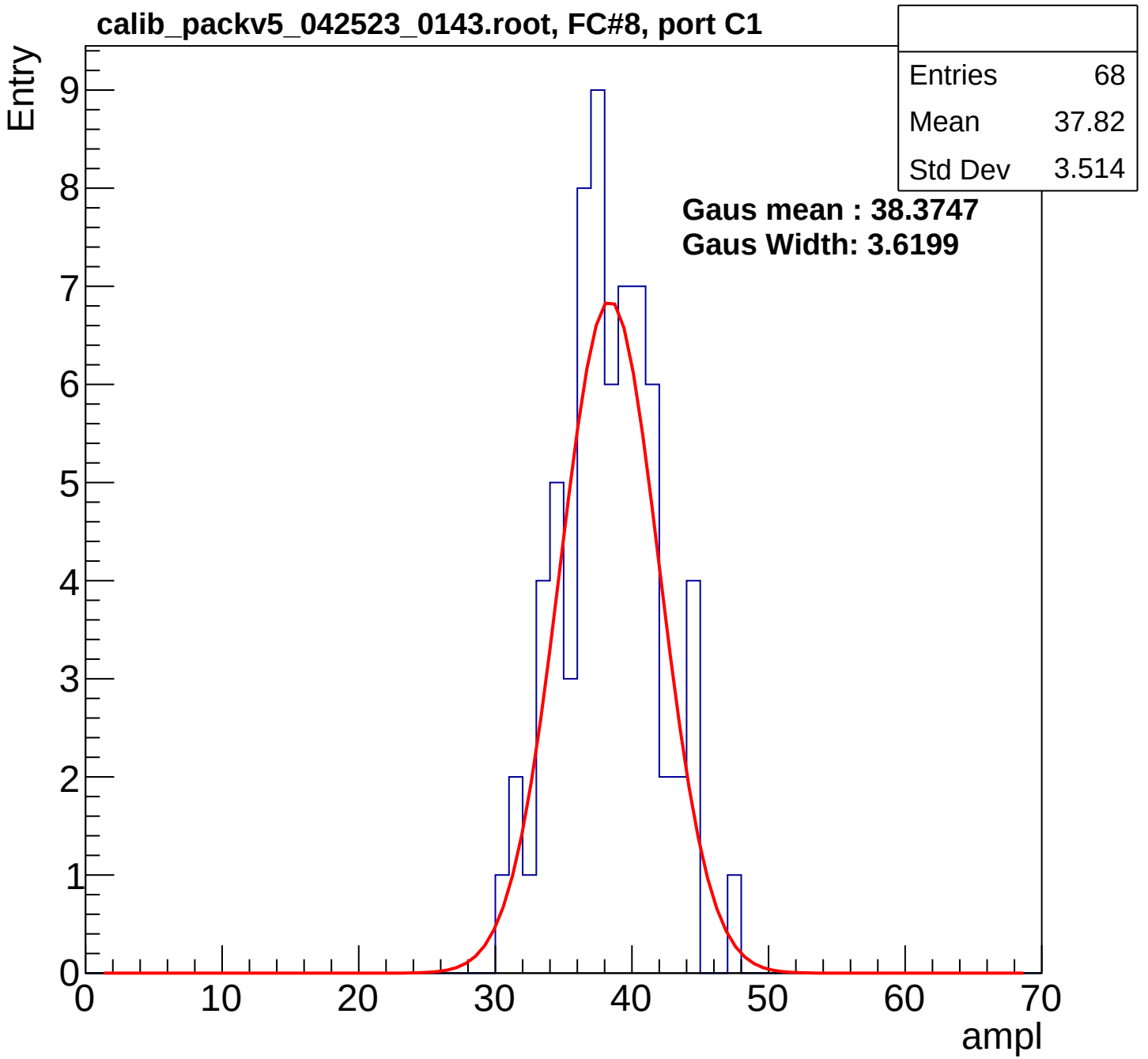
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch127, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 37.82
Std Dev 3.514
Gaus mean : 38.3747
Gaus Width: 3.6199
Entry
ampl
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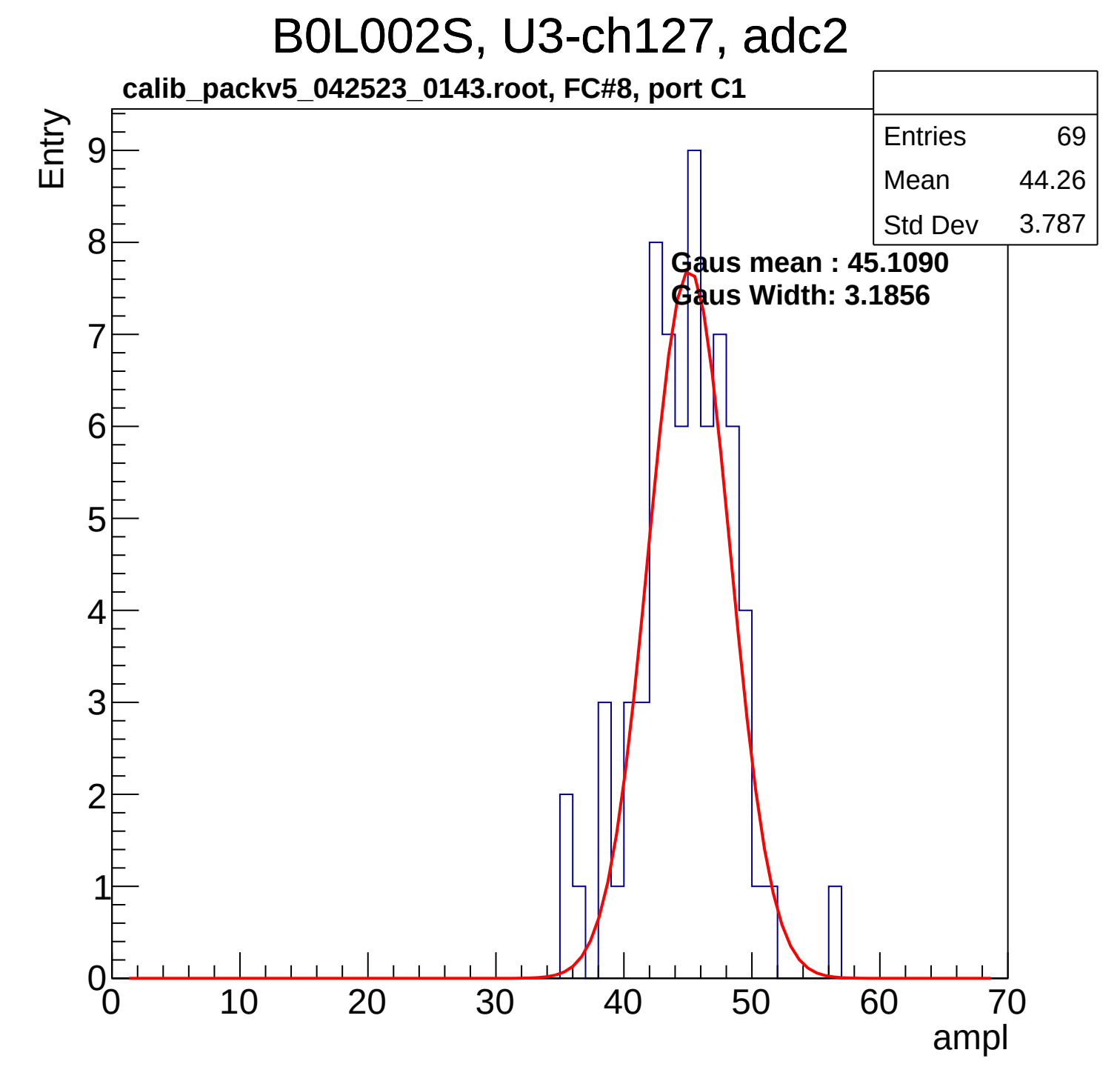
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch127, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44.26
Std Dev 3.787
Gaus mean : 45.1090
Gaus Width: 3.1856
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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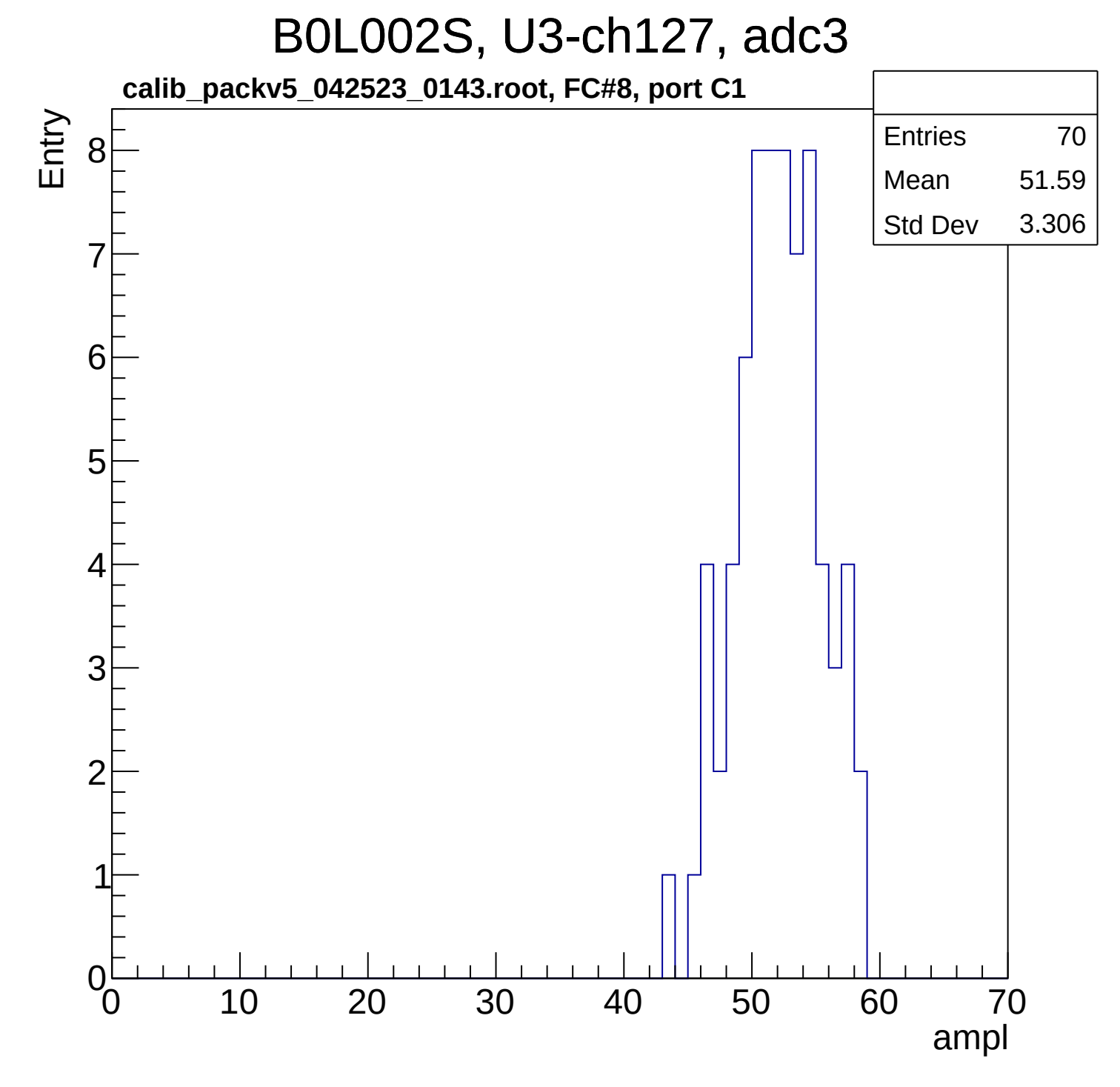
B0L002S, U3-ch127, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 51.59
Std Dev 3.306
Entry
ampl
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1
2
3
4
5
6
7
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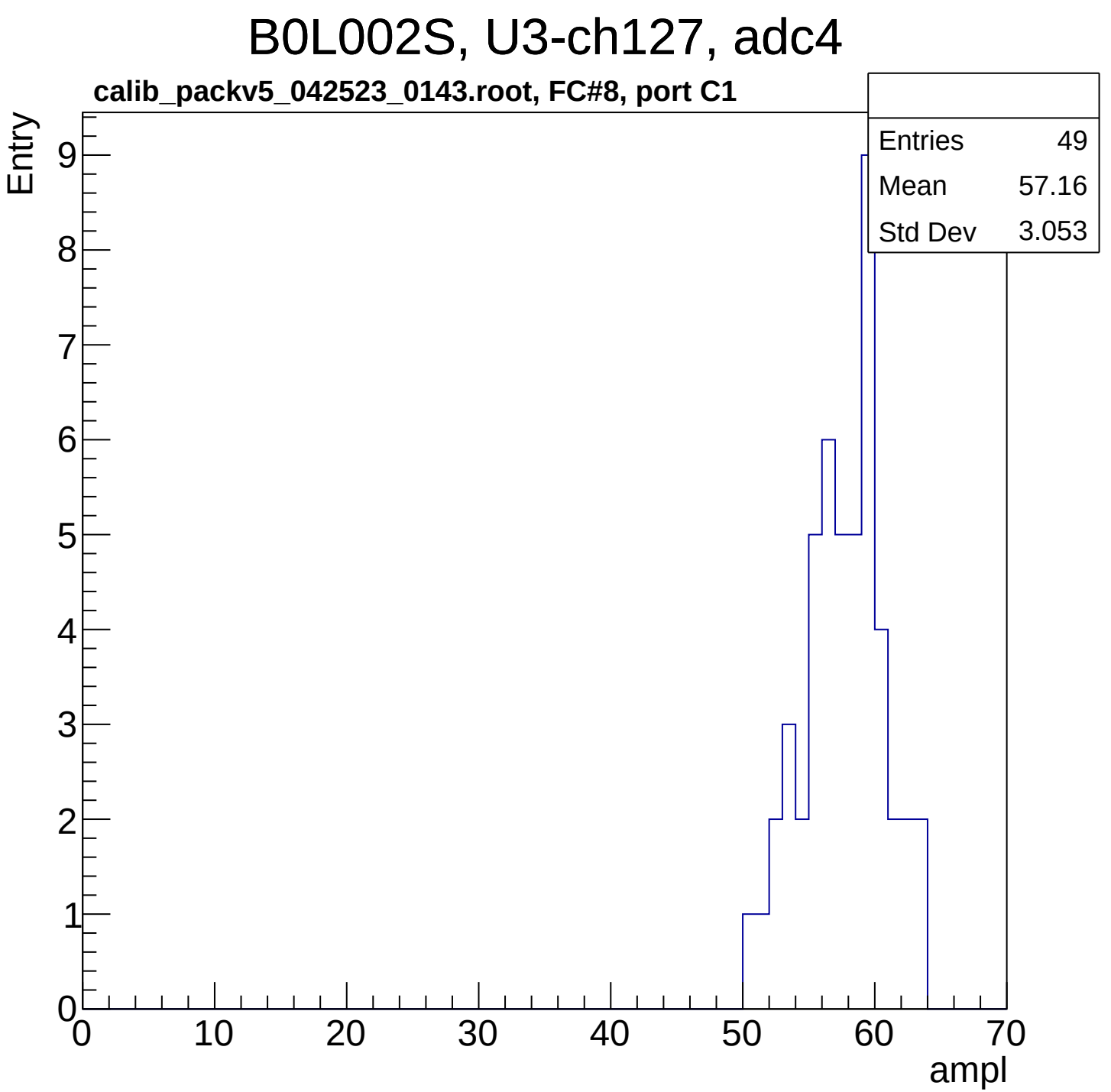
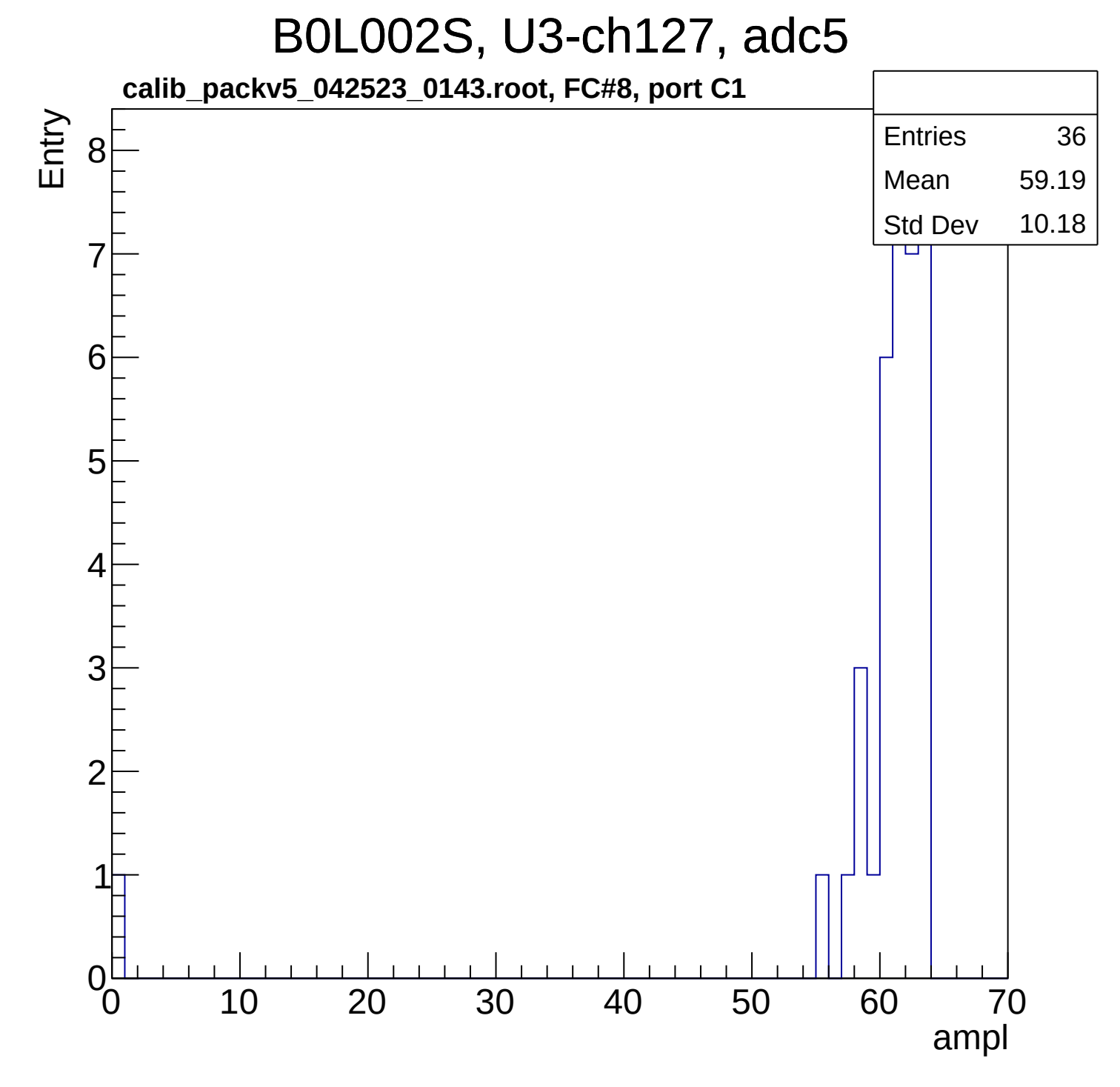
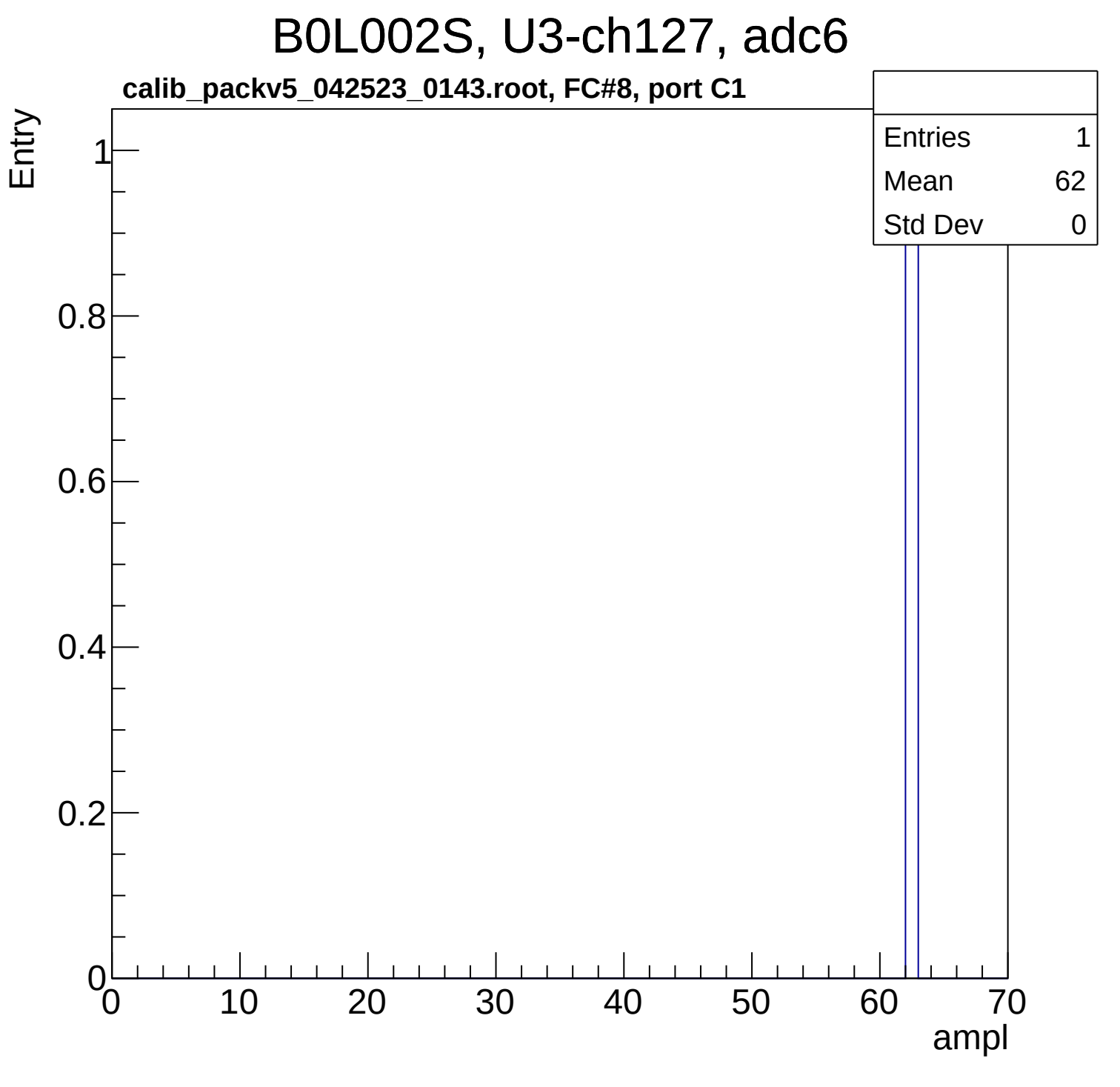
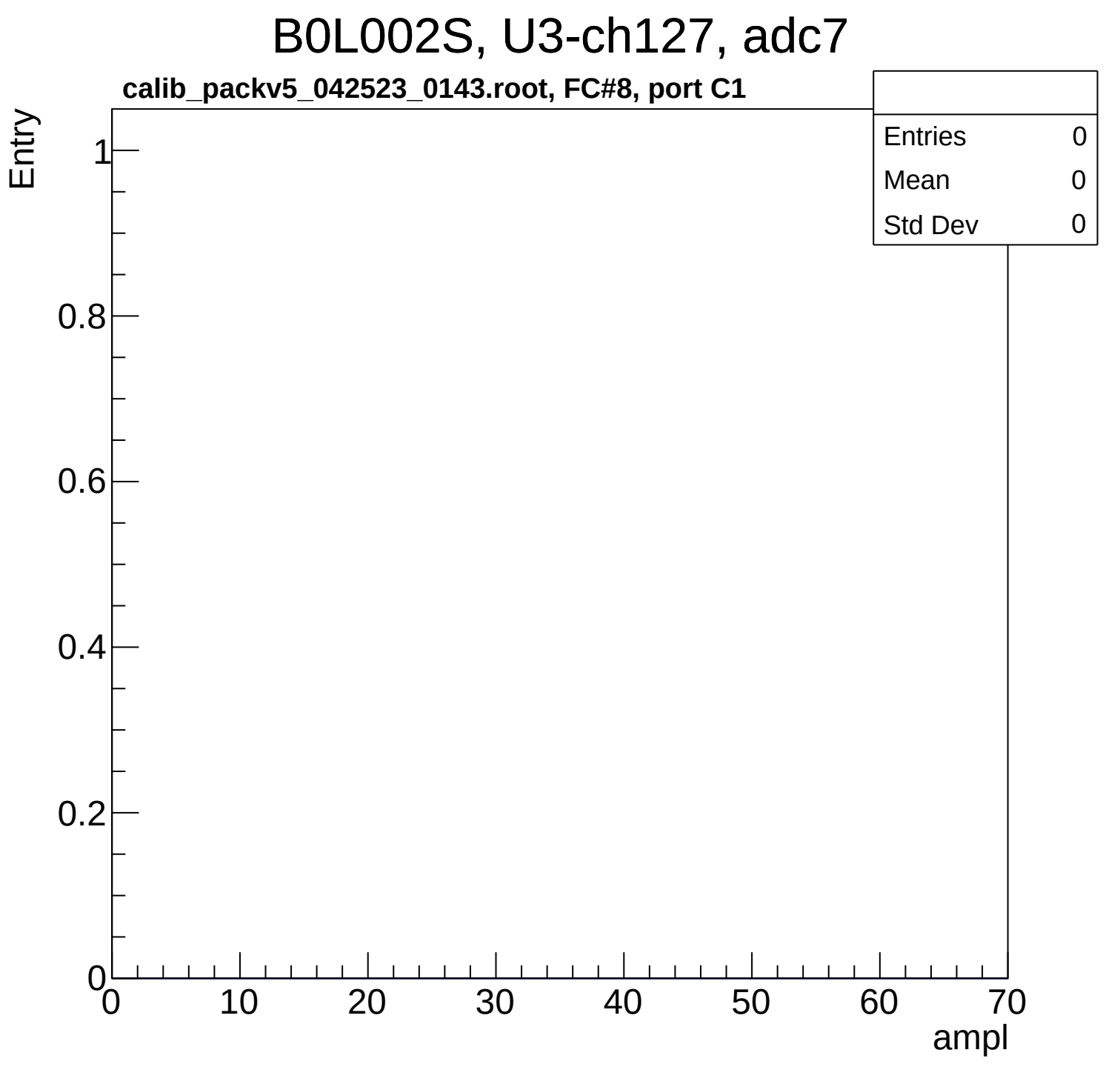
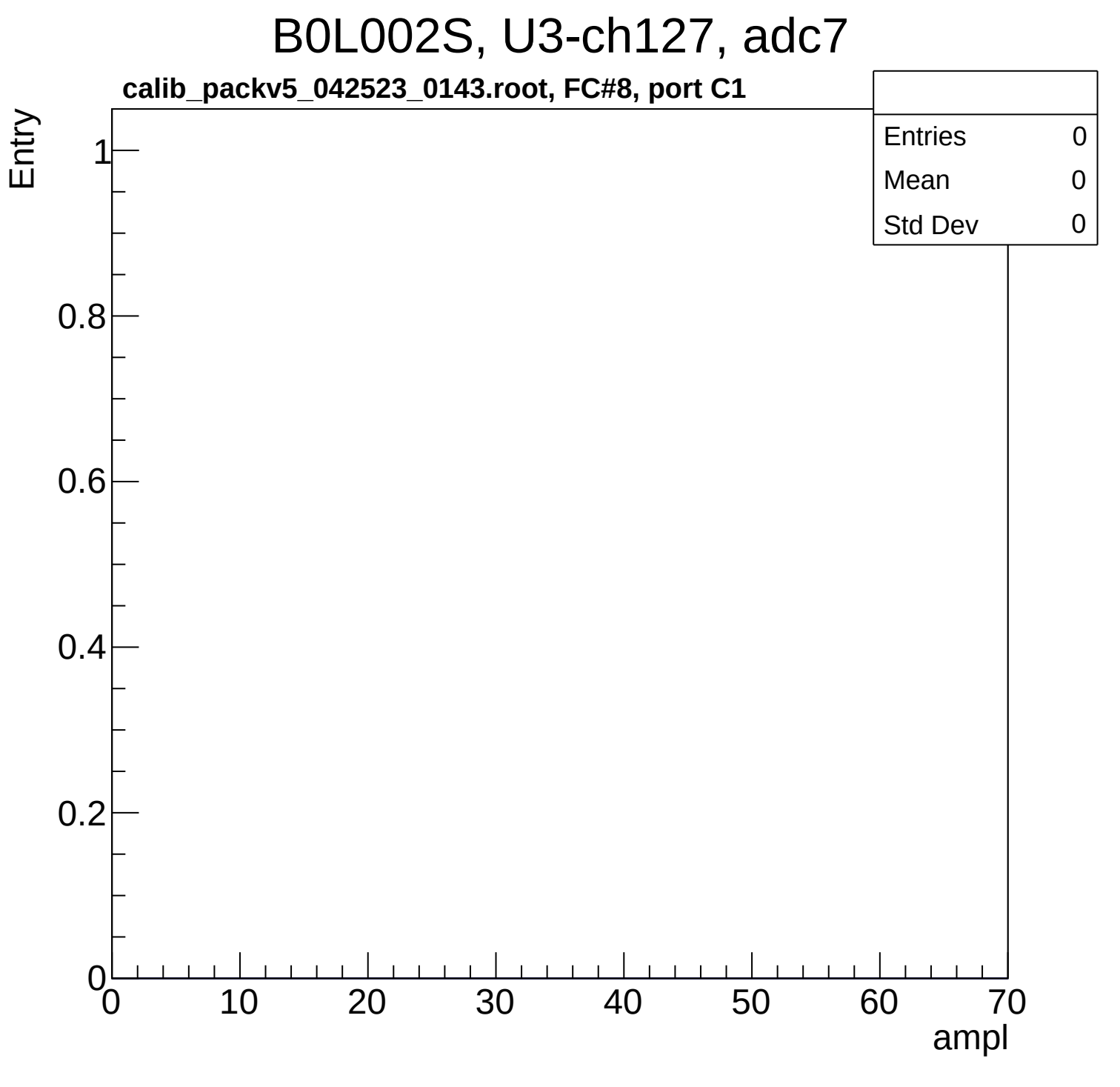
0
10
20
30
40
50
60
70


B0L002S, U3-ch127, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 57.16
Std Dev 3.053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U3-ch127, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.19
Std Dev 10.18
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L002S, U3-ch127, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U3-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl