

B0L000S, U22-ch0

calib_packv5_042523_0143.root, FC#5, port B1

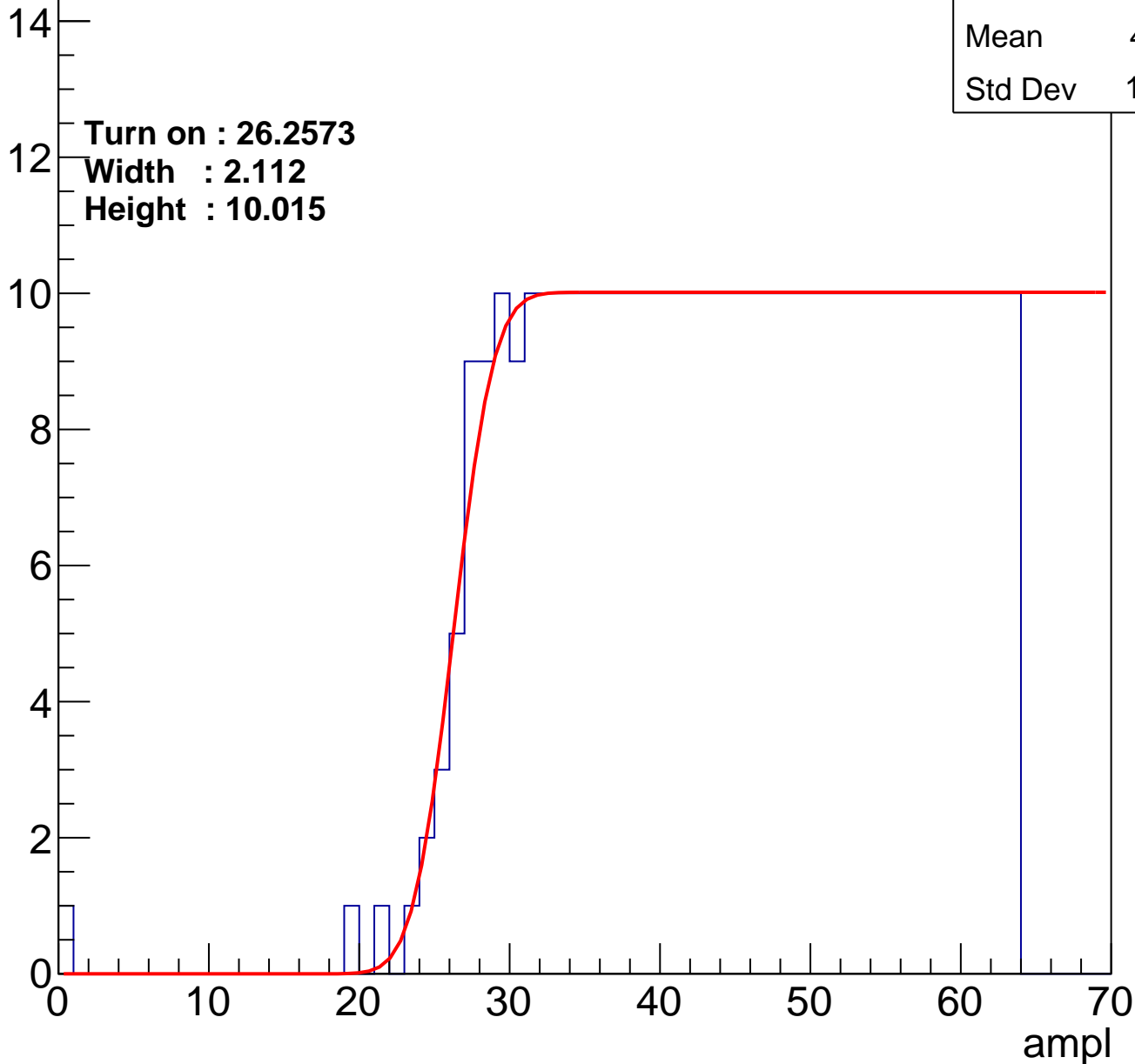
Entry

Entries	381
Mean	44.31
Std Dev	11.32

Turn on : 26.2573

Width : 2.112

Height : 10.015



B0L000S, U22-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 26.2998

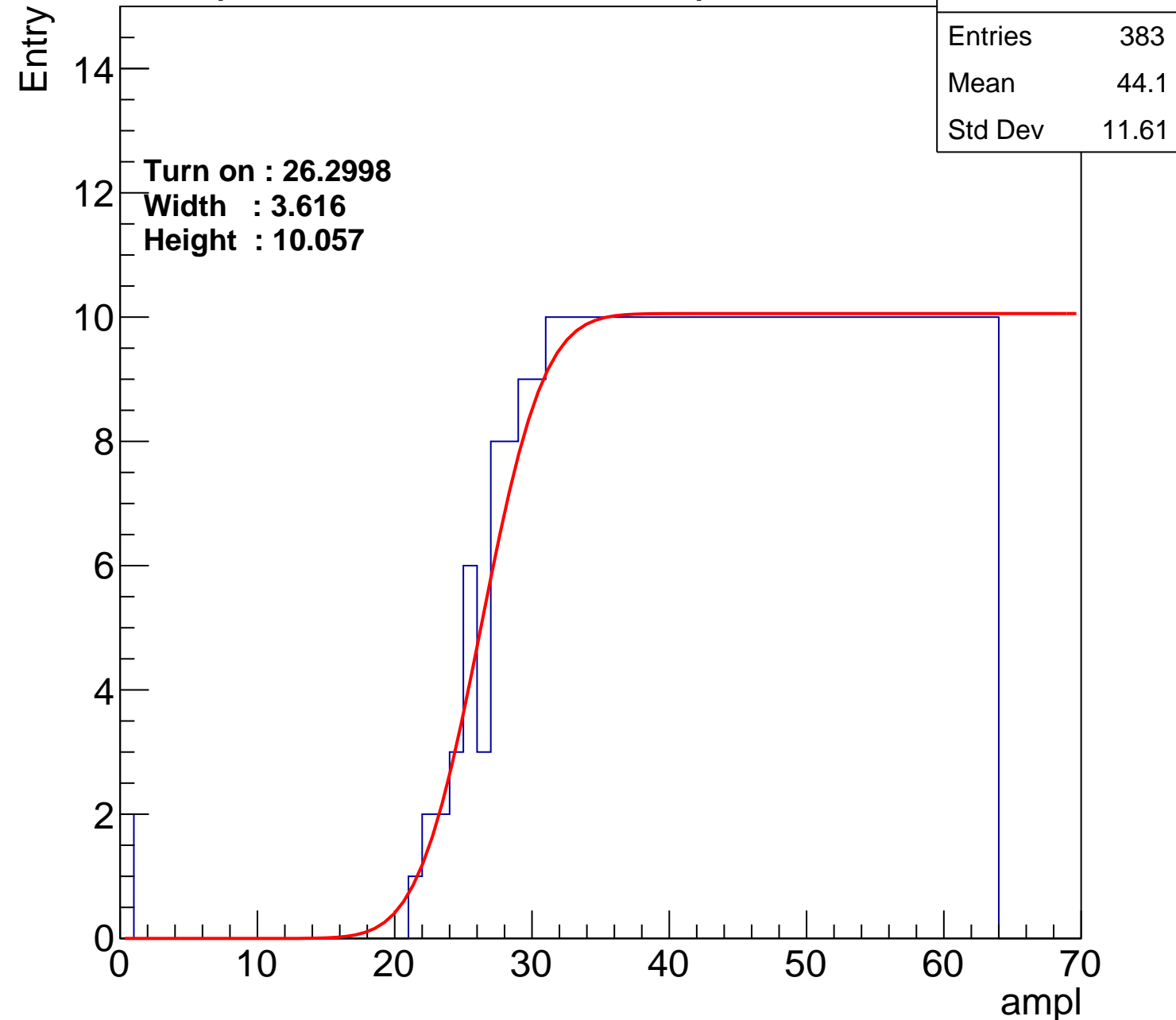
Width : 3.616

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.69
Std Dev	11.3

Turn on : 26.9757

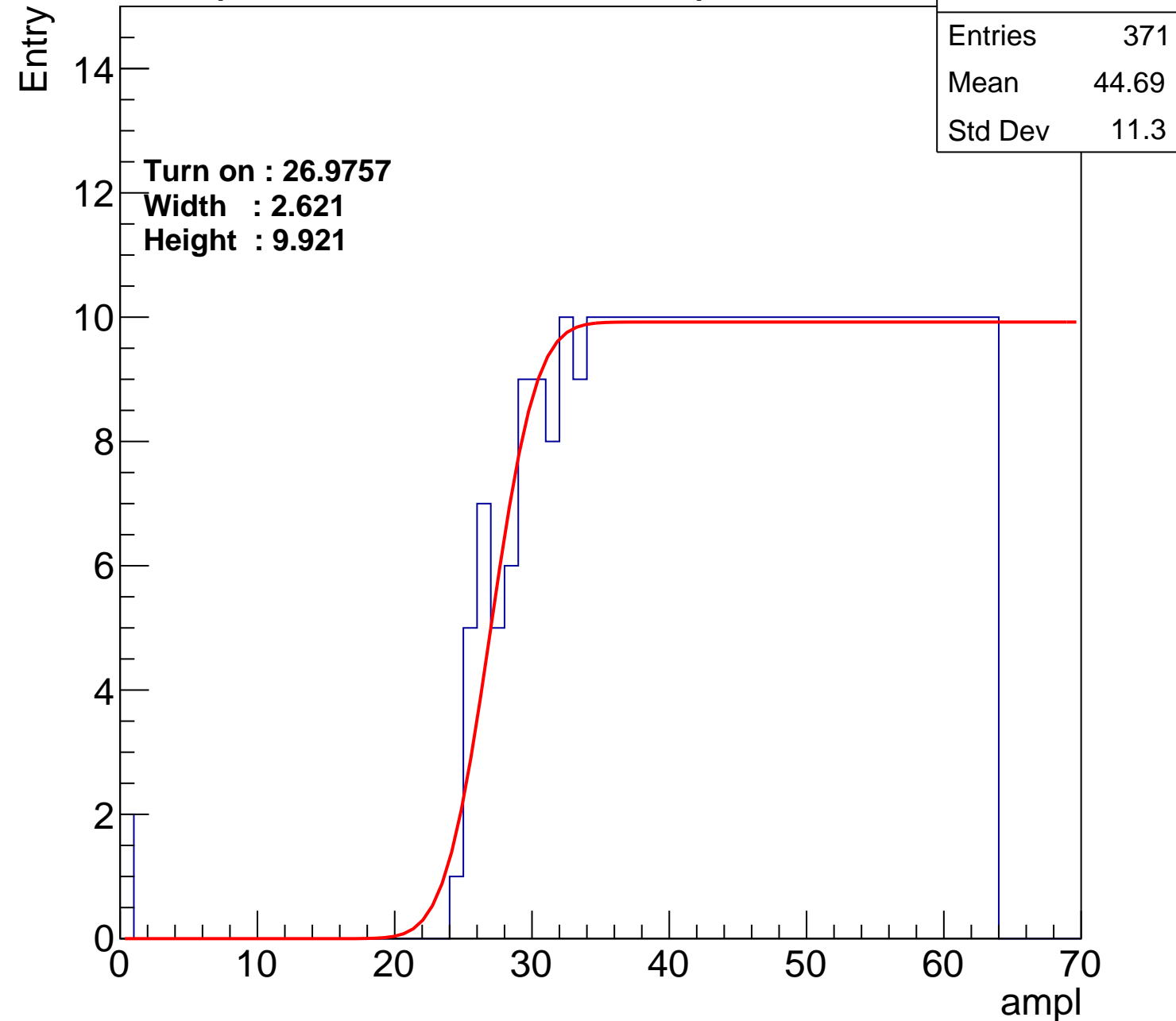
Width : 2.621

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.75
Std Dev	11.22

Turn on : 27.1502

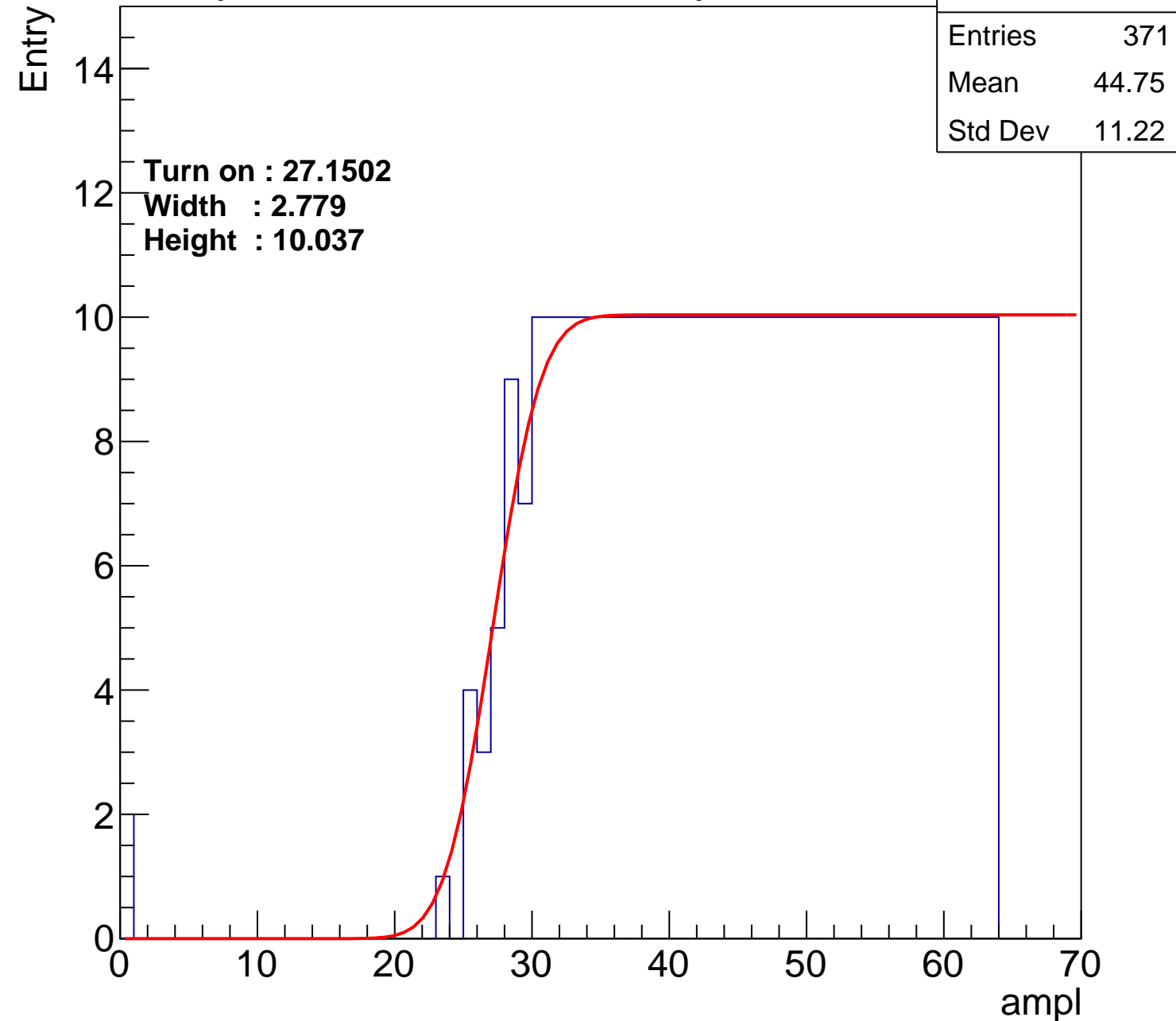
Width : 2.779

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.5332

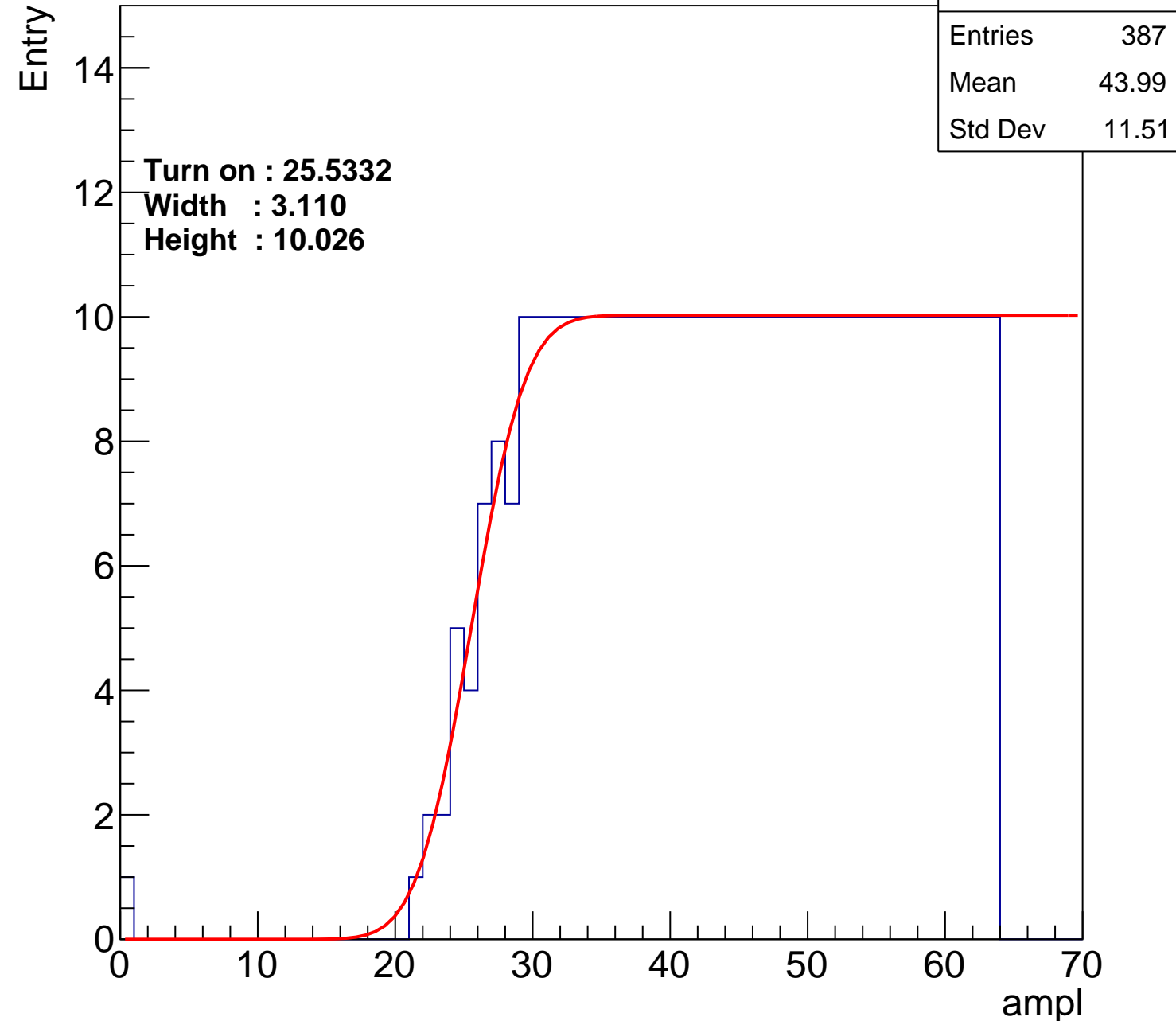
Width : 3.110

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch5

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.09
Std Dev	10.94

Turn on : 27.5153

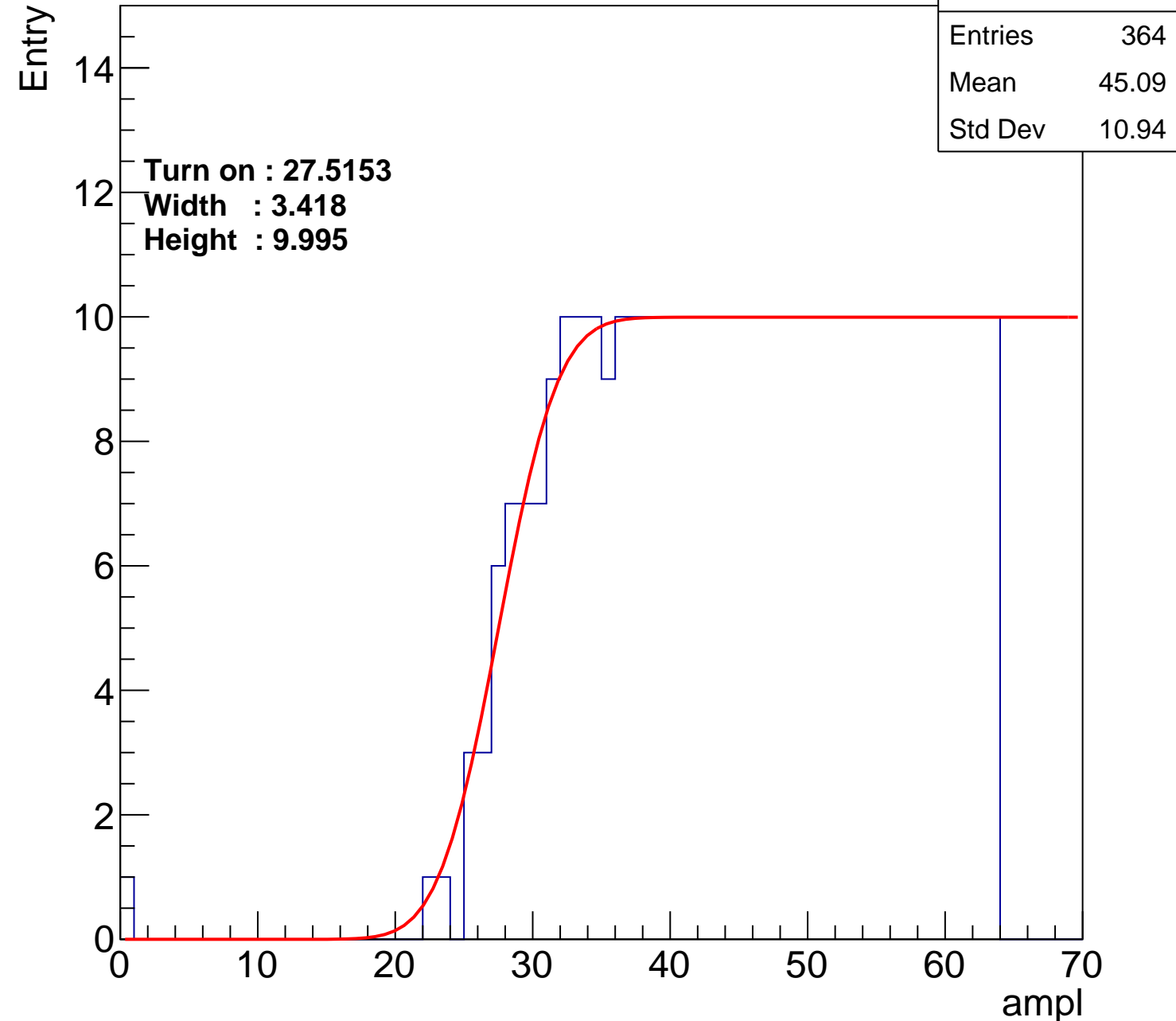
Width : 3.418

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch6

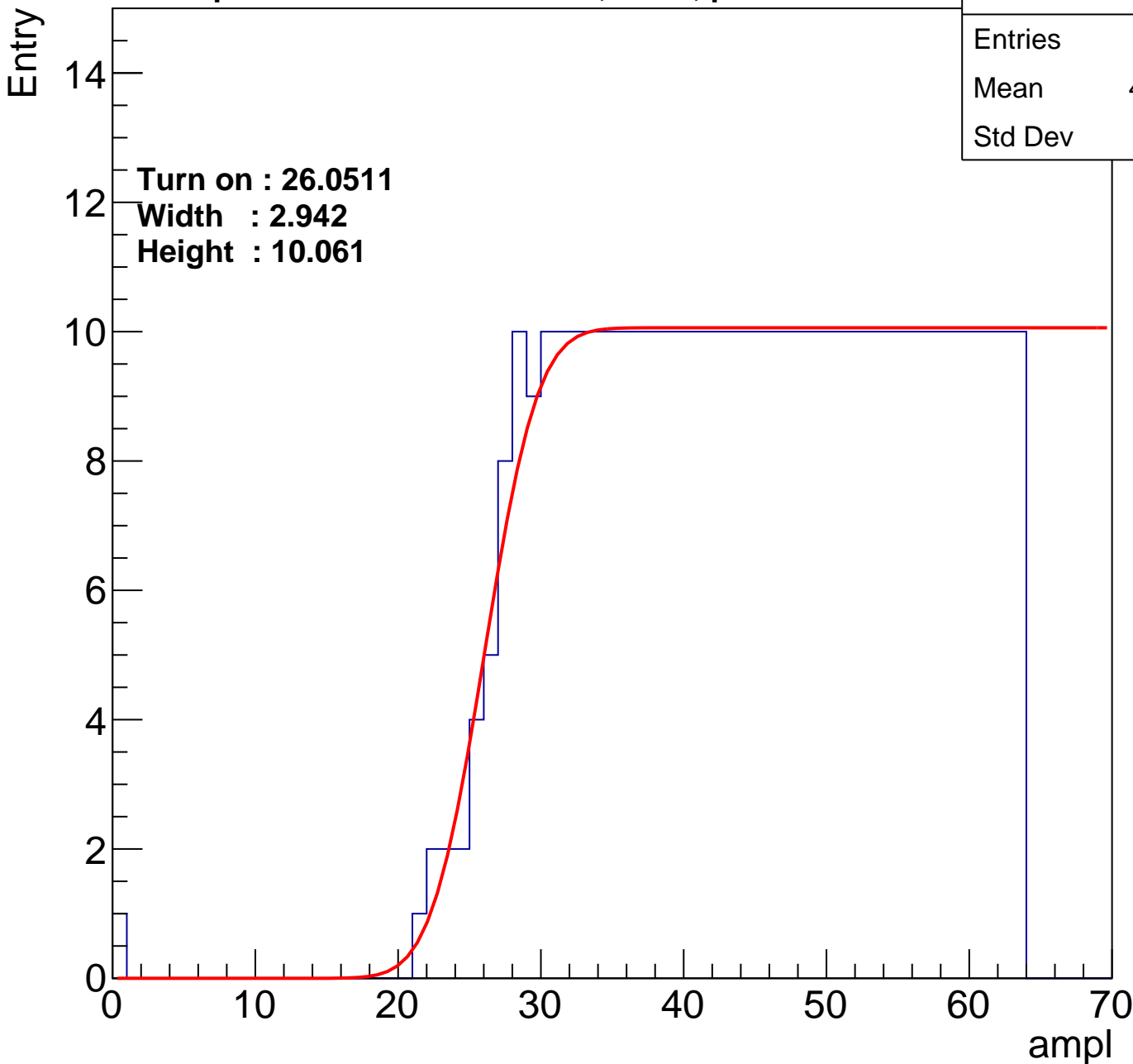
calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.16
Std Dev	11.4

Turn on : 26.0511

Width : 2.942

Height : 10.061



B0L000S, U22-ch7

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.2
Std Dev	11.51

Turn on : 25.8390

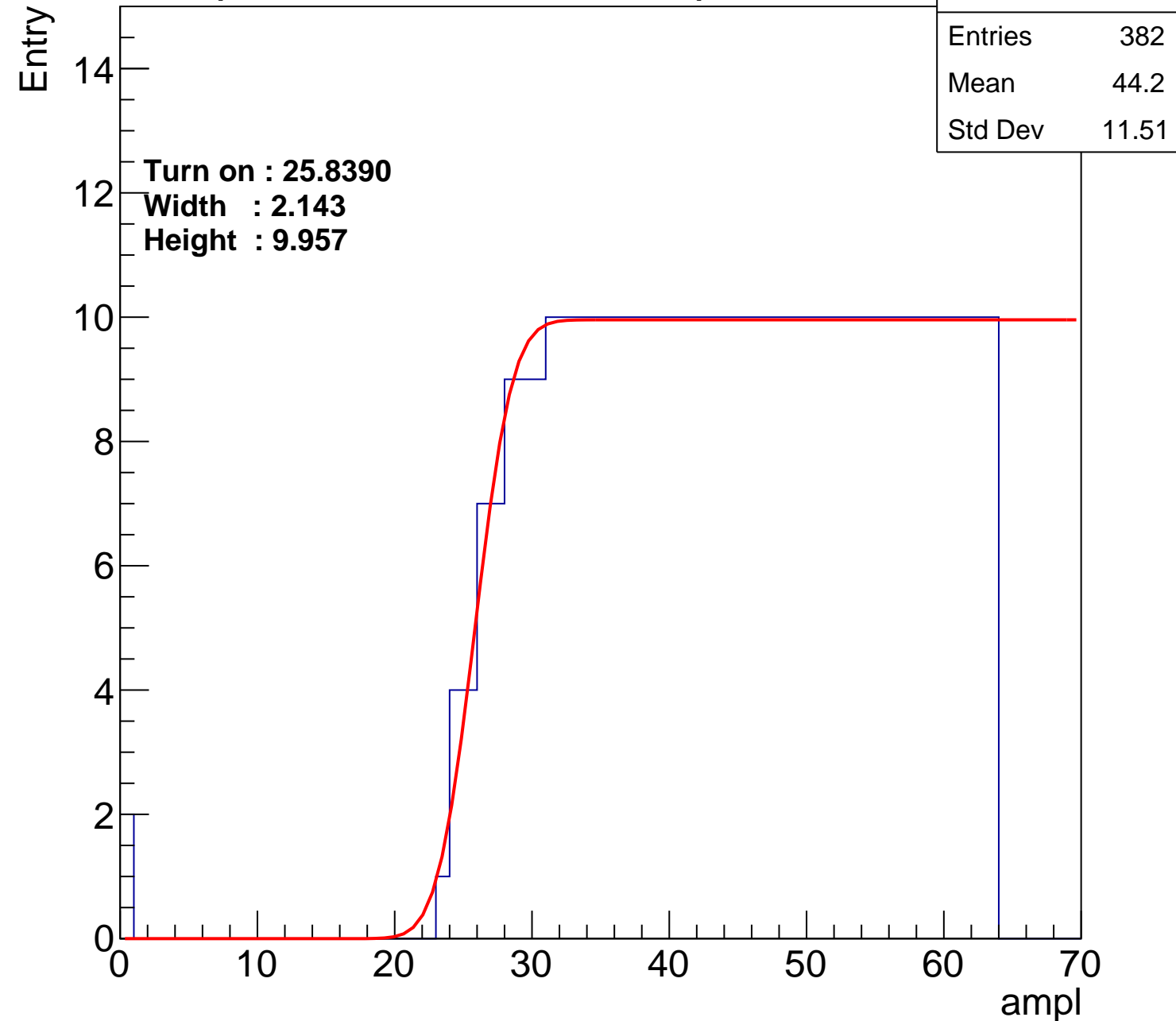
Width : 2.143

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	394
Mean	43.55
Std Dev	11.91

Turn on : 24.6311

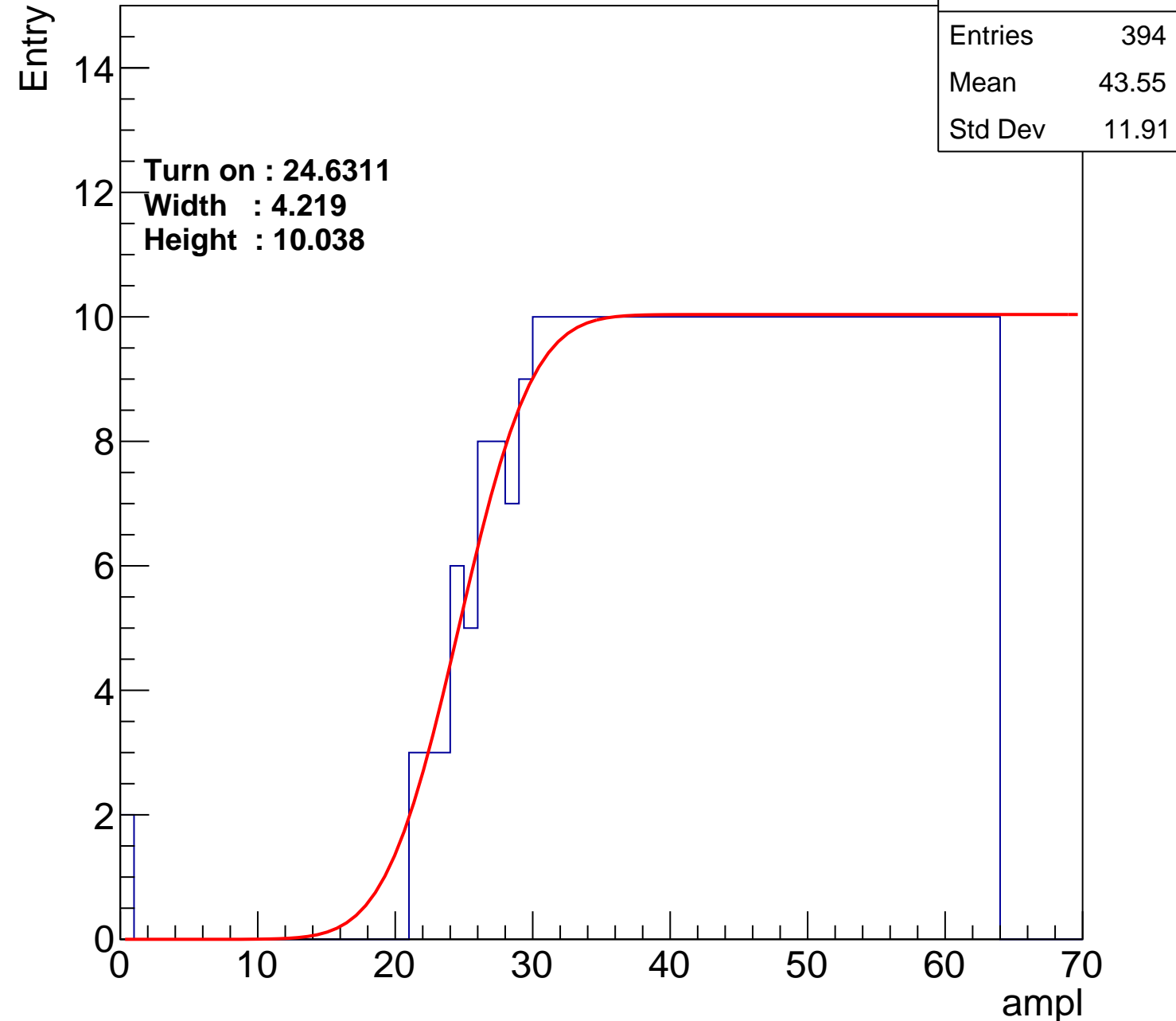
Width : 4.219

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch9

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.77
Std Dev	11.24

Turn on : 27.4265

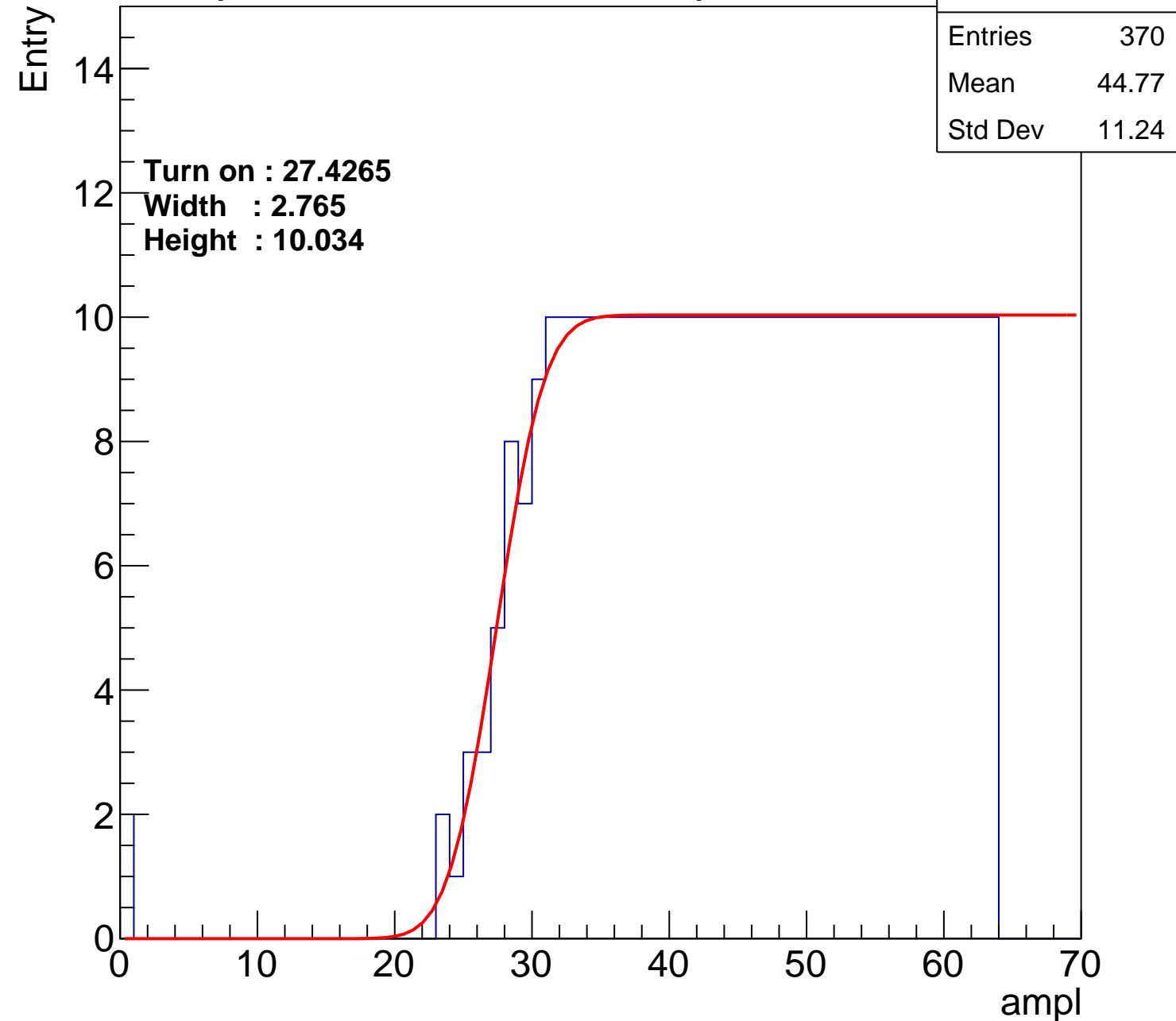
Width : 2.765

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch10

calib_packv5_042523_0143.root, FC#5, port B1

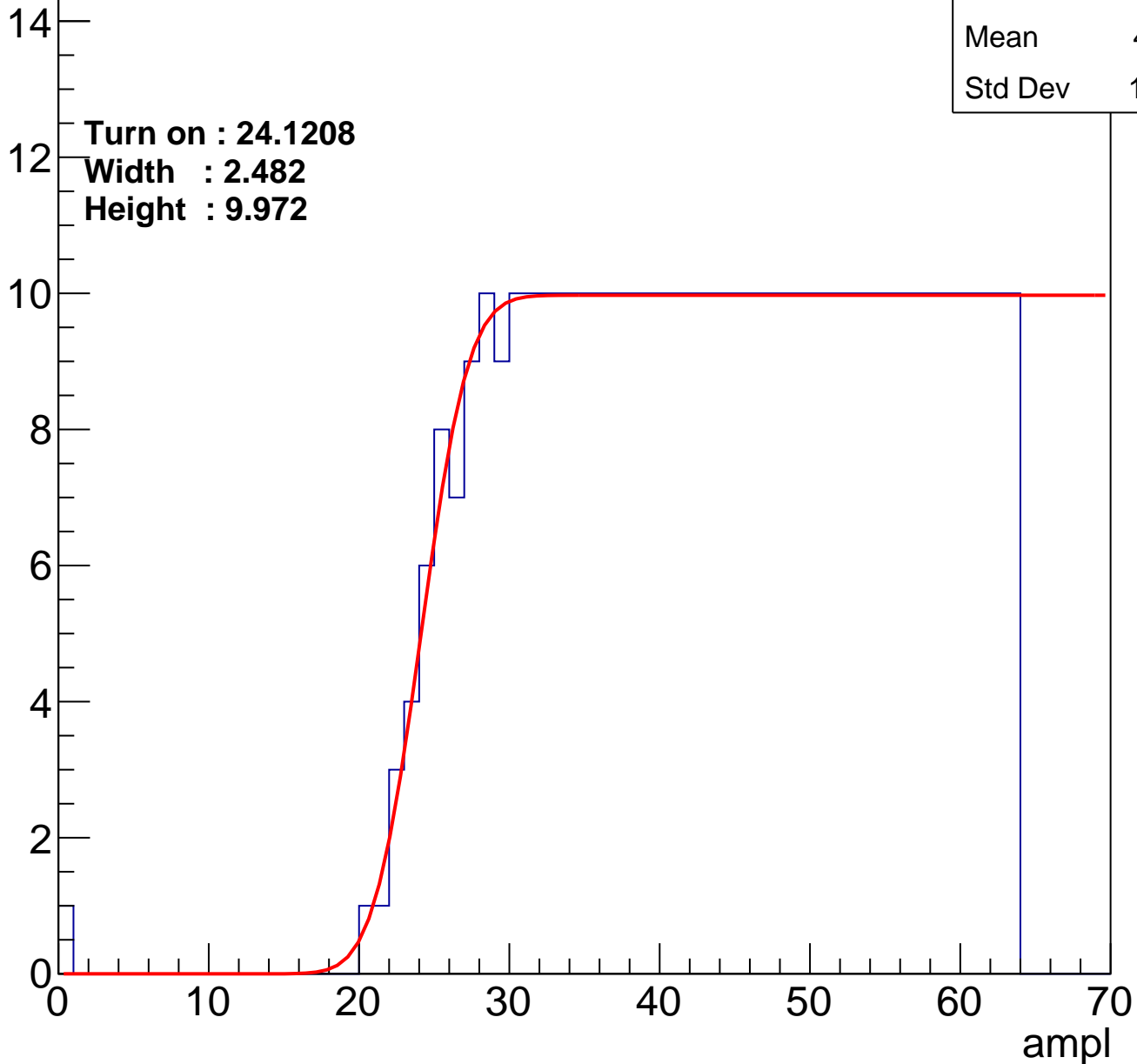
Entries	399
Mean	43.41
Std Dev	11.82

Turn on : 24.1208

Width : 2.482

Height : 9.972

Entry



B0L000S, U22-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.19
Std Dev	11.57

Turn on : 26.8844

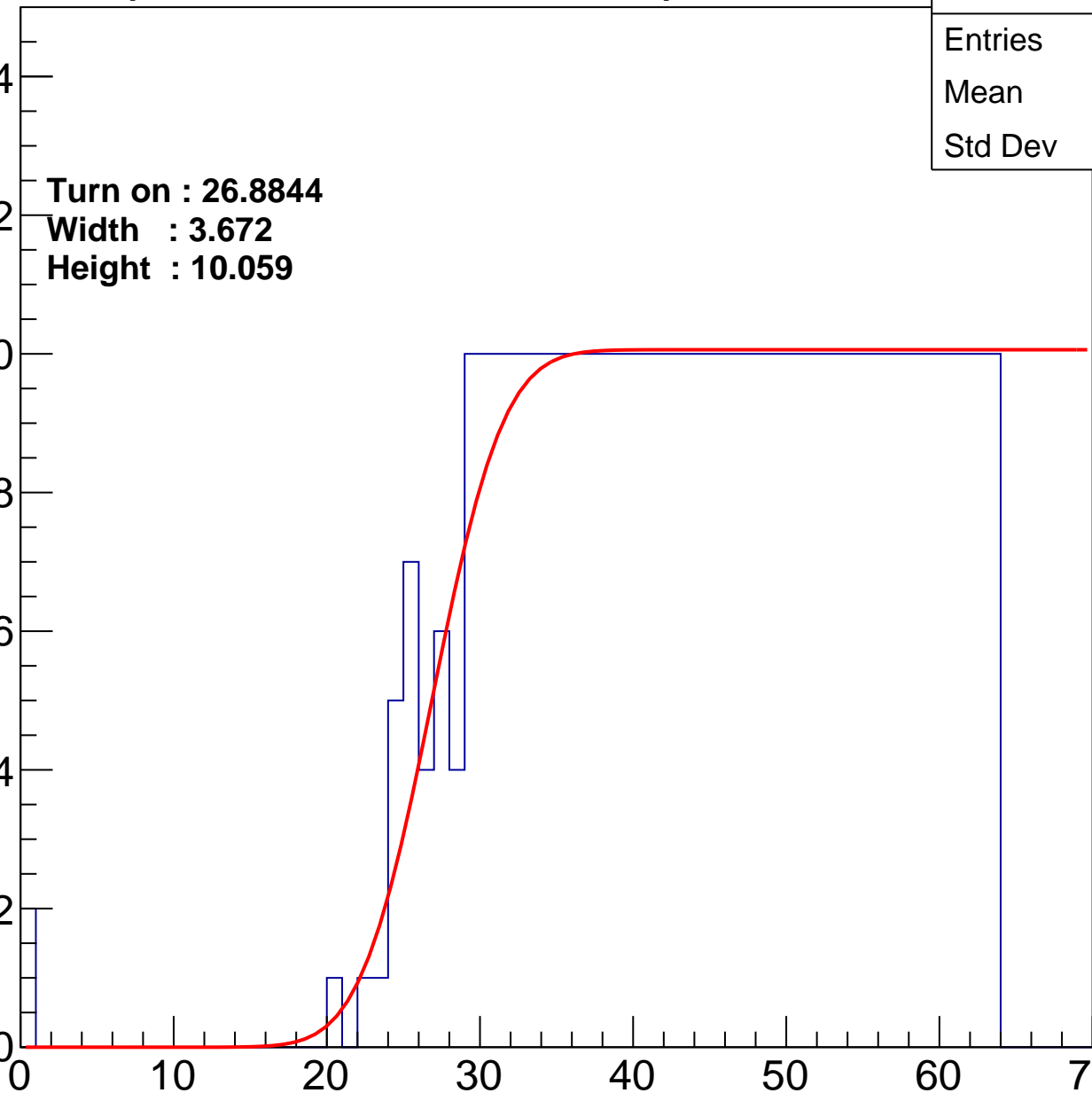
Width : 3.672

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.67
Std Dev	11.95

Turn on : 25.9274

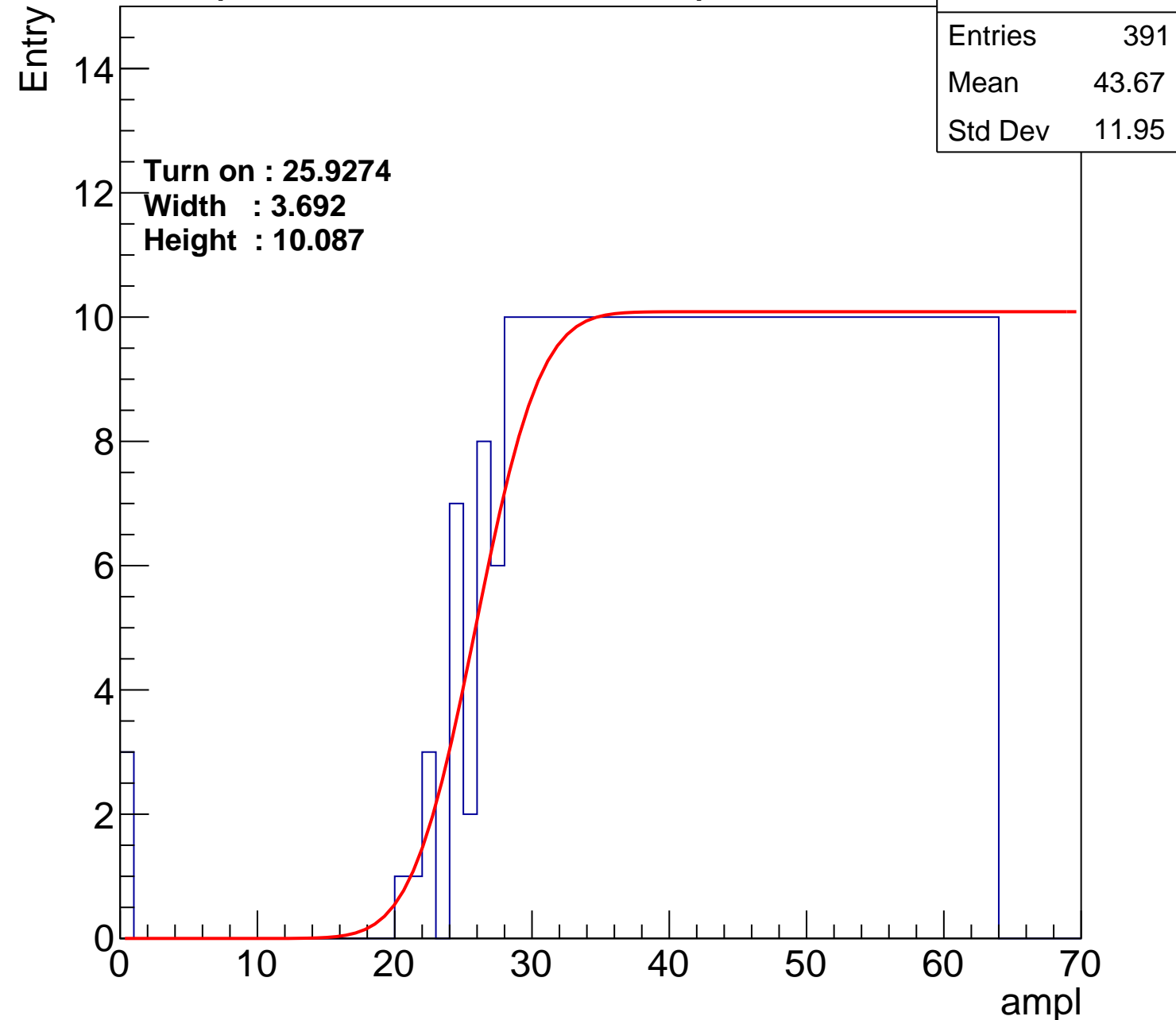
Width : 3.692

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch13

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.32
Std Dev	11.5

Turn on : 29.4934

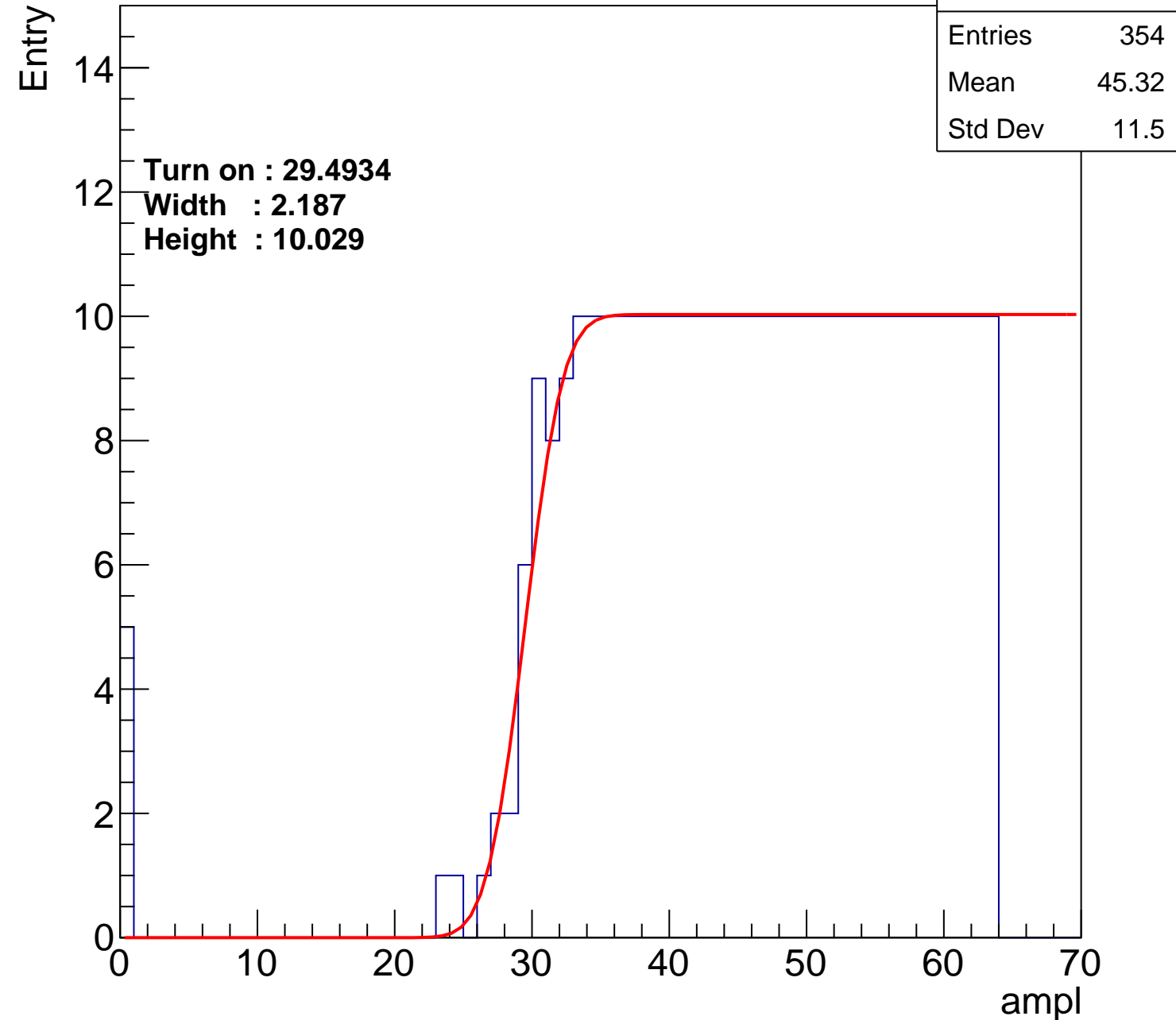
Width : 2.187

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch14

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.98
Std Dev	10.98

Turn on : 27.4402

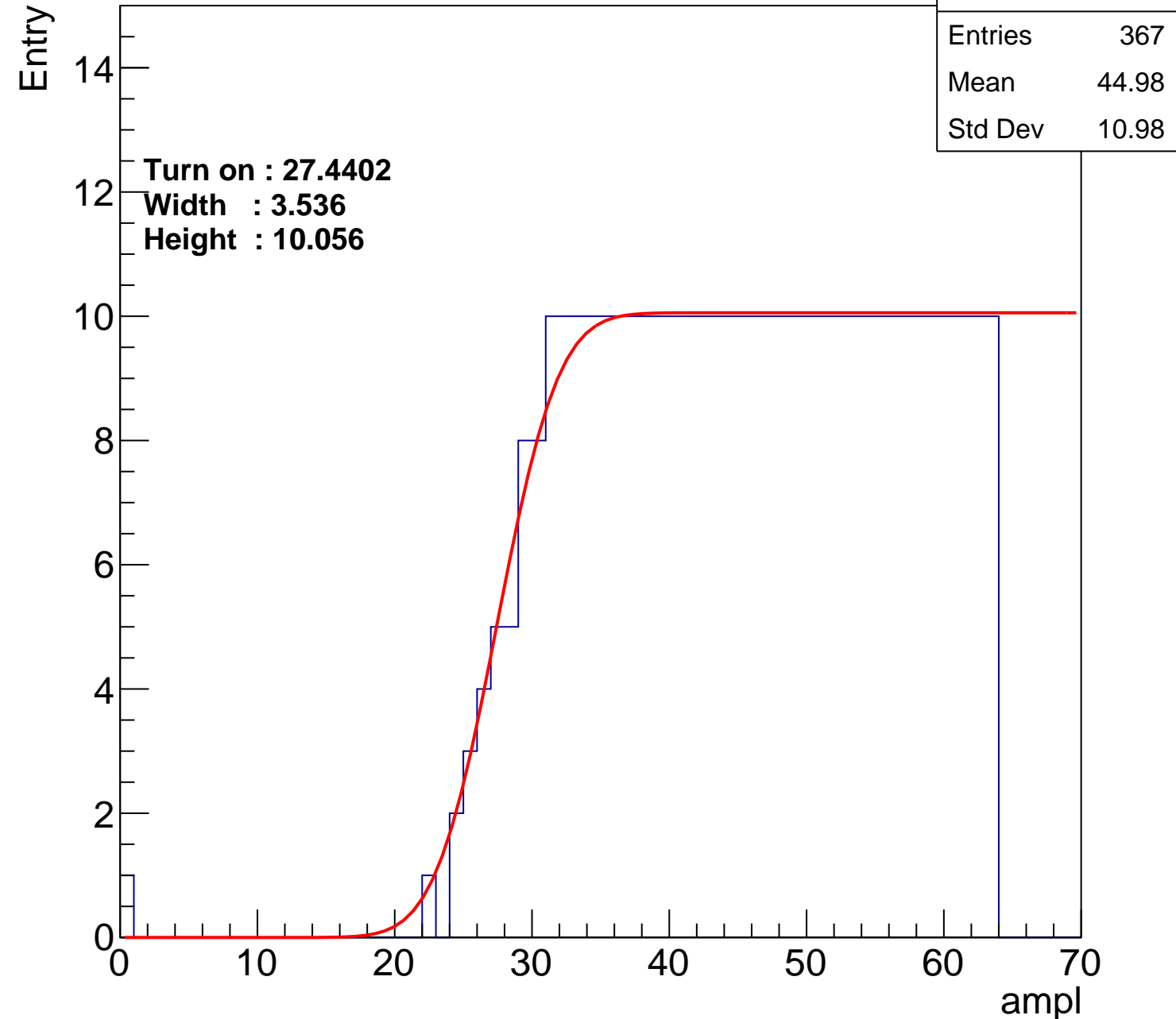
Width : 3.536

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch15

calib_packv5_042523_0143.root, FC#5, port B1

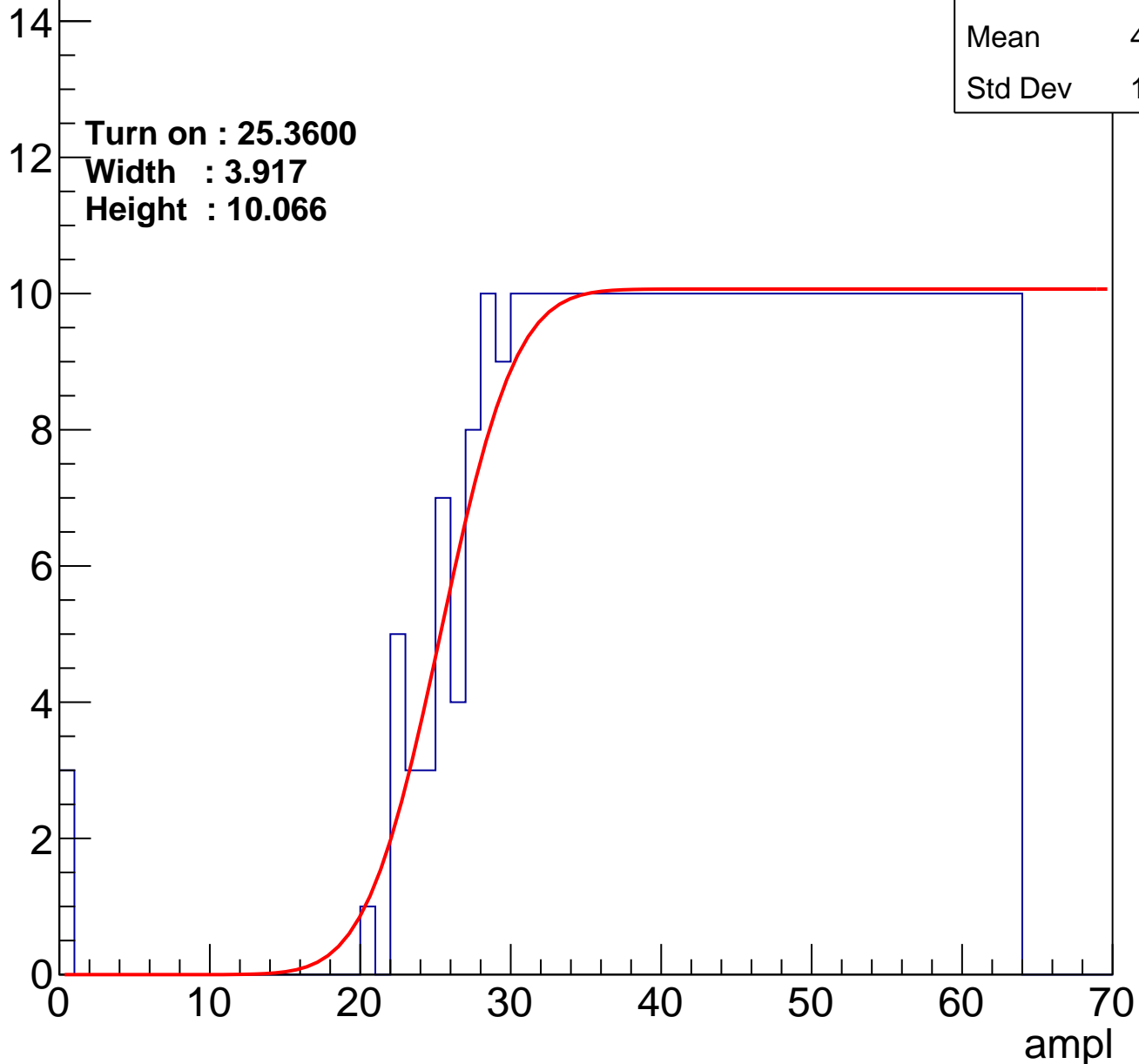
Entries	393
Mean	43.55
Std Dev	12.02

Turn on : 25.3600

Width : 3.917

Height : 10.066

Entry



B0L000S, U22-ch16

calib_packv5_042523_0143.root, FC#5, port B1

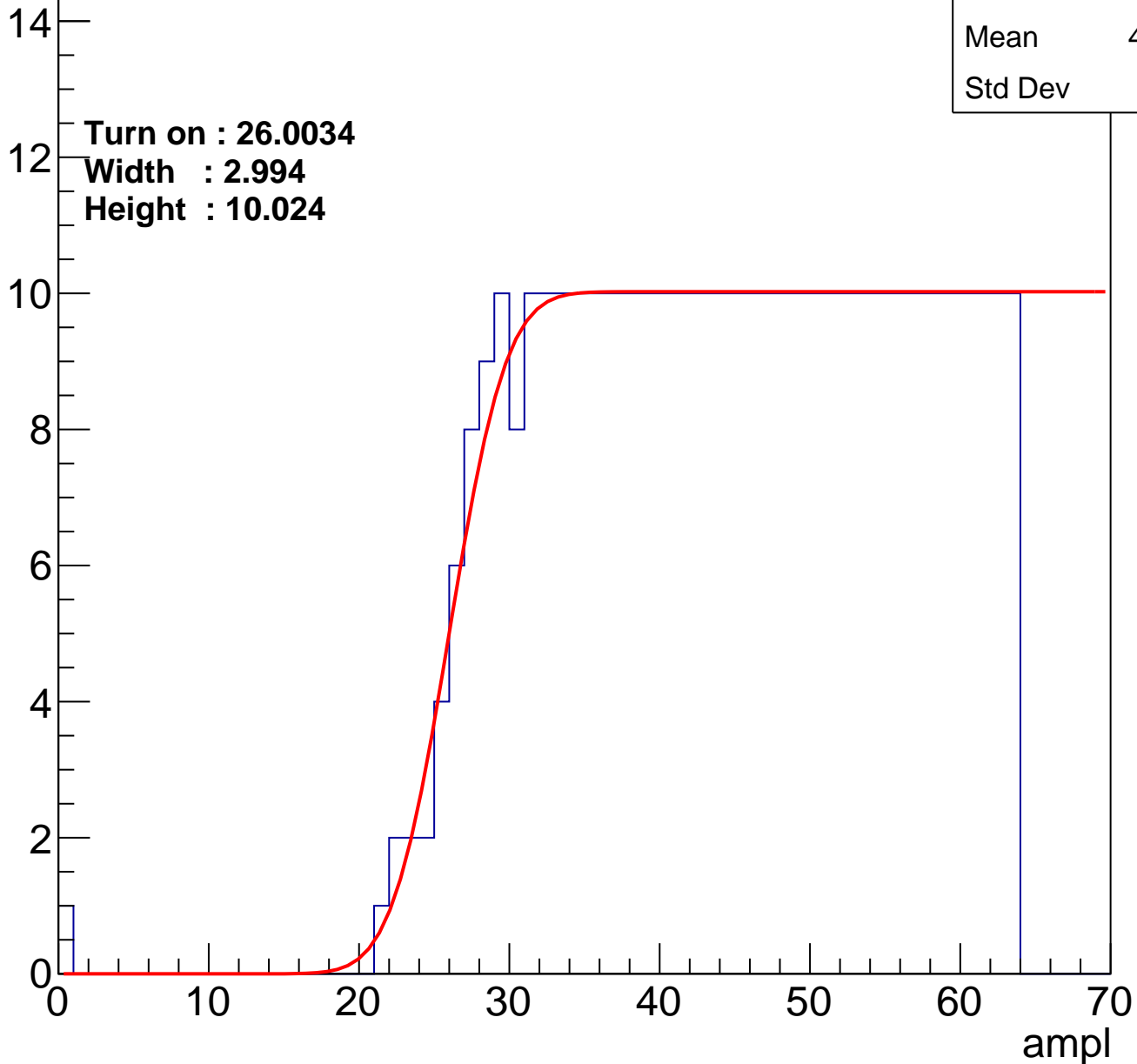
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 26.0034

Width : 2.994

Height : 10.024

Entry



B0L000S, U22-ch17

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.34
Std Dev	10.91

Turn on : 28.5403

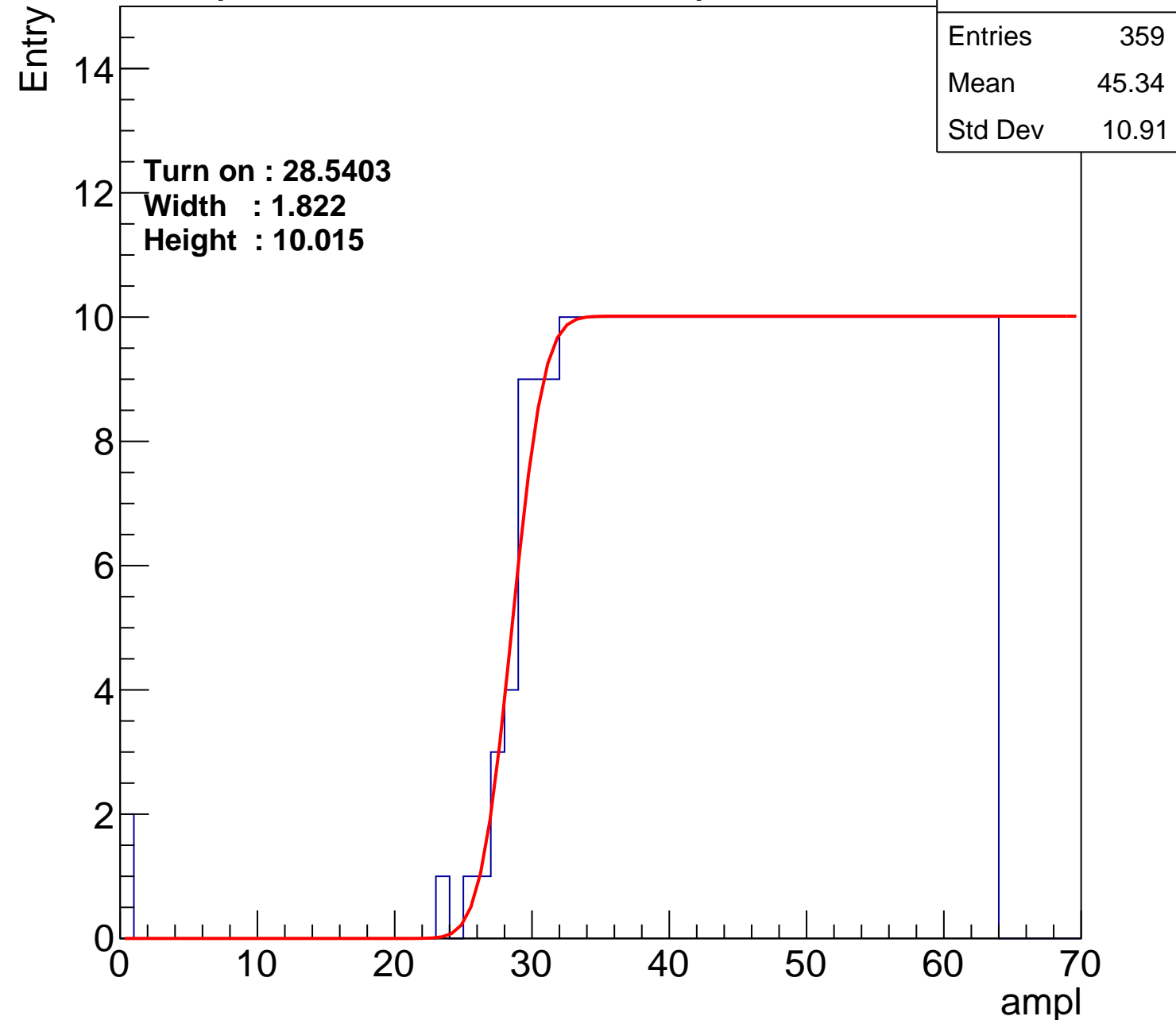
Width : 1.822

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch18

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 27.9850

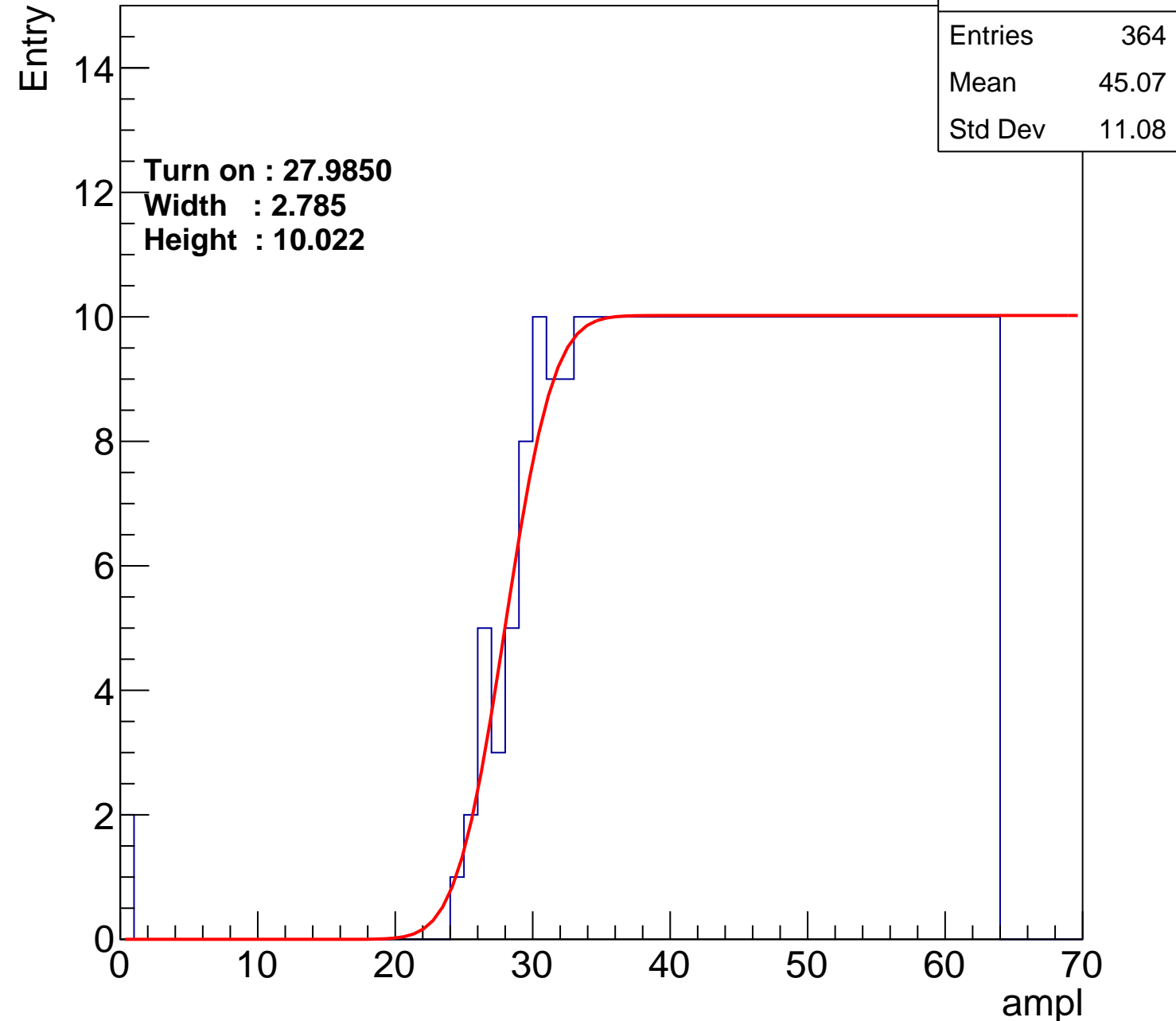
Width : 2.785

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.96
Std Dev	10.95

Turn on : 27.4627

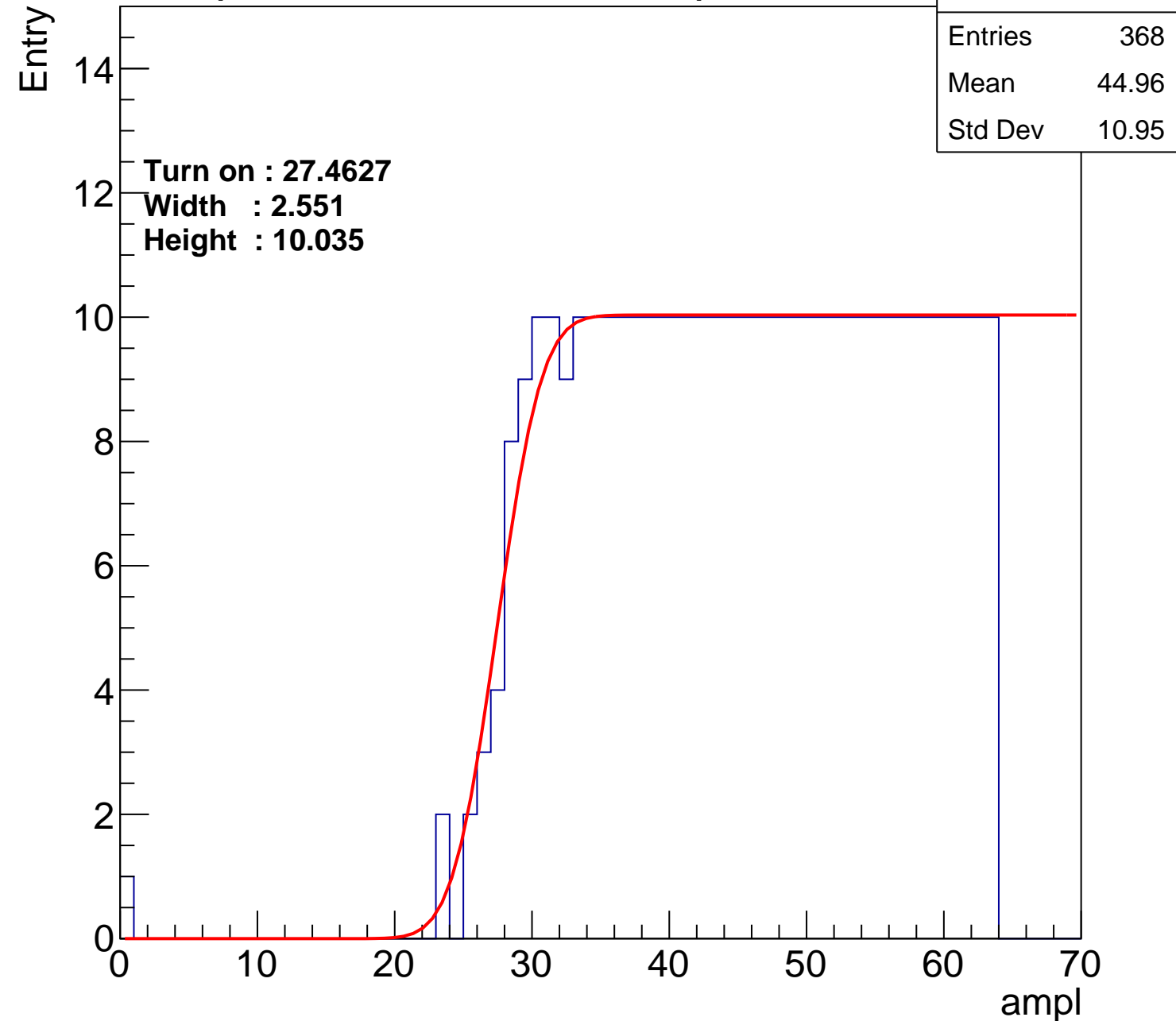
Width : 2.551

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch20

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	45.02
Std Dev	10.9

Turn on : 27.0431

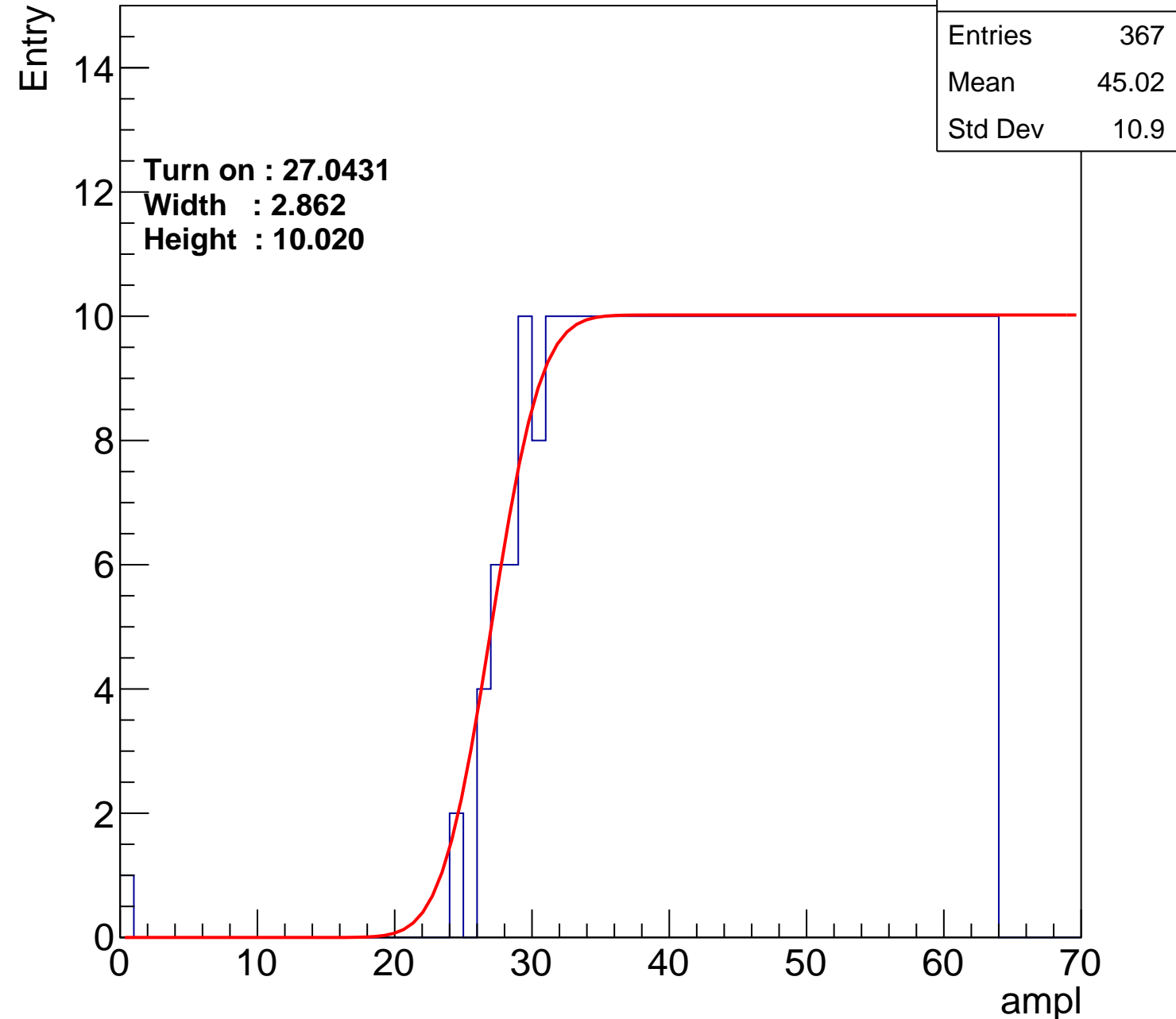
Width : 2.862

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch21

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.52
Std Dev	10.62

Turn on : 28.5987

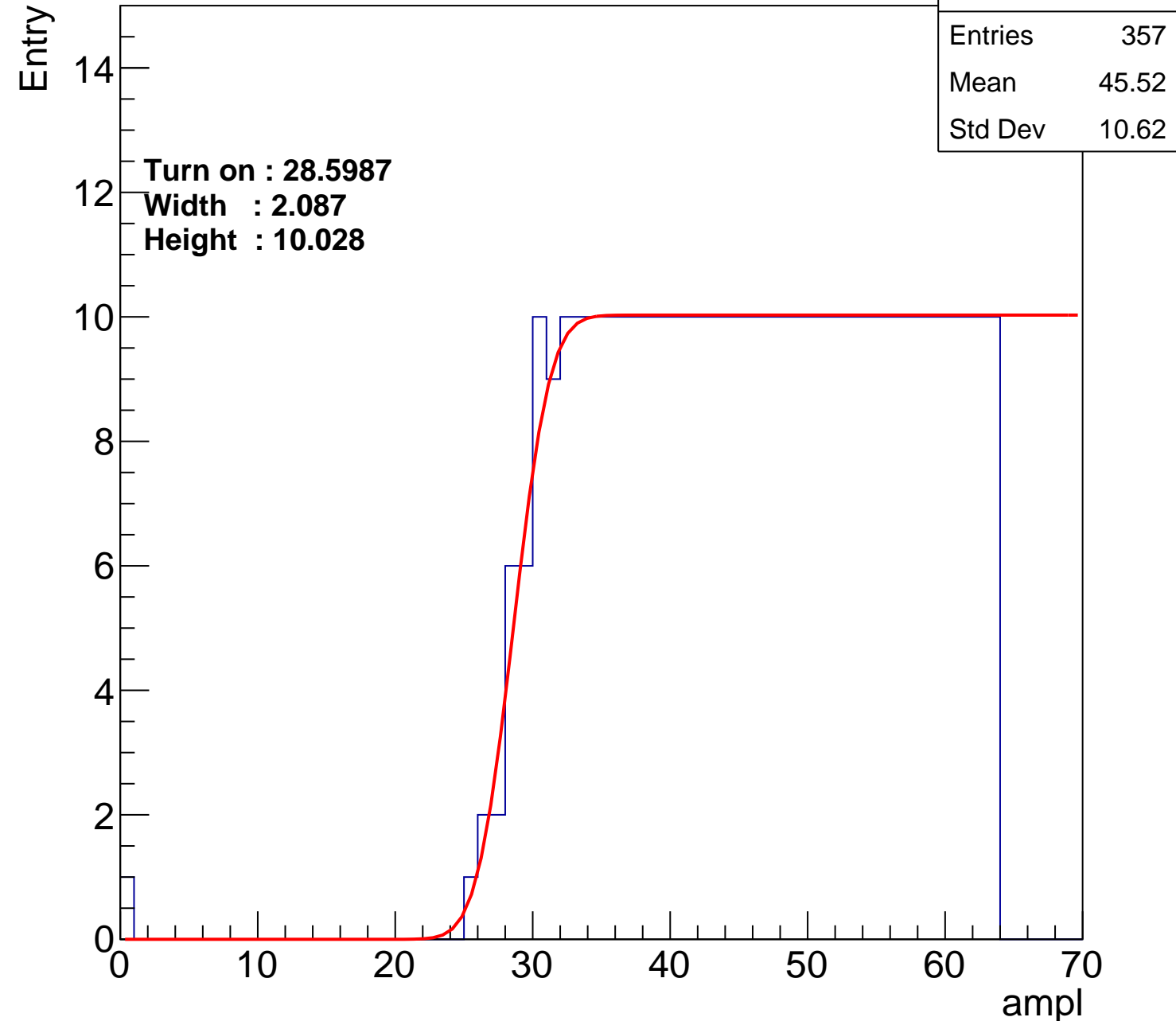
Width : 2.087

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch22

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 26.6799

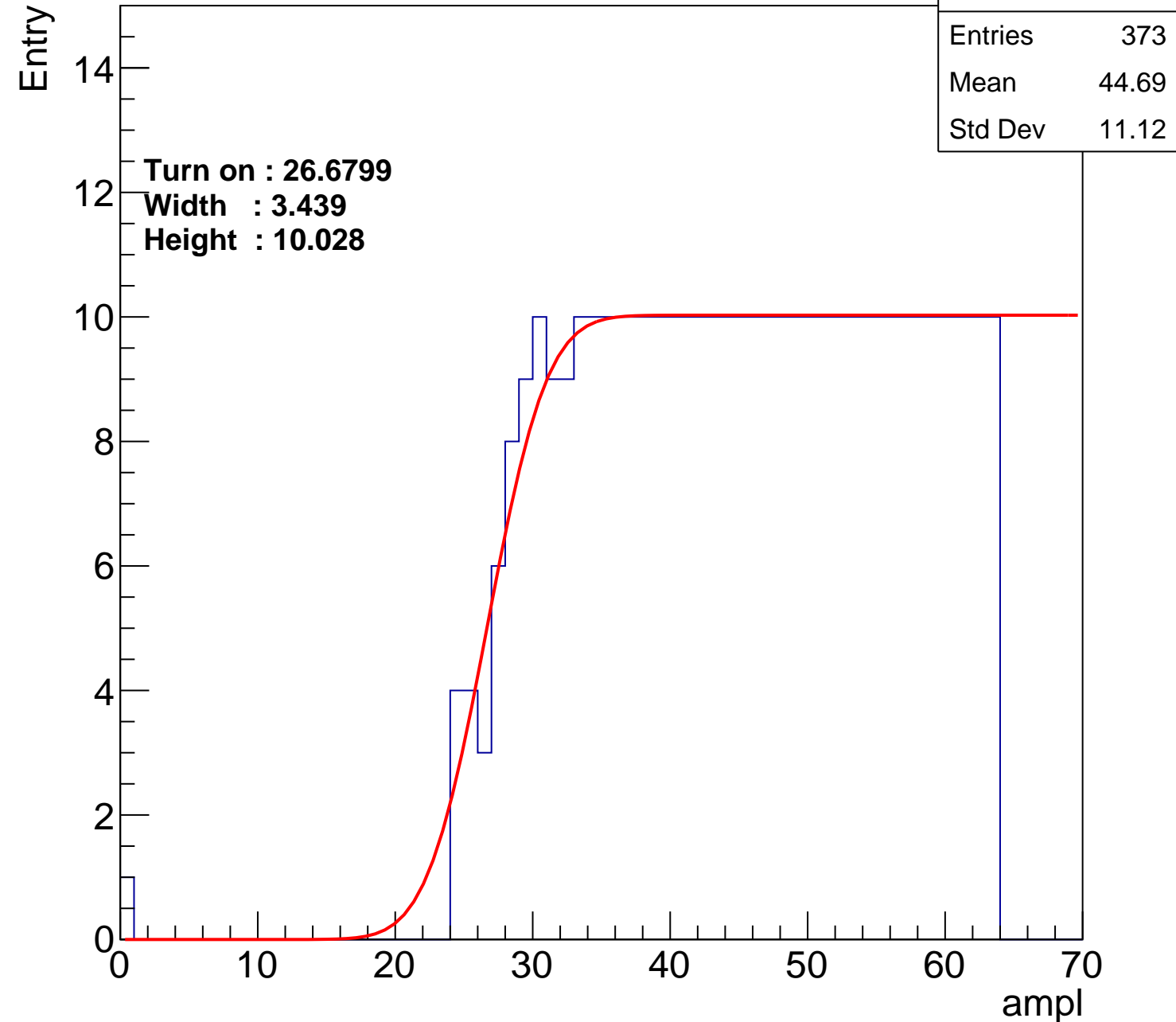
Width : 3.439

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch23

calib_packv5_042523_0143.root, FC#5, port B1

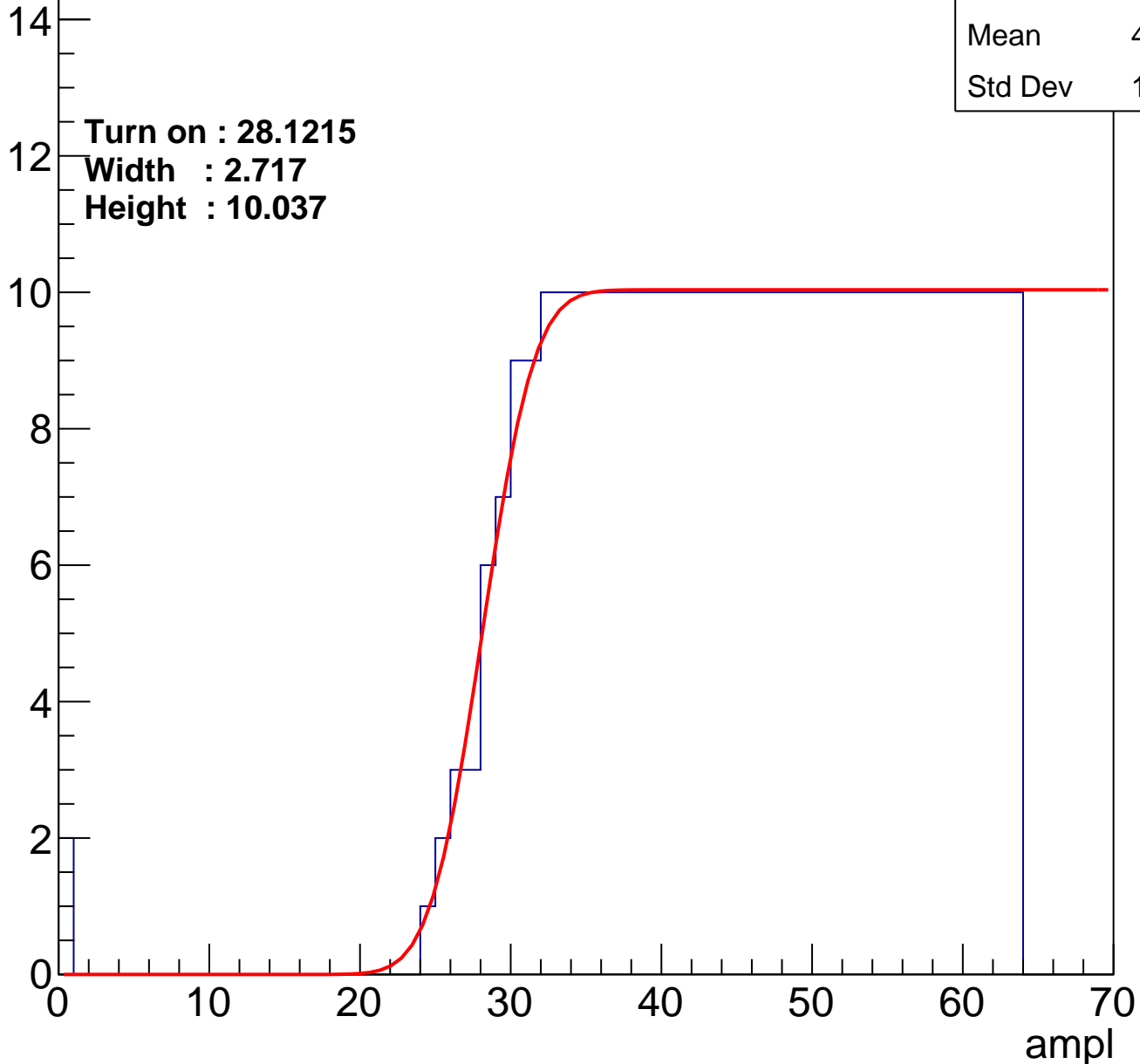
Entries	362
Mean	45.17
Std Dev	11.02

Turn on : 28.1215

Width : 2.717

Height : 10.037

Entry



B0L000S, U22-ch24

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.62
Std Dev	11.45

Turn on : 27.2887

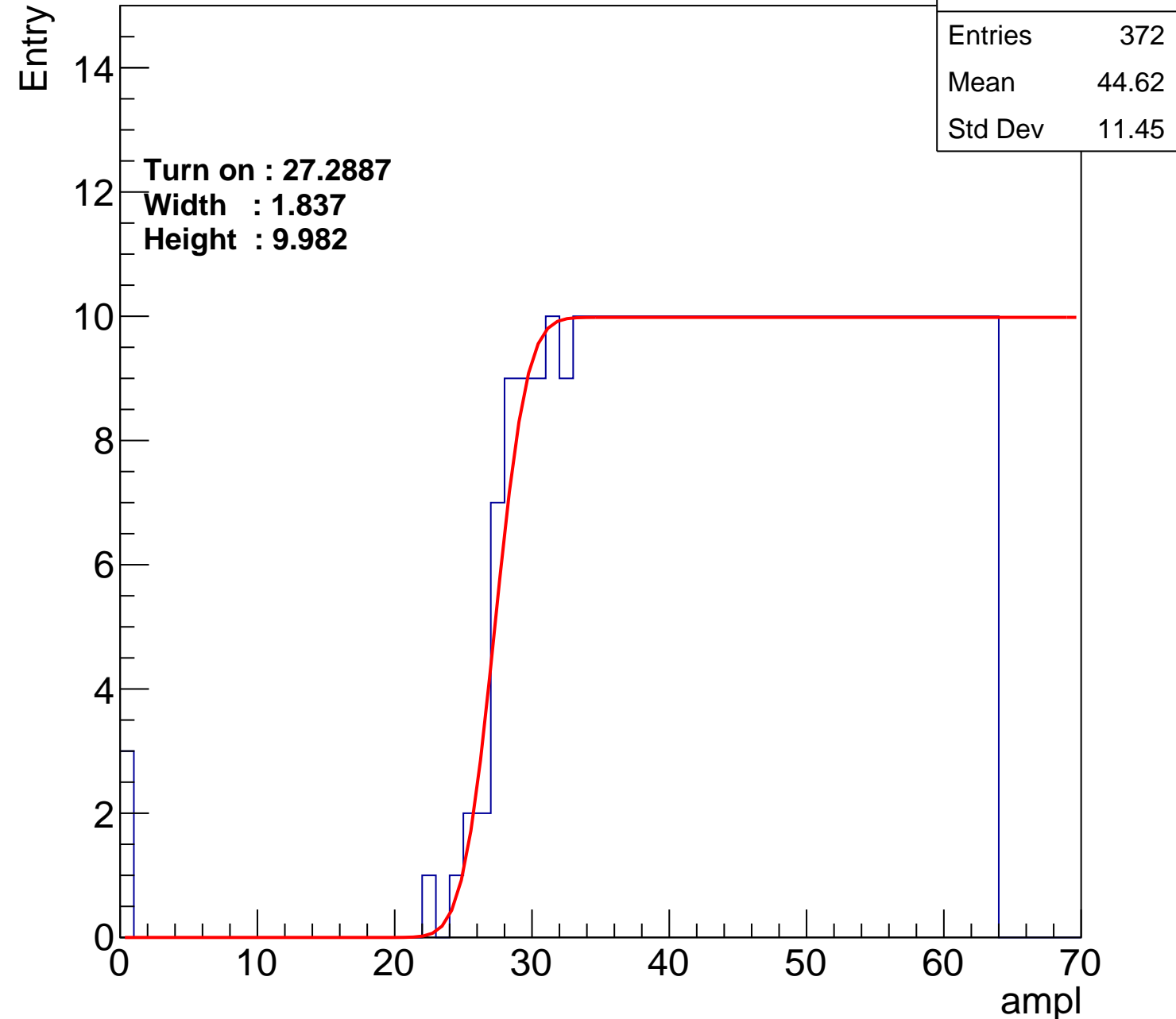
Width : 1.837

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch25

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.1086

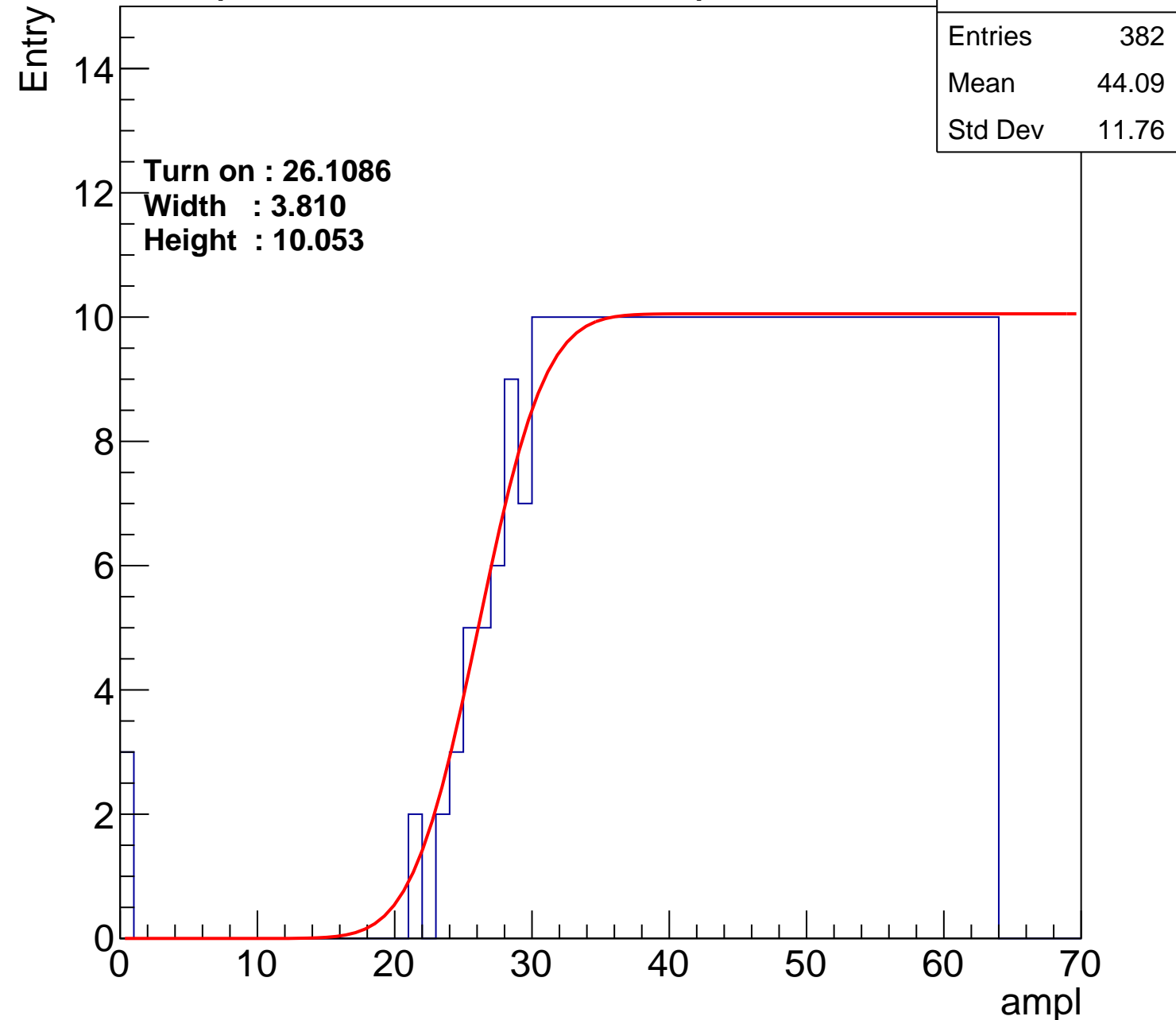
Width : 3.810

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch26

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.86
Std Dev	11.69

Turn on : 25.2847

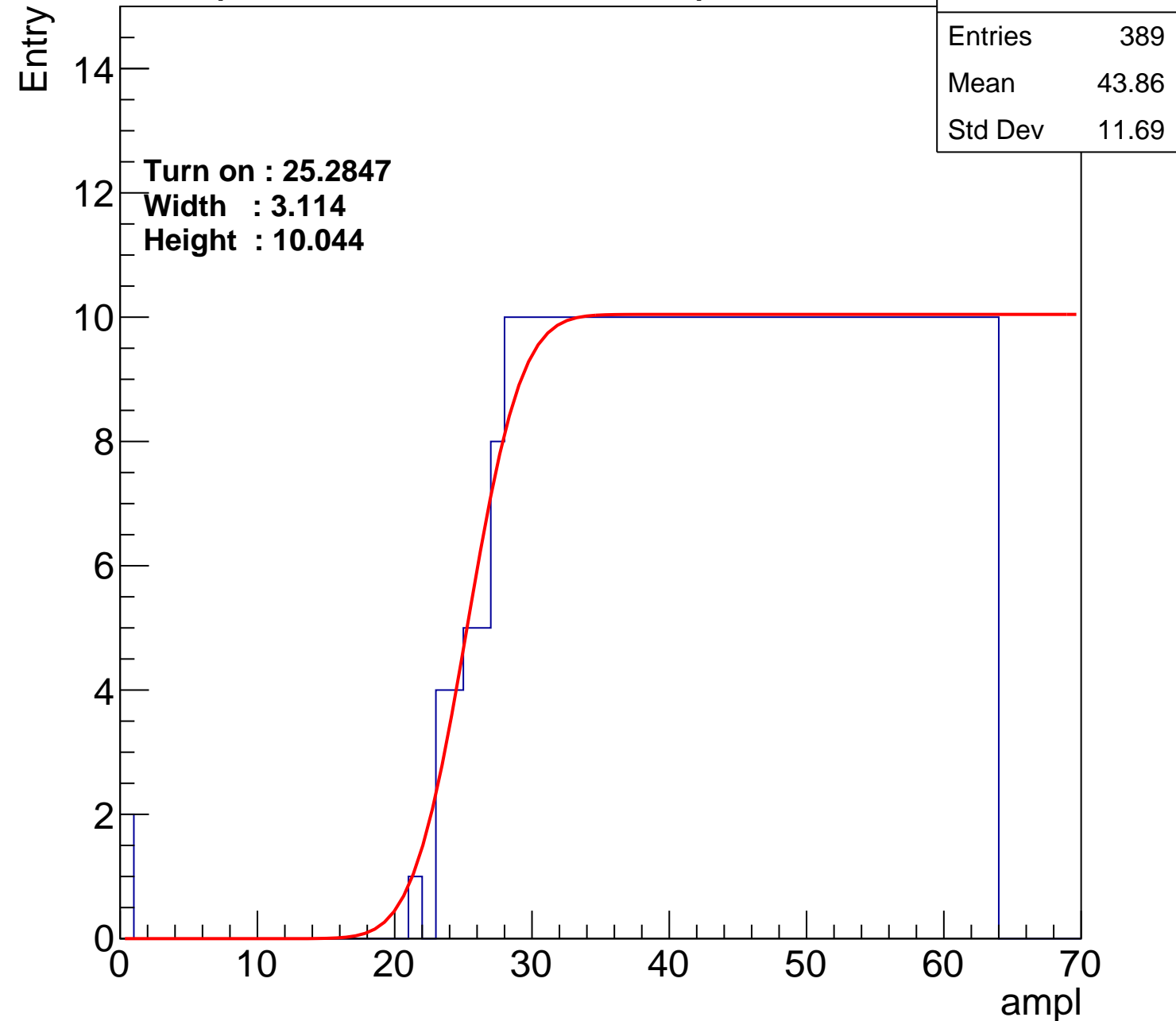
Width : 3.114

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch27

calib_packv5_042523_0143.root, FC#5, port B1

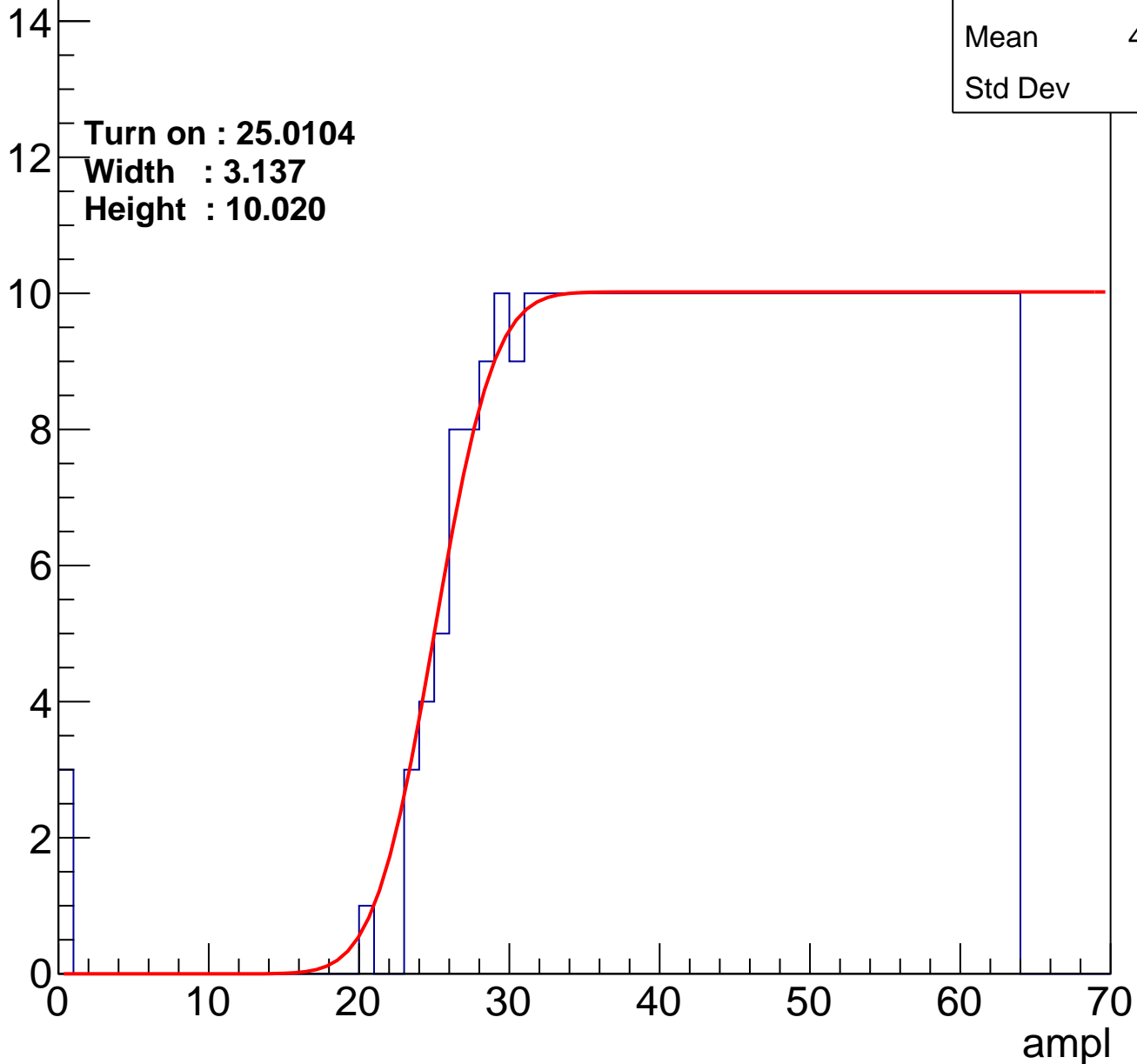
Entries	390
Mean	43.73
Std Dev	11.9

Turn on : 25.0104

Width : 3.137

Height : 10.020

Entry



B0L000S, U22-ch28

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 27.0753

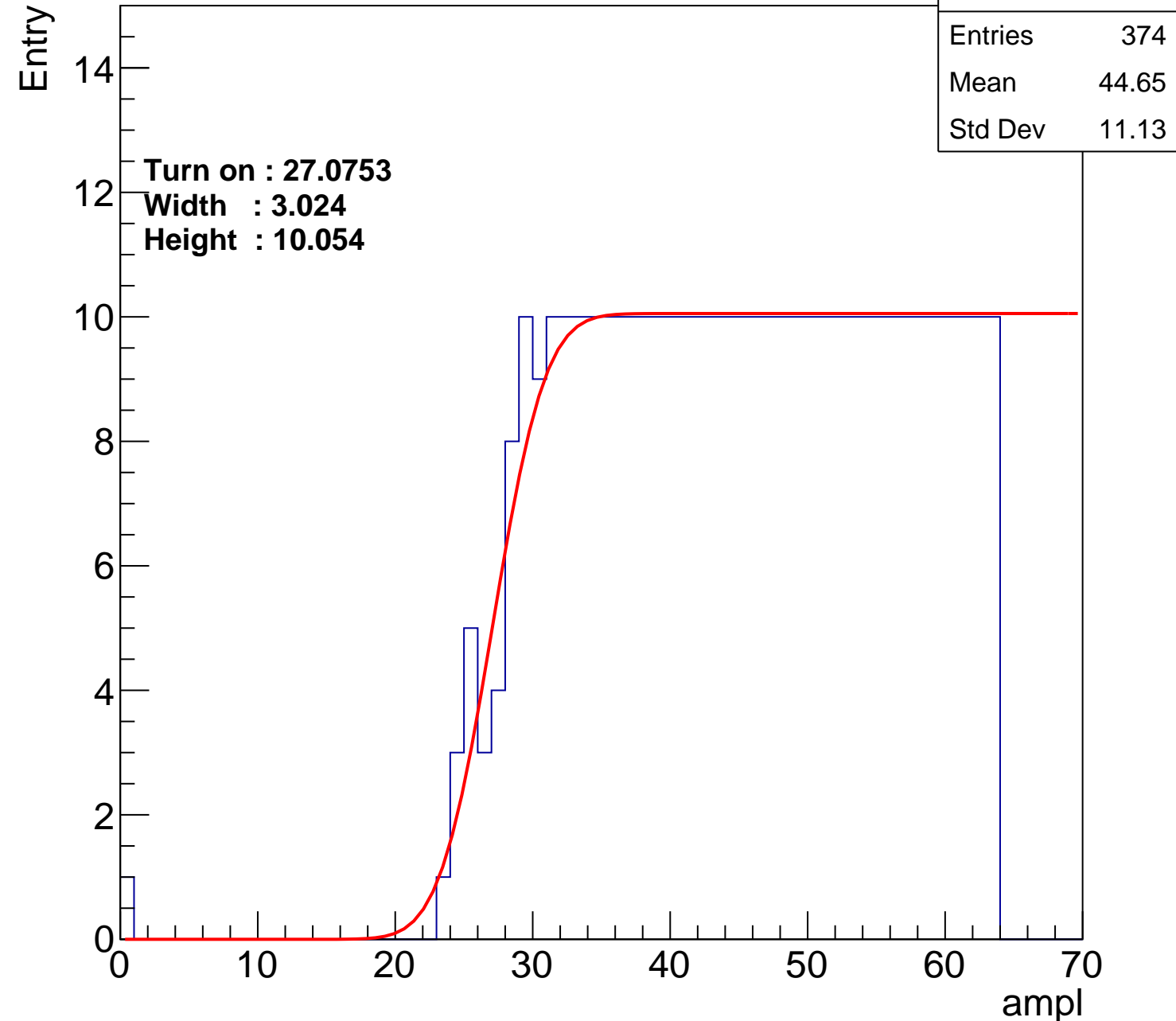
Width : 3.024

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch29

calib_packv5_042523_0143.root, FC#5, port B1

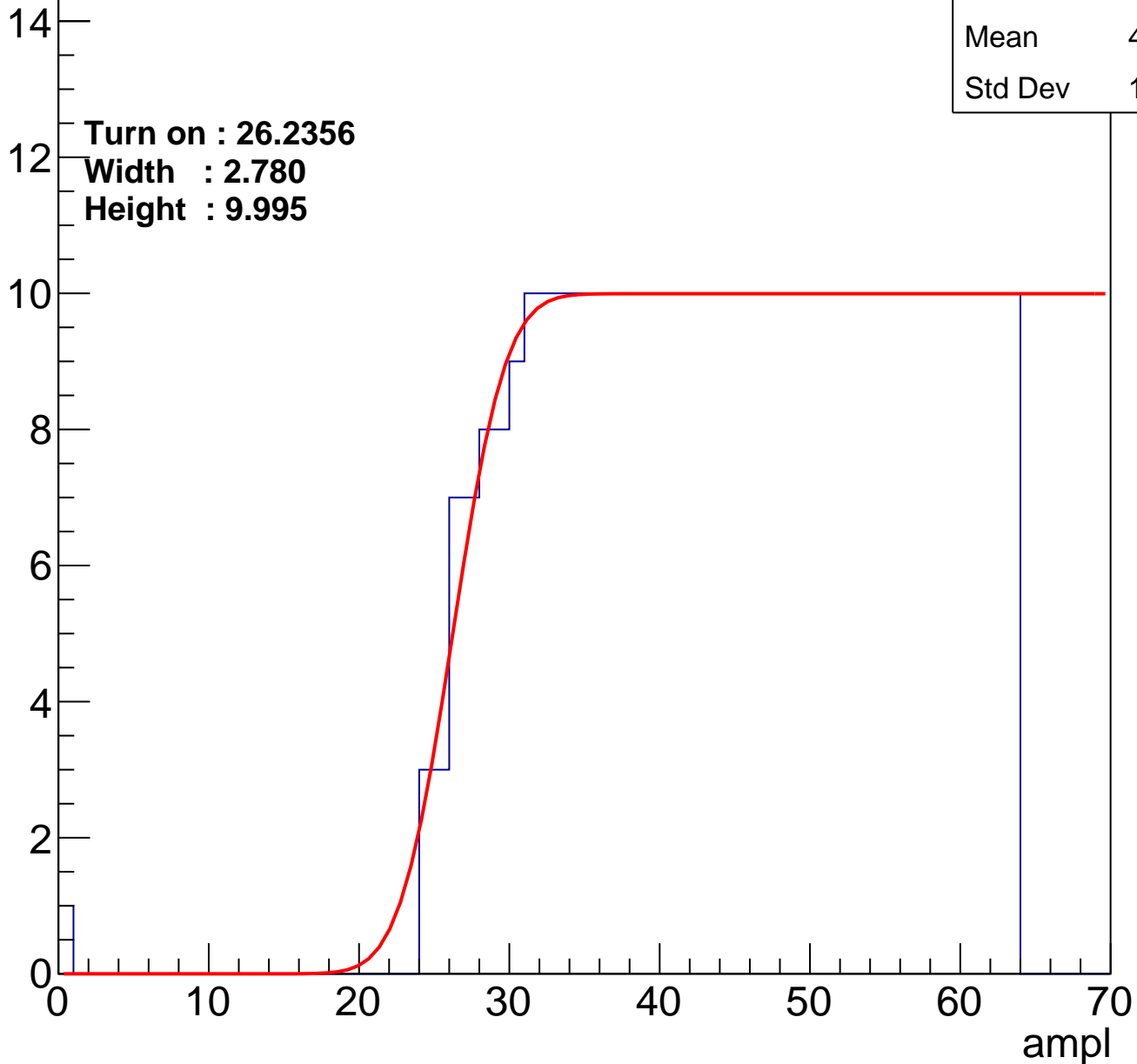
Entries	376
Mean	44.56
Std Dev	11.17

Turn on : 26.2356

Width : 2.780

Height : 9.995

Entry



B0L000S, U22-ch30

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.81
Std Dev	11.71

Turn on : 25.2317

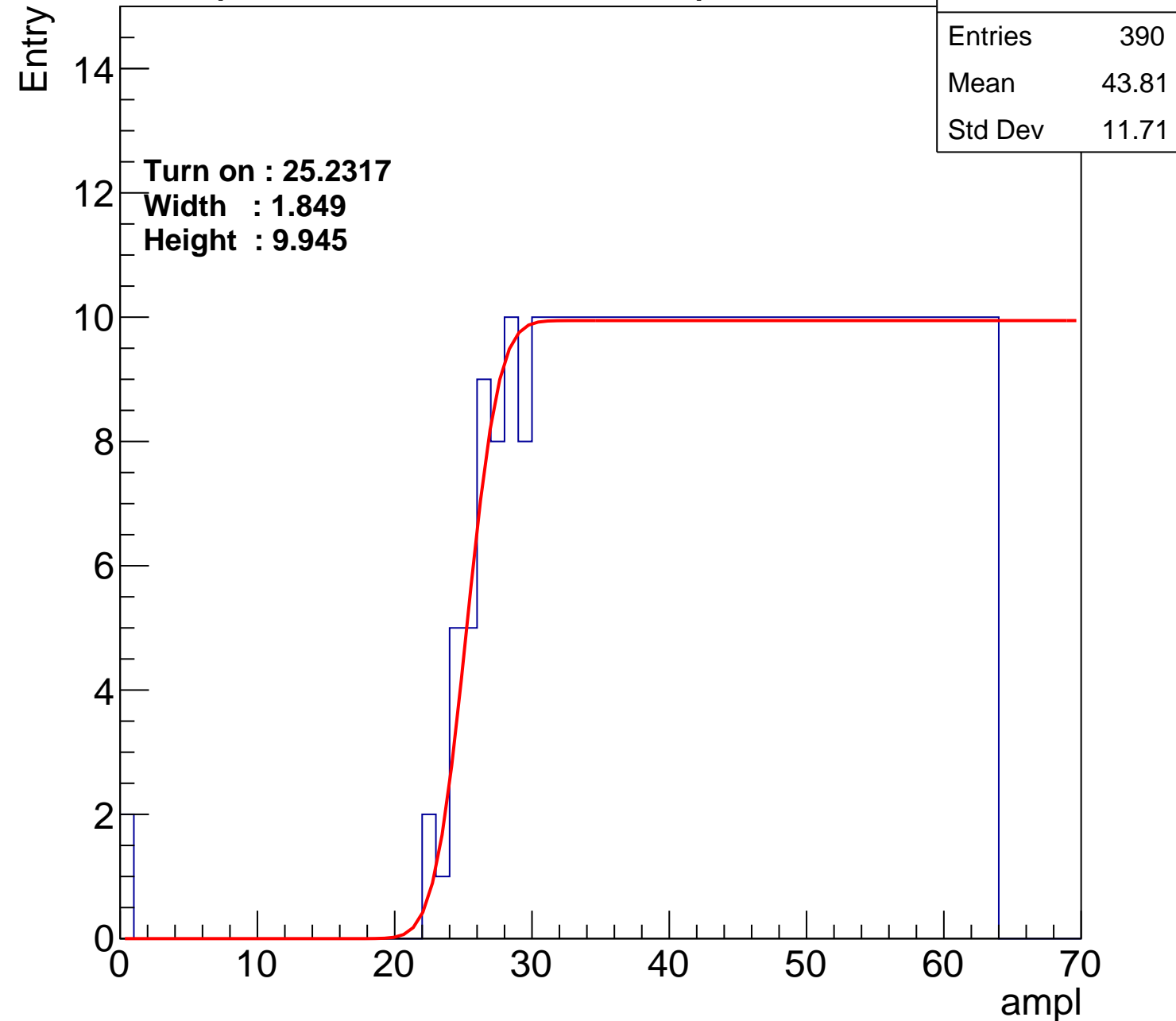
Width : 1.849

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch31

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.39
Std Dev	11.49

Turn on : 26.8715

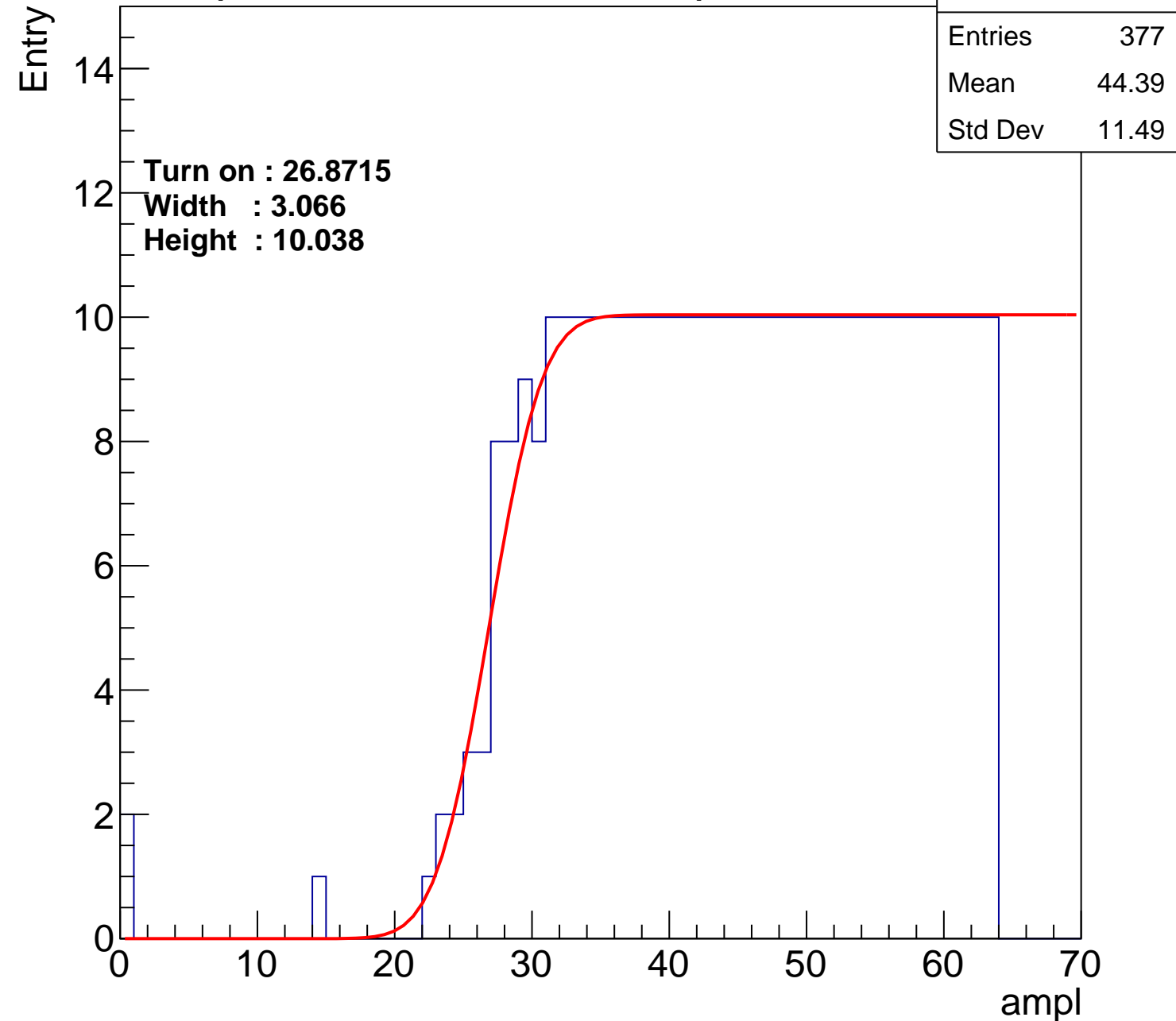
Width : 3.066

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch32

calib_packv5_042523_0143.root, FC#5, port B1

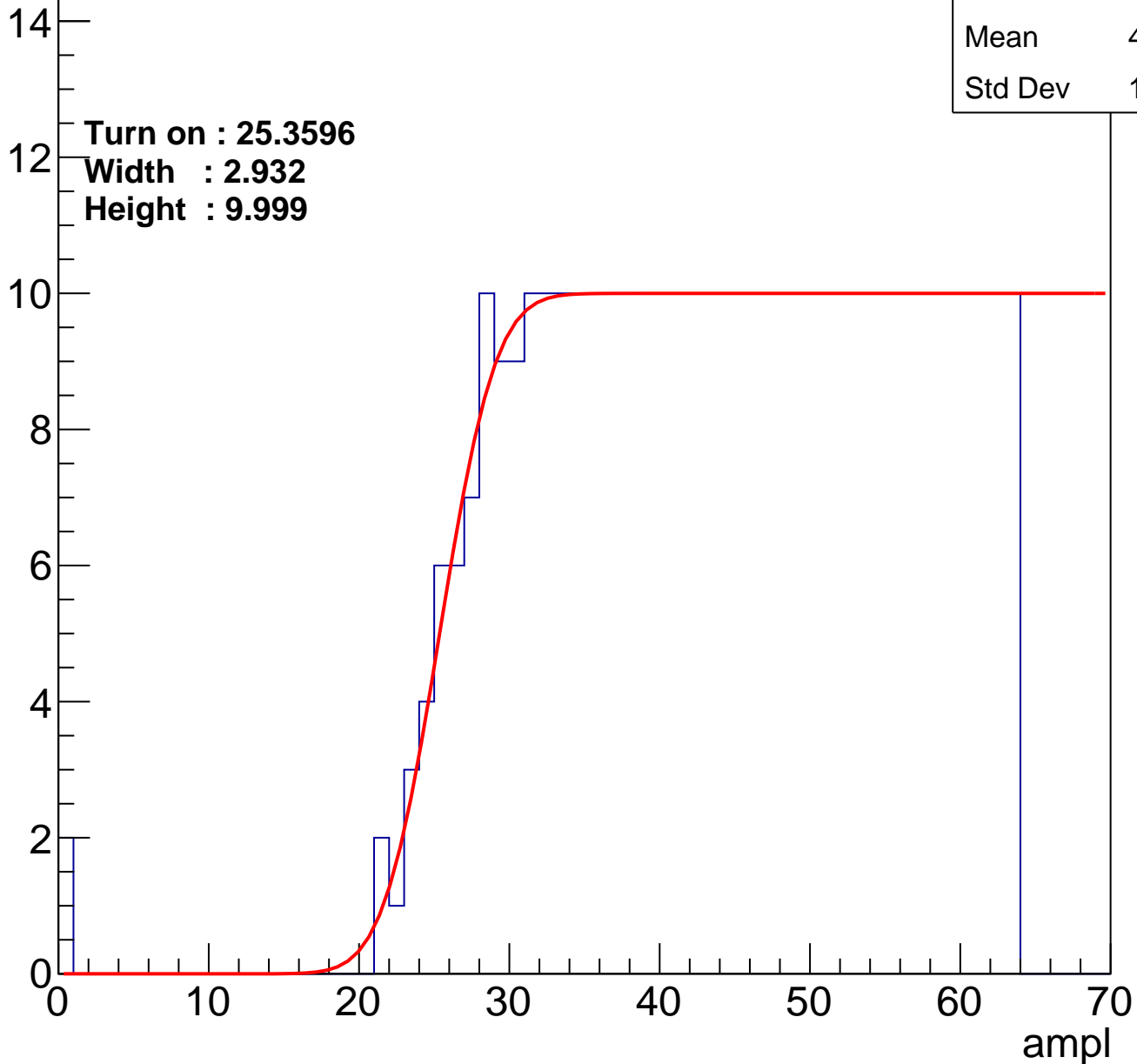
Entries	389
Mean	43.82
Std Dev	11.75

Turn on : 25.3596

Width : 2.932

Height : 9.999

Entry



B0L000S, U22-ch33

calib_packv5_042523_0143.root, FC#5, port B1

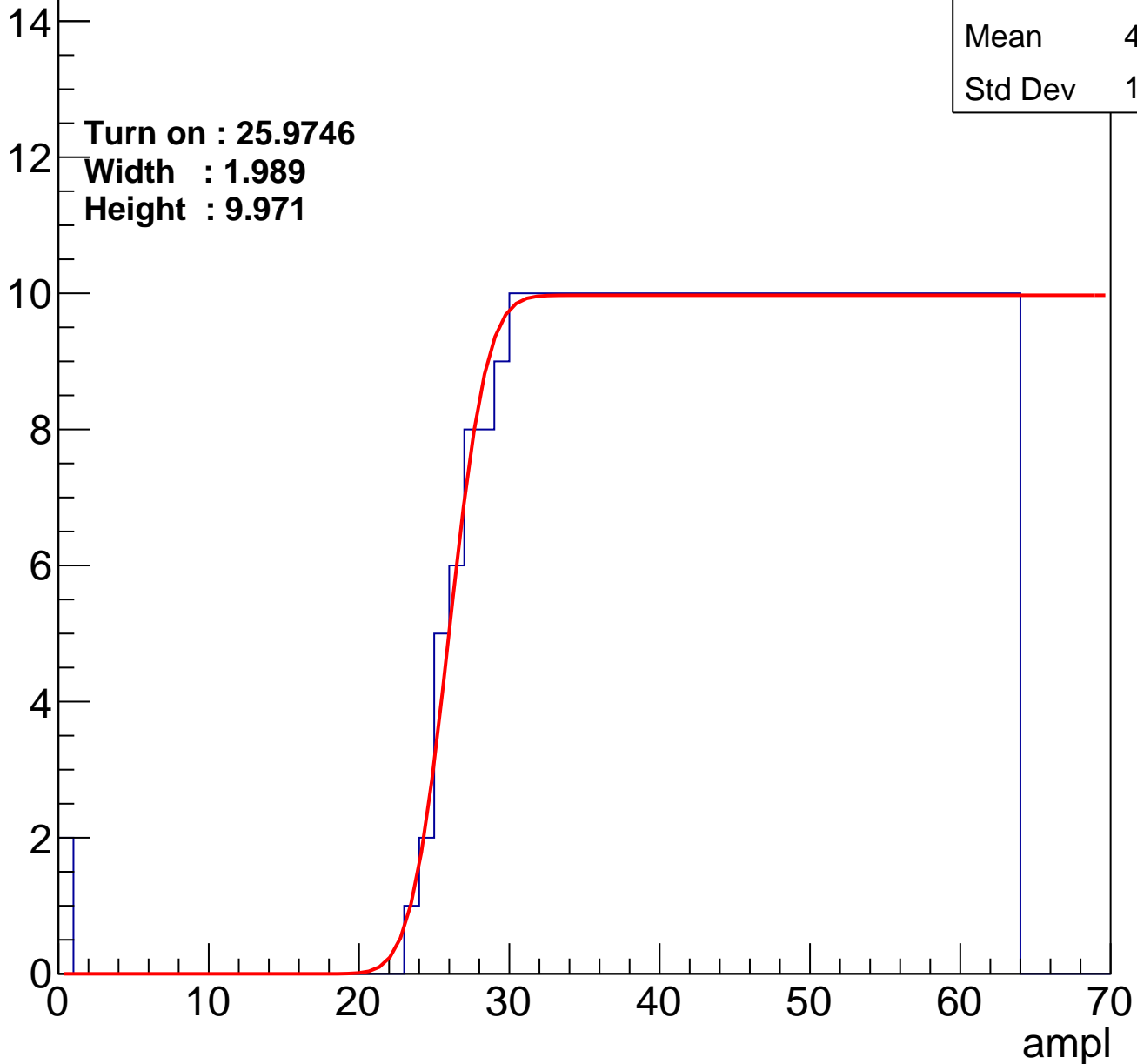
Entries	381
Mean	44.26
Std Dev	11.46

Turn on : 25.9746

Width : 1.989

Height : 9.971

Entry



B0L000S, U22-ch34

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.6721

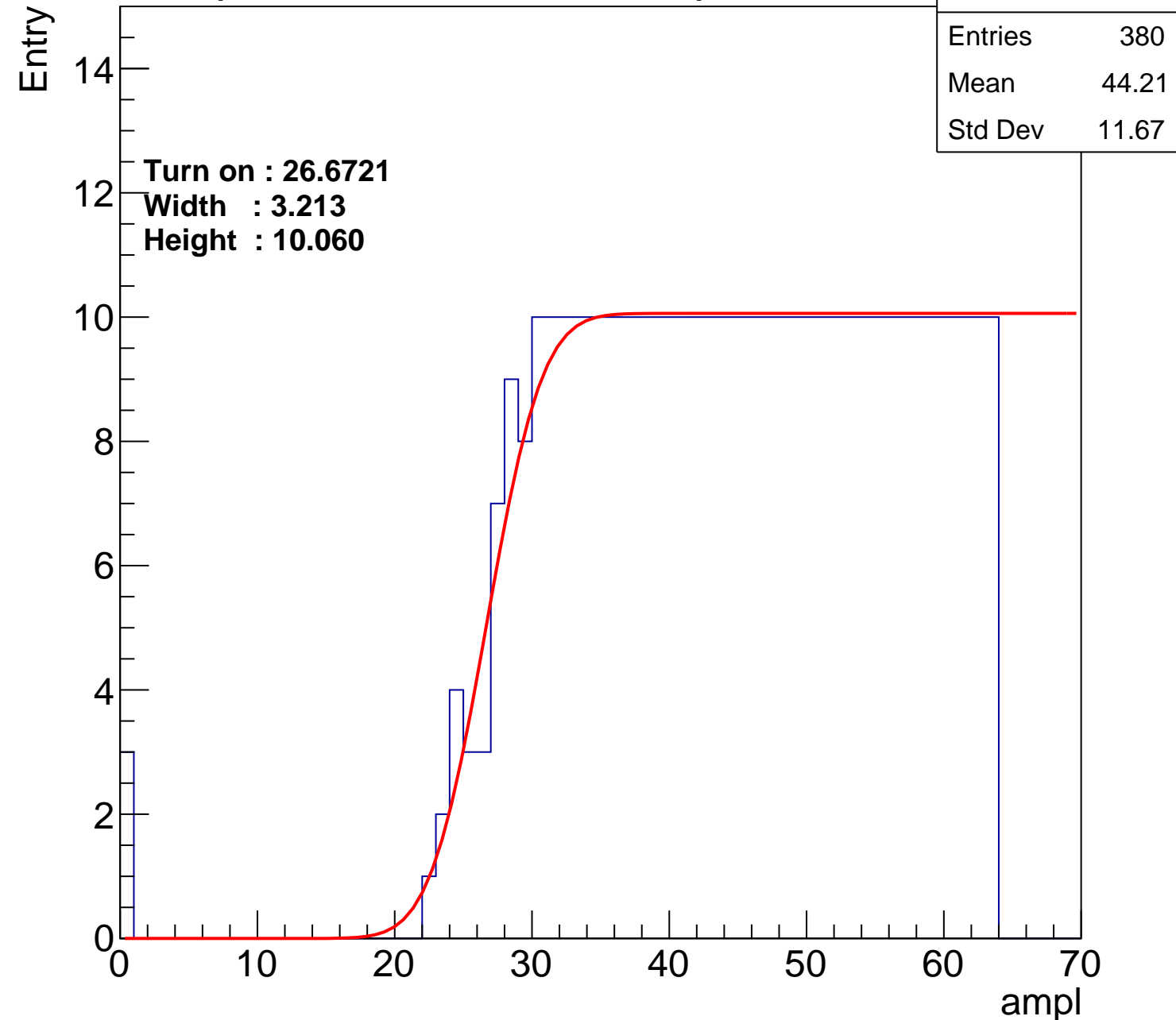
Width : 3.213

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch35

calib_packv5_042523_0143.root, FC#5, port B1

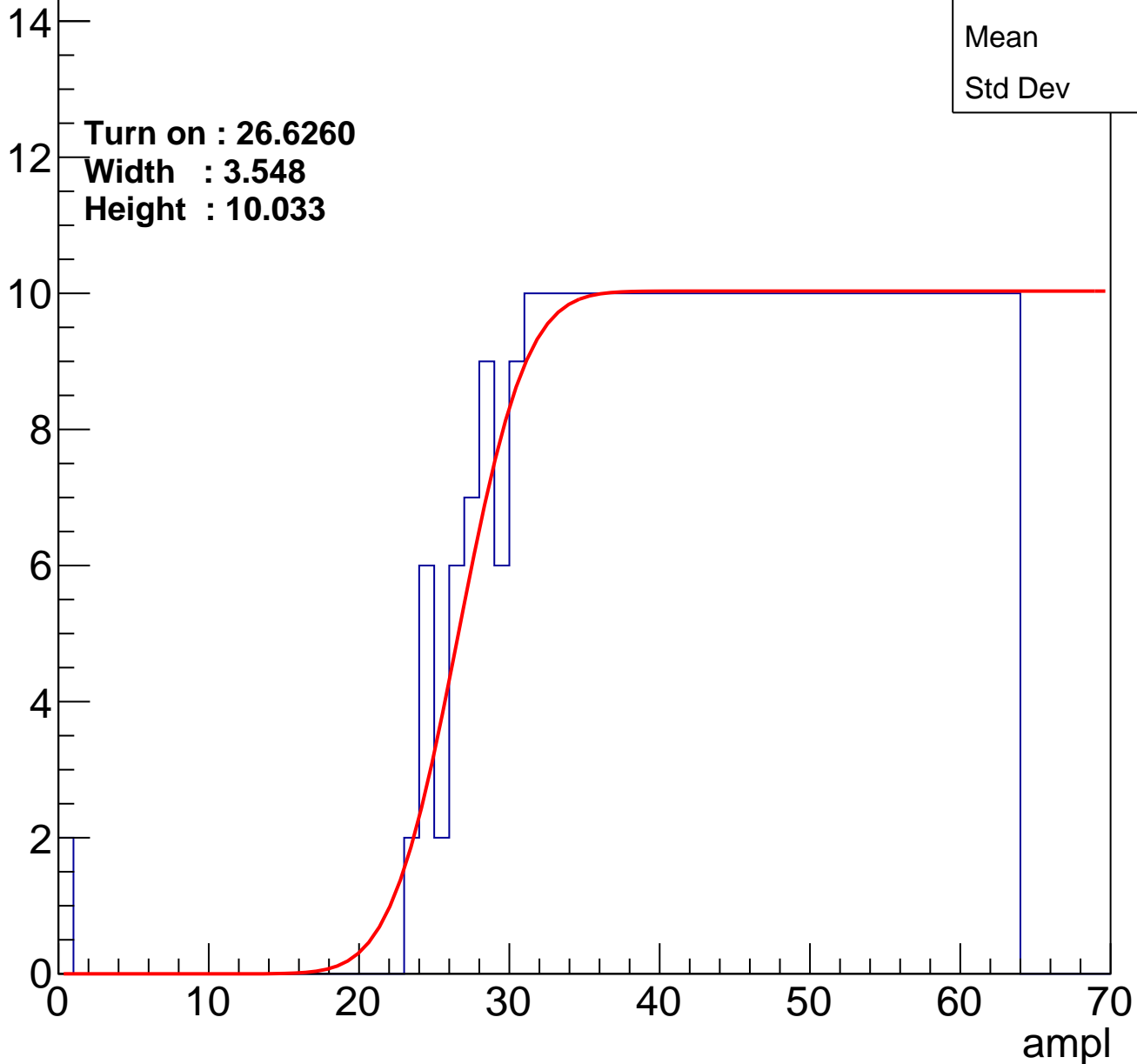
Entries	379
Mean	44.3
Std Dev	11.5

Turn on : 26.6260

Width : 3.548

Height : 10.033

Entry



B0L000S, U22-ch36

calib_packv5_042523_0143.root, FC#5, port B1

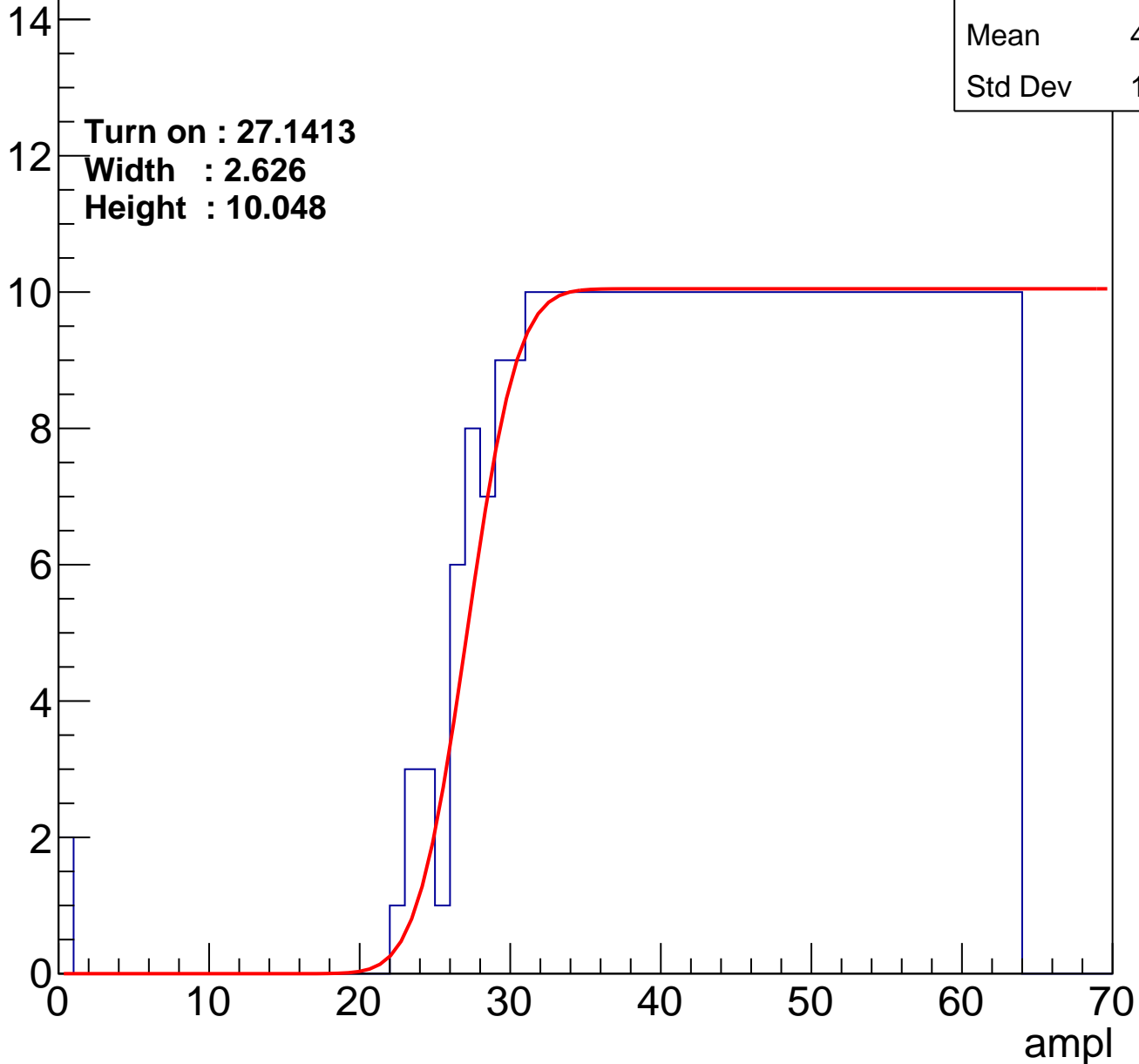
Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 27.1413

Width : 2.626

Height : 10.048

Entry



B0L000S, U22-ch37

calib_packv5_042523_0143.root, FC#5, port B1

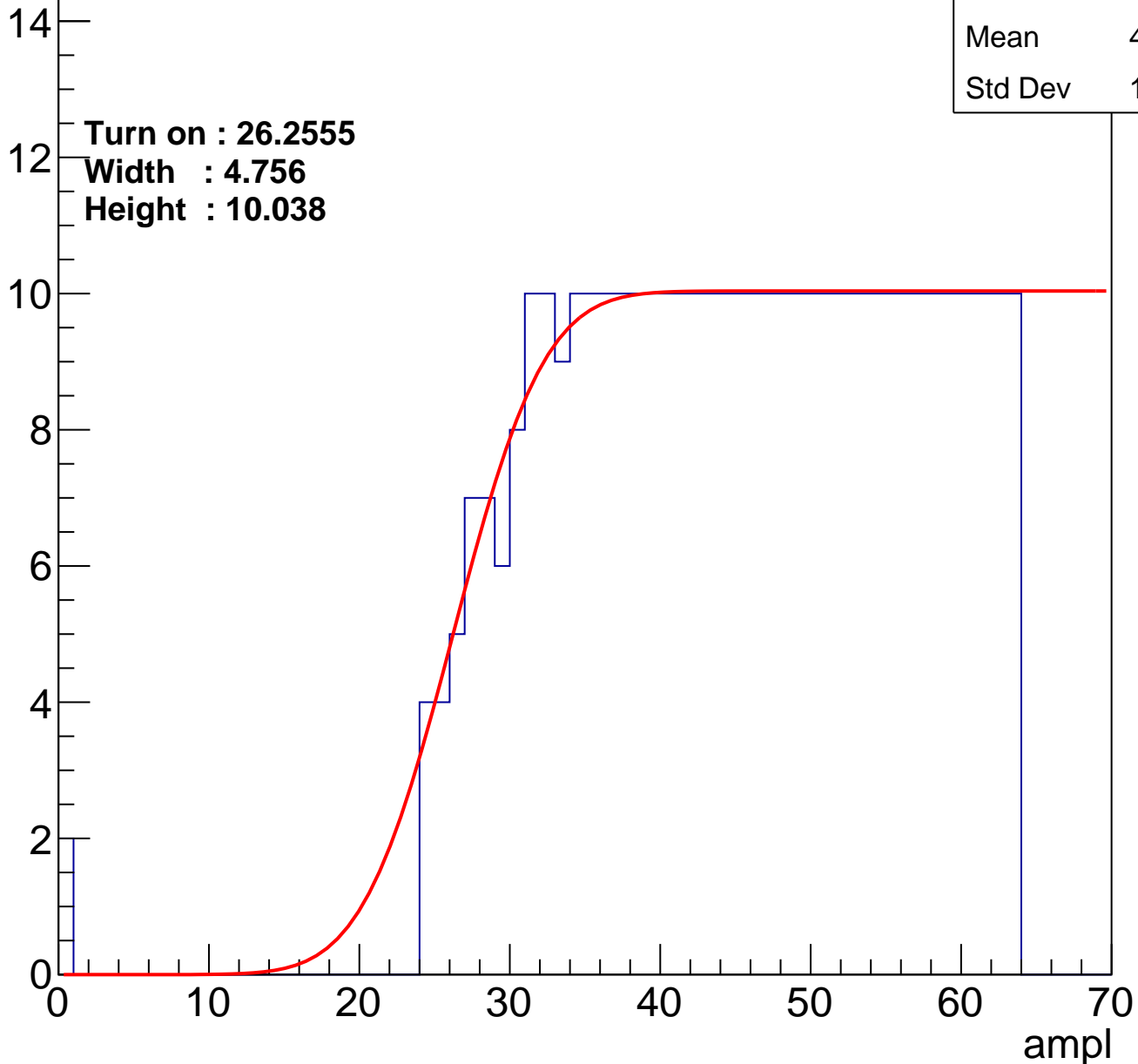
Entries	372
Mean	44.63
Std Dev	11.35

Turn on : 26.2555

Width : 4.756

Height : 10.038

Entry



B0L000S, U22-ch38

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 27.0199

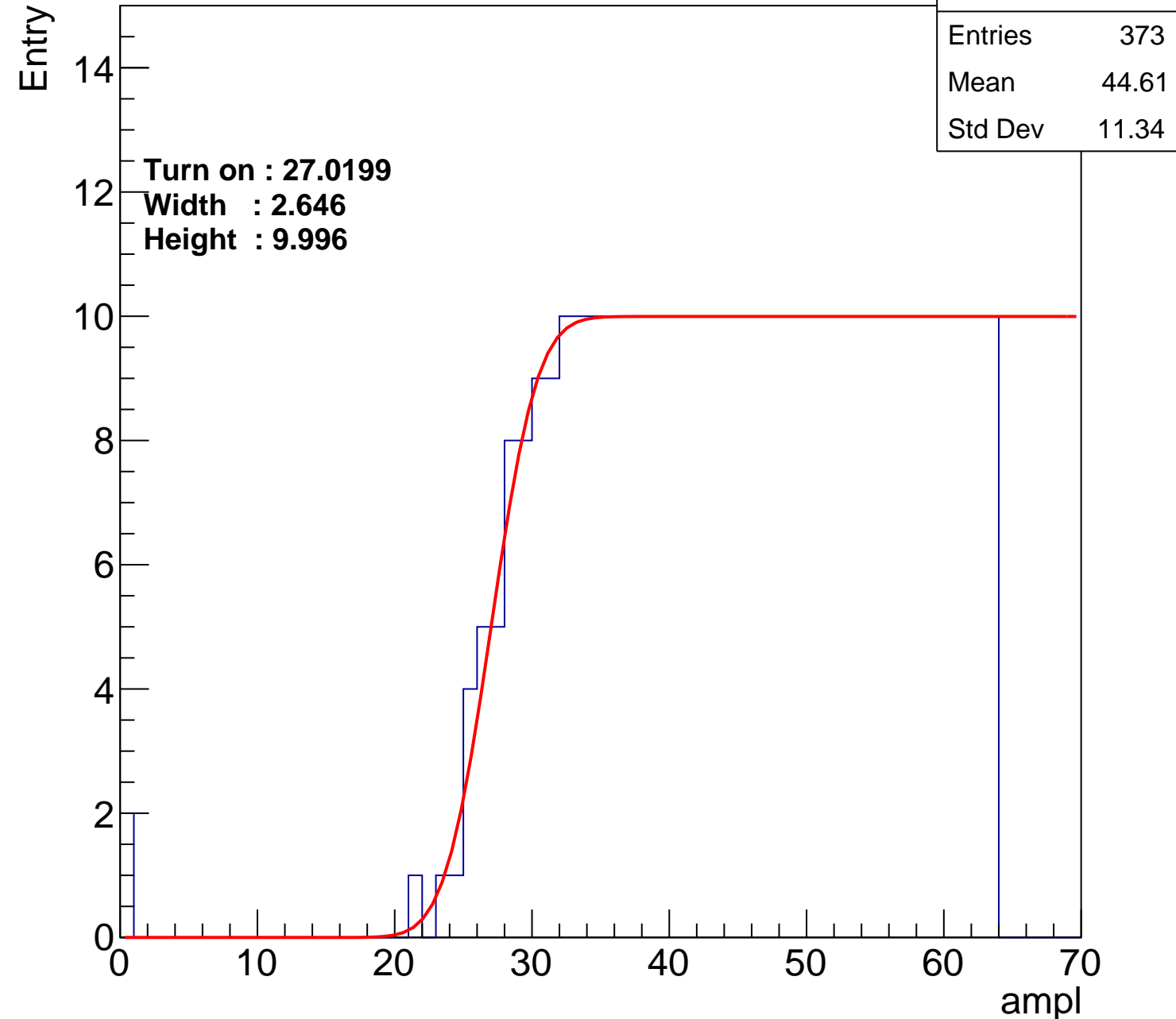
Width : 2.646

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch39

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.37
Std Dev	11.39

Turn on : 26.0925

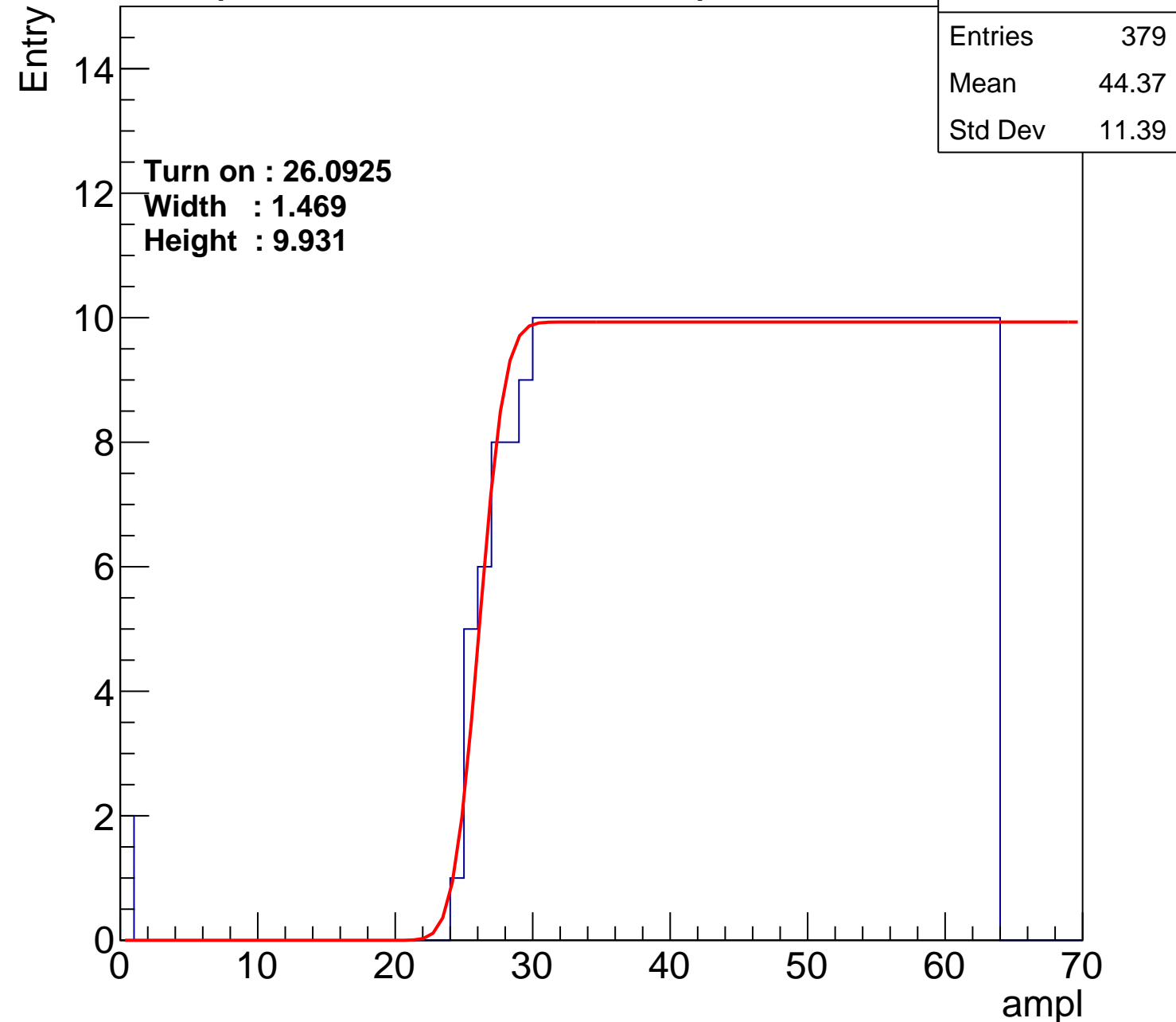
Width : 1.469

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch40

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.33
Std Dev	11.32

Turn on : 26.1386

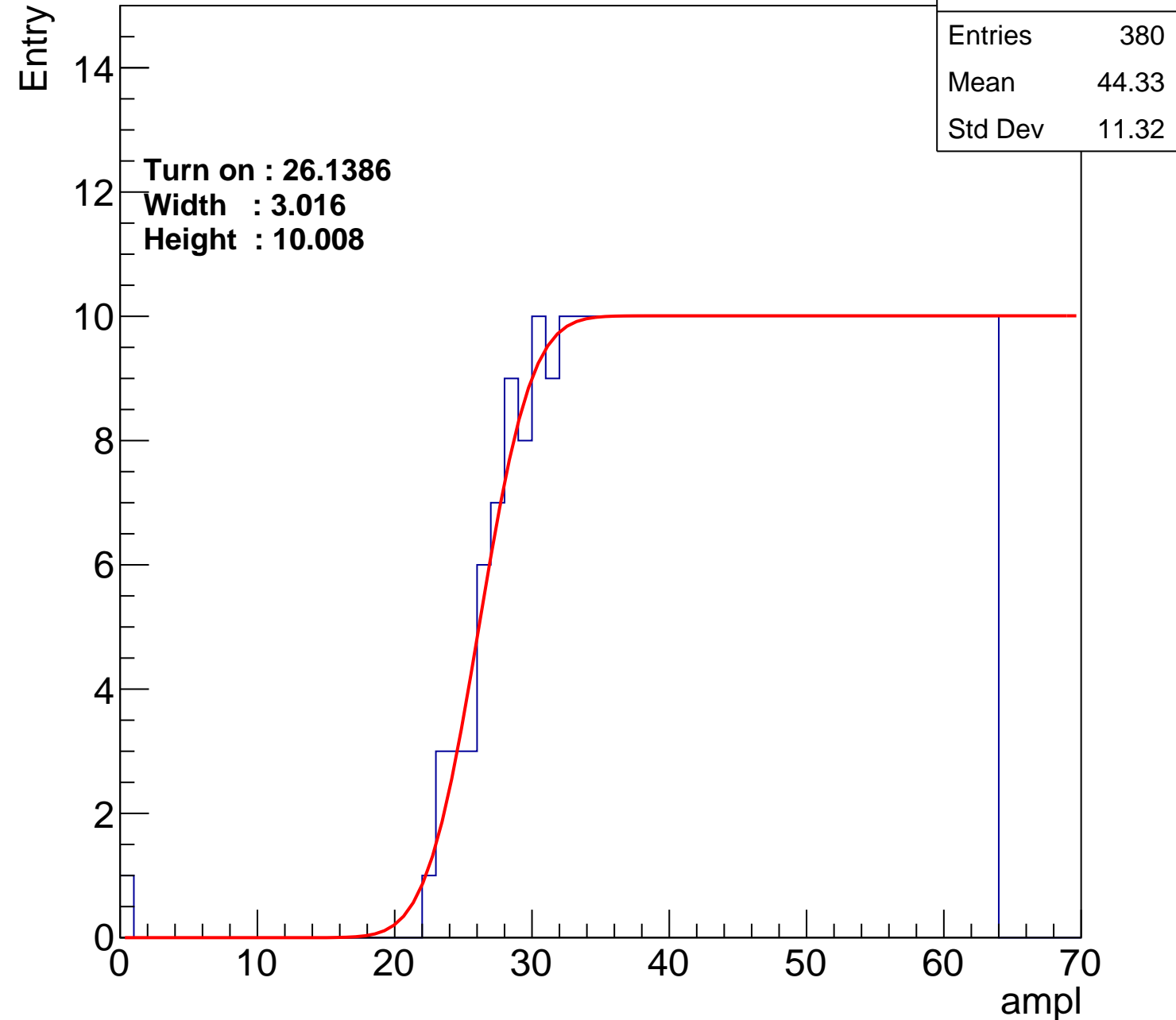
Width : 3.016

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch41

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.76
Std Dev	11.43

Turn on : 27.5213

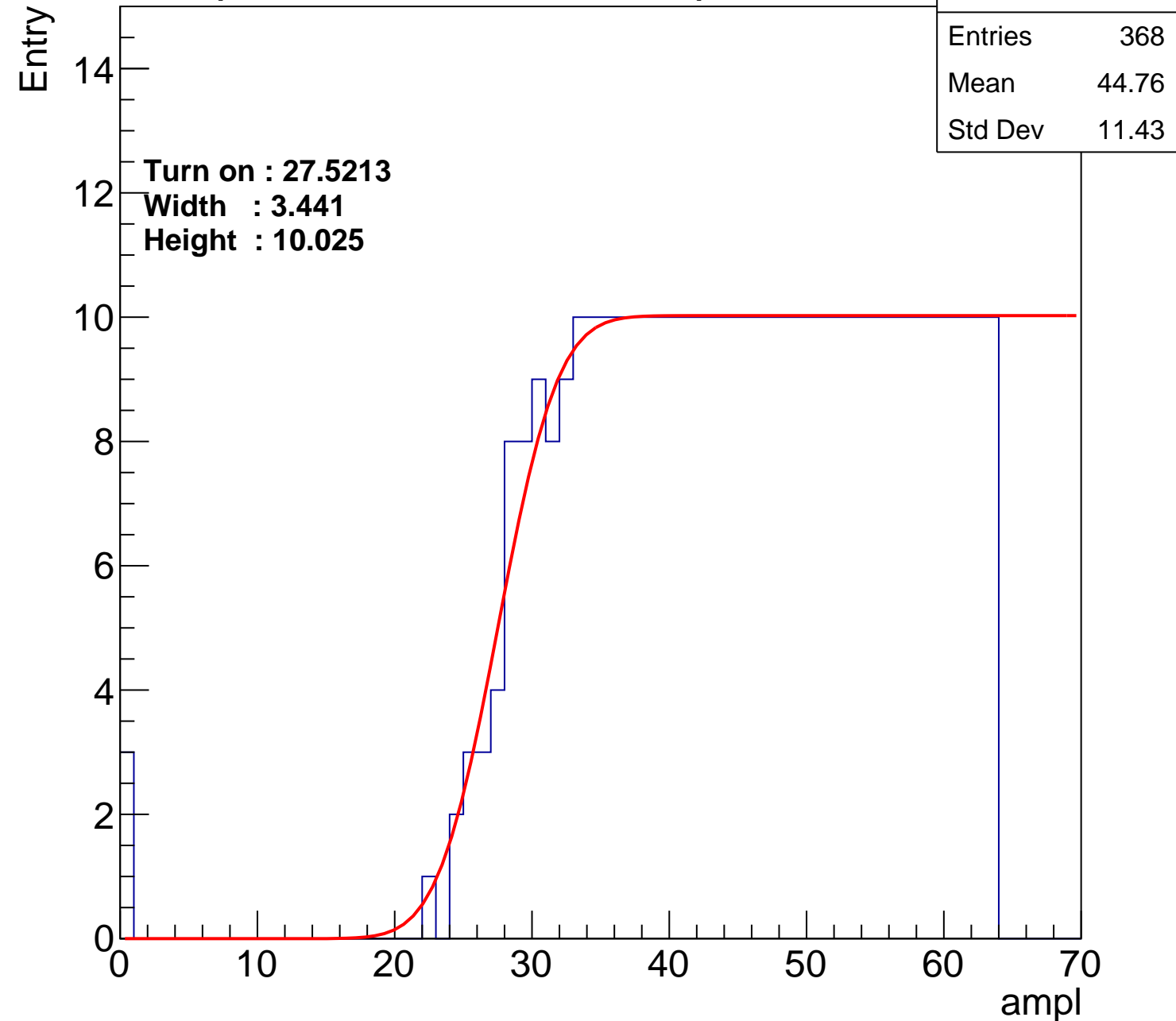
Width : 3.441

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch42

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.4489

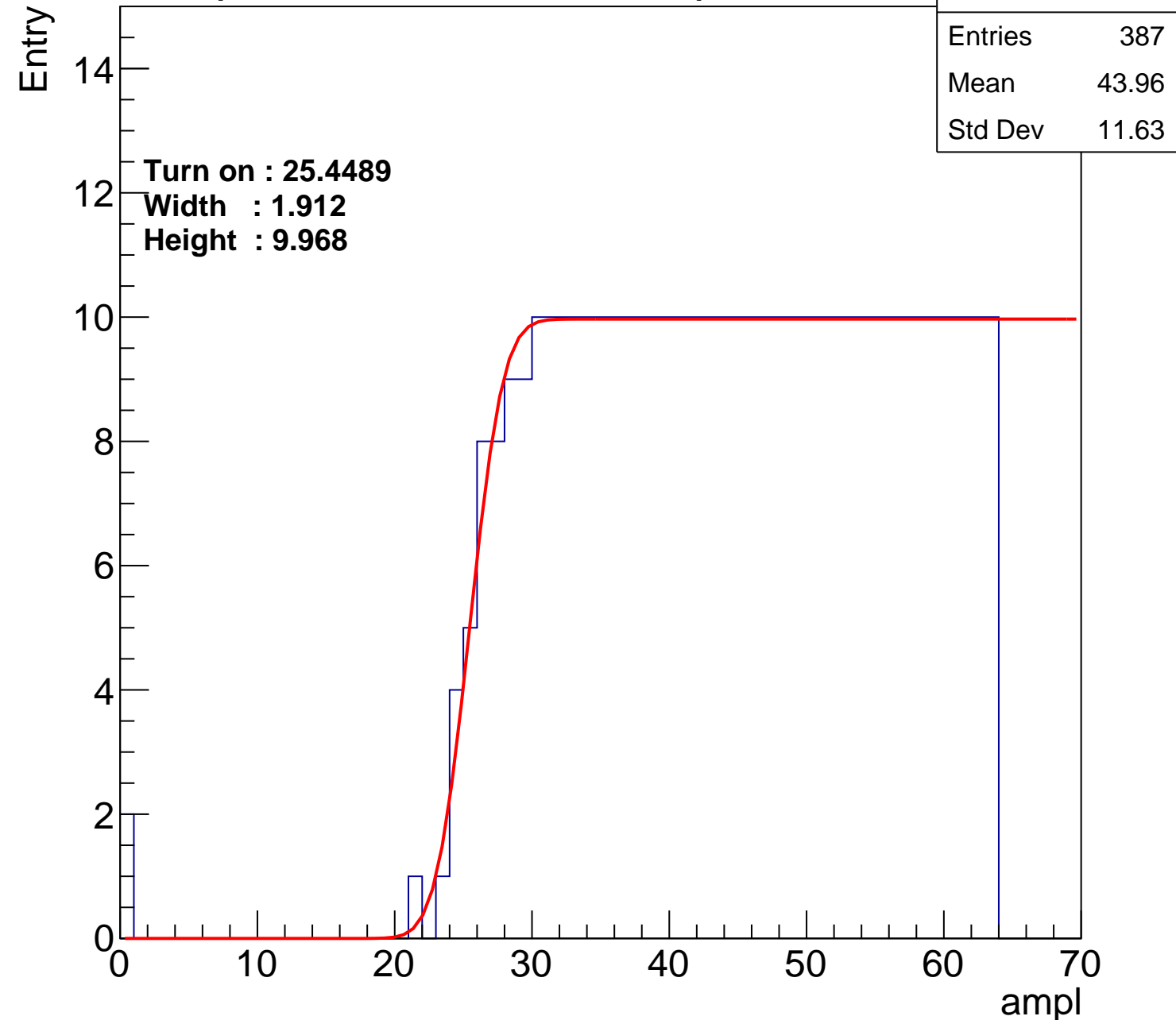
Width : 1.912

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch43

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.19
Std Dev	11.39

Turn on : 26.7934

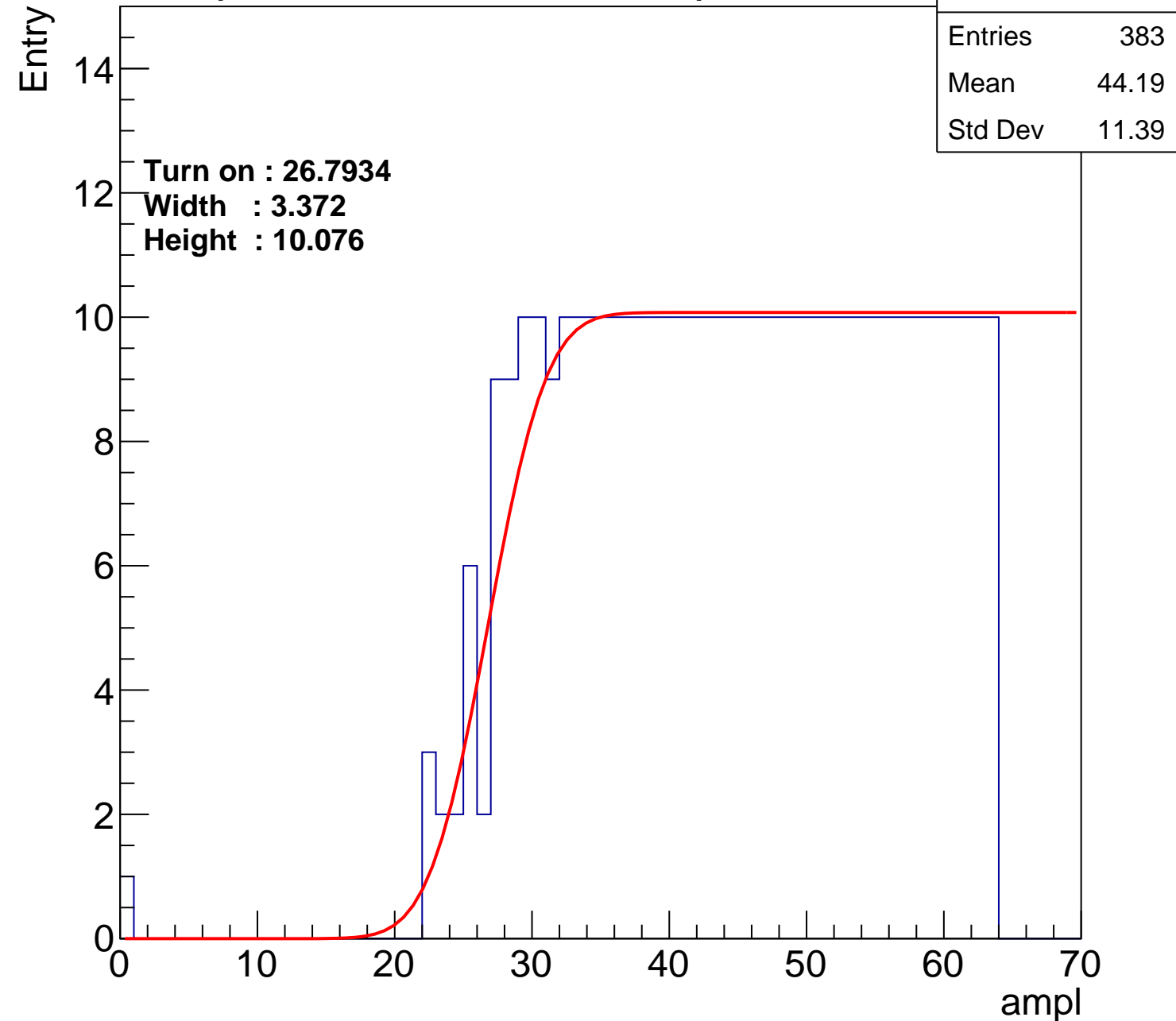
Width : 3.372

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch44

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.09
Std Dev	10.9

Turn on : 27.8997

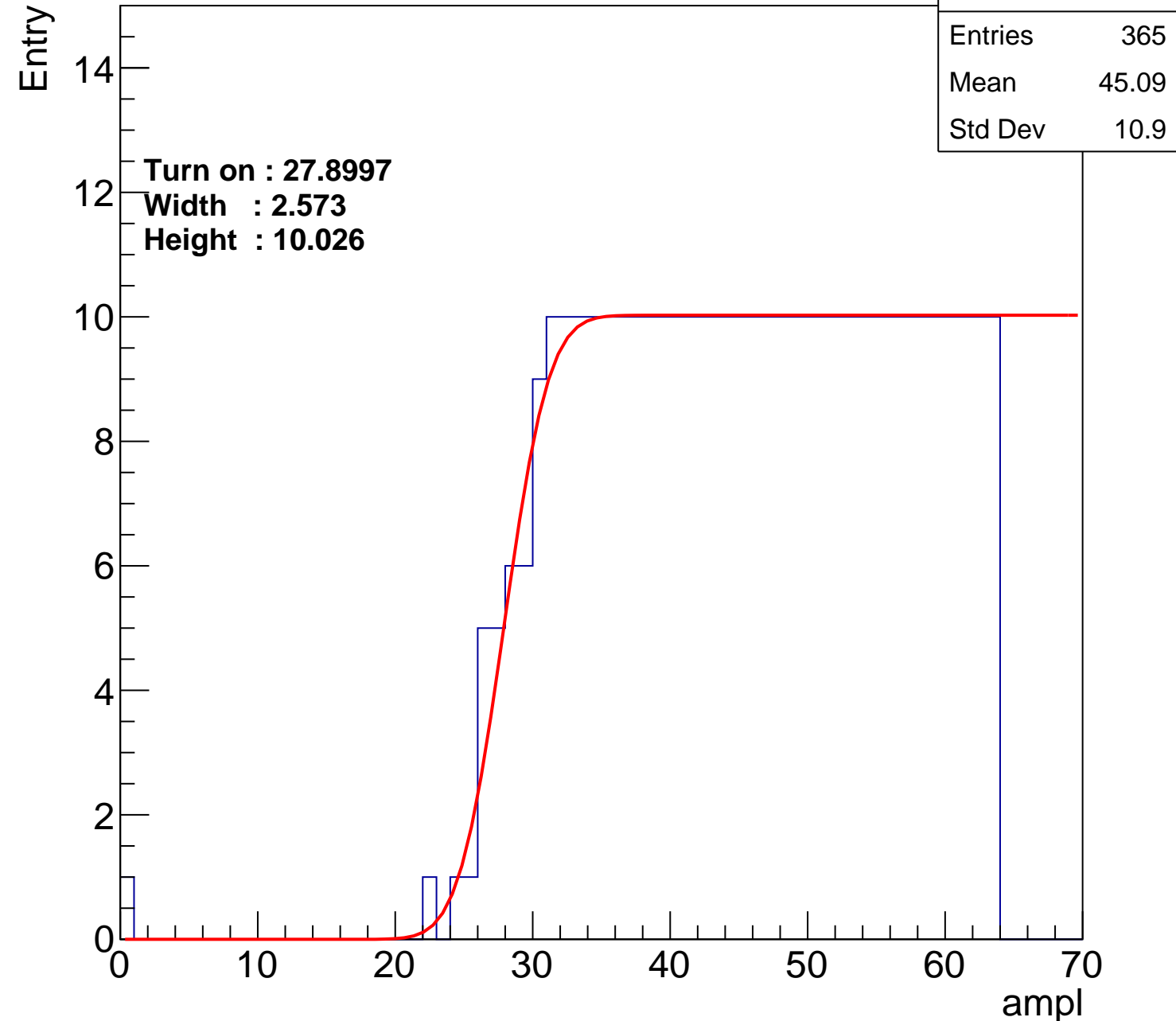
Width : 2.573

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch45

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.57
Std Dev	12.63

Turn on : 26.3540

Width : 2.402

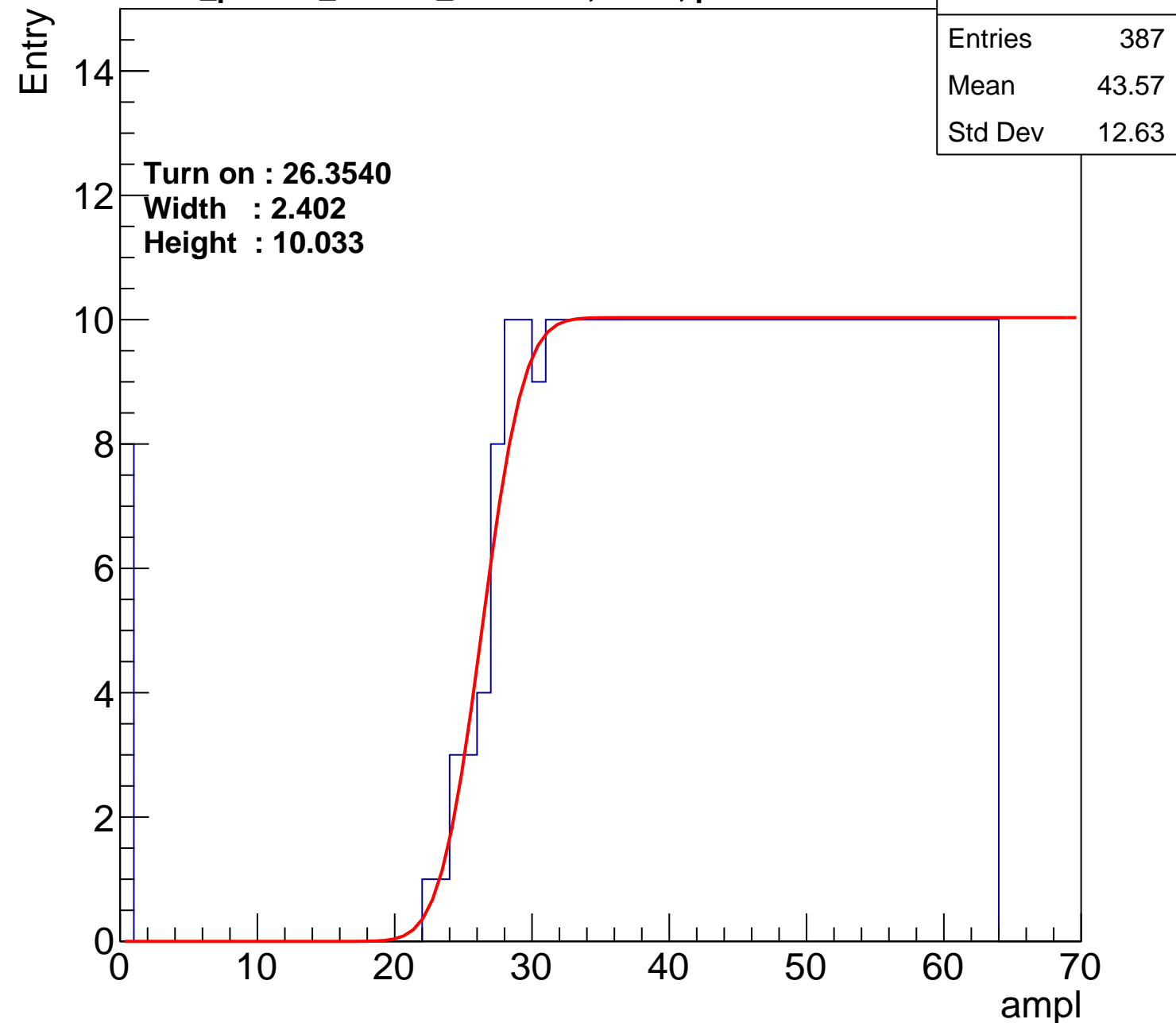
Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U22-ch46

calib_packv5_042523_0143.root, FC#5, port B1

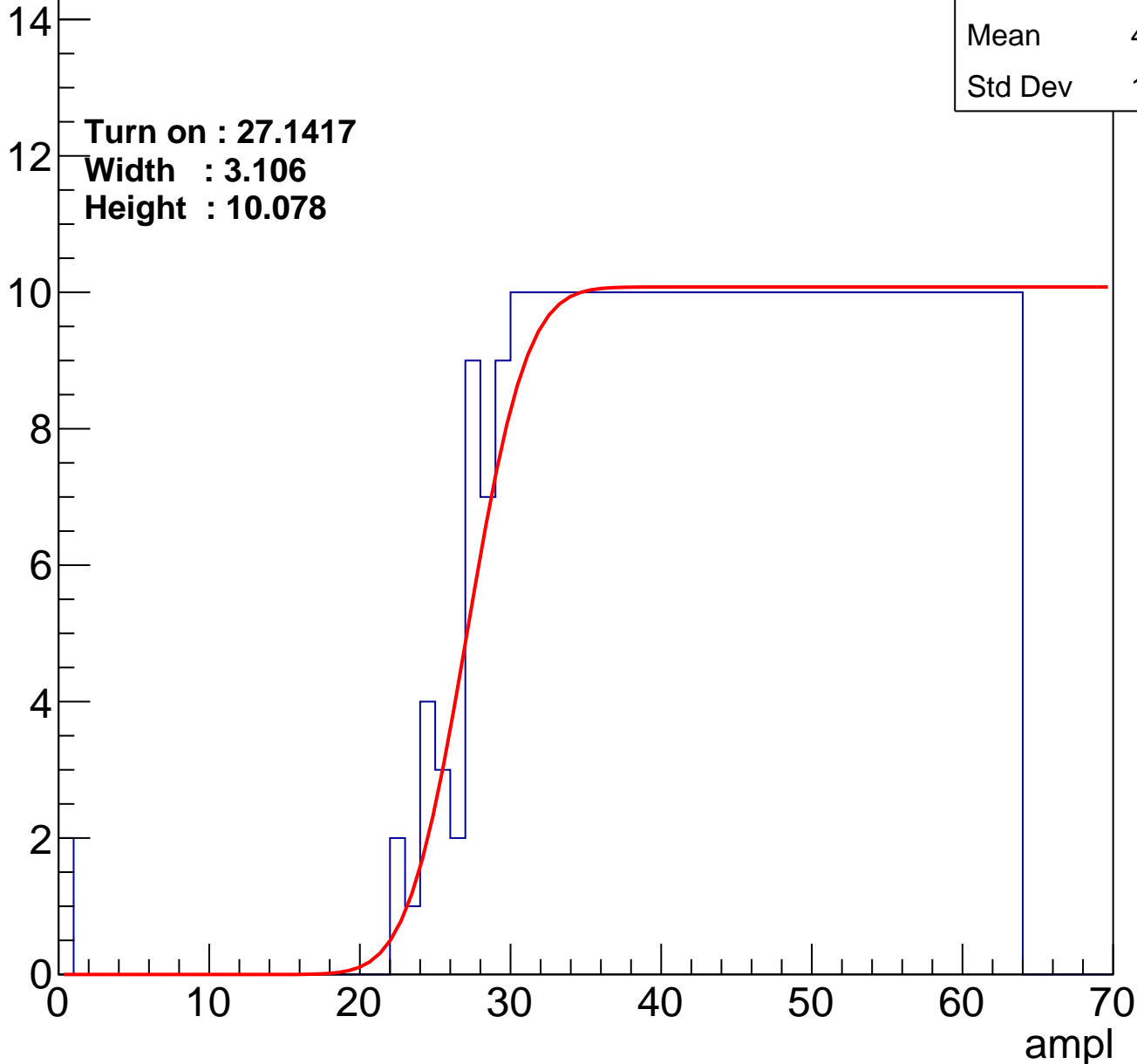
Entries	379
Mean	44.33
Std Dev	11.47

Turn on : 27.1417

Width : 3.106

Height : 10.078

Entry



B0L000S, U22-ch47

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.1102

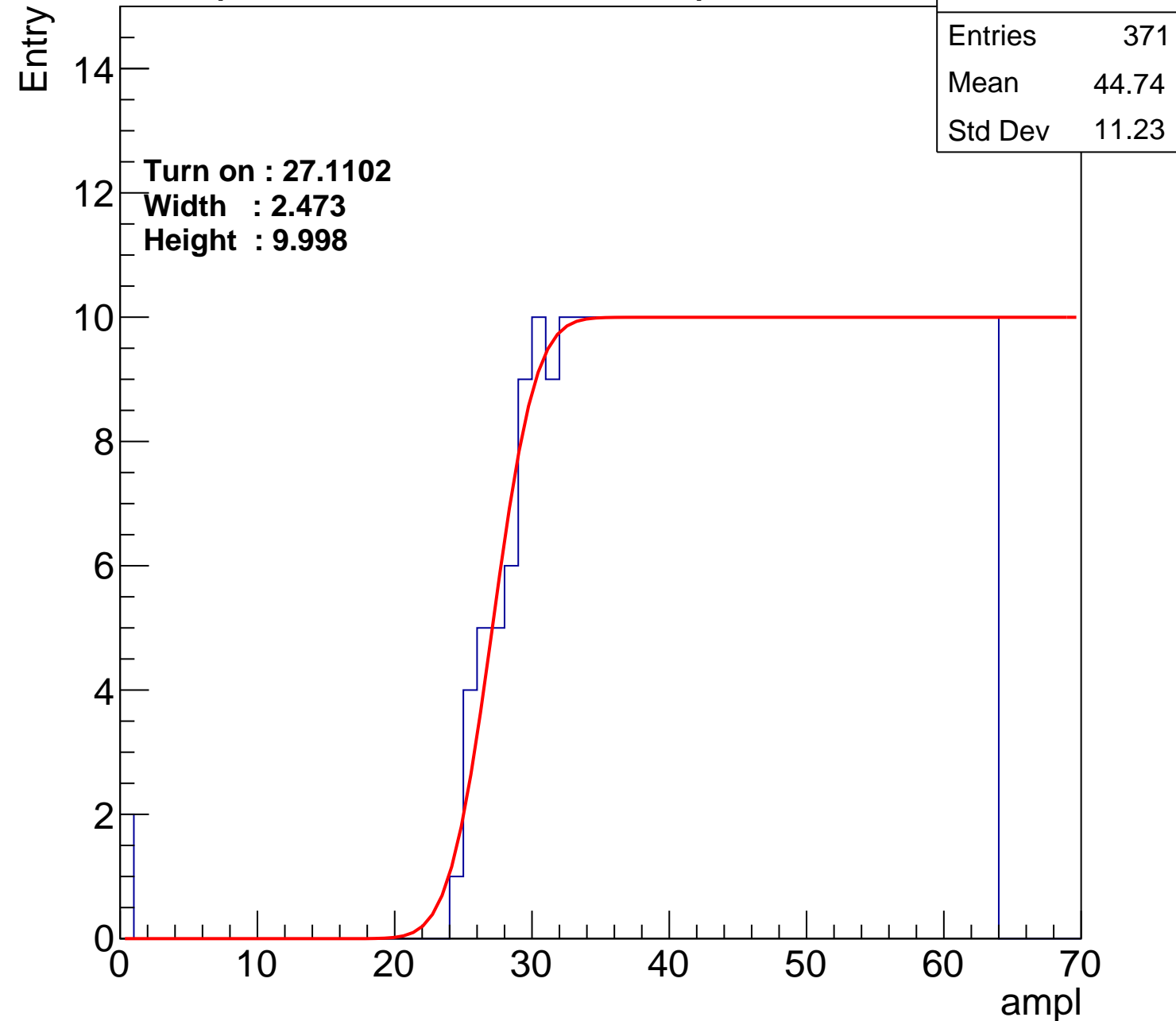
Width : 2.473

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.78
Std Dev	11.76

Turn on : 25.5359

Width : 3.187

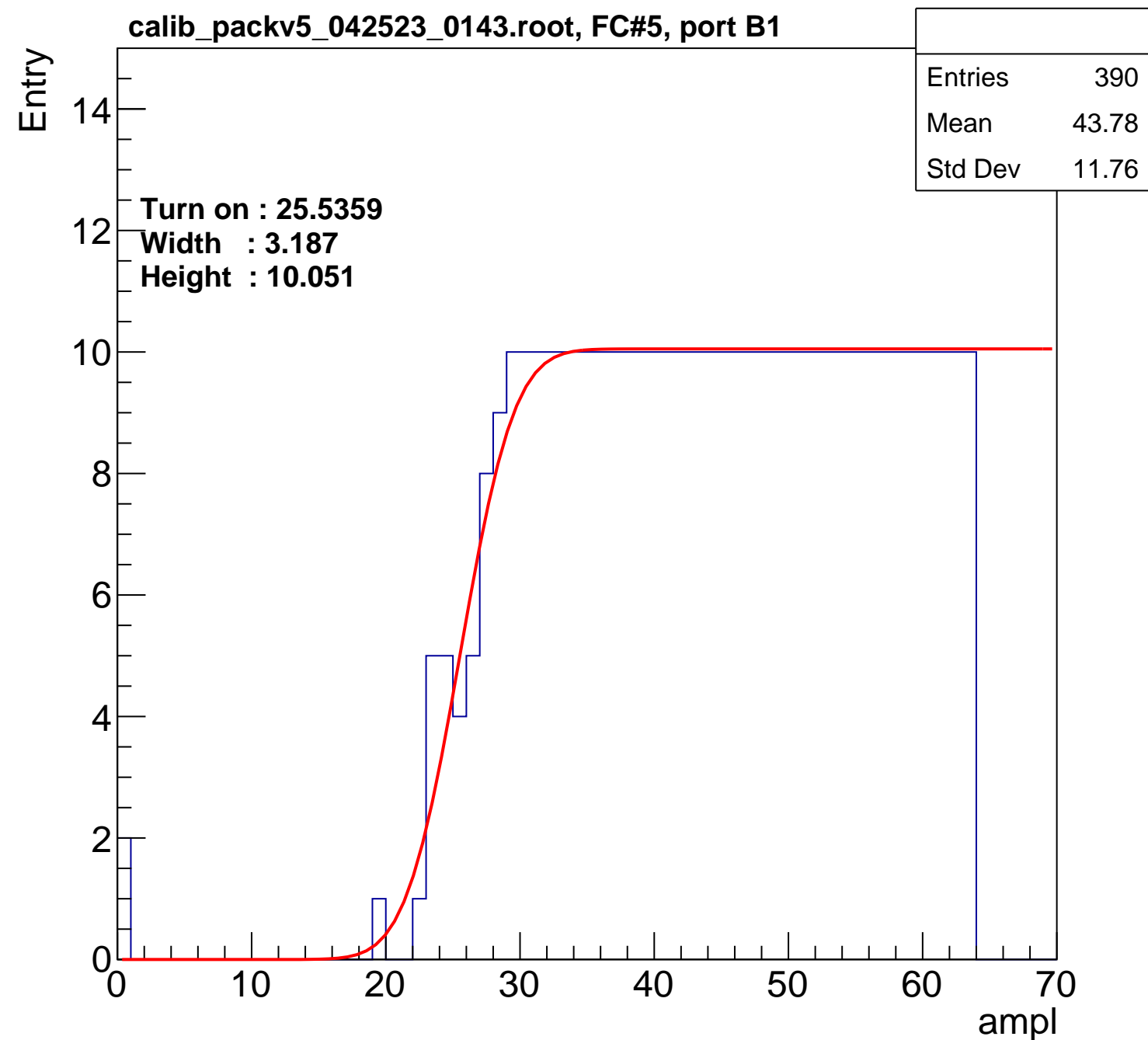
Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U22-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.4218

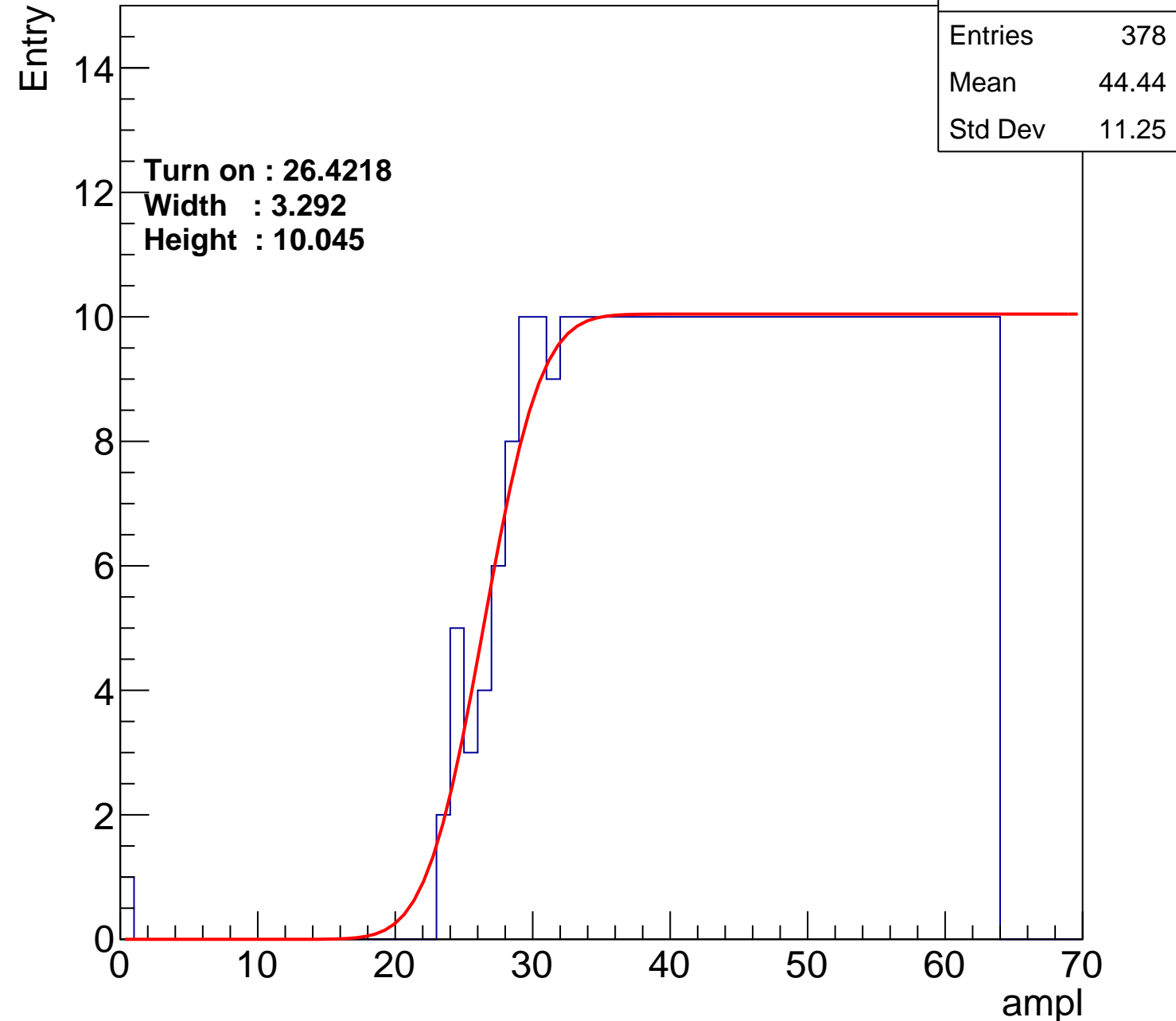
Width : 3.292

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch50

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.16
Std Dev	11.19

Turn on : 28.5719

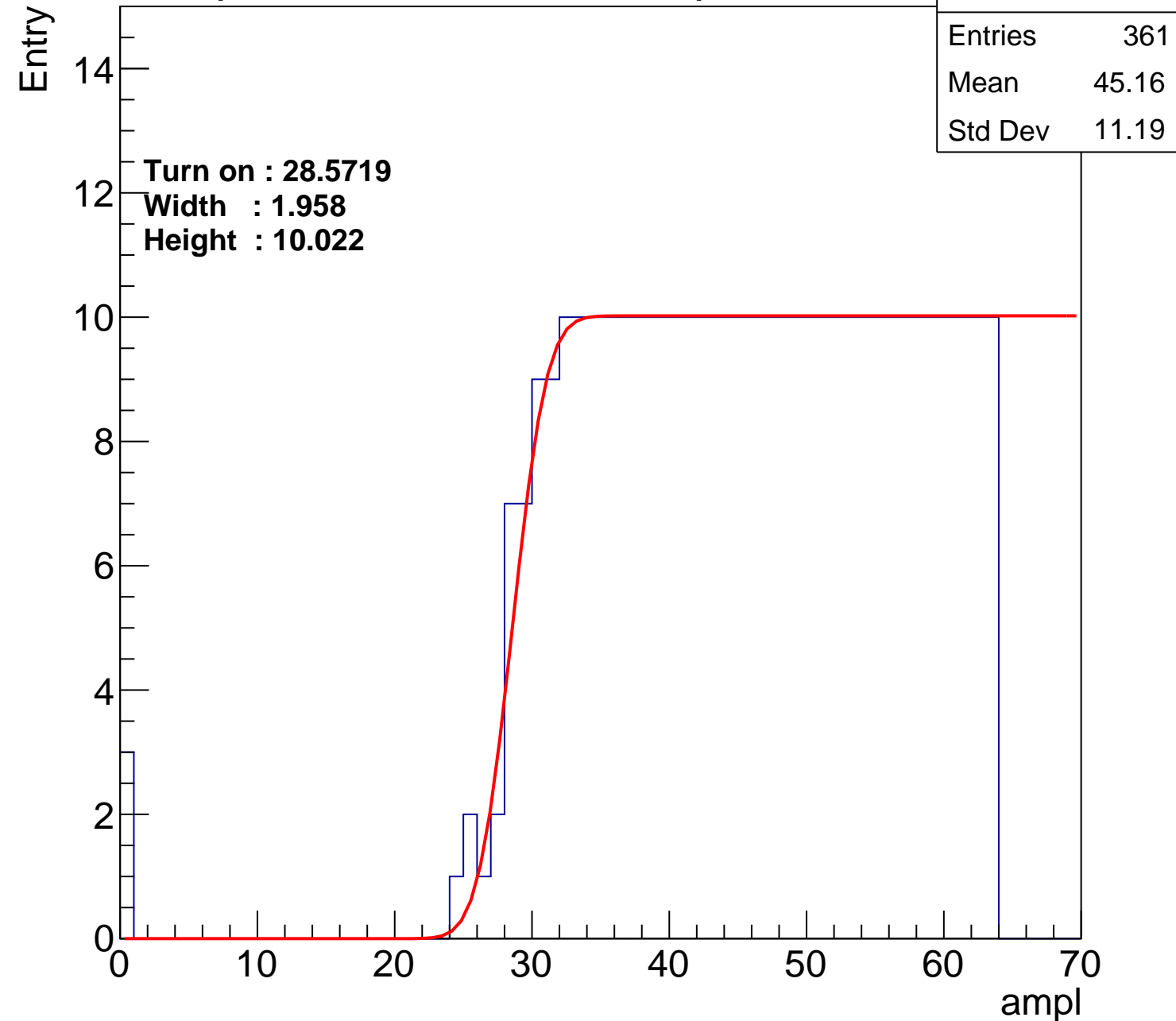
Width : 1.958

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch51

calib_packv5_042523_0143.root, FC#5, port B1

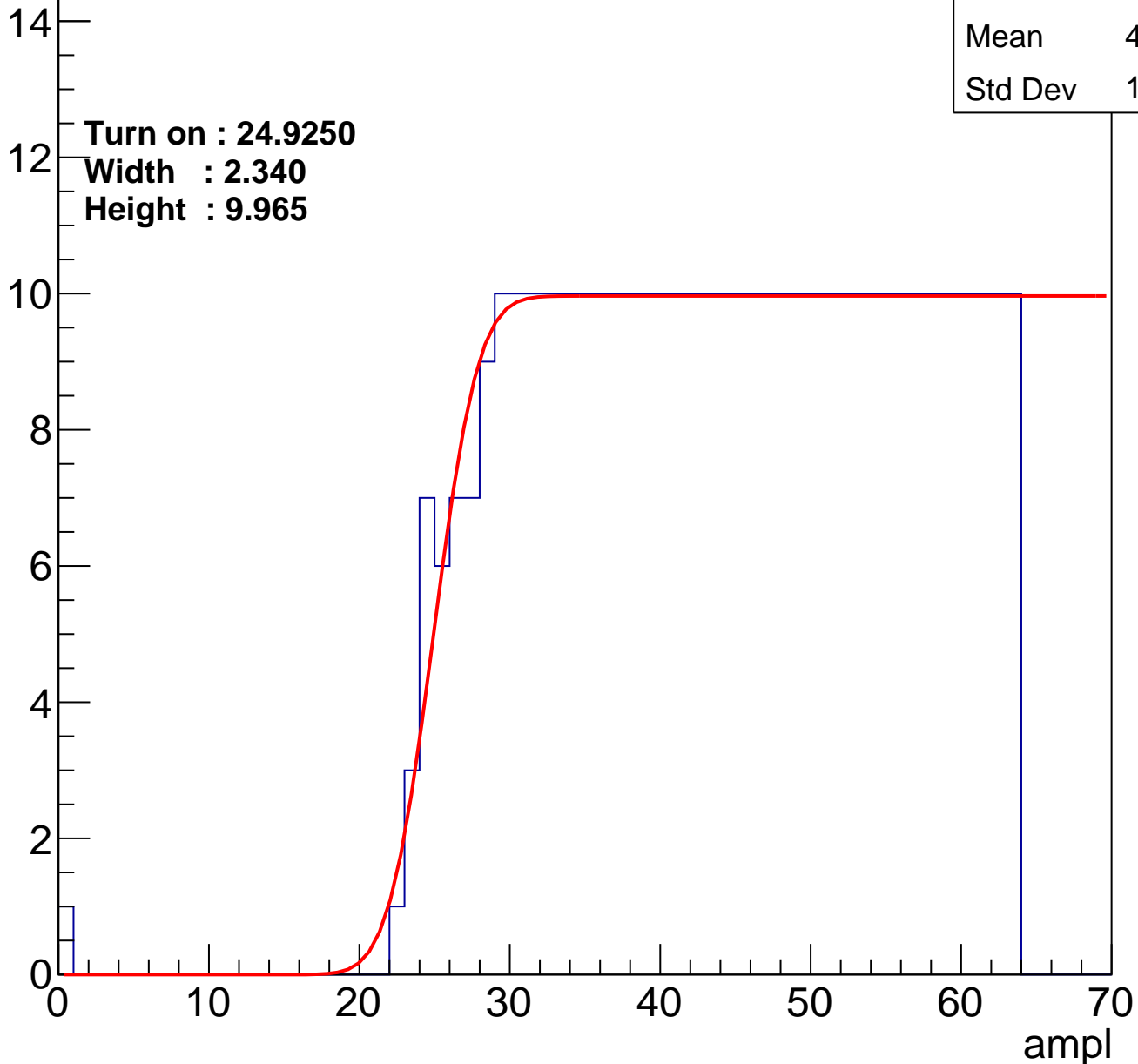
Entries	391
Mean	43.82
Std Dev	11.58

Turn on : 24.9250

Width : 2.340

Height : 9.965

Entry



B0L000S, U22-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.26
Std Dev	11.77

Turn on : 27.2194

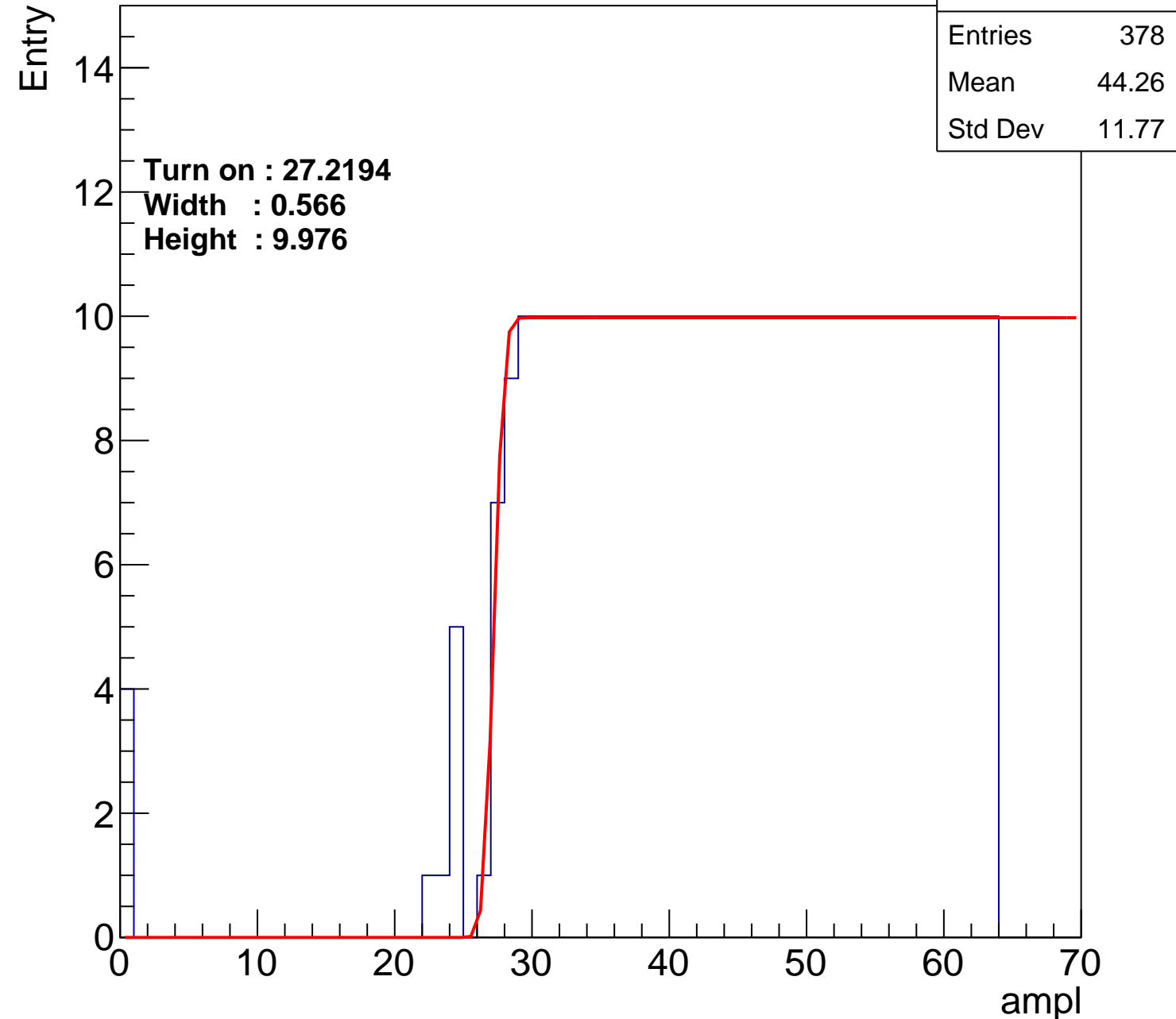
Width : 0.566

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch53

calib_packv5_042523_0143.root, FC#5, port B1

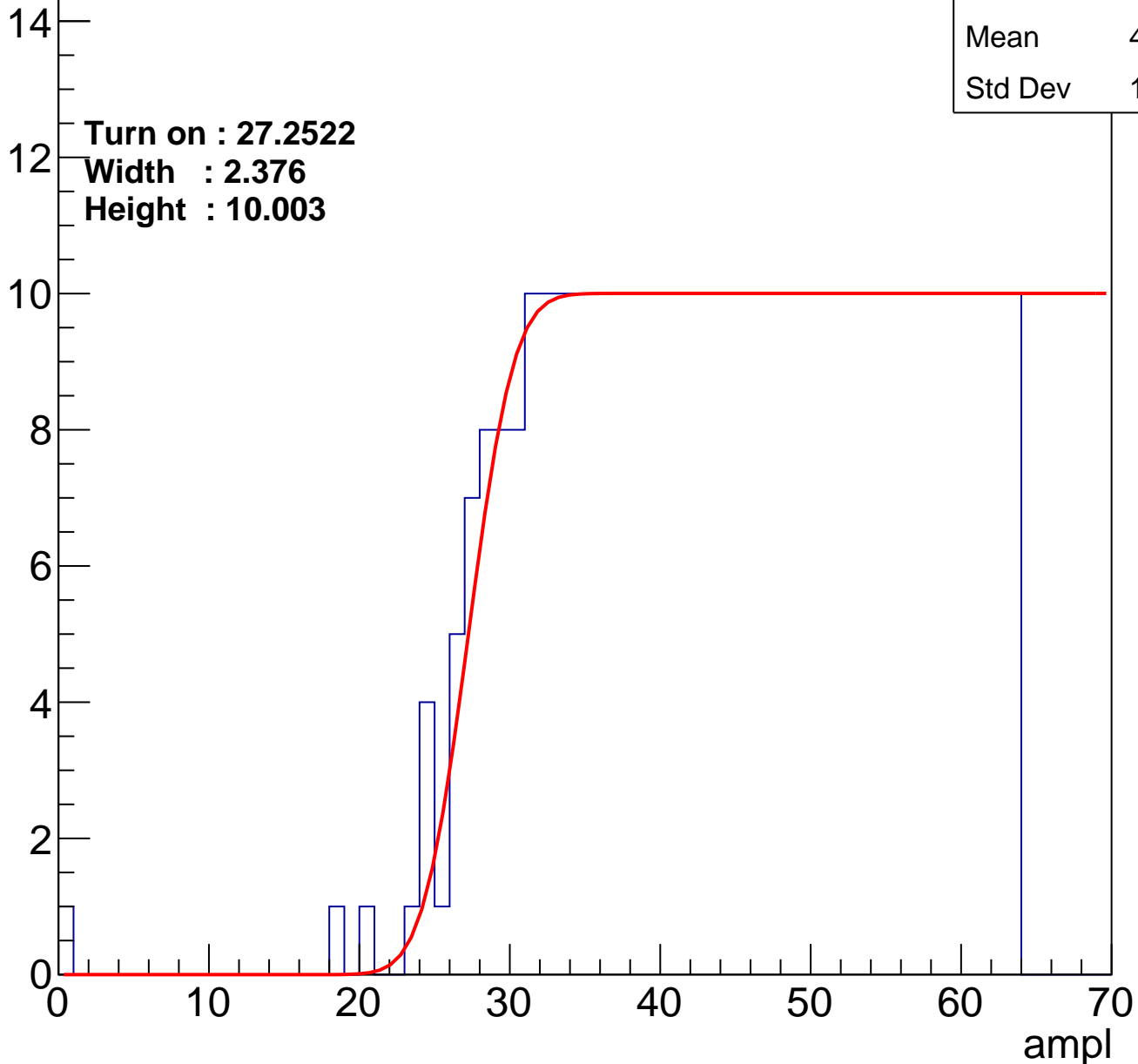
Entries	375
Mean	44.55
Std Dev	11.25

Turn on : 27.2522

Width : 2.376

Height : 10.003

Entry



B0L000S, U22-ch54

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.98
Std Dev	11.64

Turn on : 26.4141

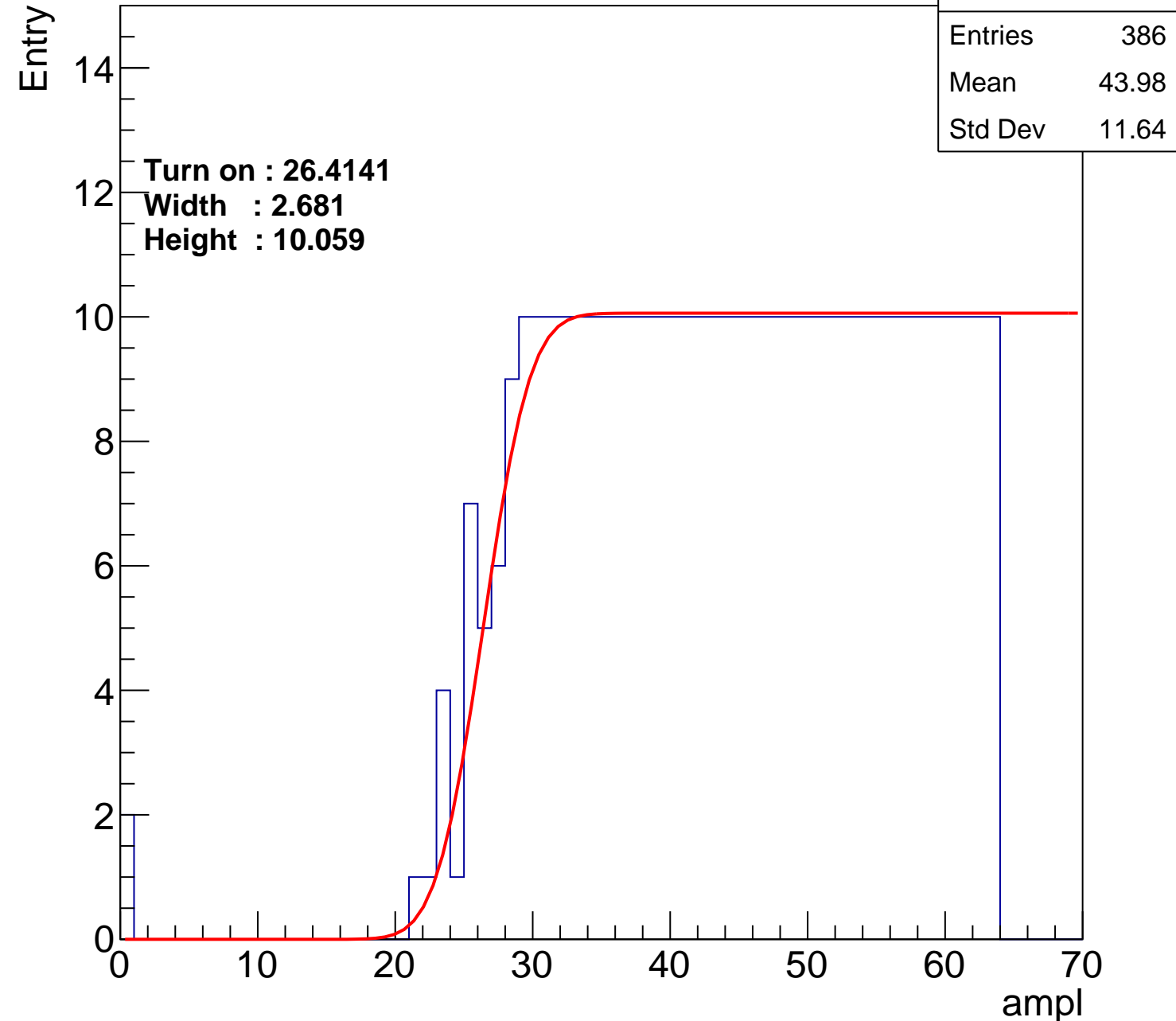
Width : 2.681

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch55

calib_packv5_042523_0143.root, FC#5, port B1

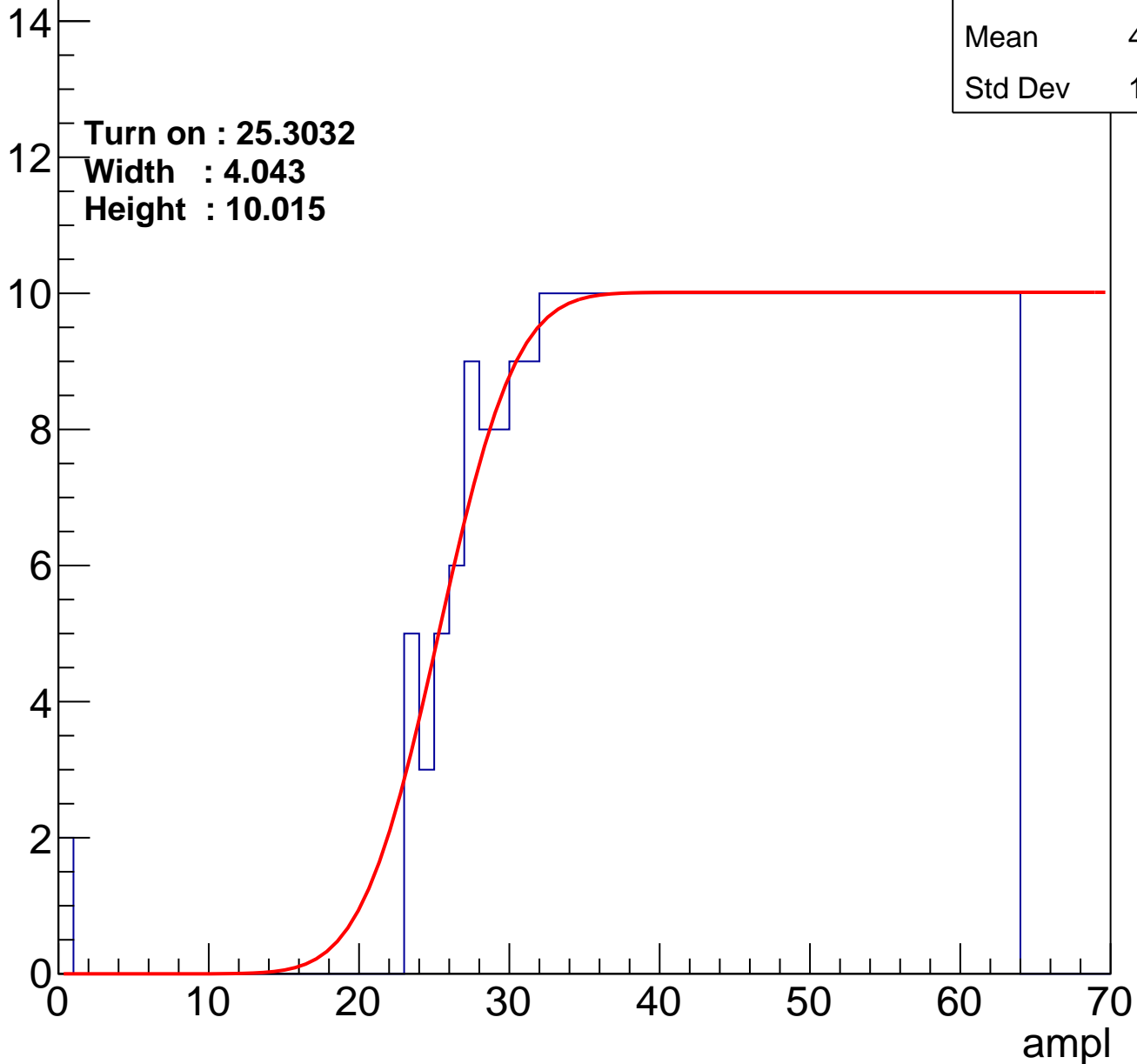
Entries	384
Mean	44.05
Std Dev	11.63

Turn on : 25.3032

Width : 4.043

Height : 10.015

Entry



B0L000S, U22-ch56

calib_packv5_042523_0143.root, FC#5, port B1

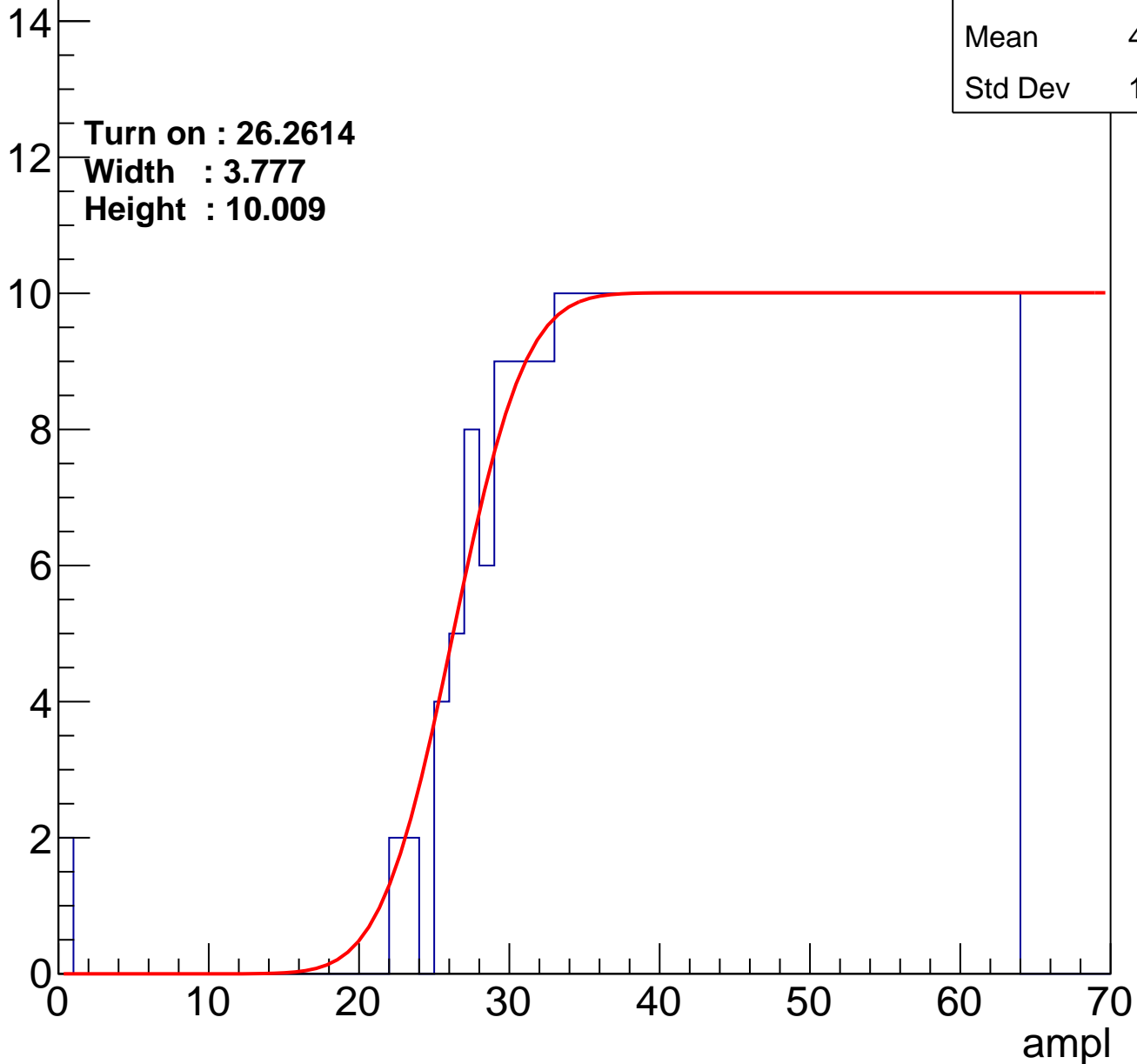
Entries	375
Mean	44.49
Std Dev	11.42

Turn on : 26.2614

Width : 3.777

Height : 10.009

Entry



calib_packv5_042523_0143.root, FC#5, port B1

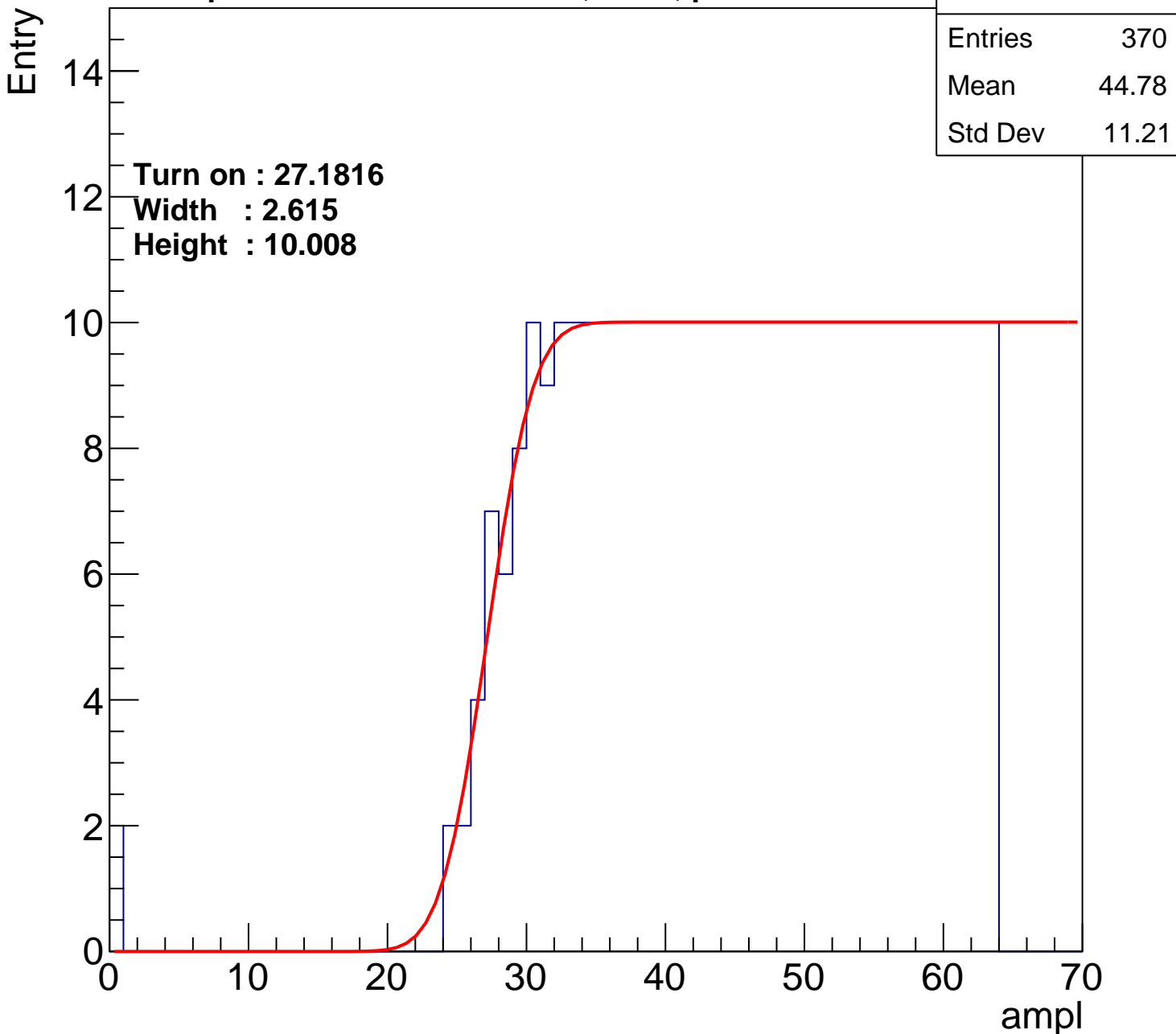
Entries	370
Mean	44.78
Std Dev	11.21

Mean	44.78
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Std Dev	11.21
---------	-------

Width : 2.615

Height : 10.008



B0L000S, U22-ch58

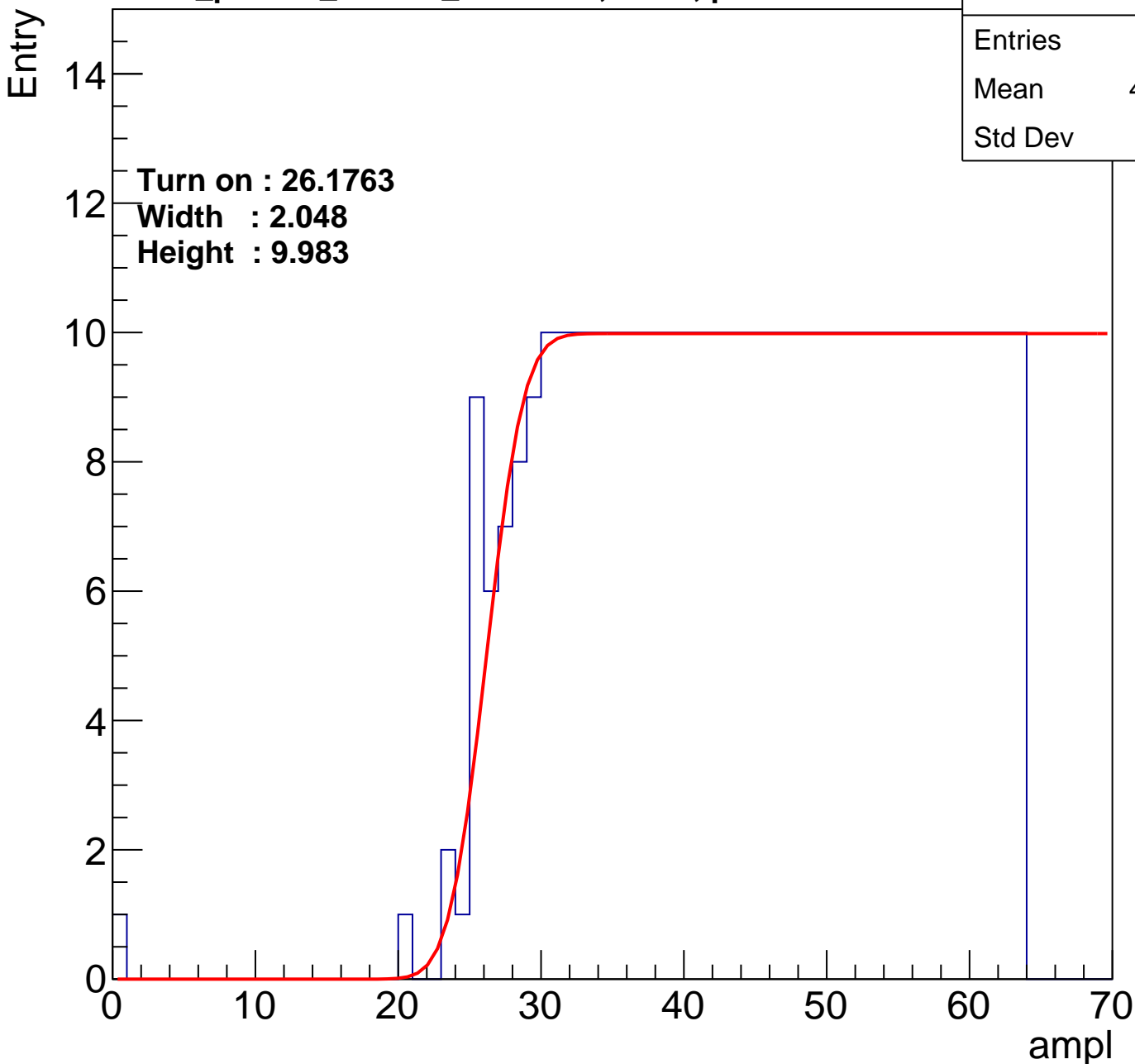
calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.15
Std Dev	11.4

Turn on : 26.1763

Width : 2.048

Height : 9.983



B0L000S, U22-ch59

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.11
Std Dev	11.41

Turn on : 28.5677

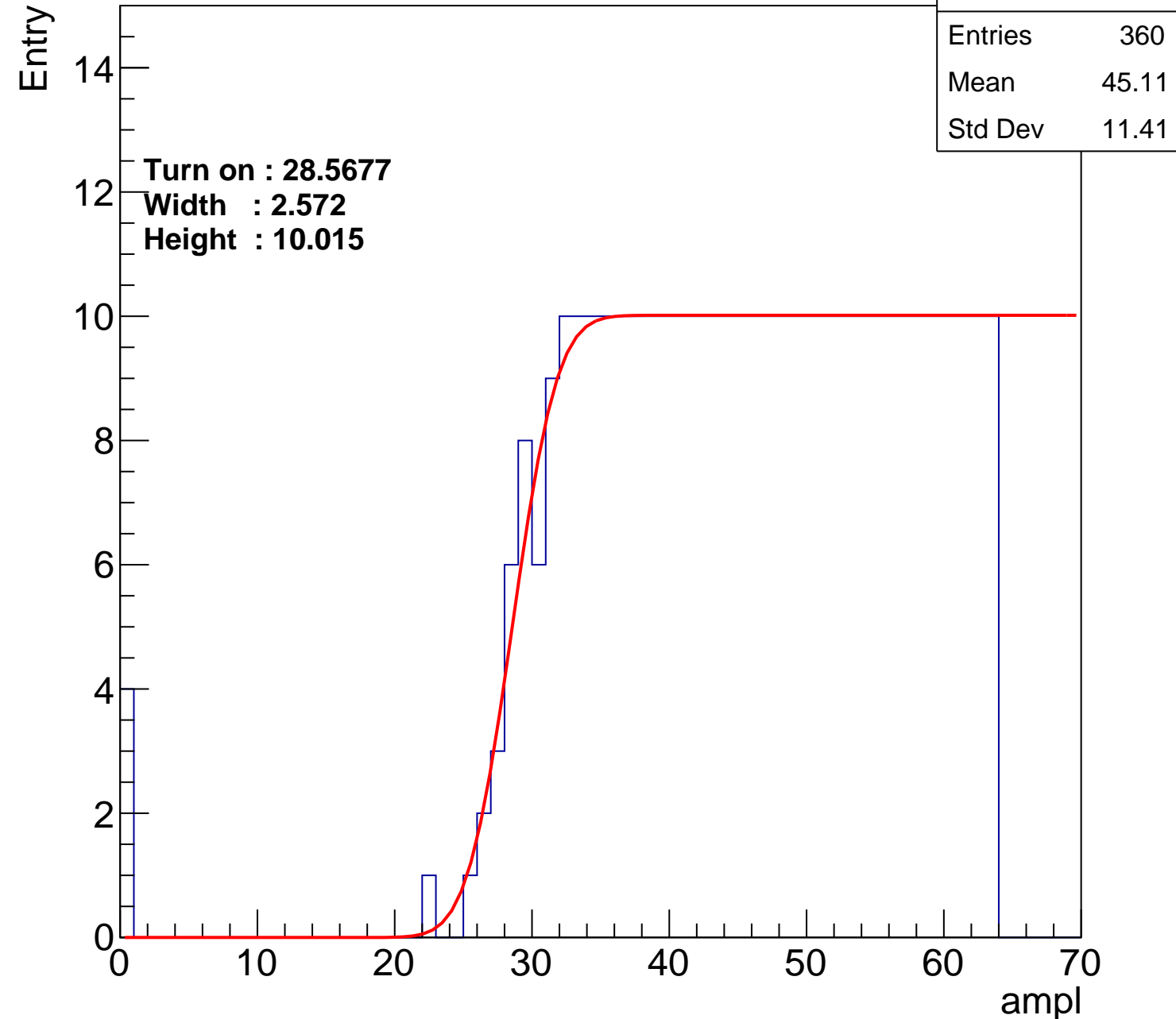
Width : 2.572

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch60

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.62
Std Dev	12.09

Turn on : 25.4844

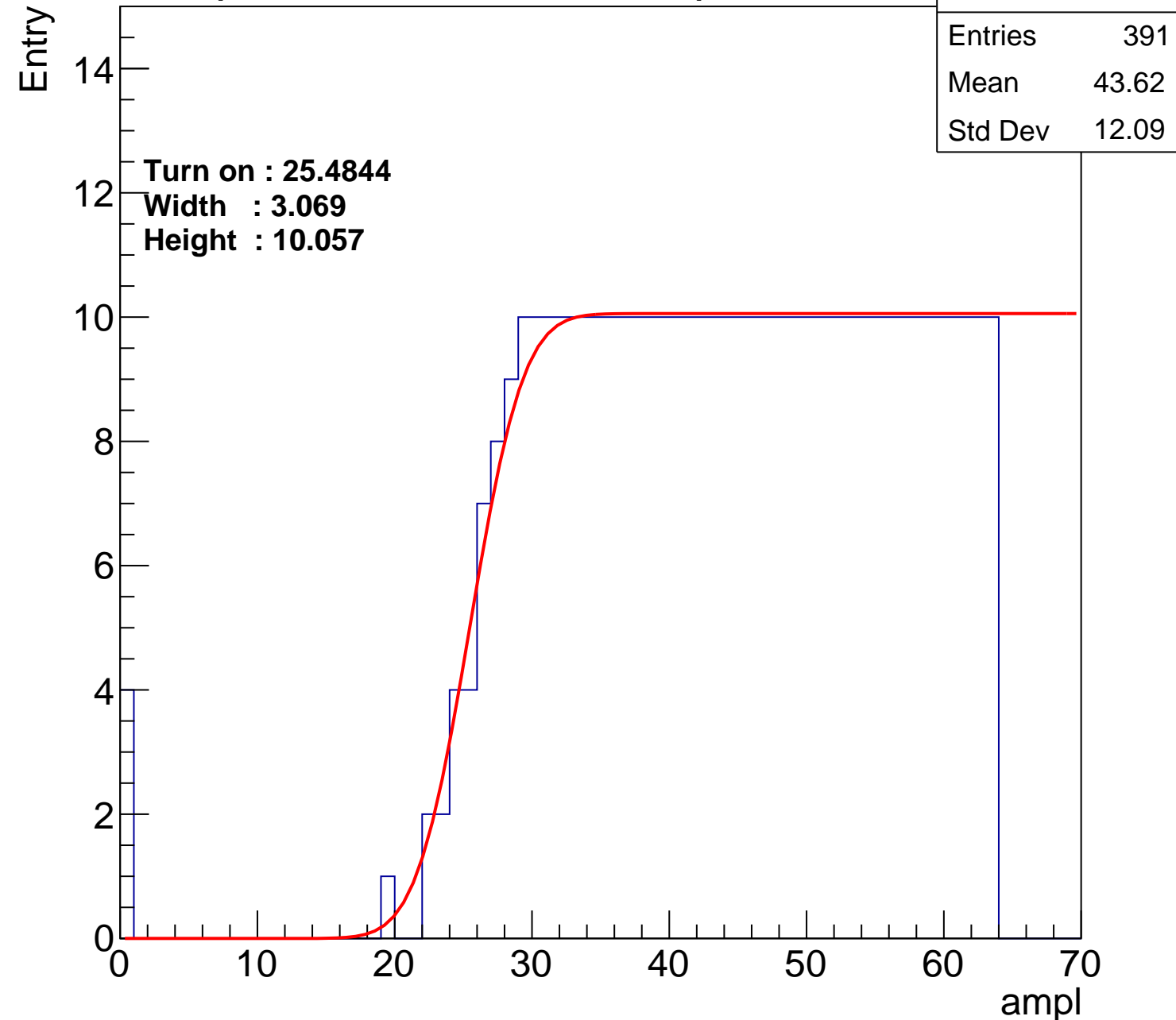
Width : 3.069

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch61

calib_packv5_042523_0143.root, FC#5, port B1

Entries	400
Mean	43.32
Std Dev	11.97

Turn on : 24.5366

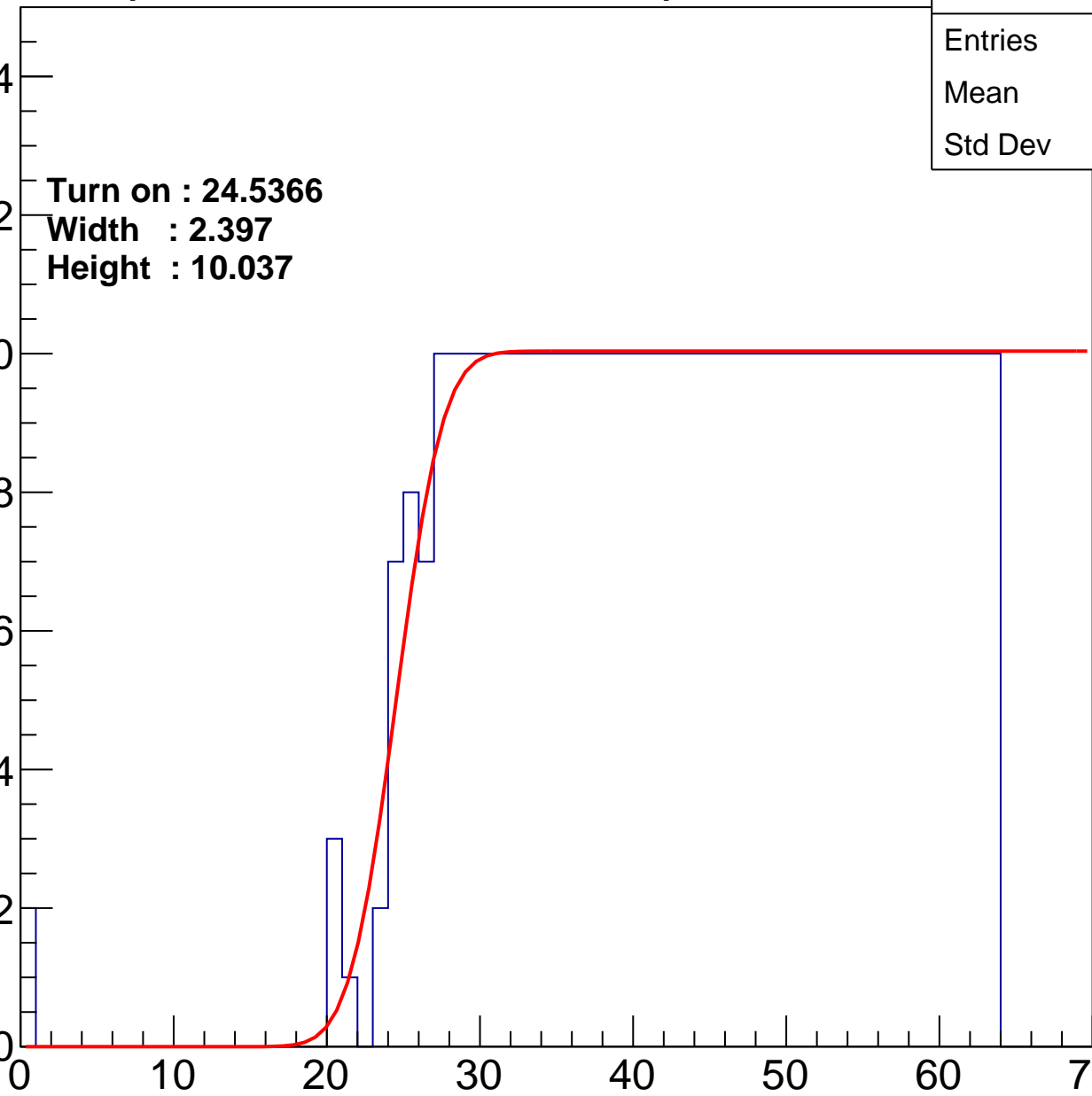
Width : 2.397

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch62

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.45
Std Dev	10.9

Turn on : 28.9388

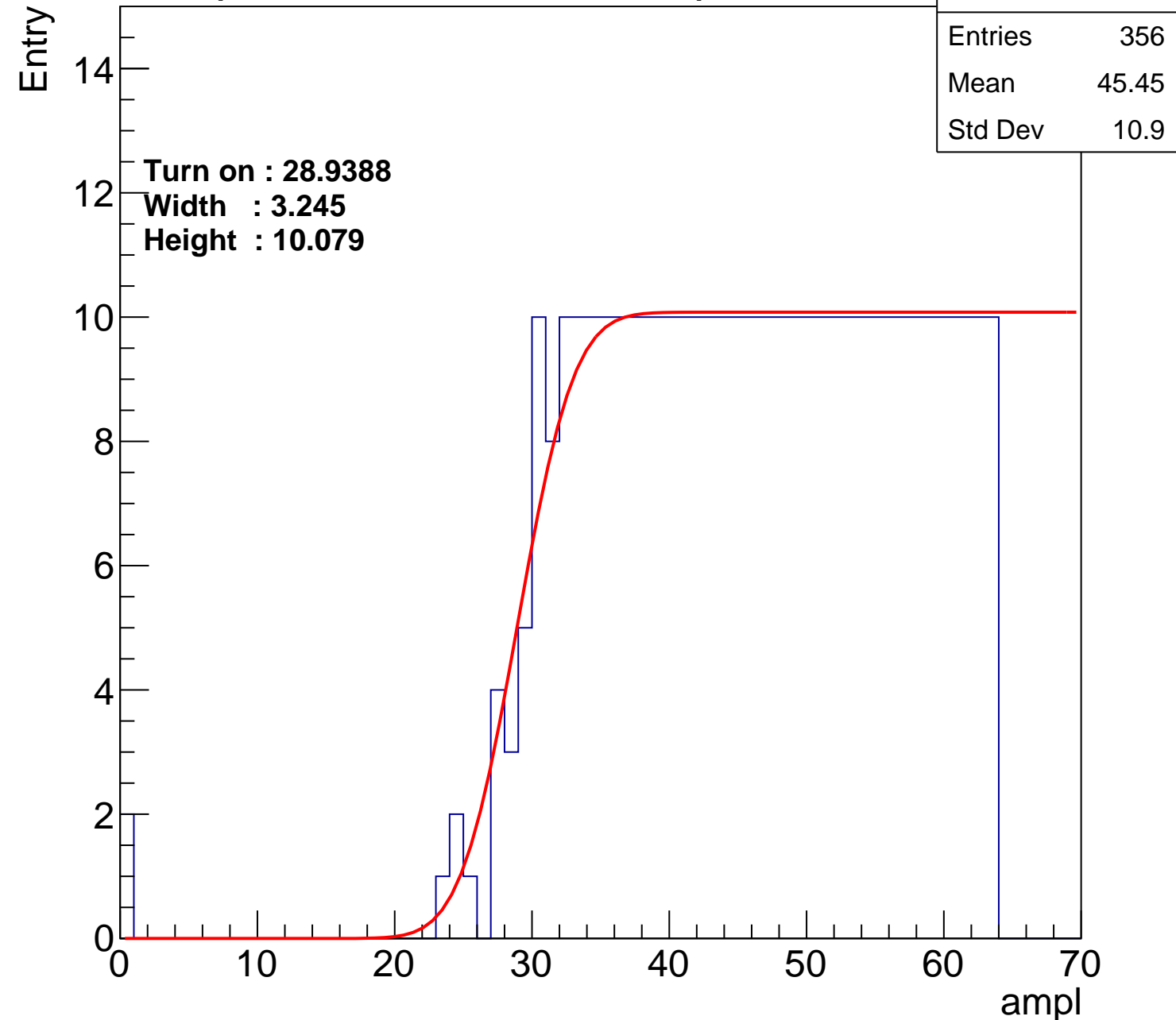
Width : 3.245

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch63

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.33
Std Dev	11.59

Turn on : 26.6475

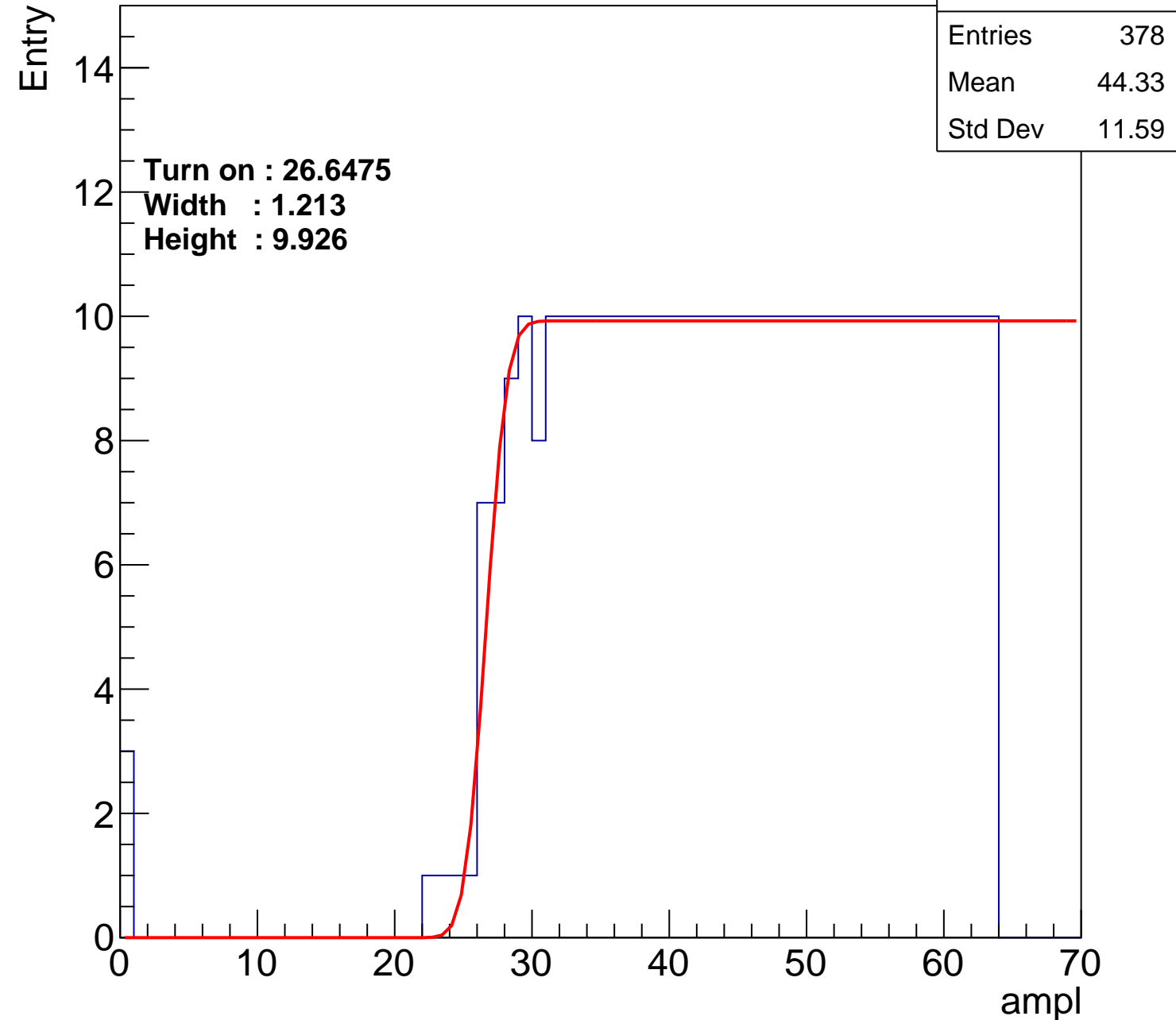
Width : 1.213

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch64

calib_packv5_042523_0143.root, FC#5, port B1

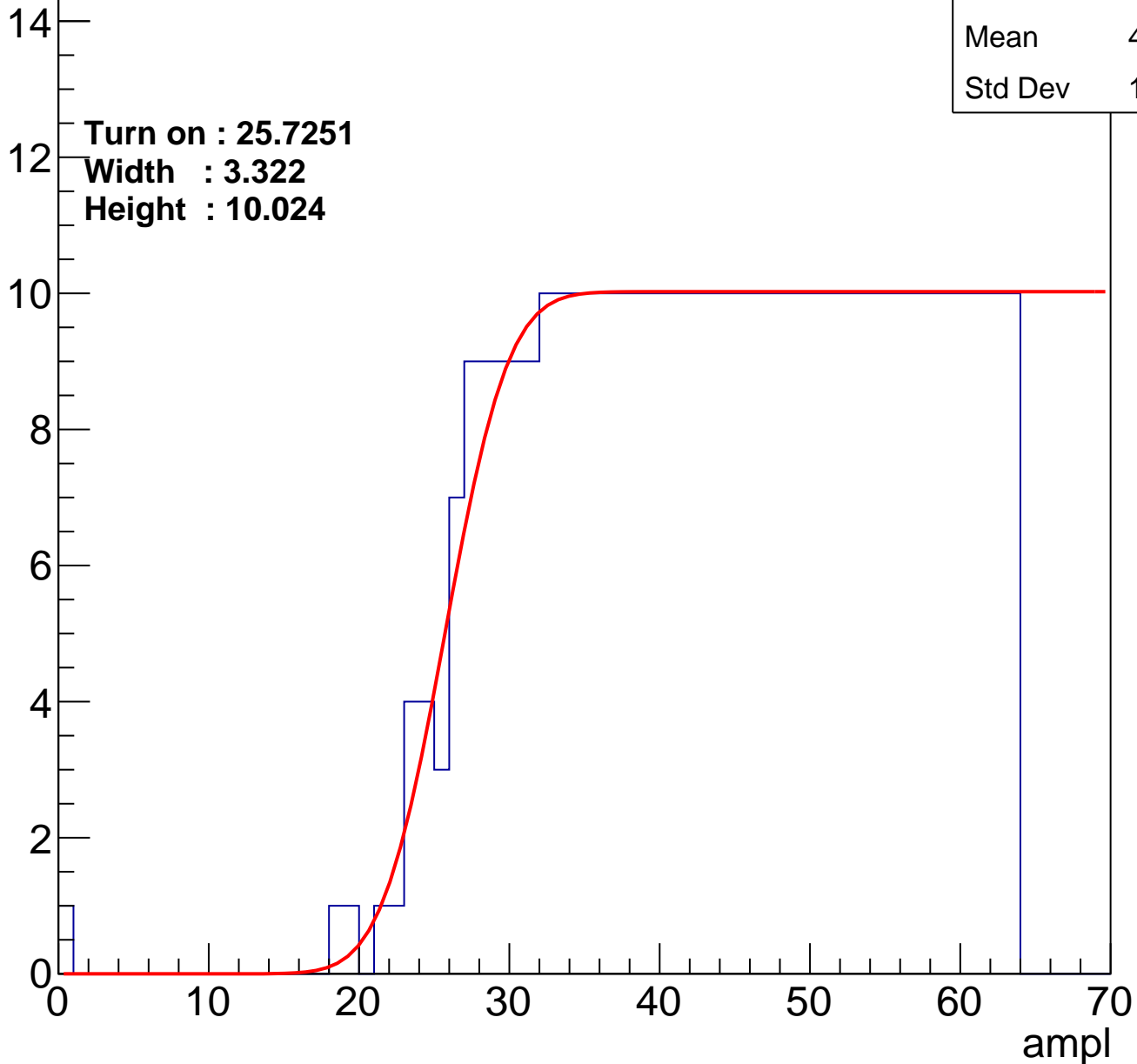
Entries	388
Mean	43.89
Std Dev	11.62

Turn on : 25.7251

Width : 3.322

Height : 10.024

Entry



B0L000S, U22-ch65

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.38
Std Dev	10.74

Turn on : 28.3448

Width : 2.773

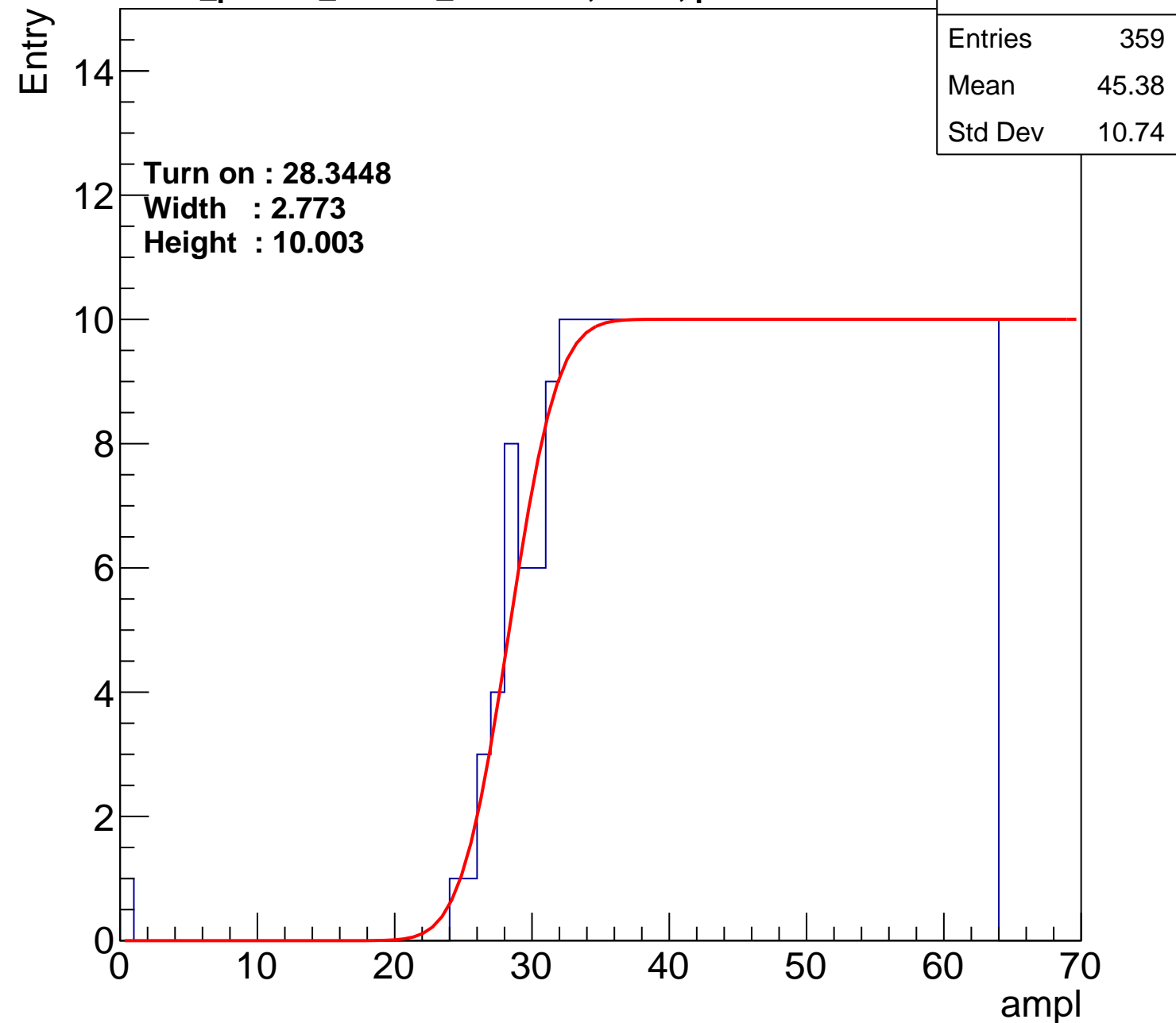
Height : 10.003

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U22-ch66

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.37
Std Dev	11.46

Turn on : 26.7007

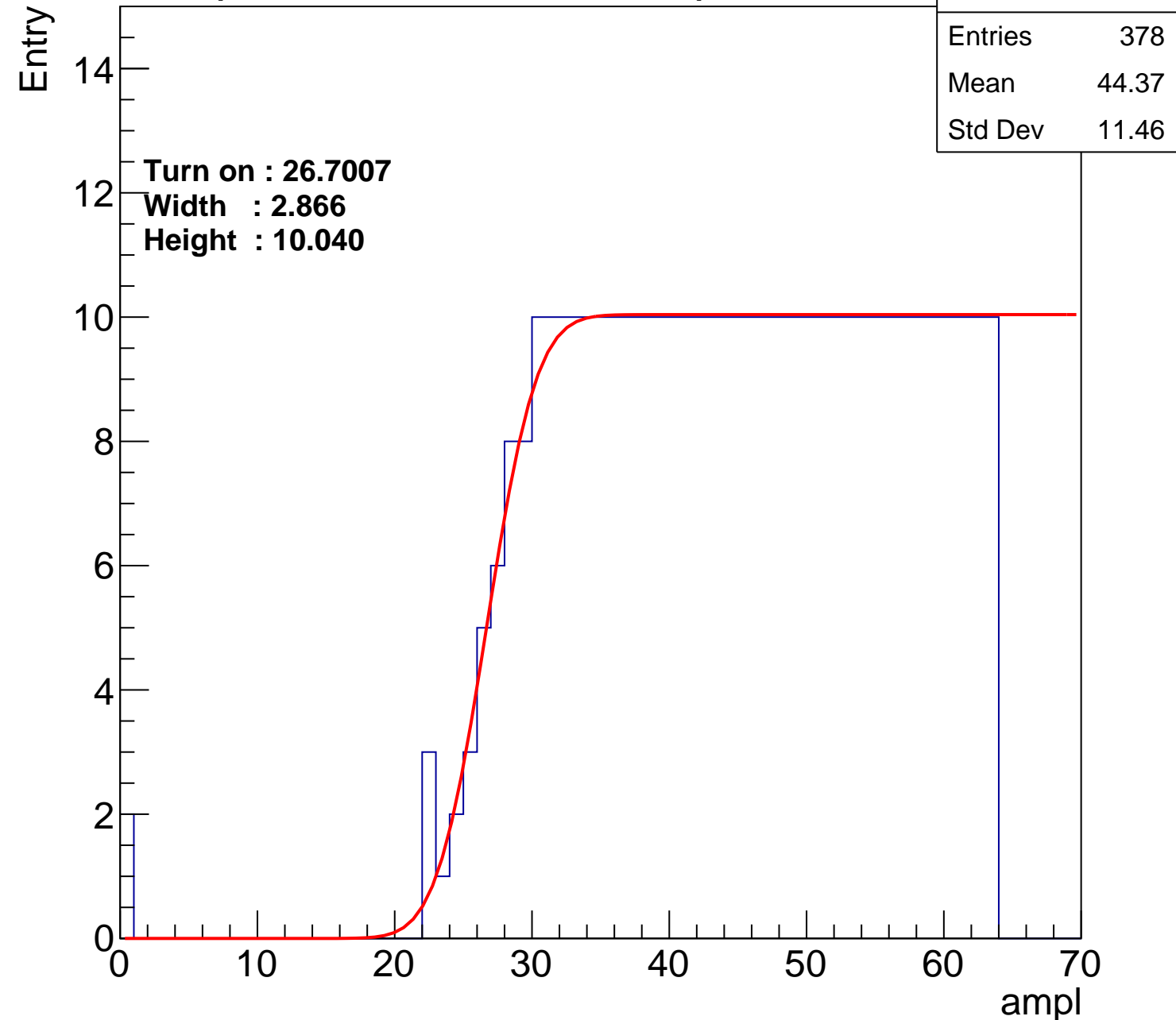
Width : 2.866

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.45
Std Dev	11.29

Turn on : 26.0146

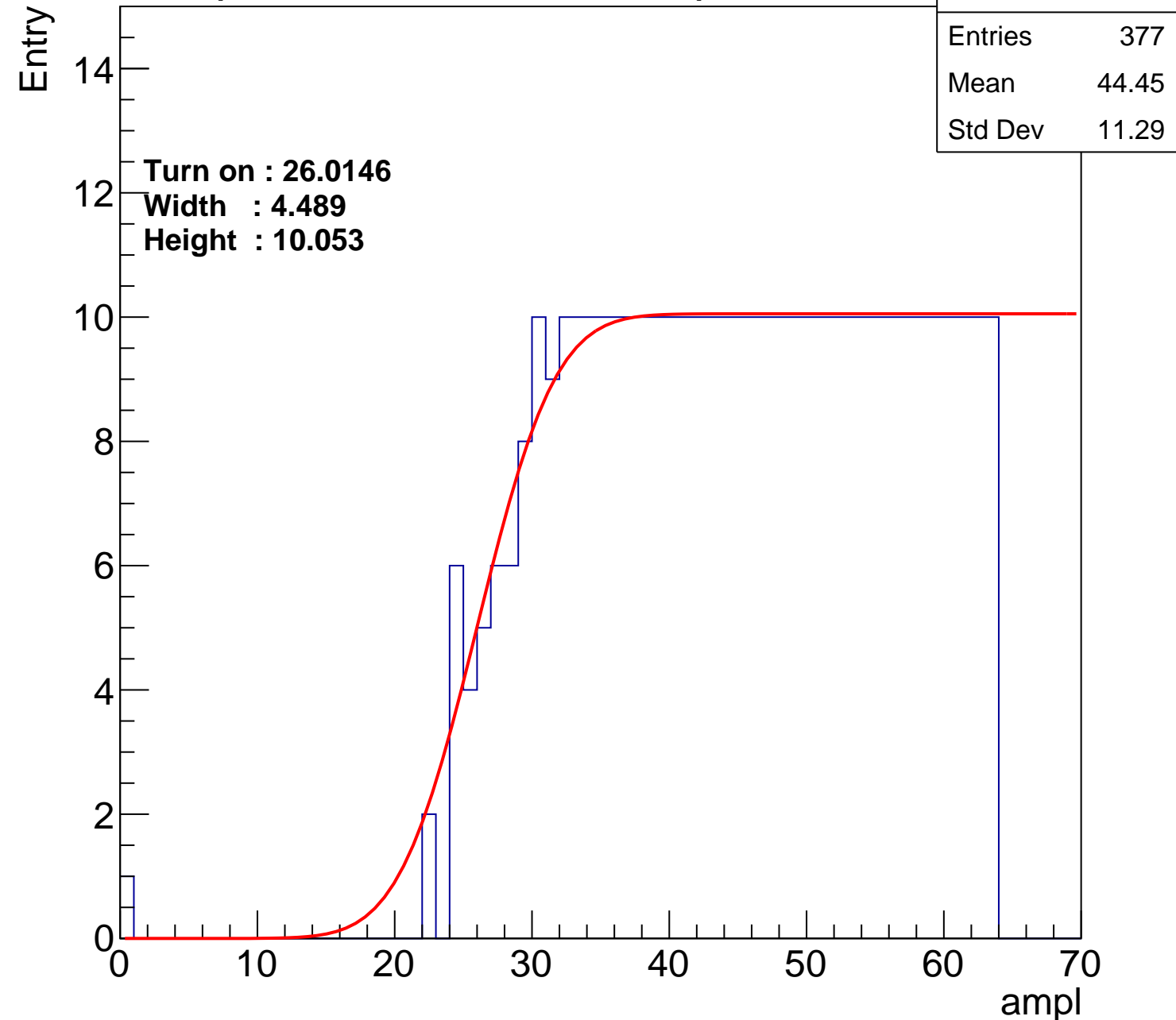
Width : 4.489

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.63
Std Dev	11.16

Turn on : 26.9634

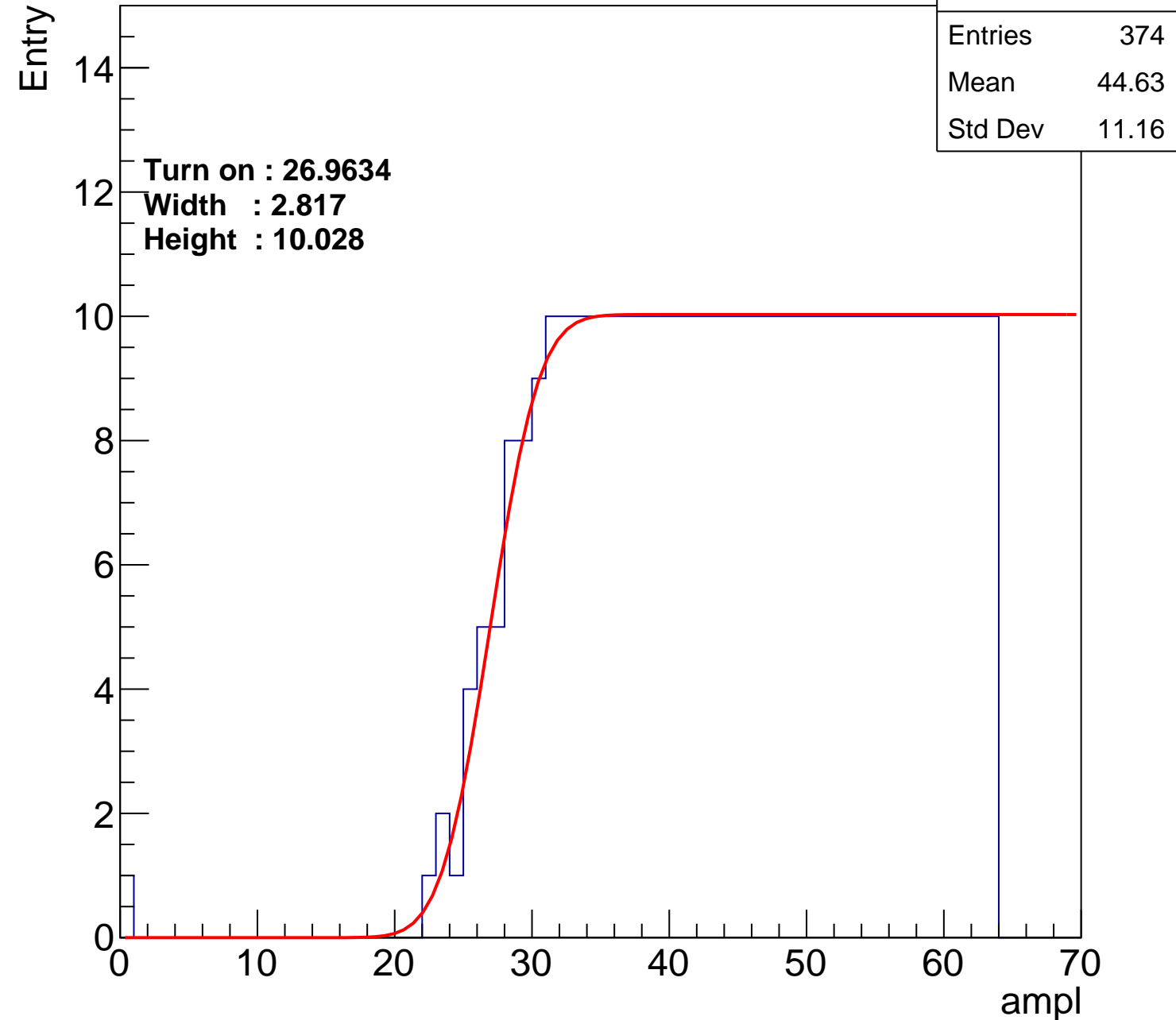
Width : 2.817

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch69

calib_packv5_042523_0143.root, FC#5, port B1

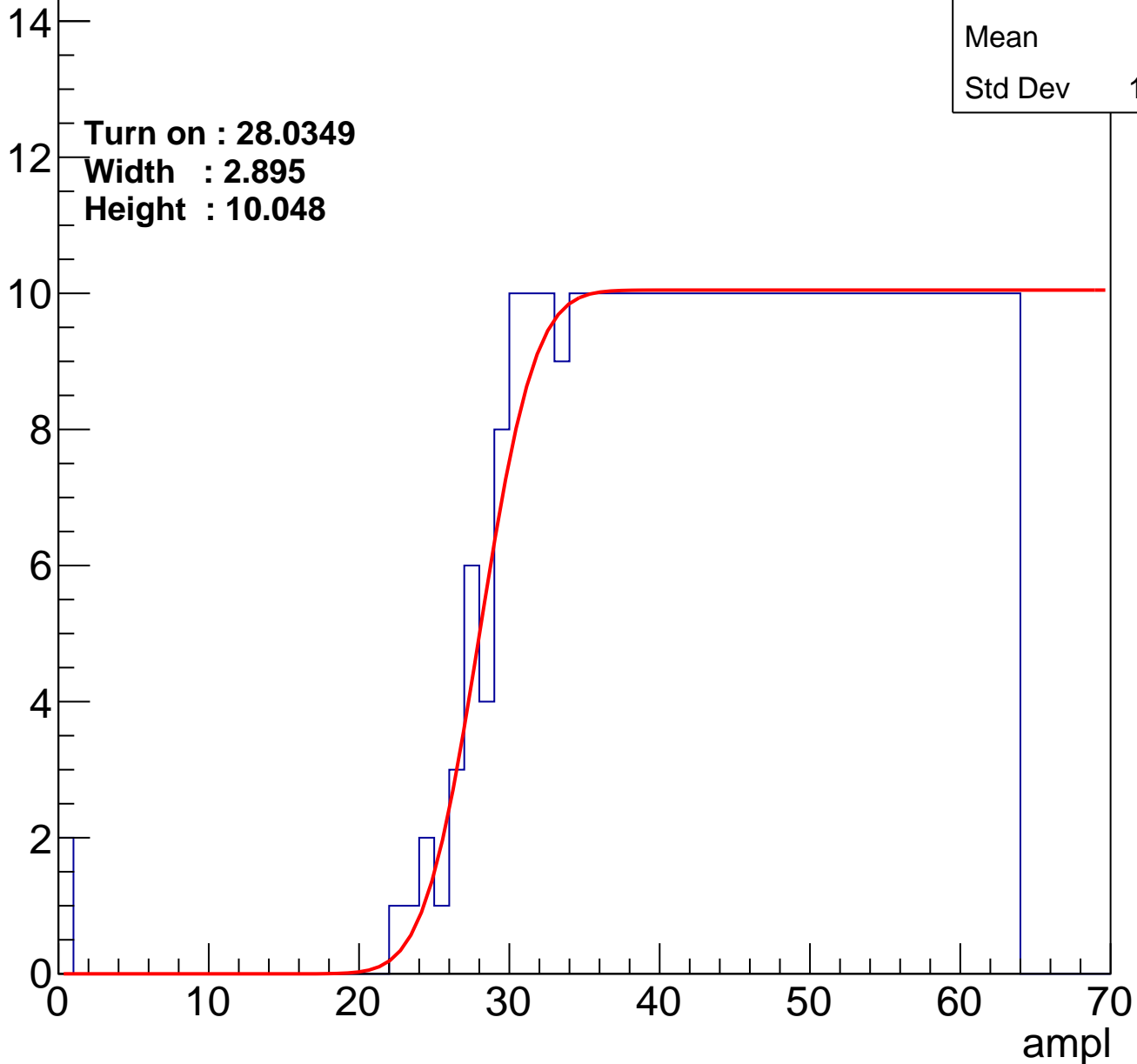
Entries	367
Mean	44.9
Std Dev	11.19

Turn on : 28.0349

Width : 2.895

Height : 10.048

Entry



B0L000S, U22-ch70

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.87
Std Dev	11.72

Turn on : 25.8728

Width : 3.387

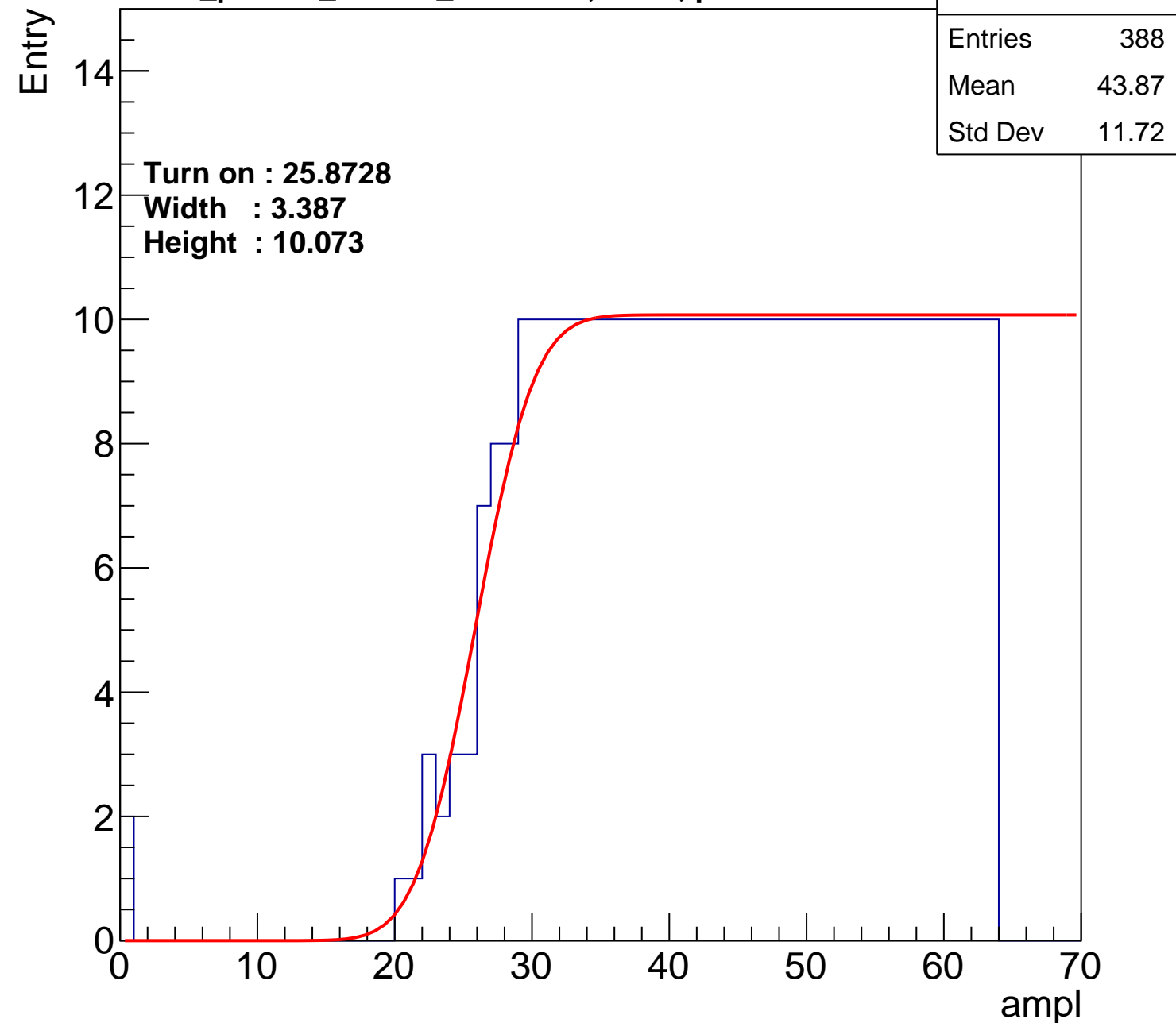
Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U22-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.9200

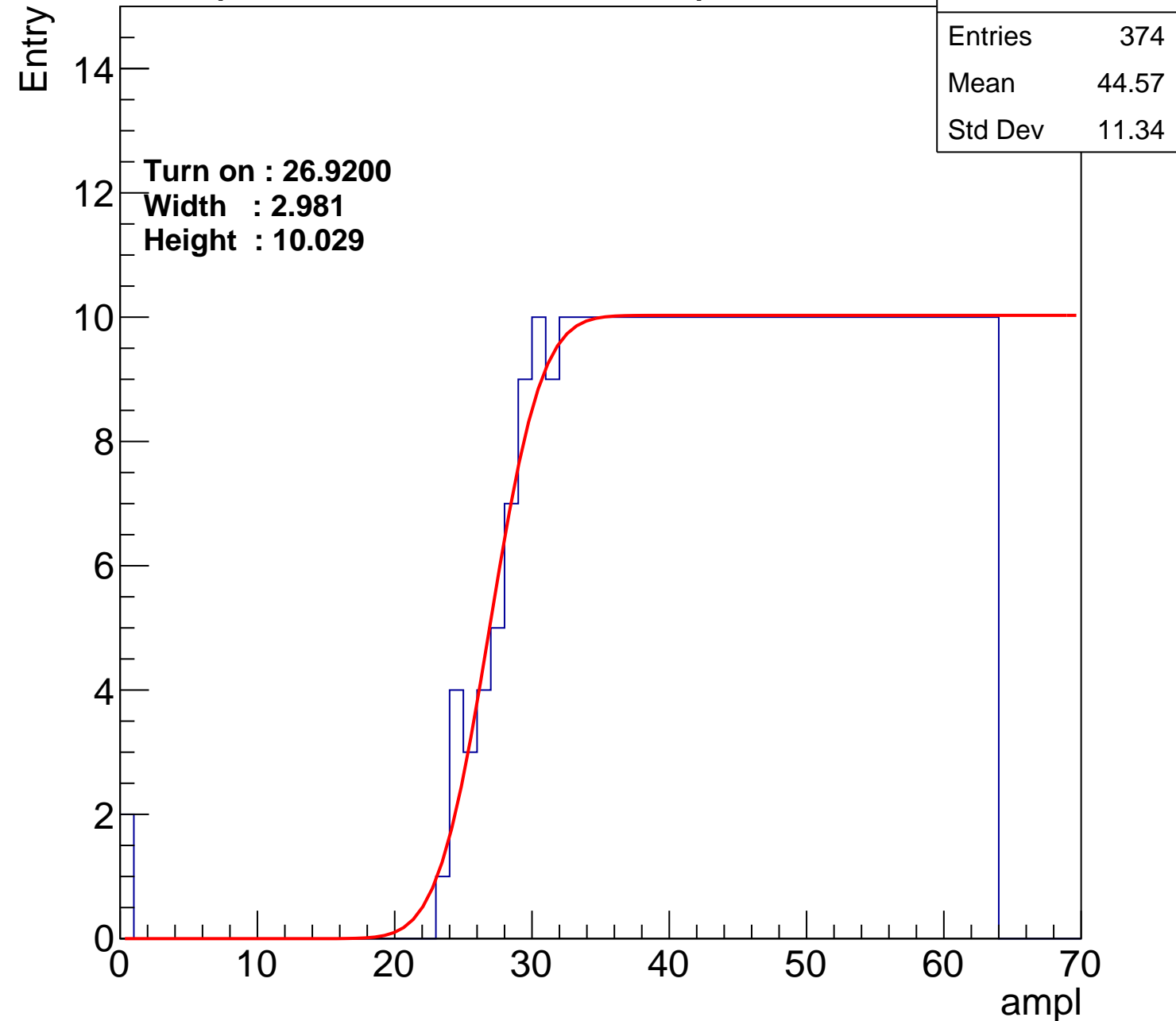
Width : 2.981

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	393
Mean	43.58
Std Dev	11.98

Turn on : 25.2326

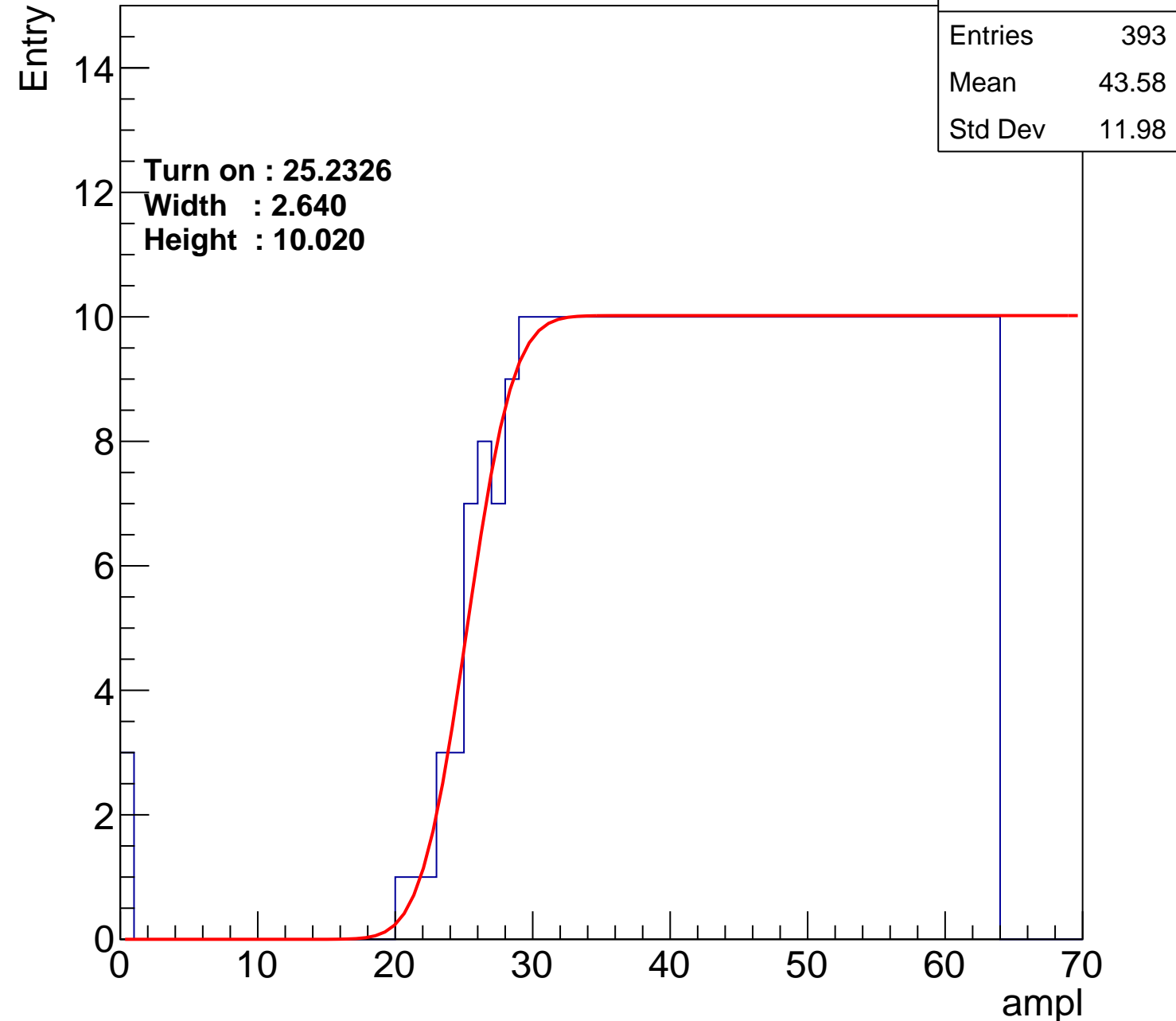
Width : 2.640

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch73

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.57
Std Dev	11.66

Turn on : 27.4907

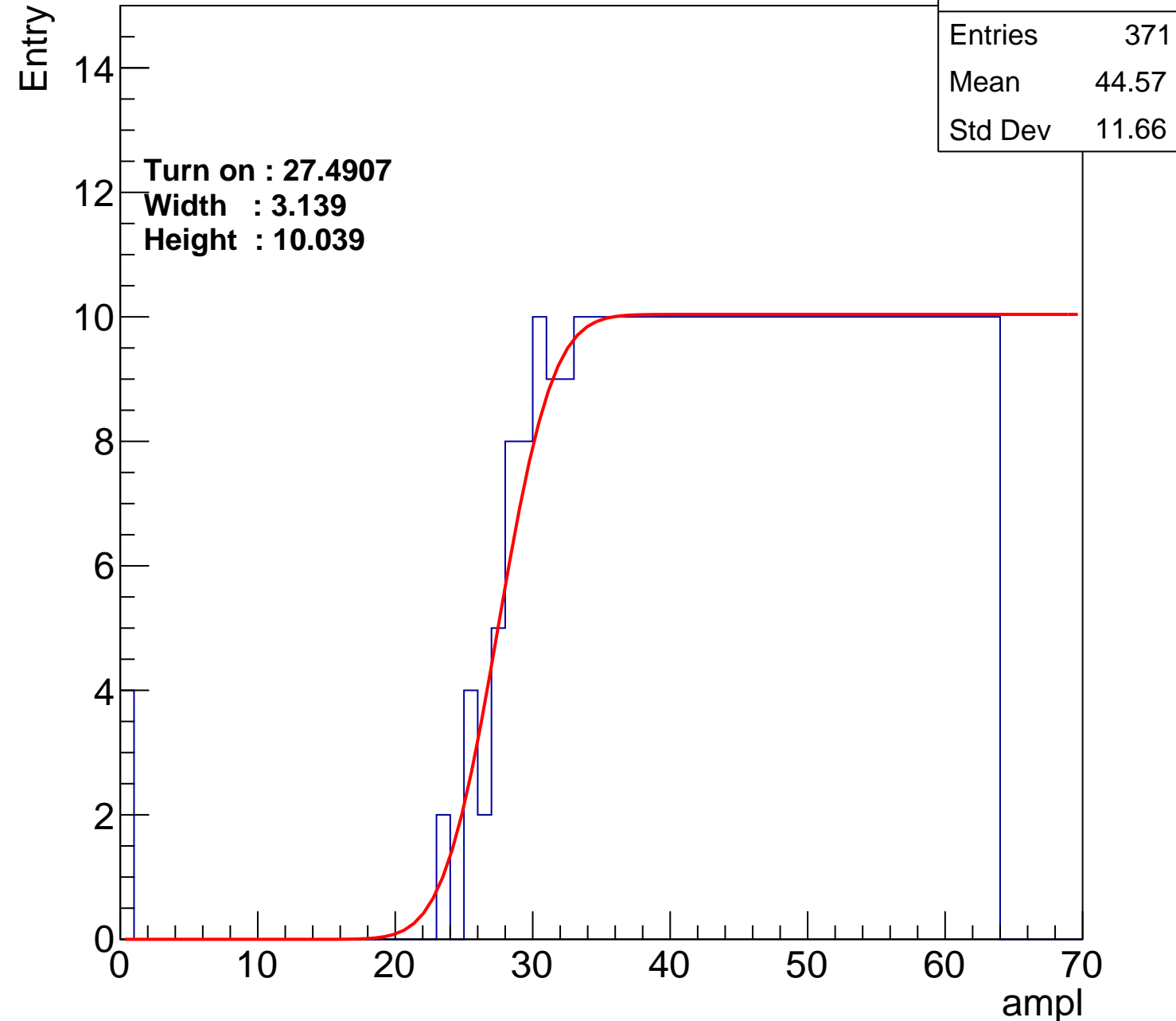
Width : 3.139

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.67
Std Dev	11.1

Turn on : 27.0365

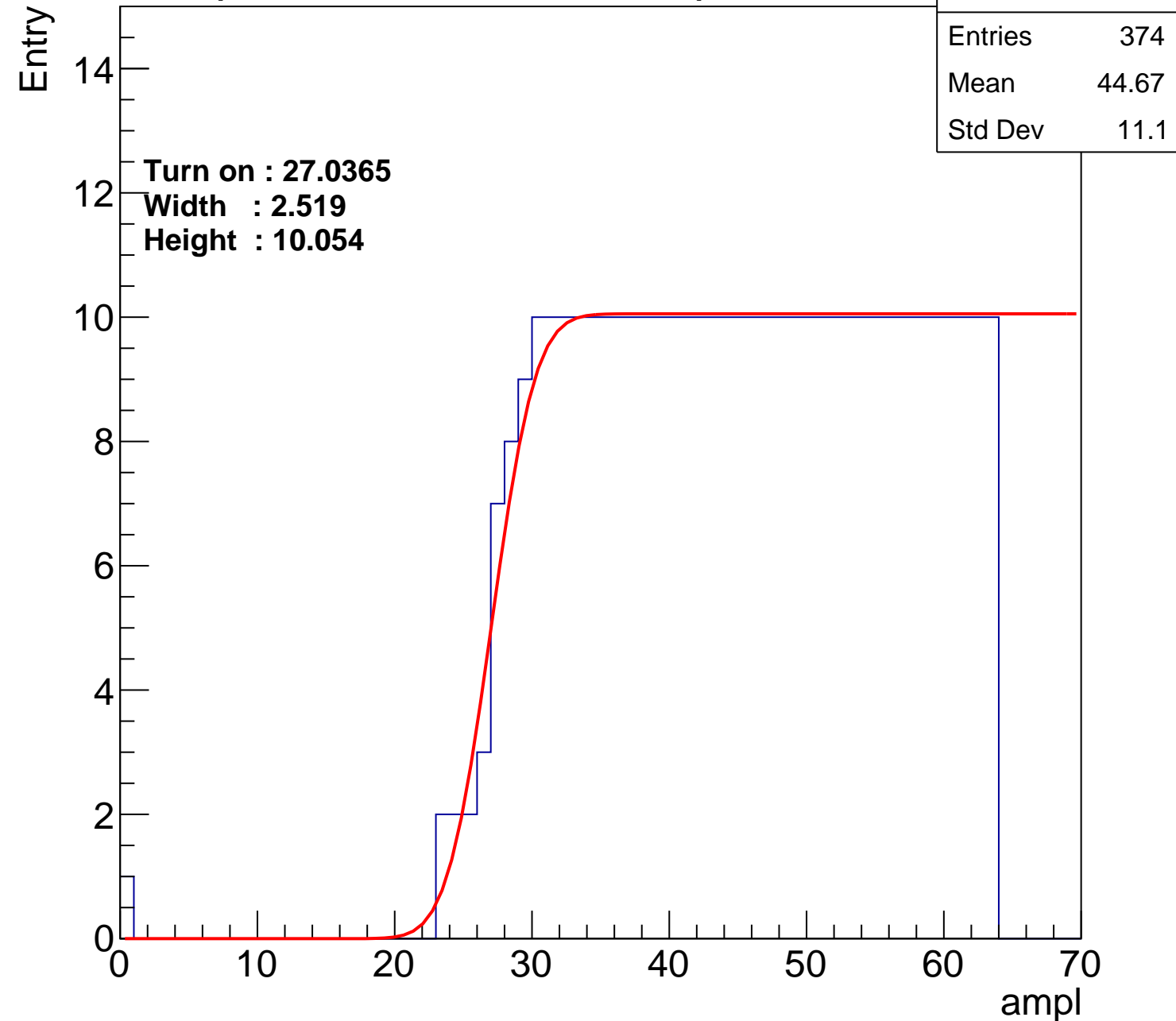
Width : 2.519

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.02
Std Dev	11.15

Turn on : 27.8688

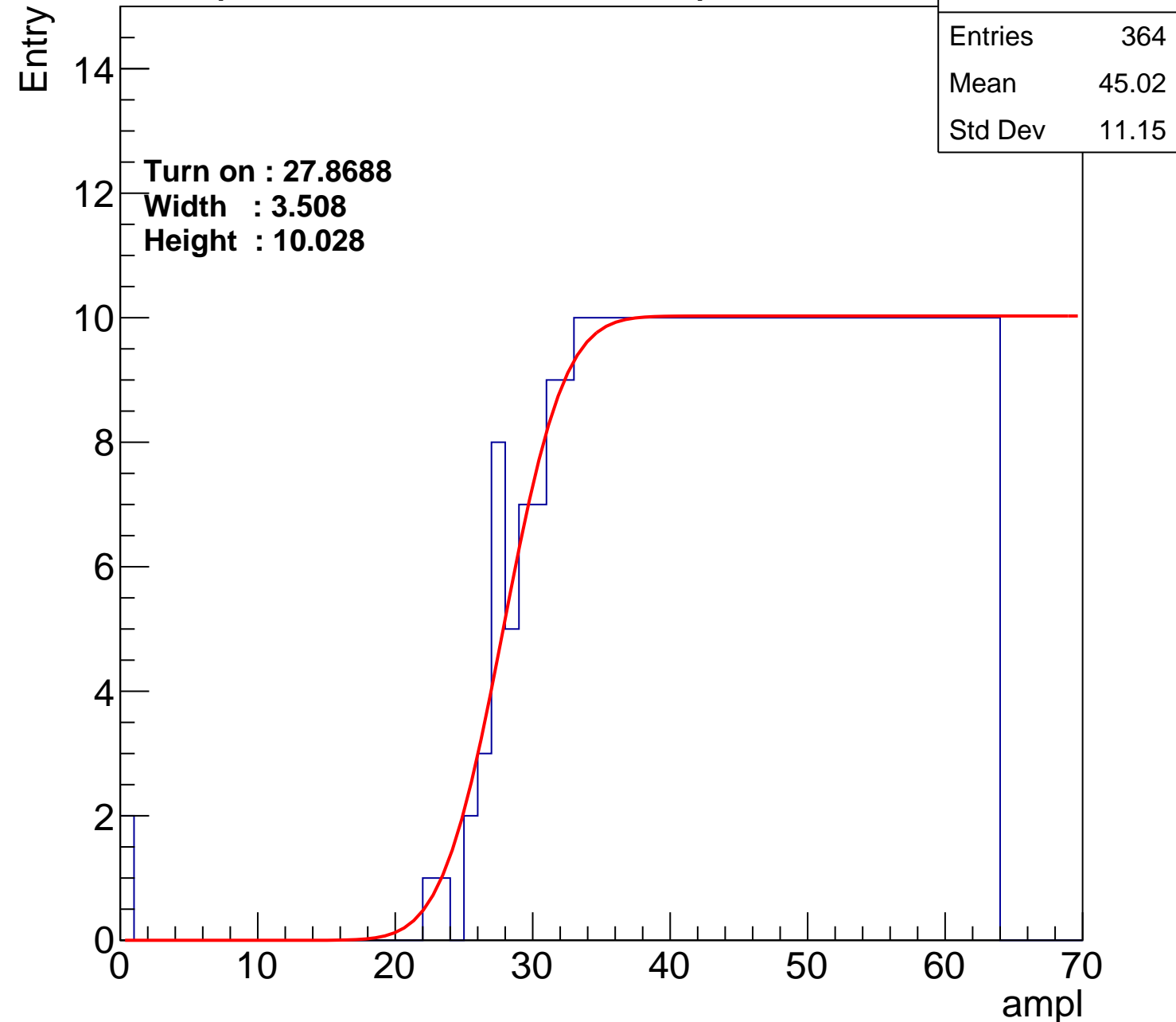
Width : 3.508

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch76

calib_packv5_042523_0143.root, FC#5, port B1

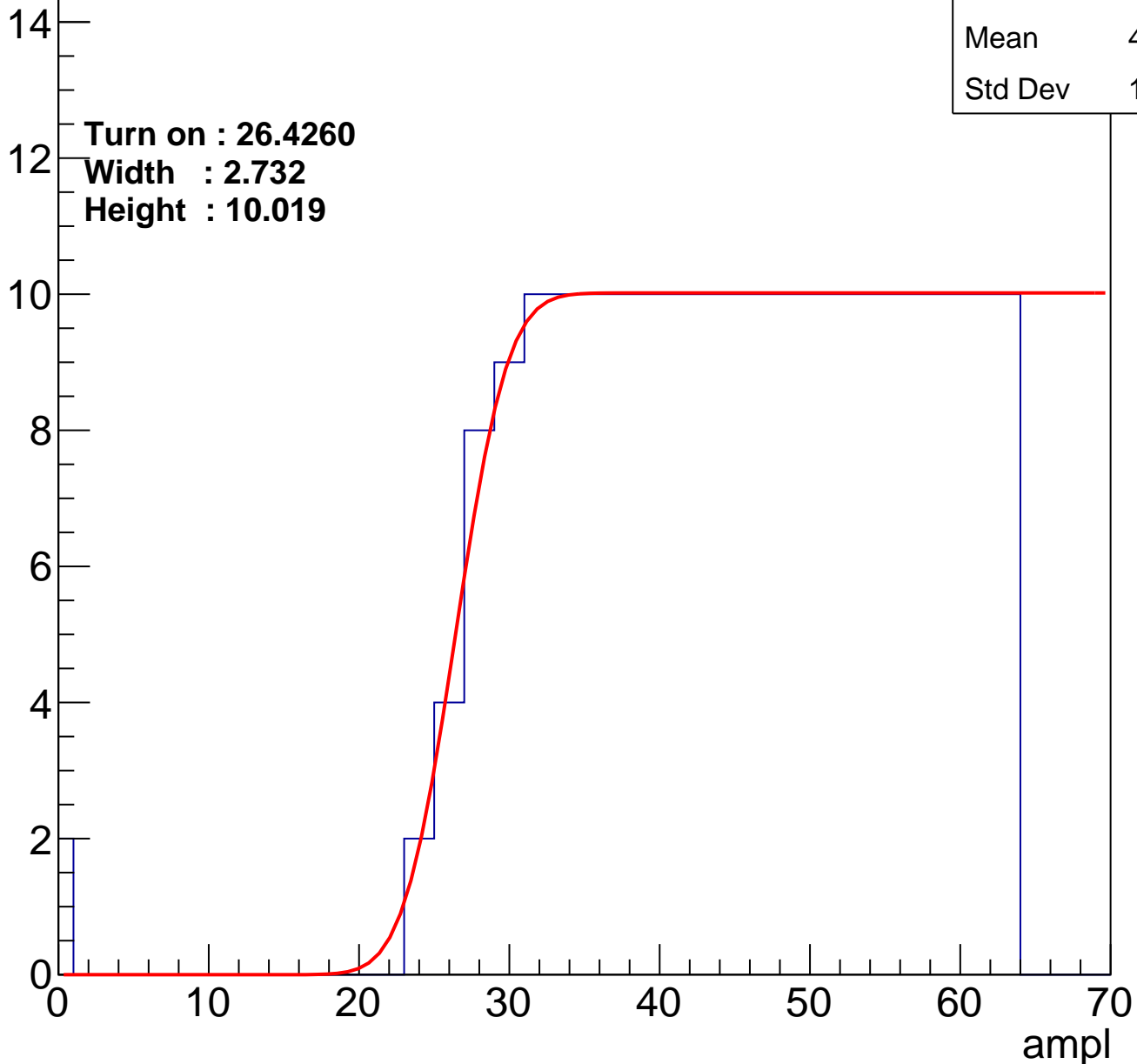
Entries	378
Mean	44.39
Std Dev	11.42

Turn on : 26.4260

Width : 2.732

Height : 10.019

Entry



B0L000S, U22-ch77

calib_packv5_042523_0143.root, FC#5, port B1

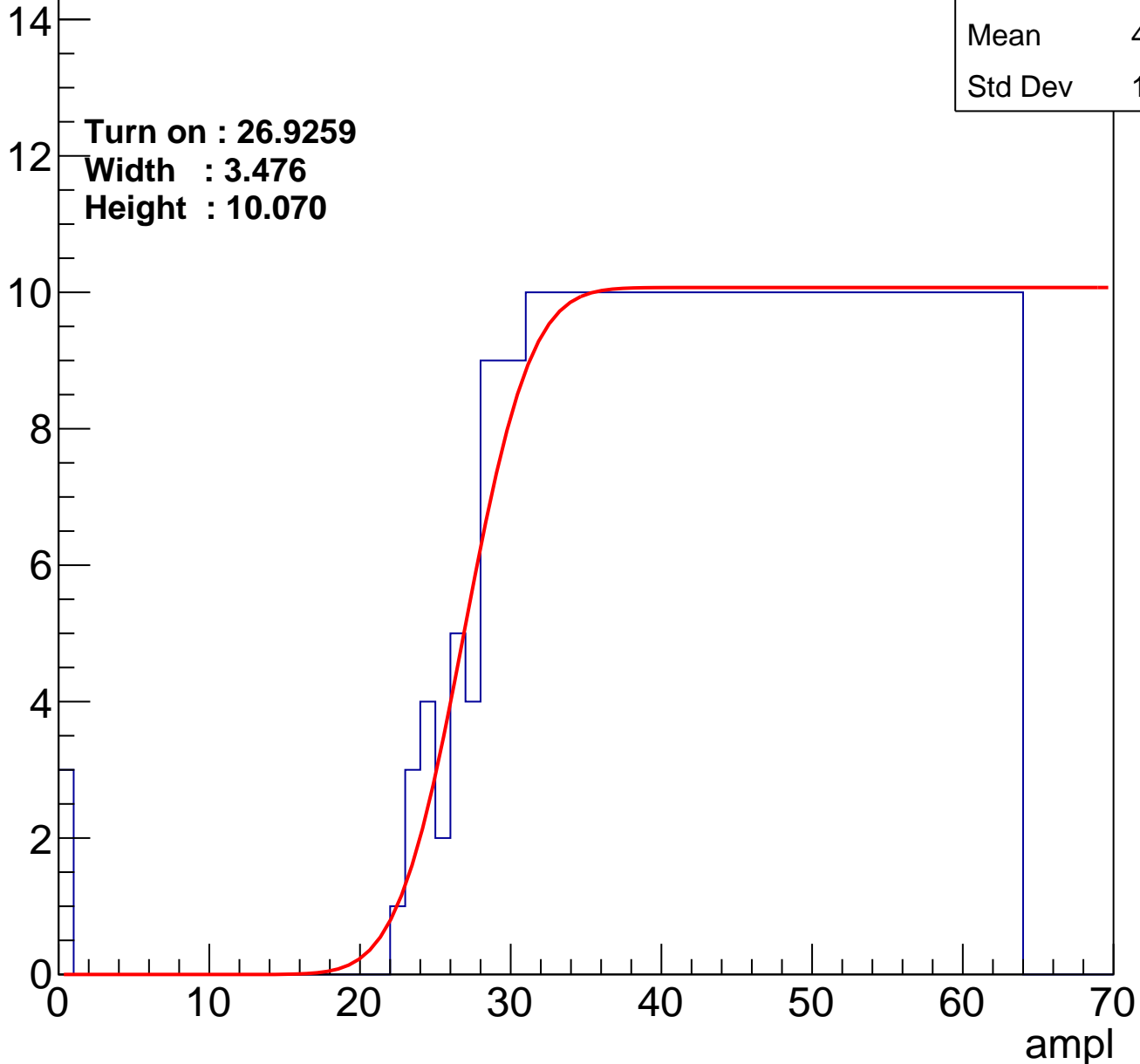
Entries	379
Mean	44.24
Std Dev	11.68

Turn on : 26.9259

Width : 3.476

Height : 10.070

Entry



B0L000S, U22-ch78

calib_packv5_042523_0143.root, FC#5, port B1

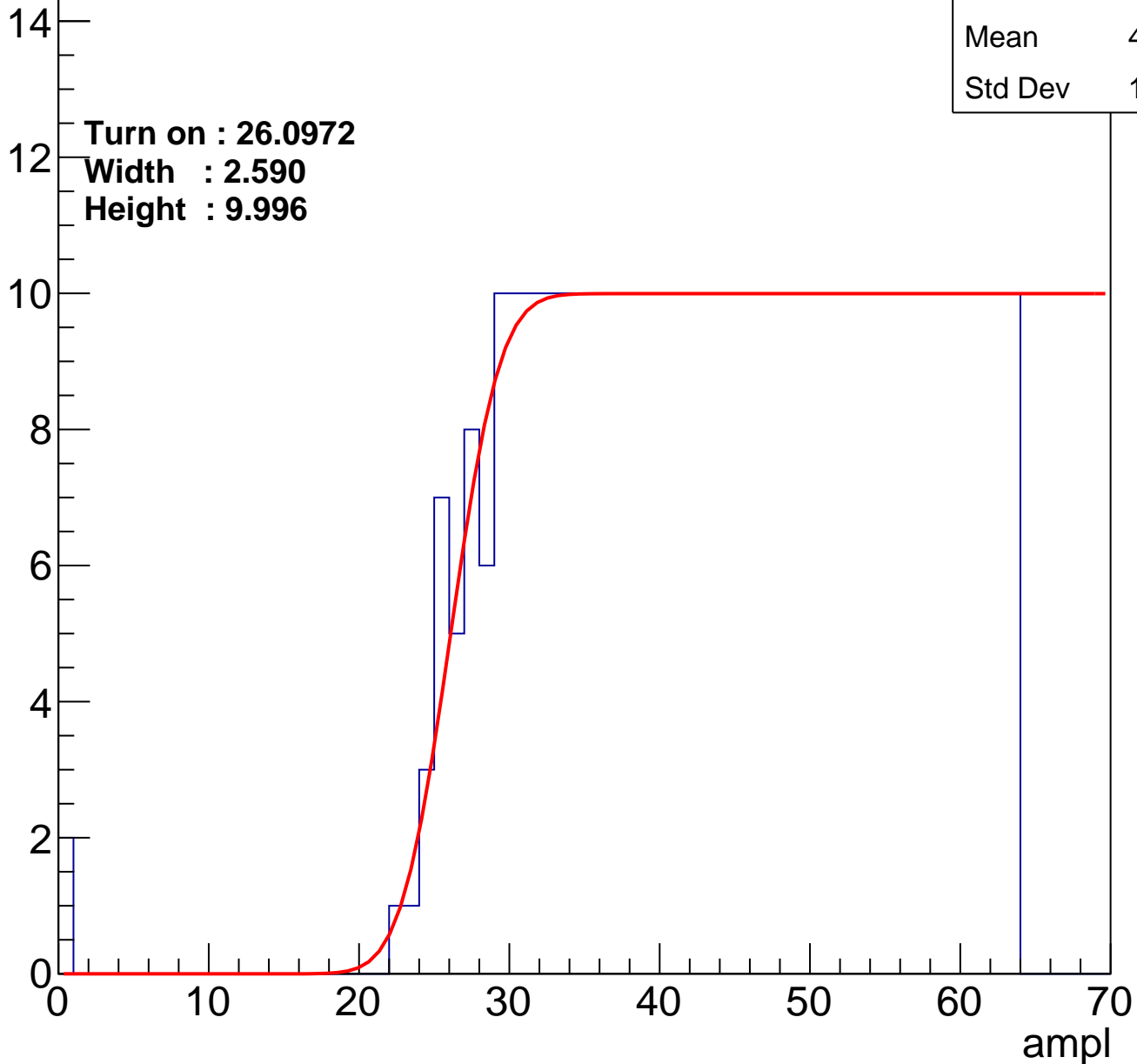
Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.0972

Width : 2.590

Height : 9.996

Entry



B0L000S, U22-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.83
Std Dev	11.33

Turn on : 27.5905

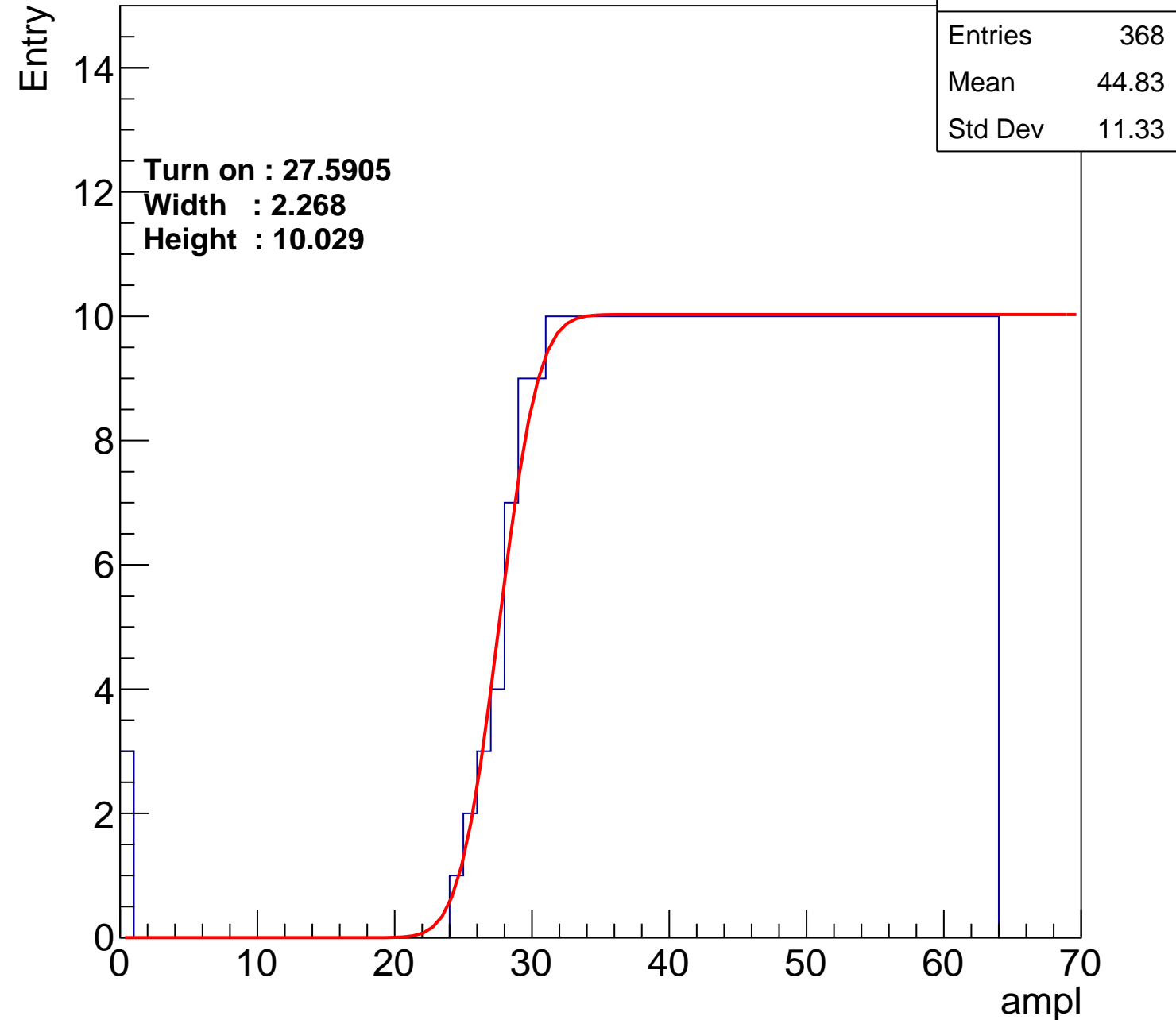
Width : 2.268

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch80

calib_packv5_042523_0143.root, FC#5, port B1

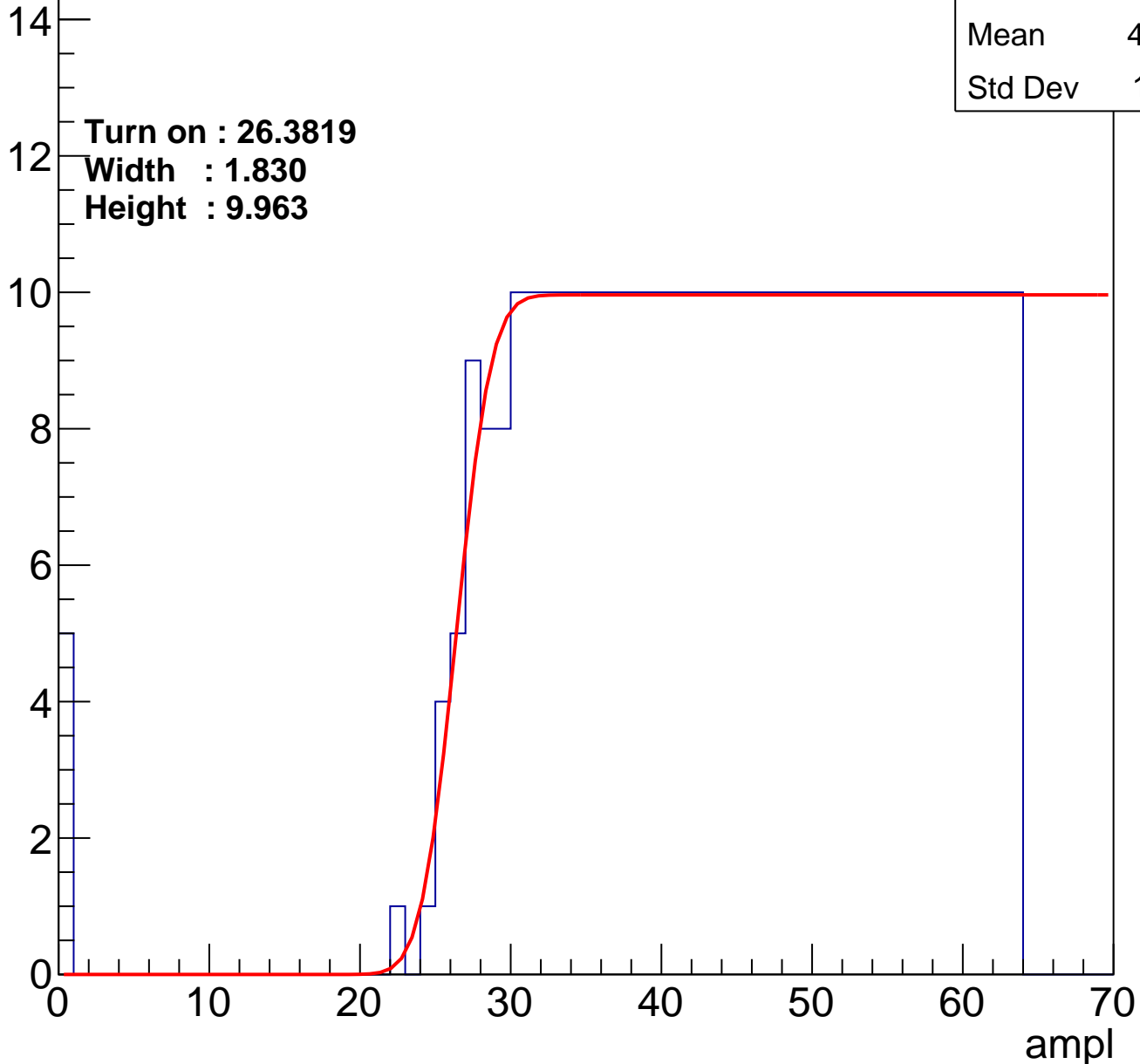
Entries	381
Mean	44.06
Std Dev	12.01

Turn on : 26.3819

Width : 1.830

Height : 9.963

Entry



B0L000S, U22-ch81

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 25.2334

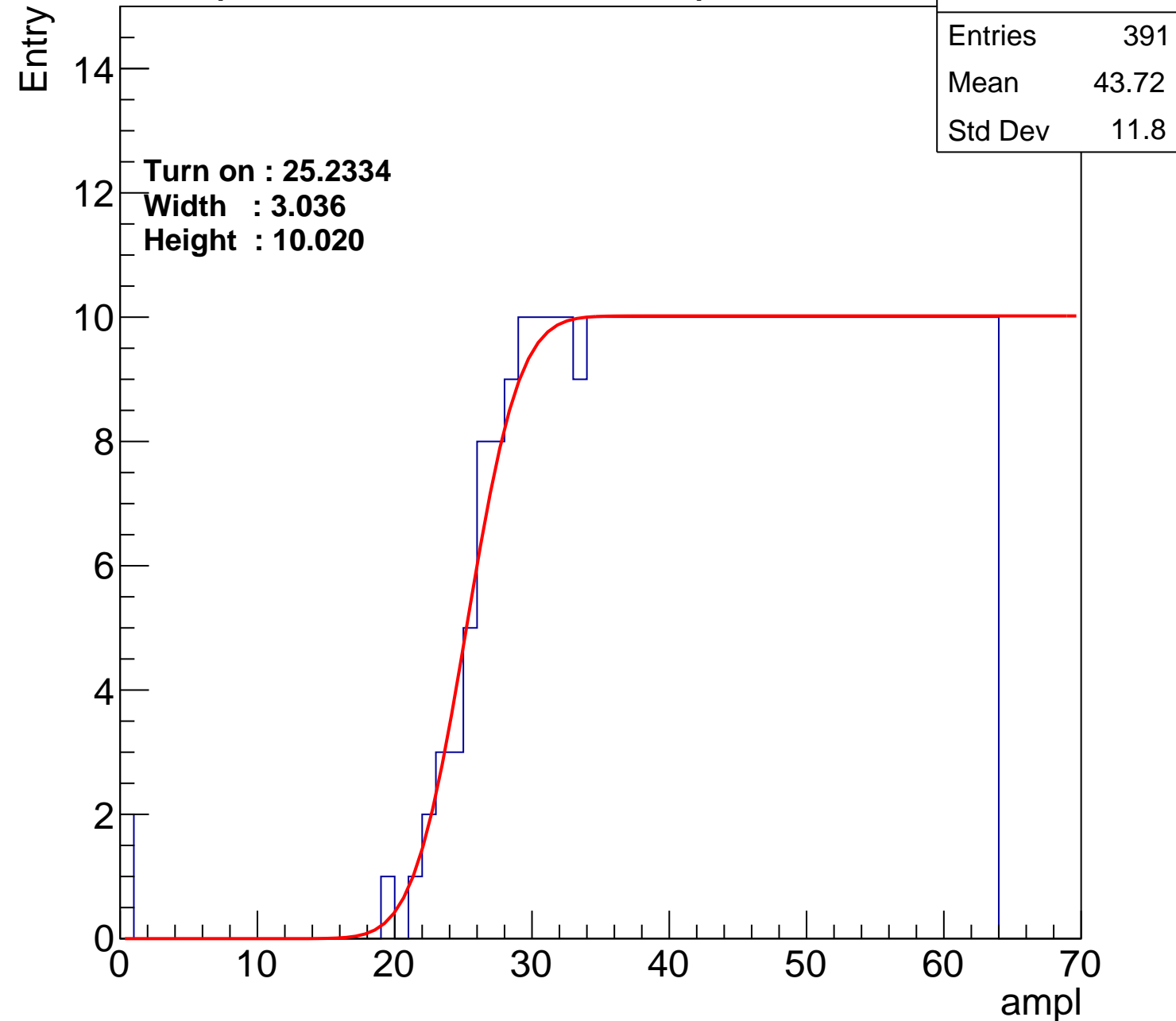
Width : 3.036

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch82

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.34
Std Dev	11.31

Turn on : 26.6642

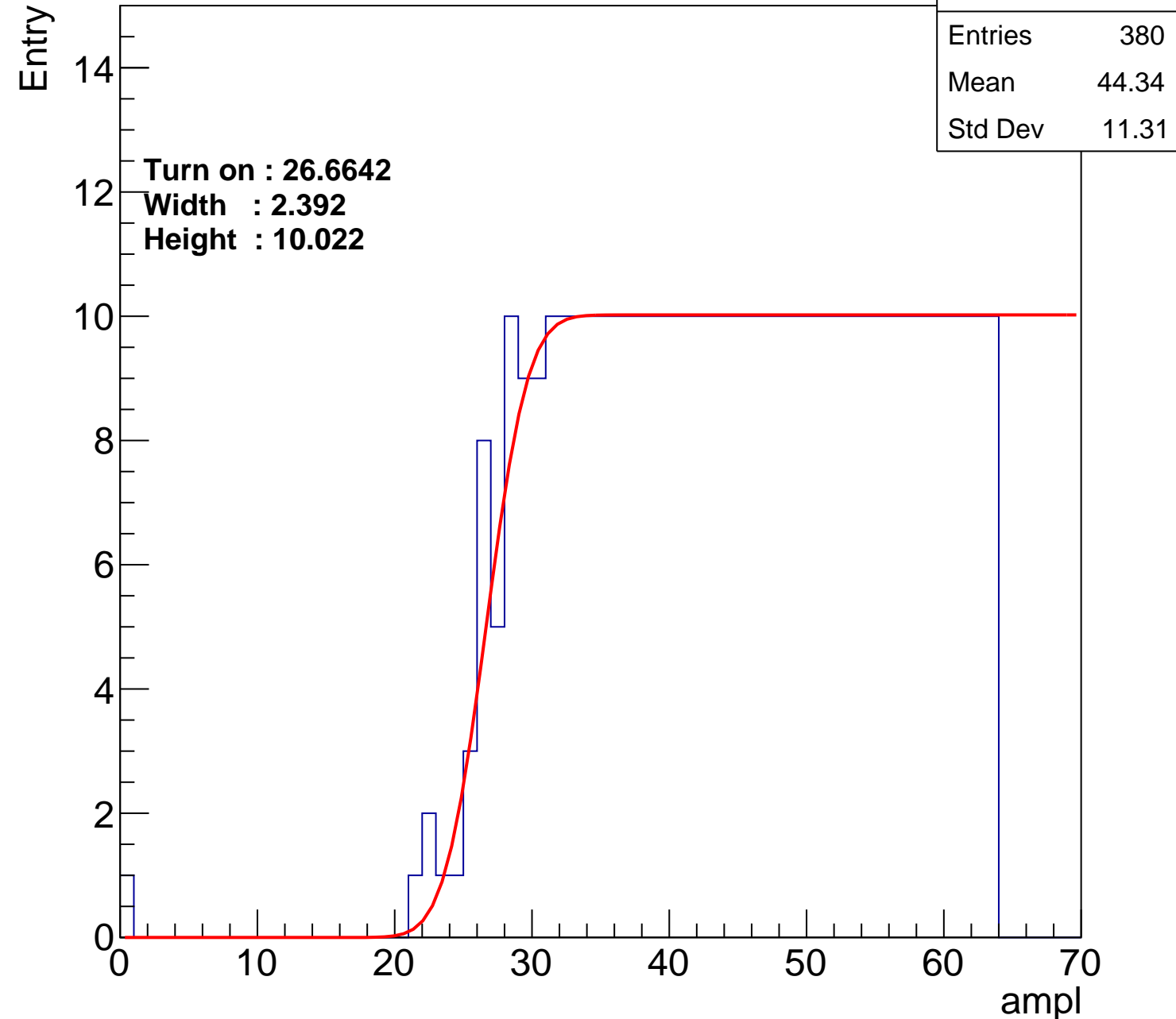
Width : 2.392

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch83

calib_packv5_042523_0143.root, FC#5, port B1

Entries	400
Mean	43.22
Std Dev	12.18

Turn on : 23.7765

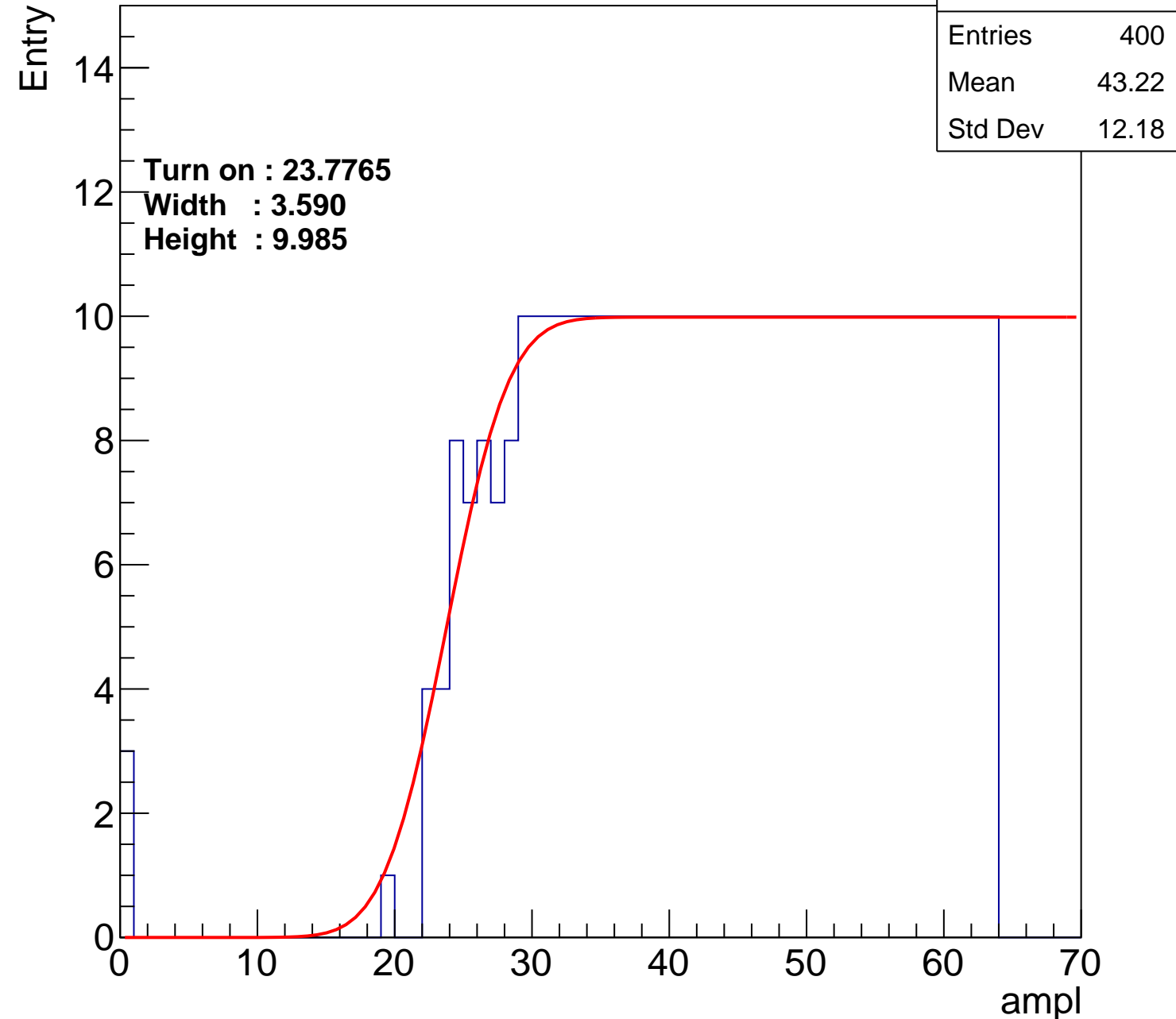
Width : 3.590

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch84

calib_packv5_042523_0143.root, FC#5, port B1

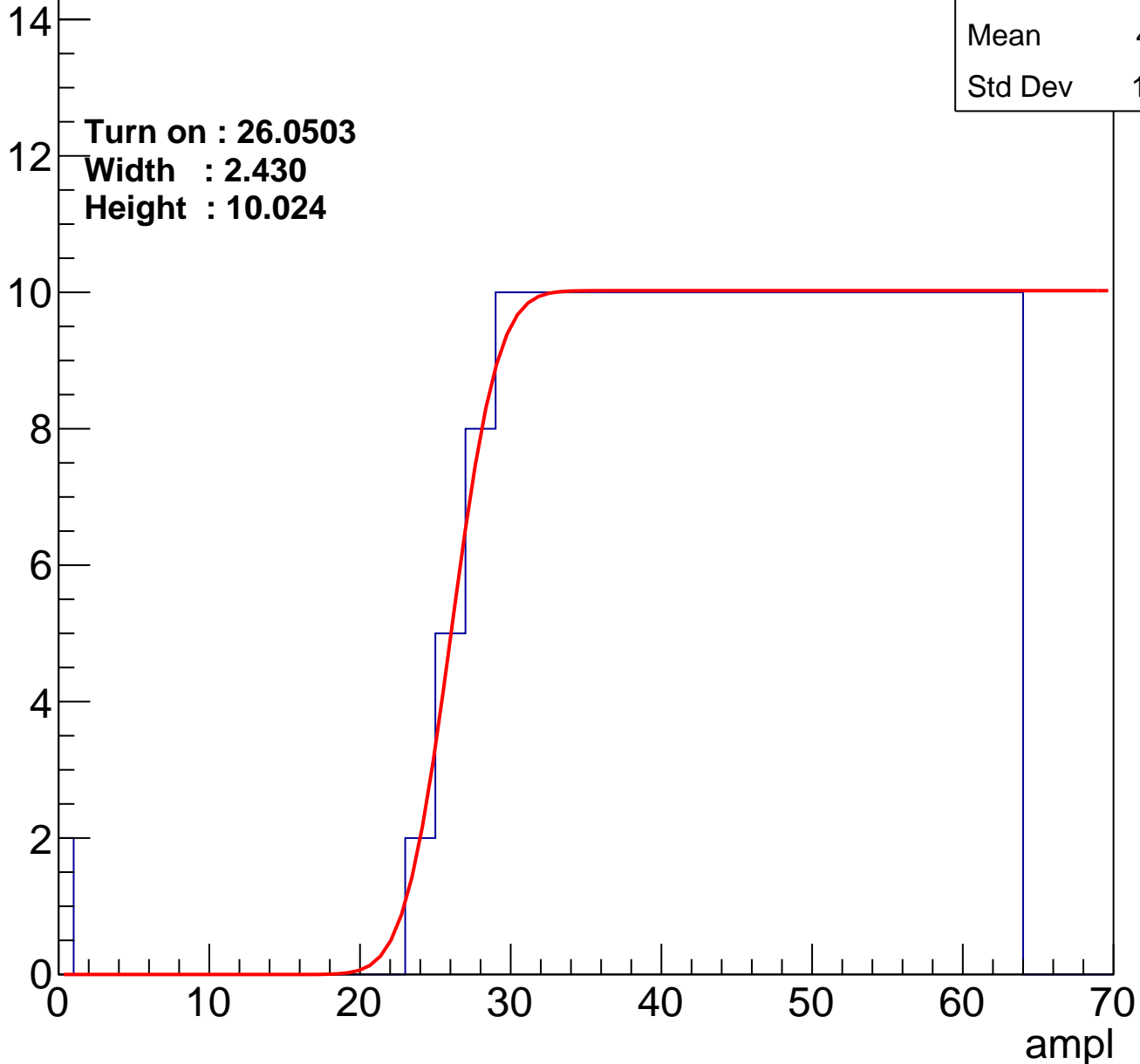
Entries	382
Mean	44.21
Std Dev	11.49

Turn on : 26.0503

Width : 2.430

Height : 10.024

Entry



B0L000S, U22-ch85

calib_packv5_042523_0143.root, FC#5, port B1

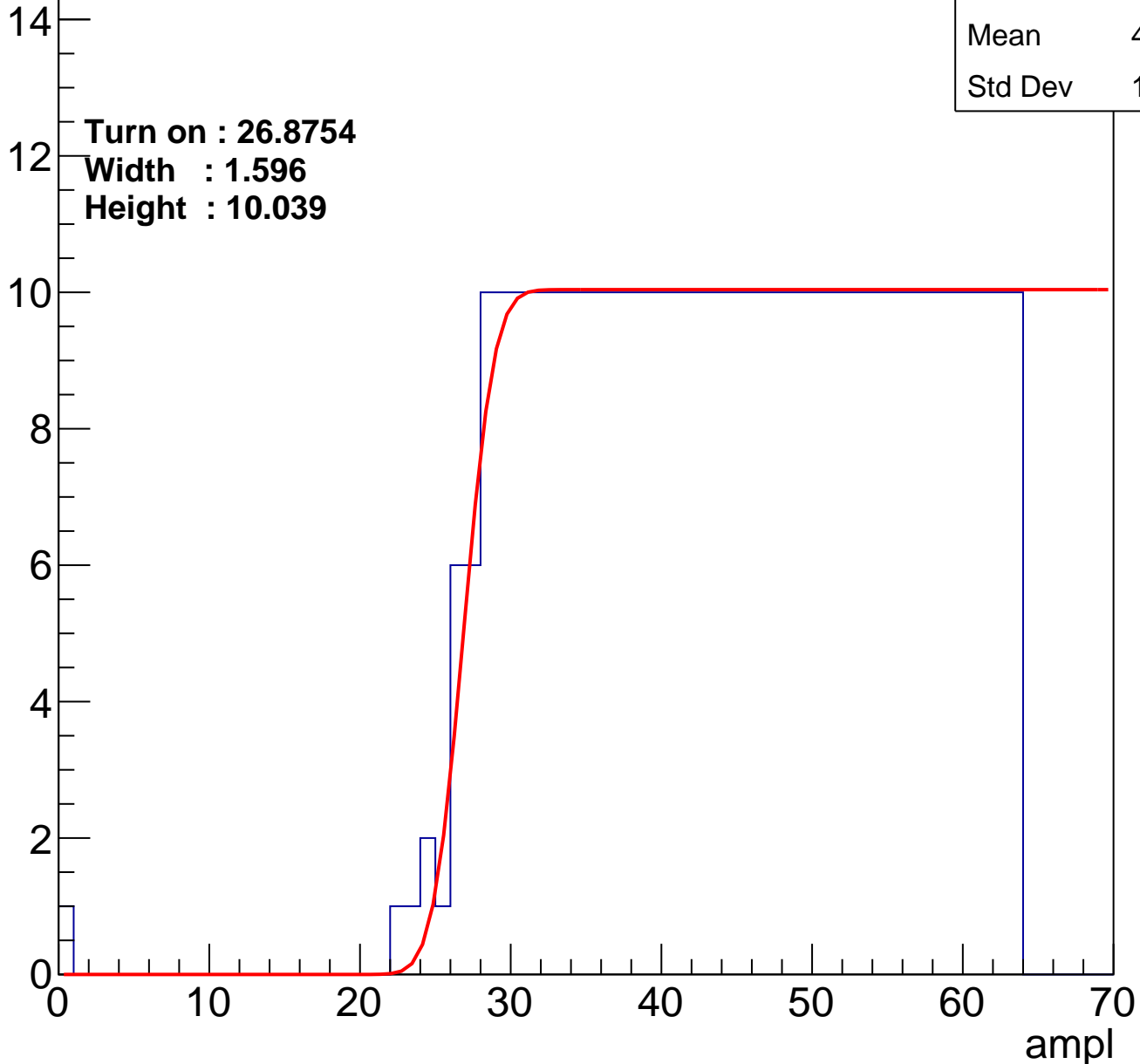
Entries	378
Mean	44.49
Std Dev	11.18

Turn on : 26.8754

Width : 1.596

Height : 10.039

Entry



B0L000S, U22-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	393
Mean	43.69
Std Dev	11.68

Turn on : 24.8397

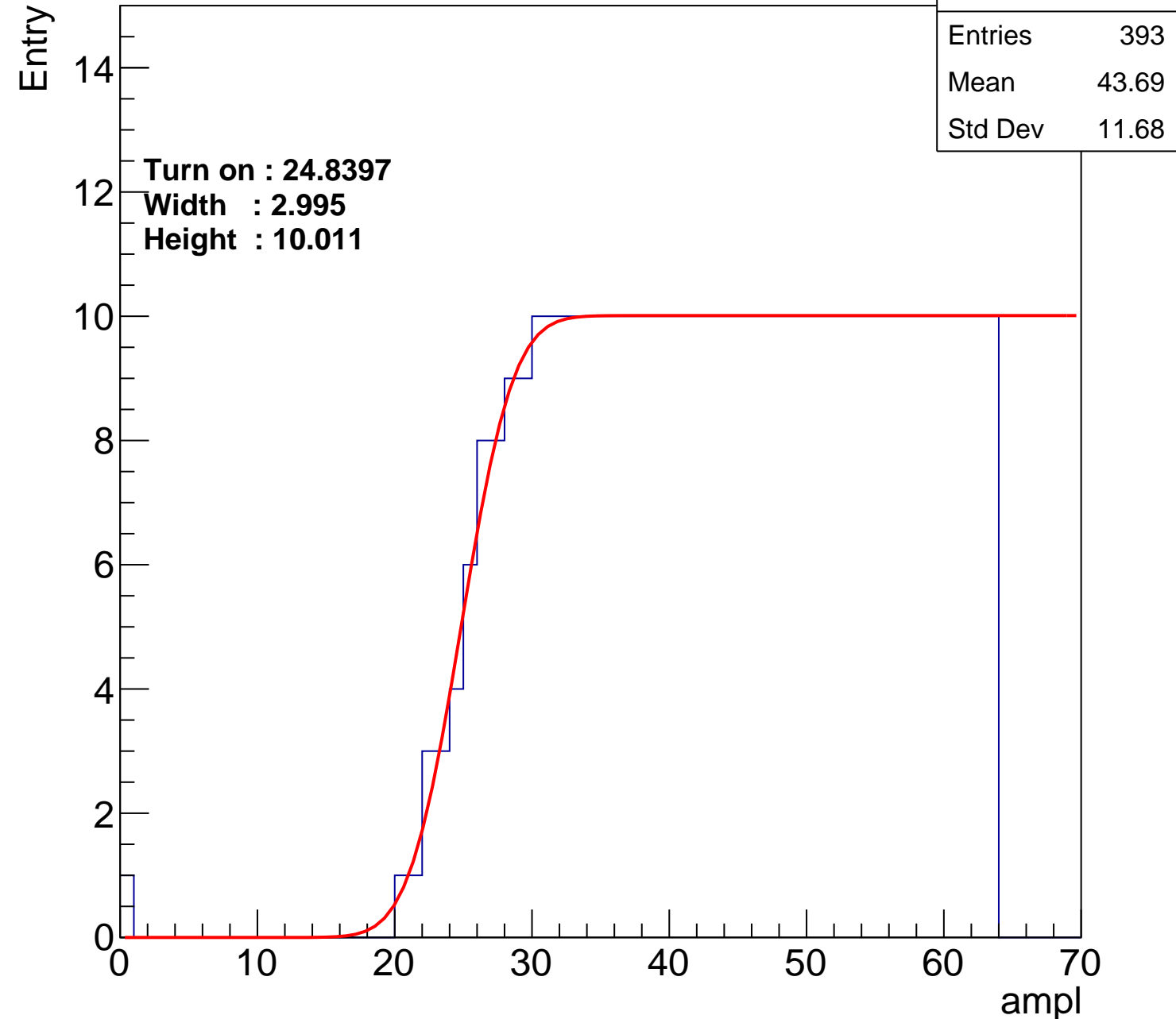
Width : 2.995

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch87

calib_packv5_042523_0143.root, FC#5, port B1

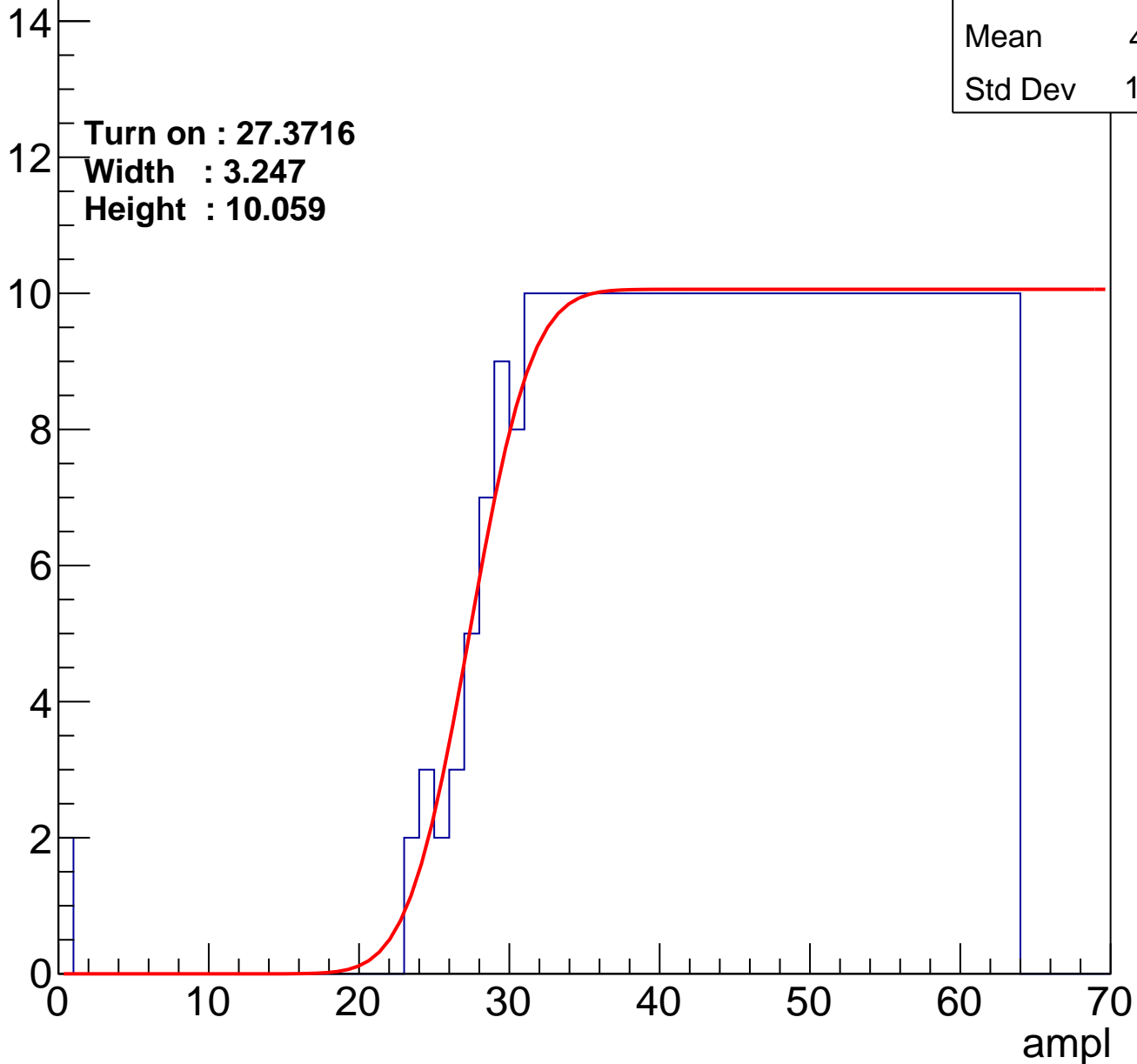
Entries	371
Mean	44.71
Std Dev	11.28

Turn on : 27.3716

Width : 3.247

Height : 10.059

Entry



B0L000S, U22-ch88

calib_packv5_042523_0143.root, FC#5, port B1

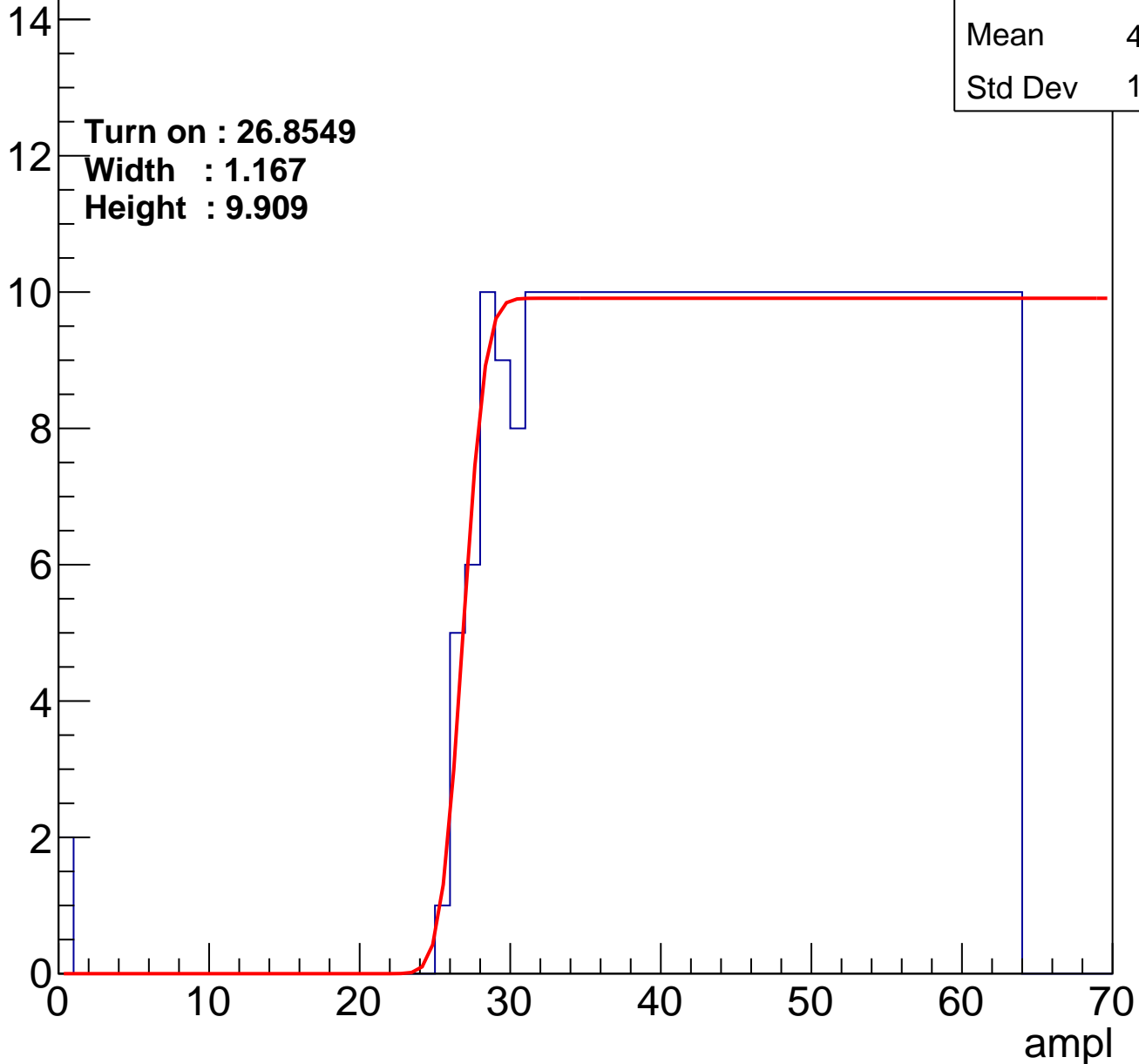
Entries	371
Mean	44.77
Std Dev	11.18

Turn on : 26.8549

Width : 1.167

Height : 9.909

Entry



B0L000S, U22-ch89

calib_packv5_042523_0143.root, FC#5, port B1

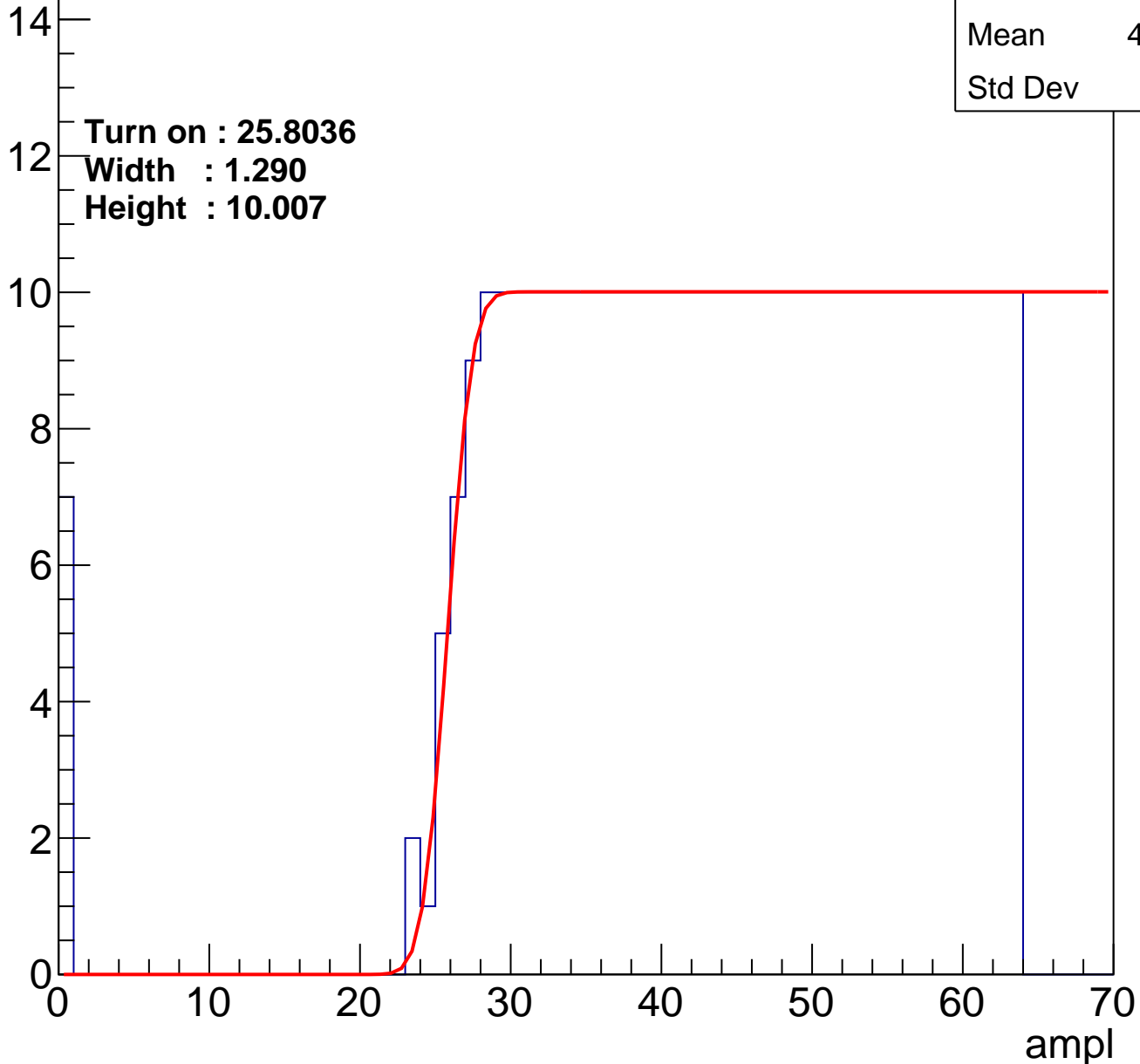
Entries	391
Mean	43.48
Std Dev	12.5

Turn on : 25.8036

Width : 1.290

Height : 10.007

Entry



B0L000S, U22-ch90

calib_packv5_042523_0143.root, FC#5, port B1

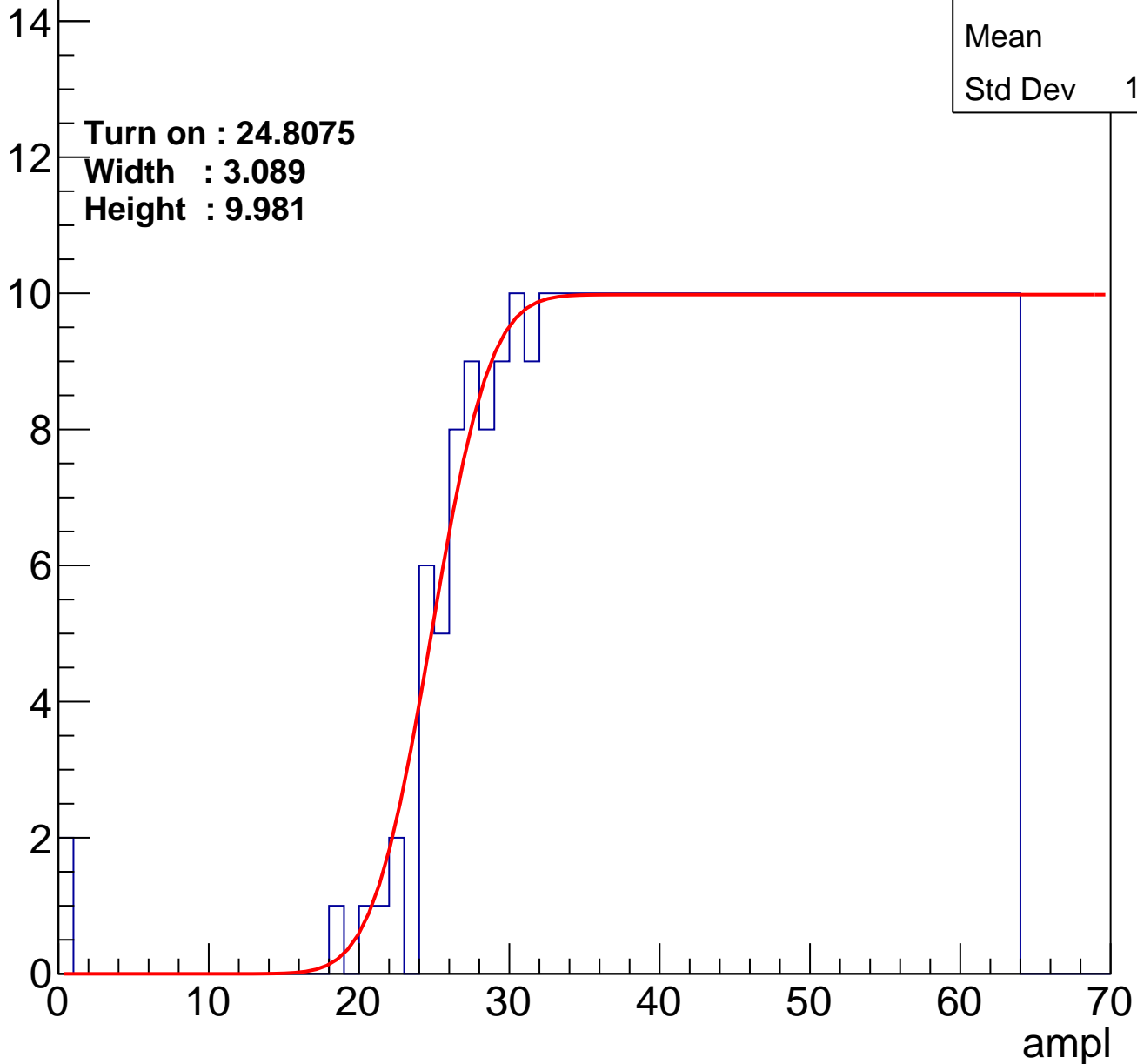
Entries	391
Mean	43.7
Std Dev	11.83

Turn on : 24.8075

Width : 3.089

Height : 9.981

Entry



B0L000S, U22-ch91

calib_packv5_042523_0143.root, FC#5, port B1

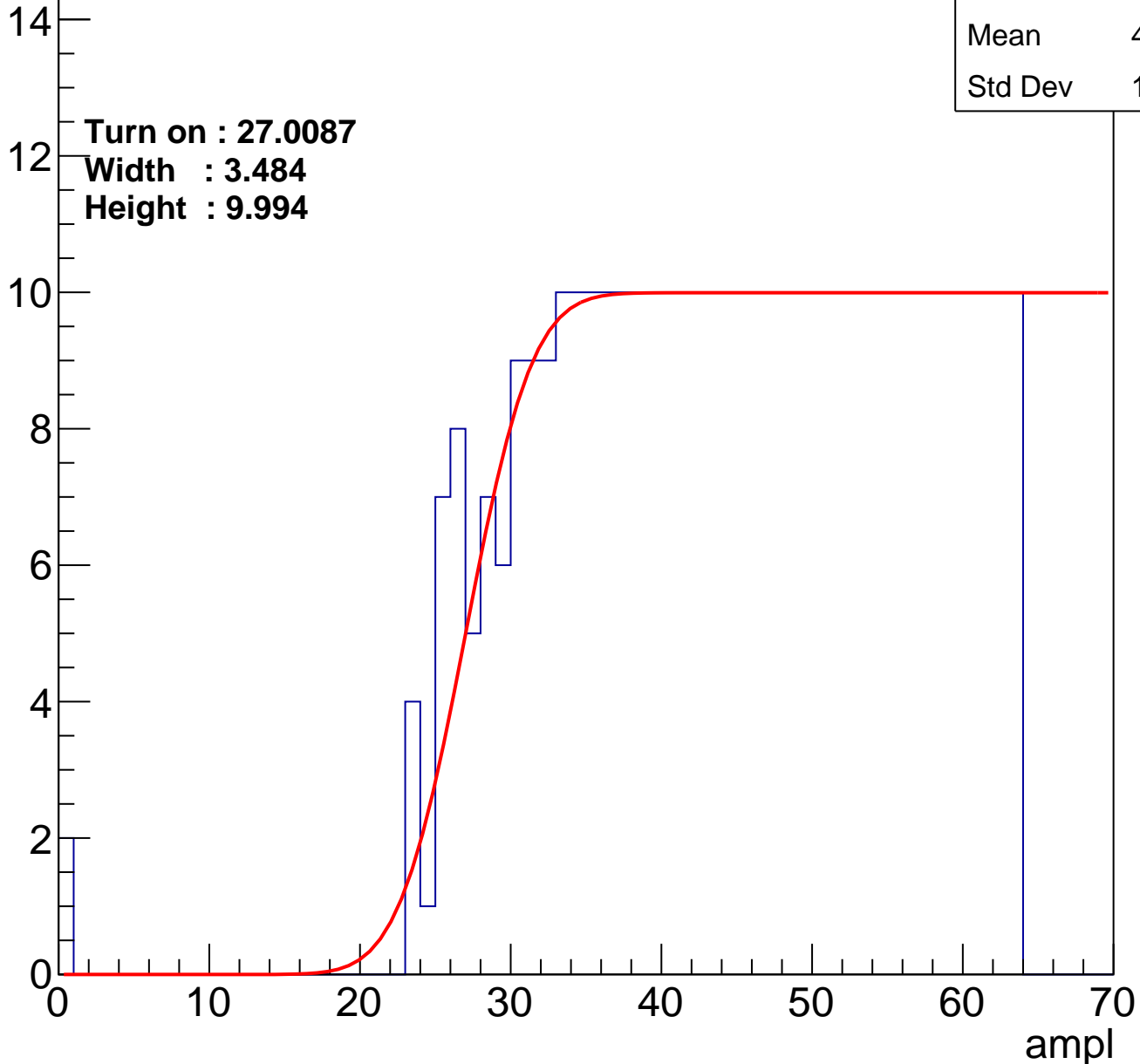
Entries	377
Mean	44.35
Std Dev	11.52

Turn on : 27.0087

Width : 3.484

Height : 9.994

Entry



B0L000S, U22-ch92

calib_packv5_042523_0143.root, FC#5, port B1

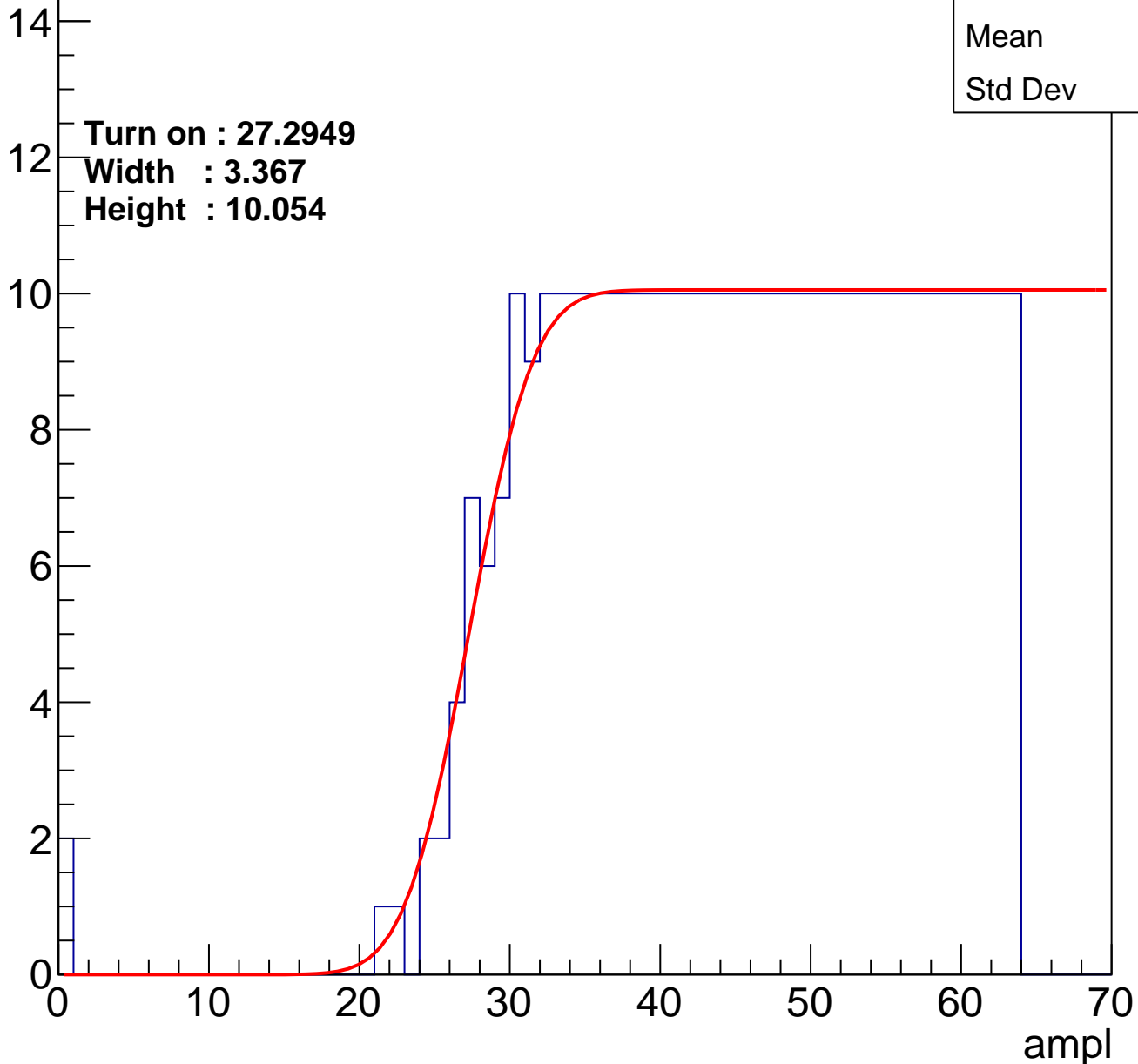
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.2949

Width : 3.367

Height : 10.054

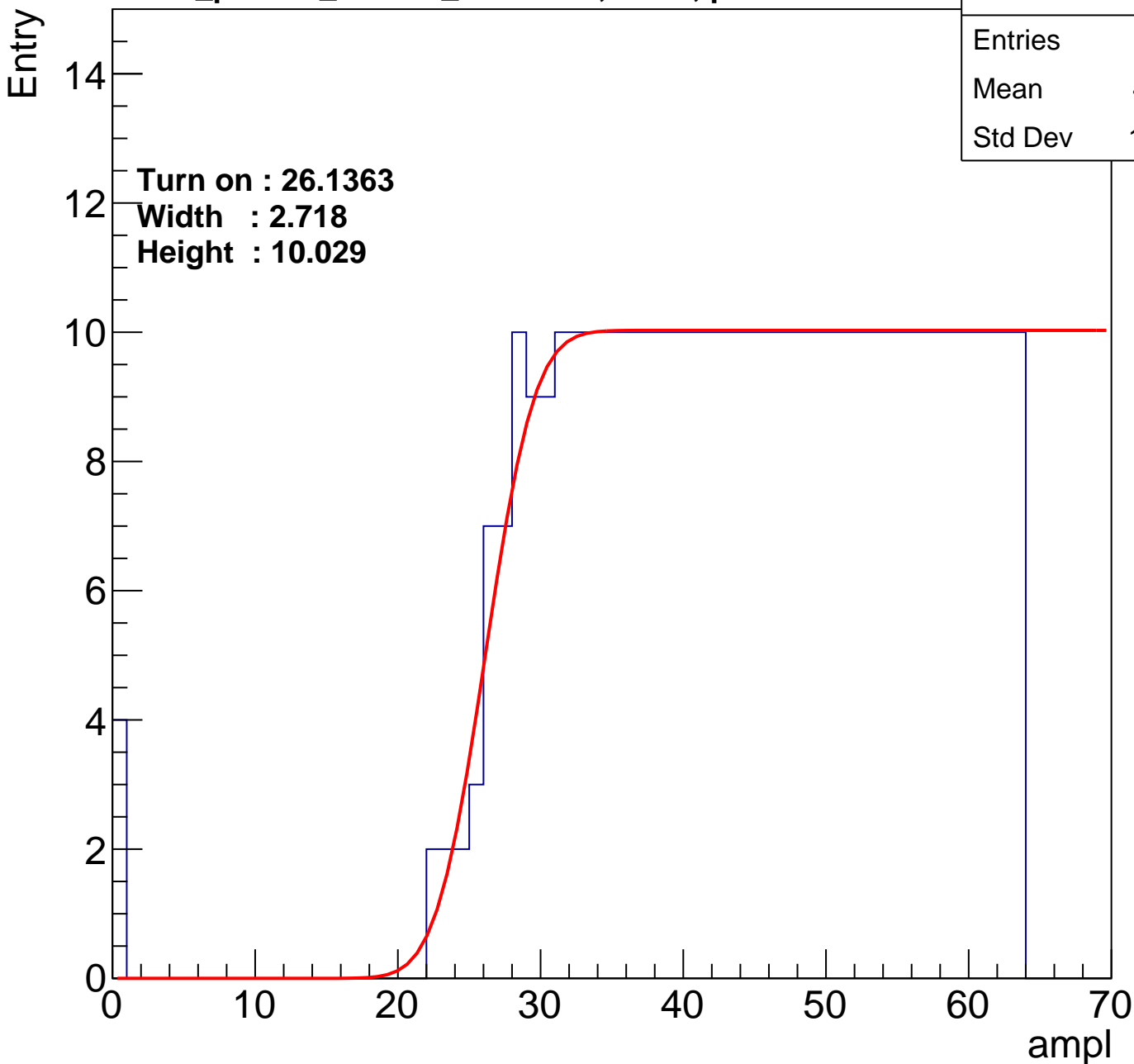
Entry



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 26.1363
Width : 2.718
Height : 10.029



B0L000S, U22-ch94

calib_packv5_042523_0143.root, FC#5, port B1

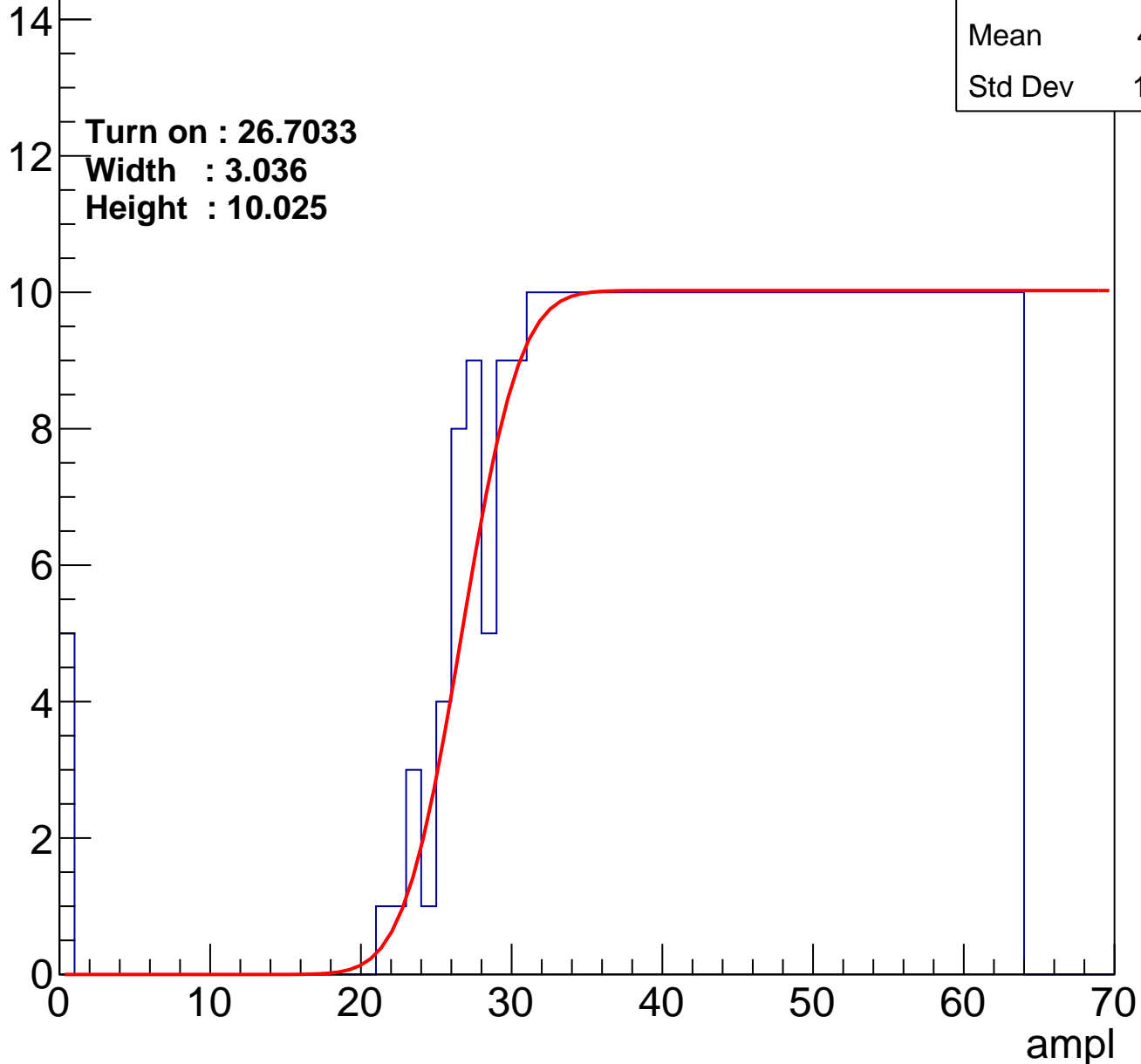
Entries	385
Mean	43.81
Std Dev	12.17

Turn on : 26.7033

Width : 3.036

Height : 10.025

Entry



B0L000S, U22-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.77
Std Dev	12.02

Turn on : 25.9386

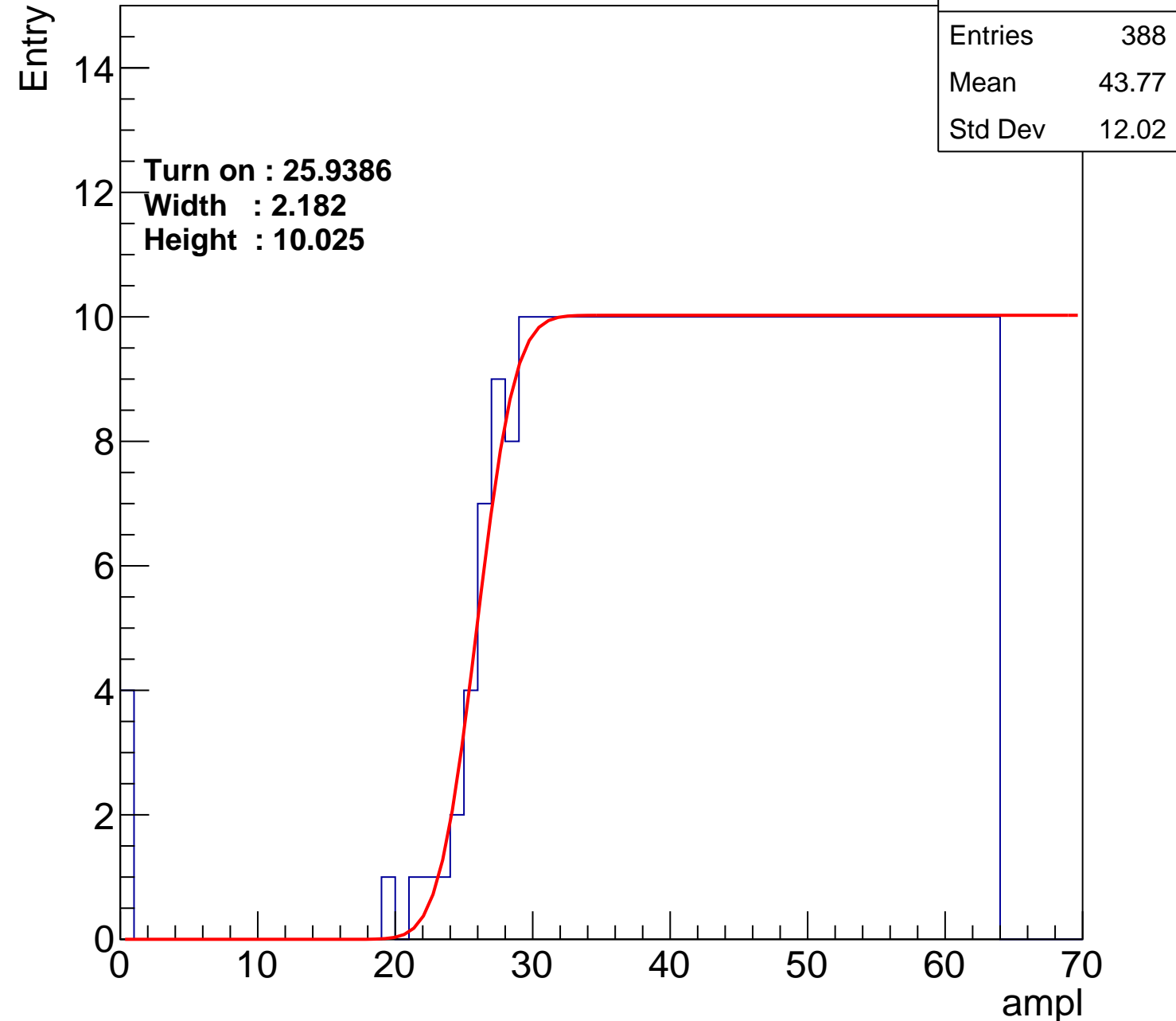
Width : 2.182

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.24
Std Dev	11.8

Turn on : 26.5346

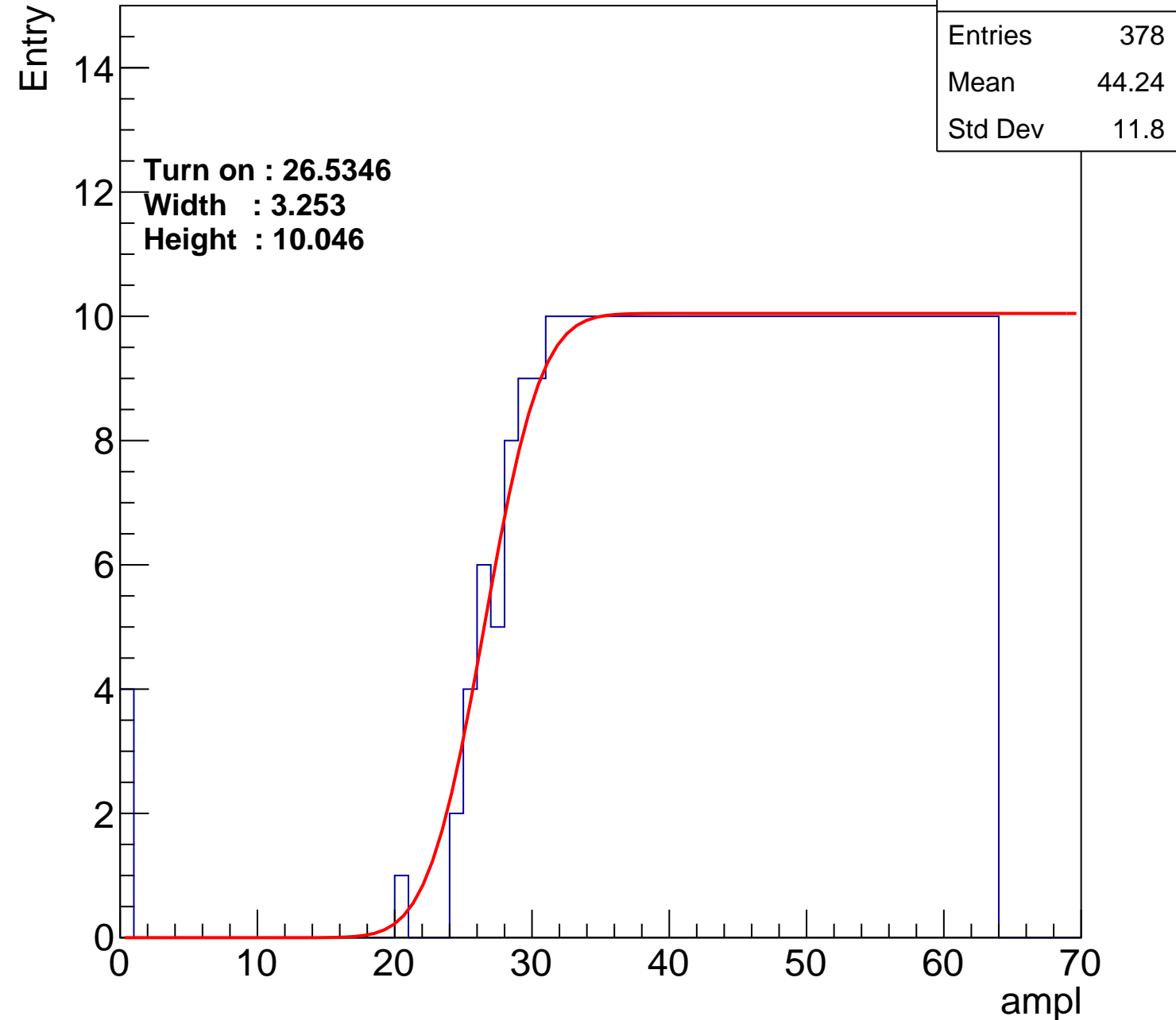
Width : 3.253

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.9797

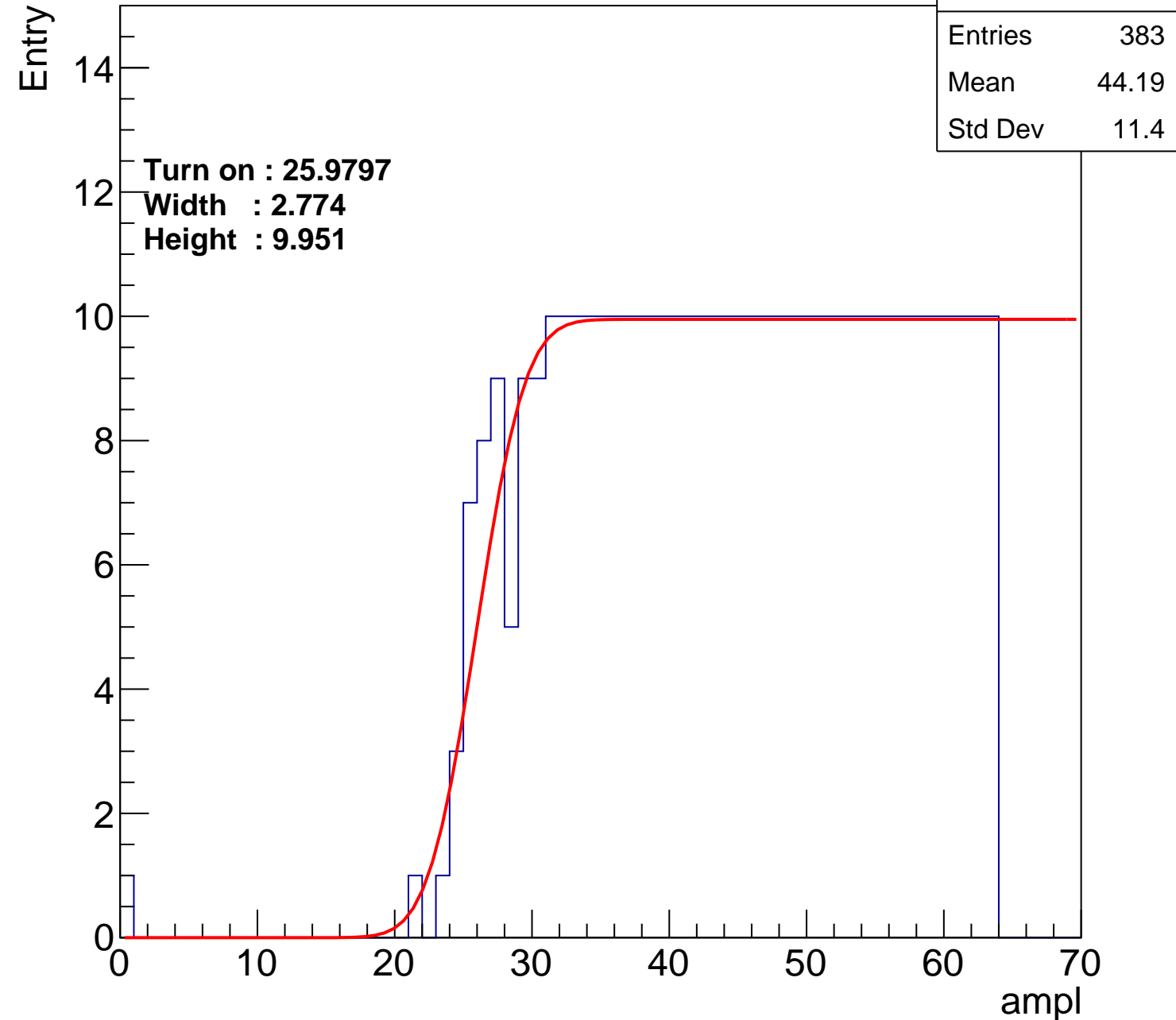
Width : 2.774

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch98

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	43.96
Std Dev	12.22

Turn on : 26.2156

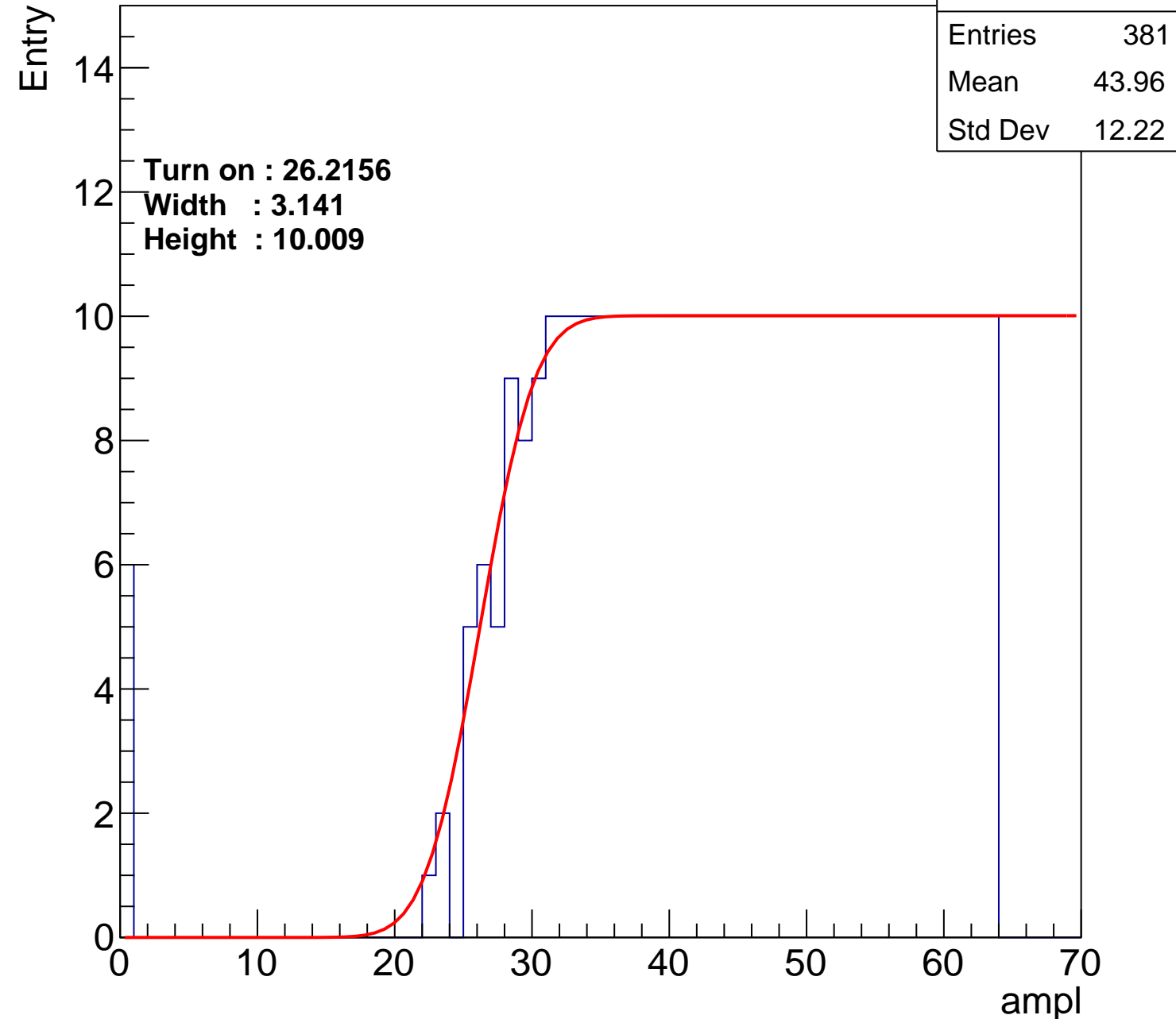
Width : 3.141

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch99

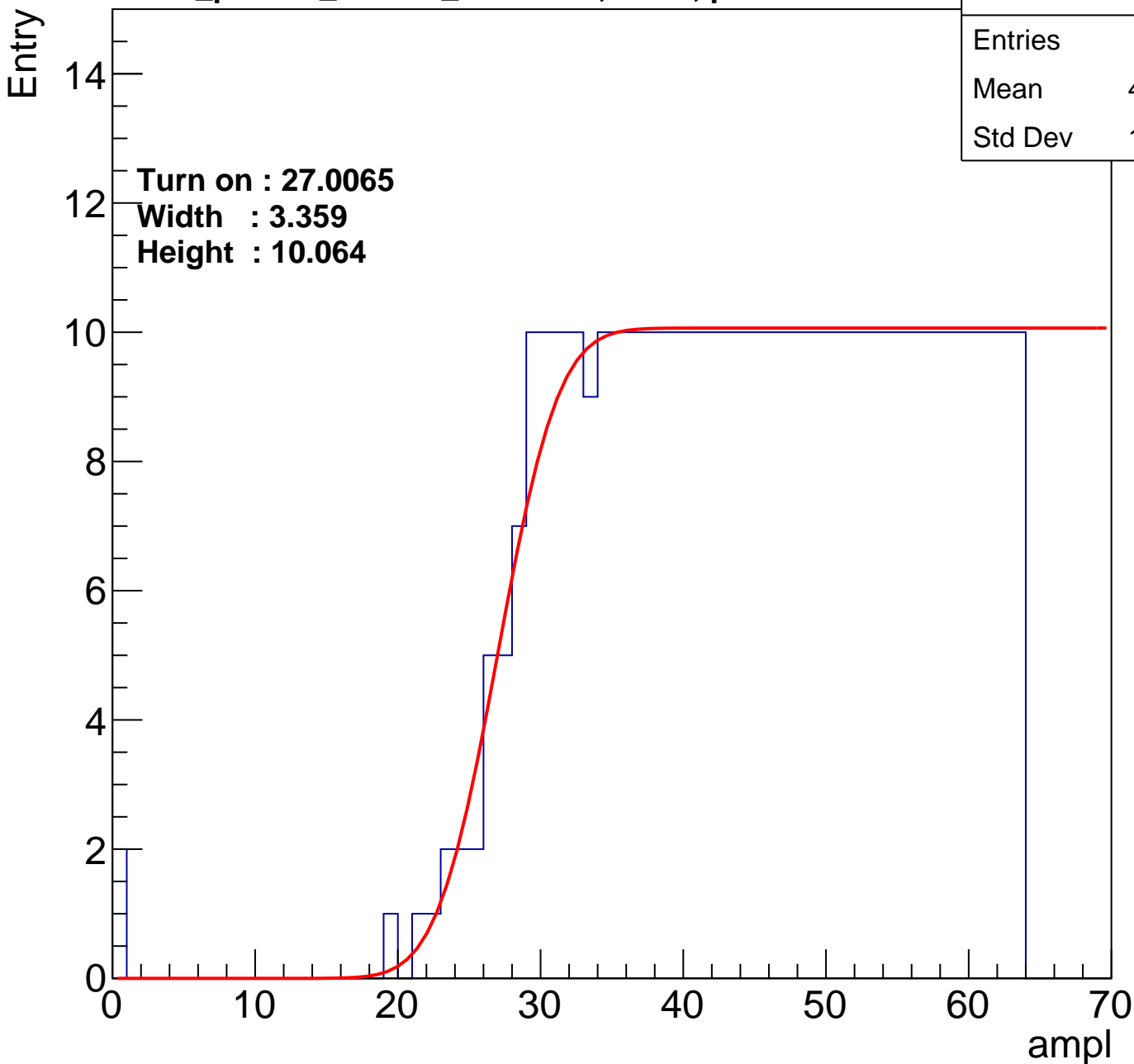
calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.39
Std Dev	11.48

Turn on : 27.0065

Width : 3.359

Height : 10.064



B0L000S, U22-ch100

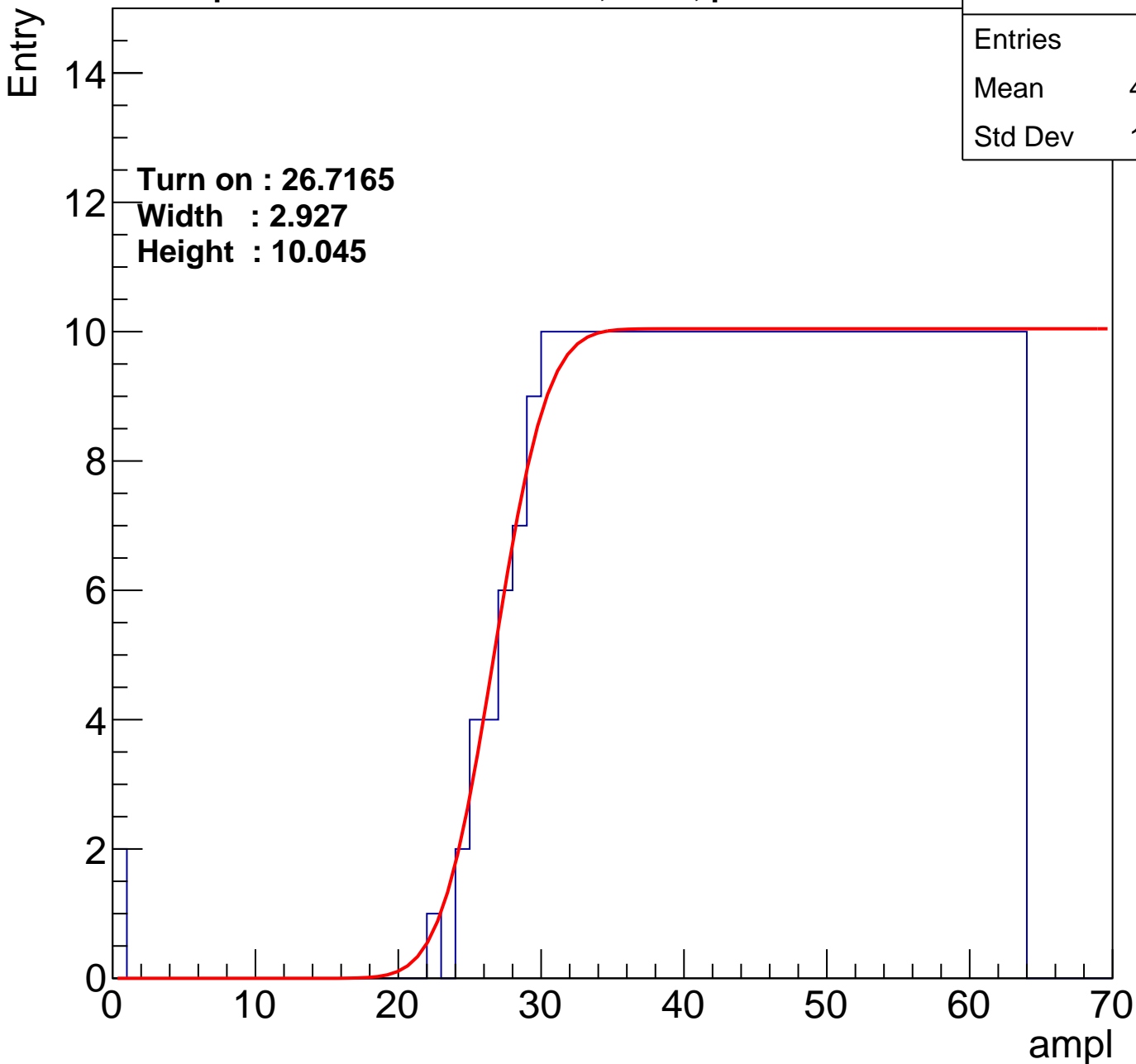
calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 26.7165

Width : 2.927

Height : 10.045



B0L000S, U22-ch101

calib_packv5_042523_0143.root, FC#5, port B1

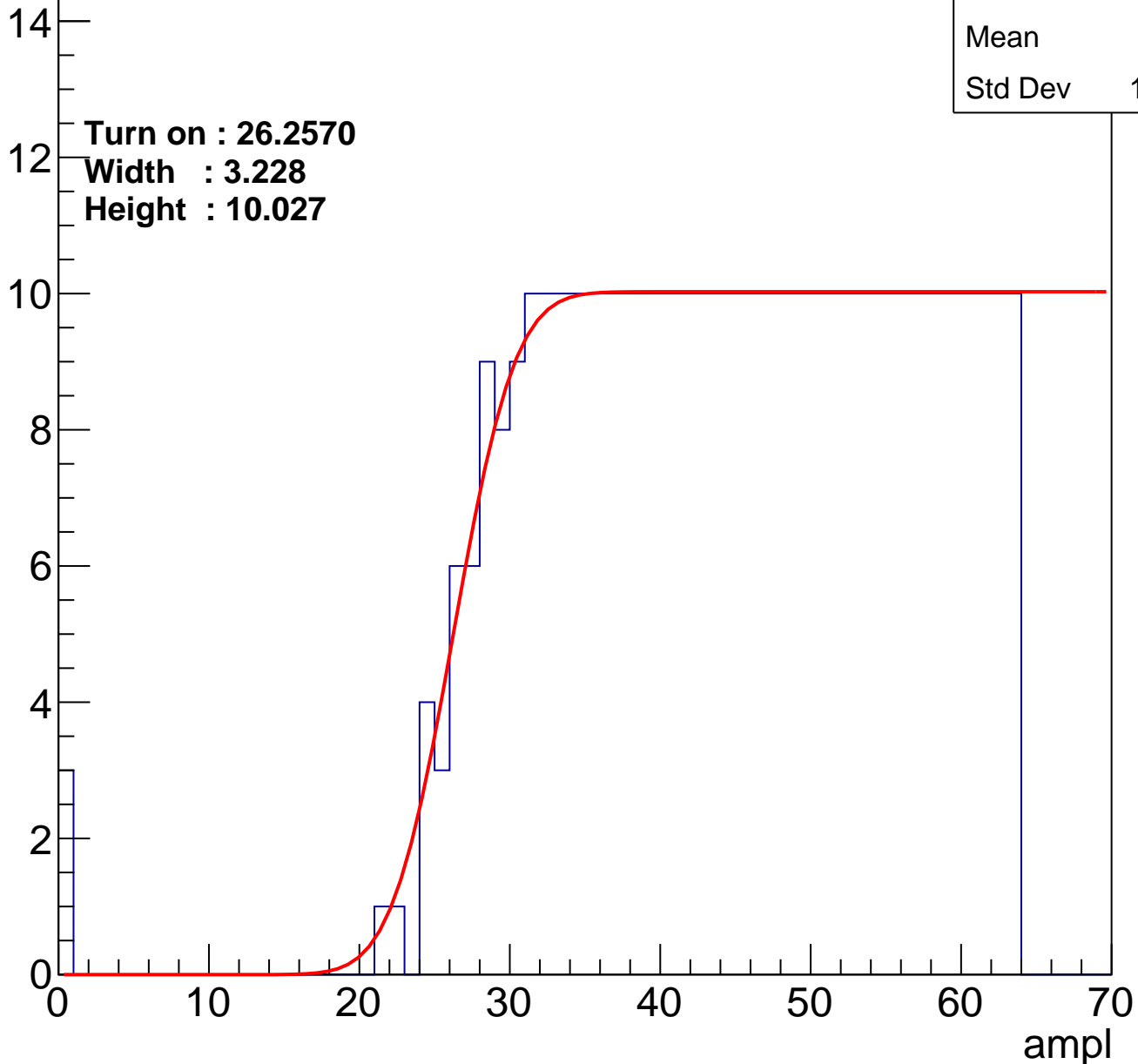
Entries	380
Mean	44.2
Std Dev	11.69

Turn on : 26.2570

Width : 3.228

Height : 10.027

Entry



B0L000S, U22-ch102

calib_packv5_042523_0143.root, FC#5, port B1

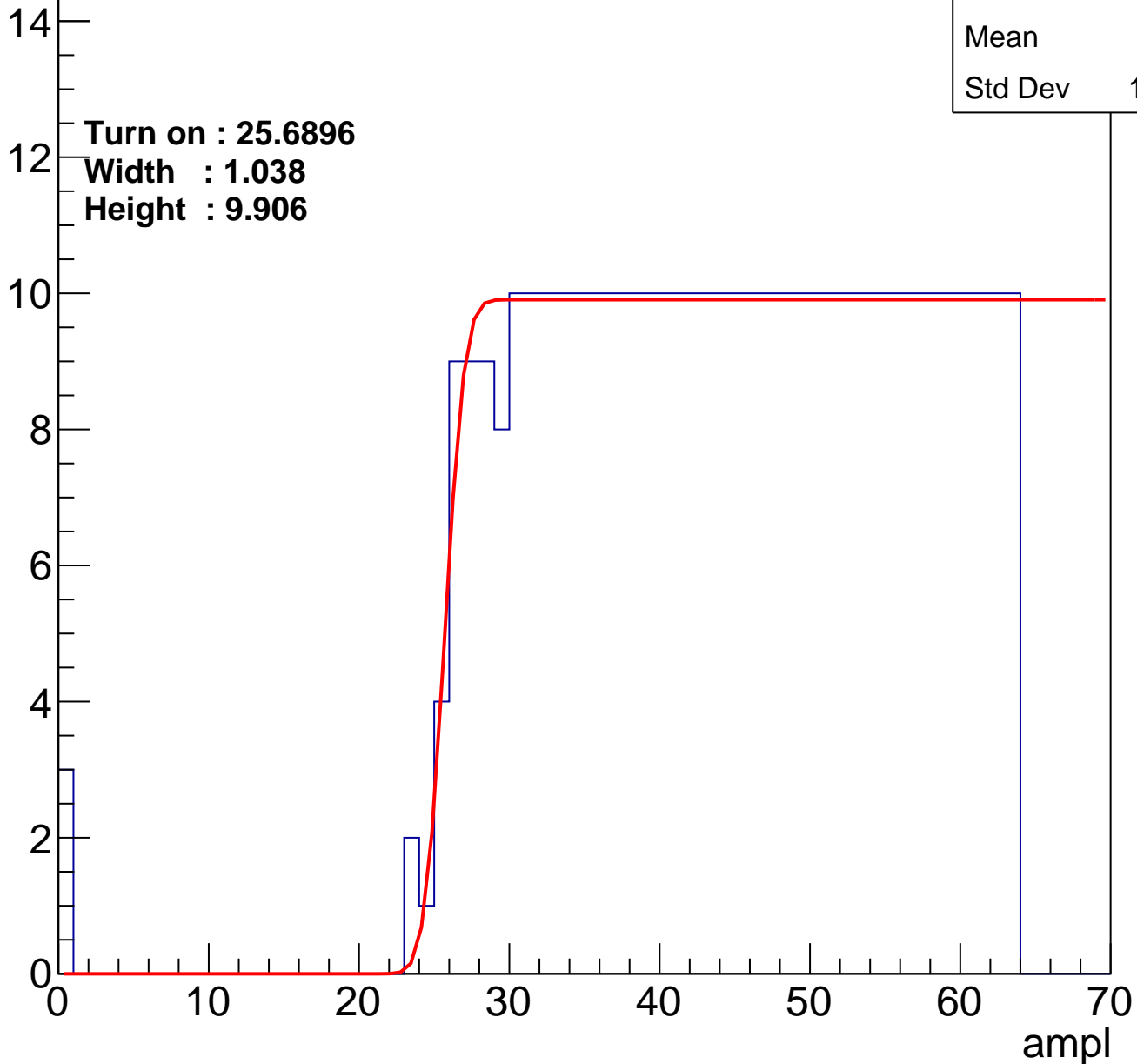
Entries	385
Mean	44
Std Dev	11.73

Turn on : 25.6896

Width : 1.038

Height : 9.906

Entry



B0L000S, U22-ch103

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.42
Std Dev	11.74

Turn on : 26.9417

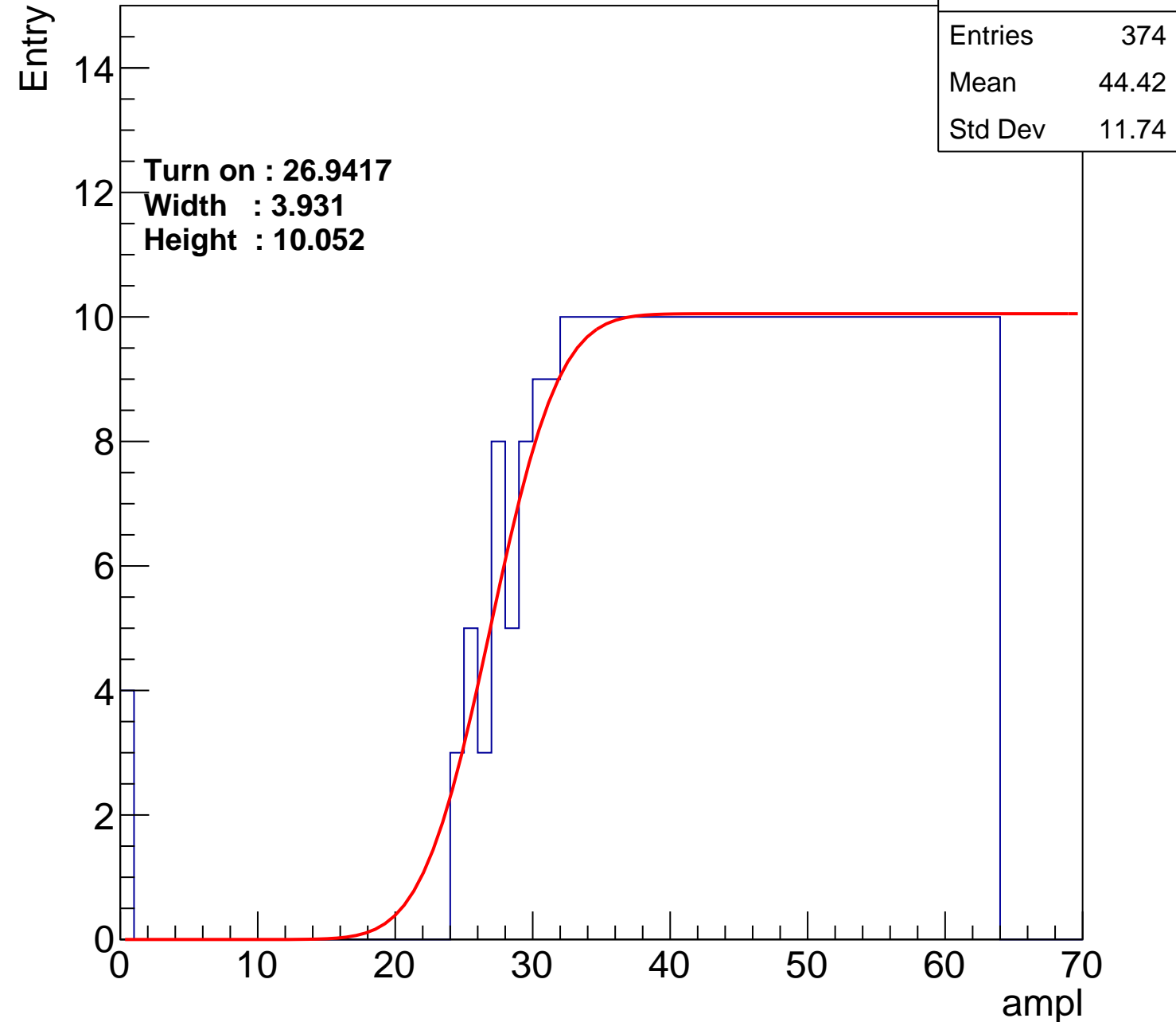
Width : 3.931

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 24.8660

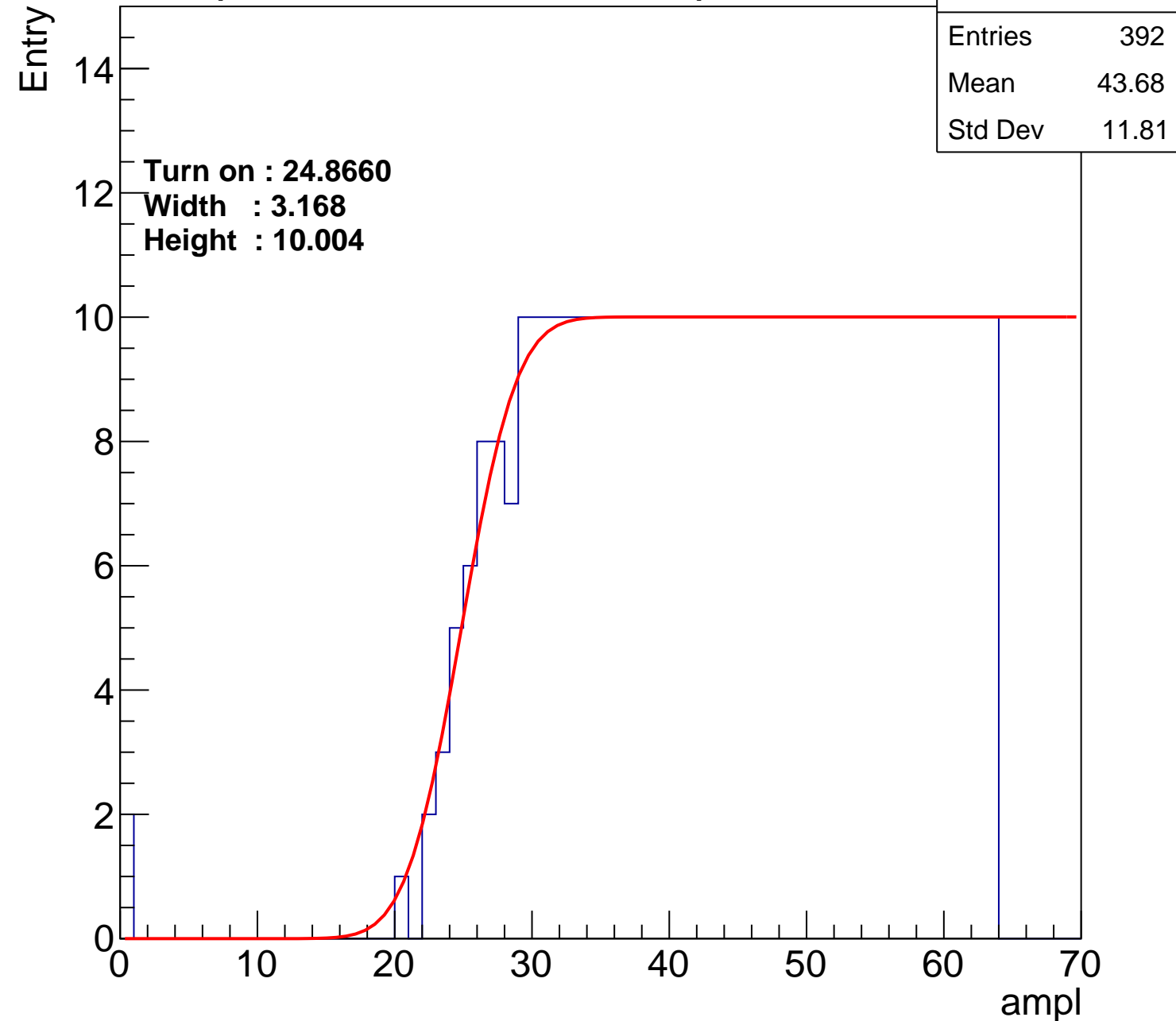
Width : 3.168

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.6
Std Dev	11.48

Turn on : 27.0085

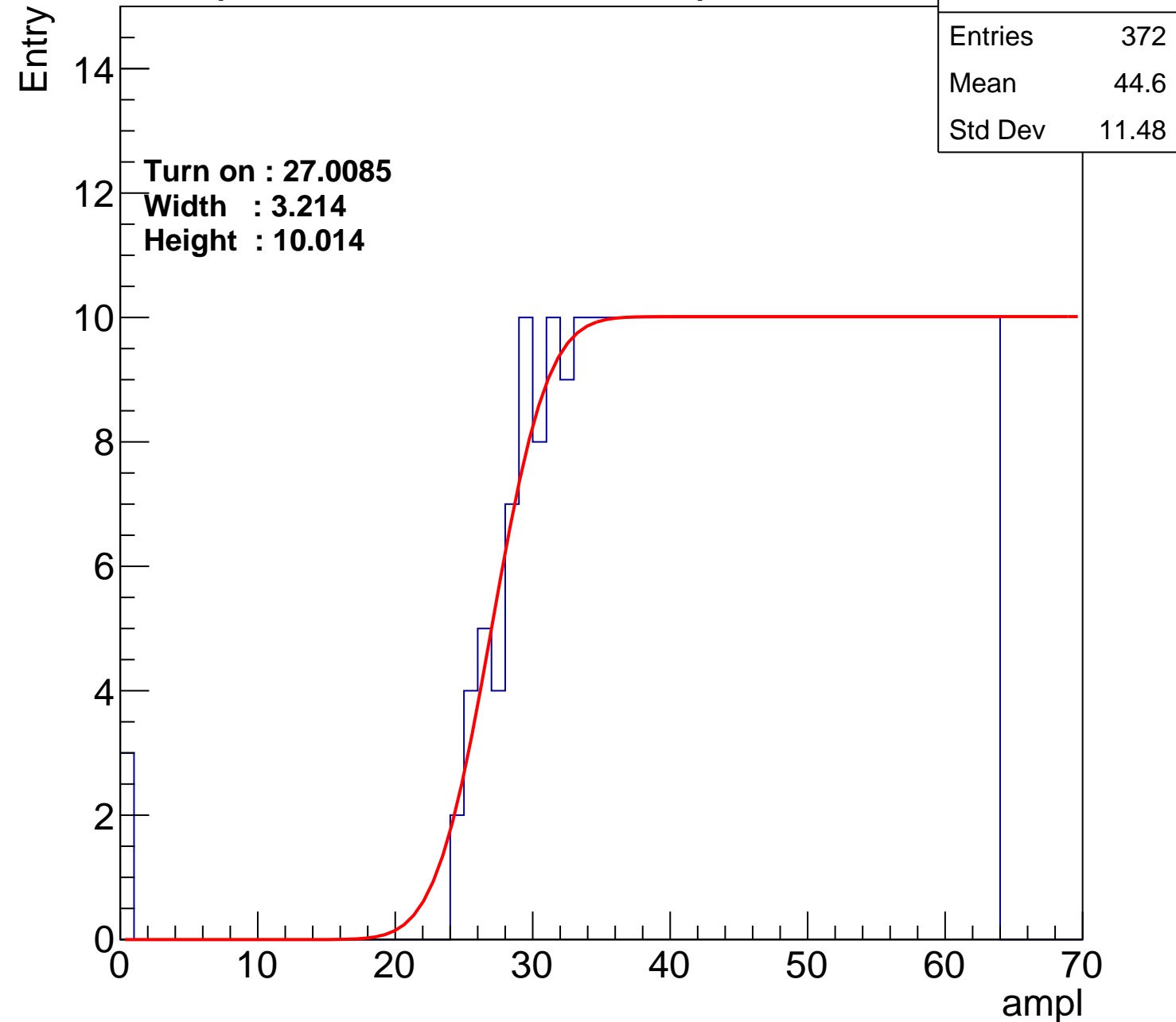
Width : 3.214

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.61
Std Dev	11.34

Turn on : 27.3881

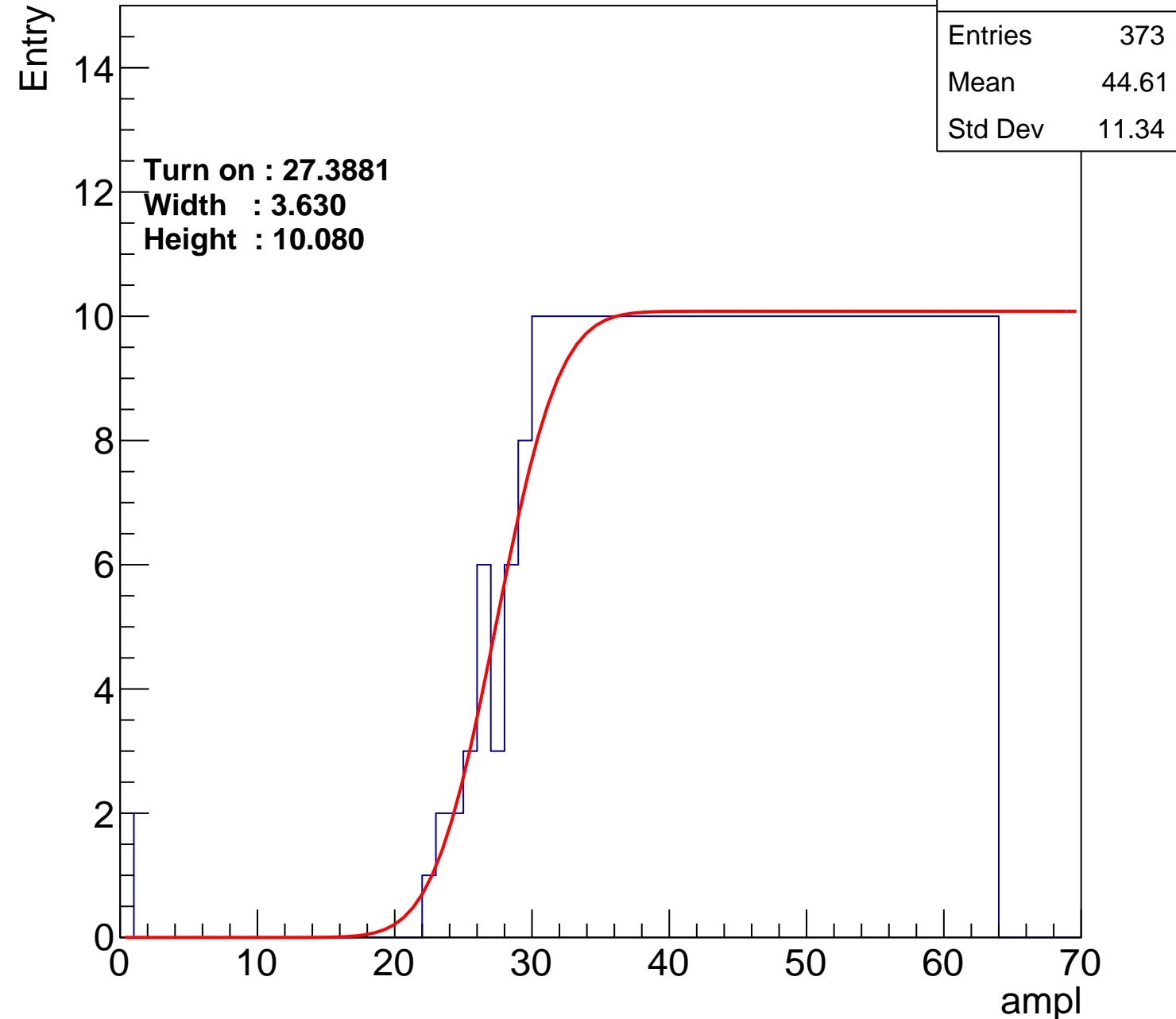
Width : 3.630

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch107

calib_packv5_042523_0143.root, FC#5, port B1

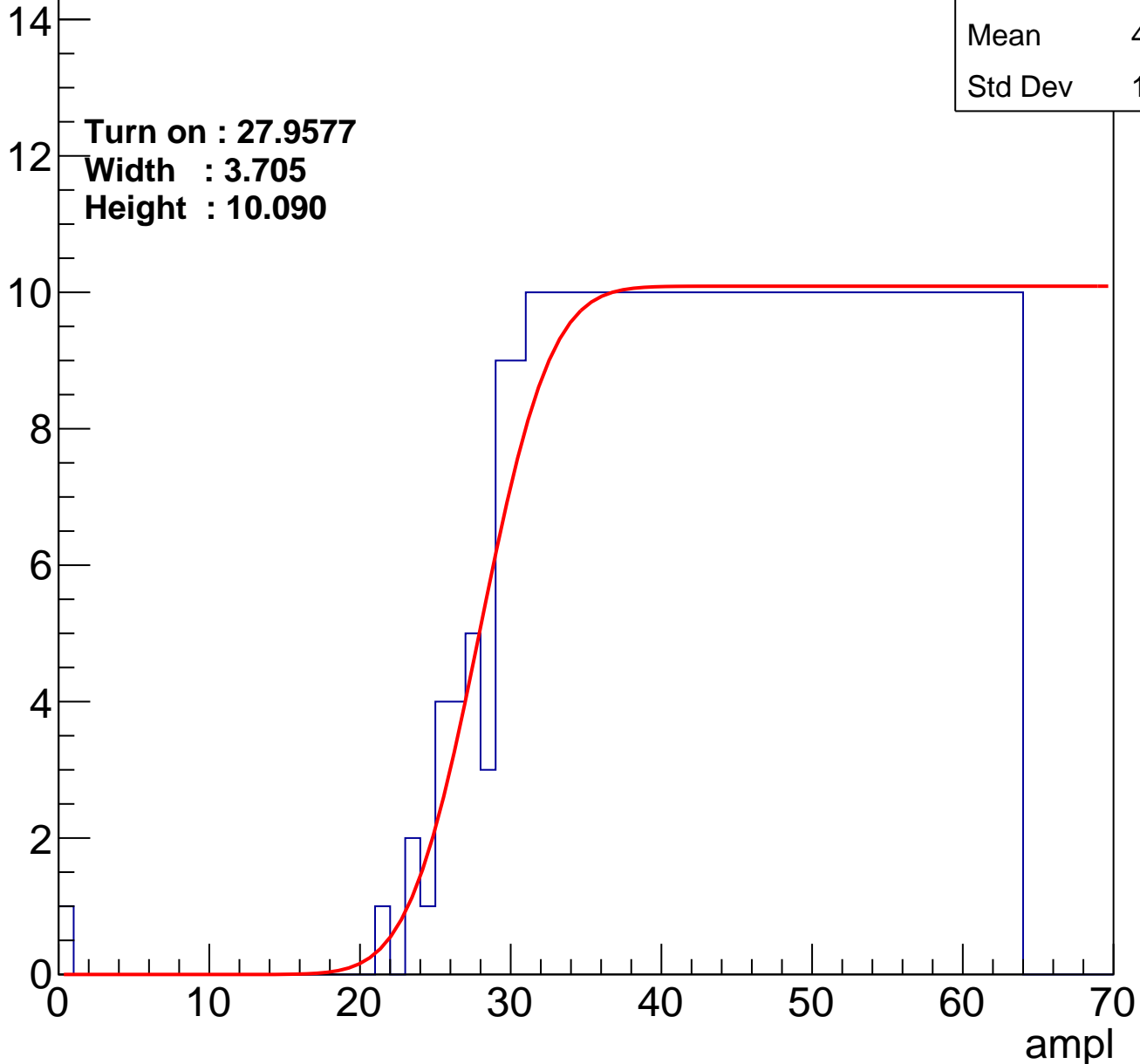
Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 27.9577

Width : 3.705

Height : 10.090

Entry



B0L000S, U22-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 26.1514

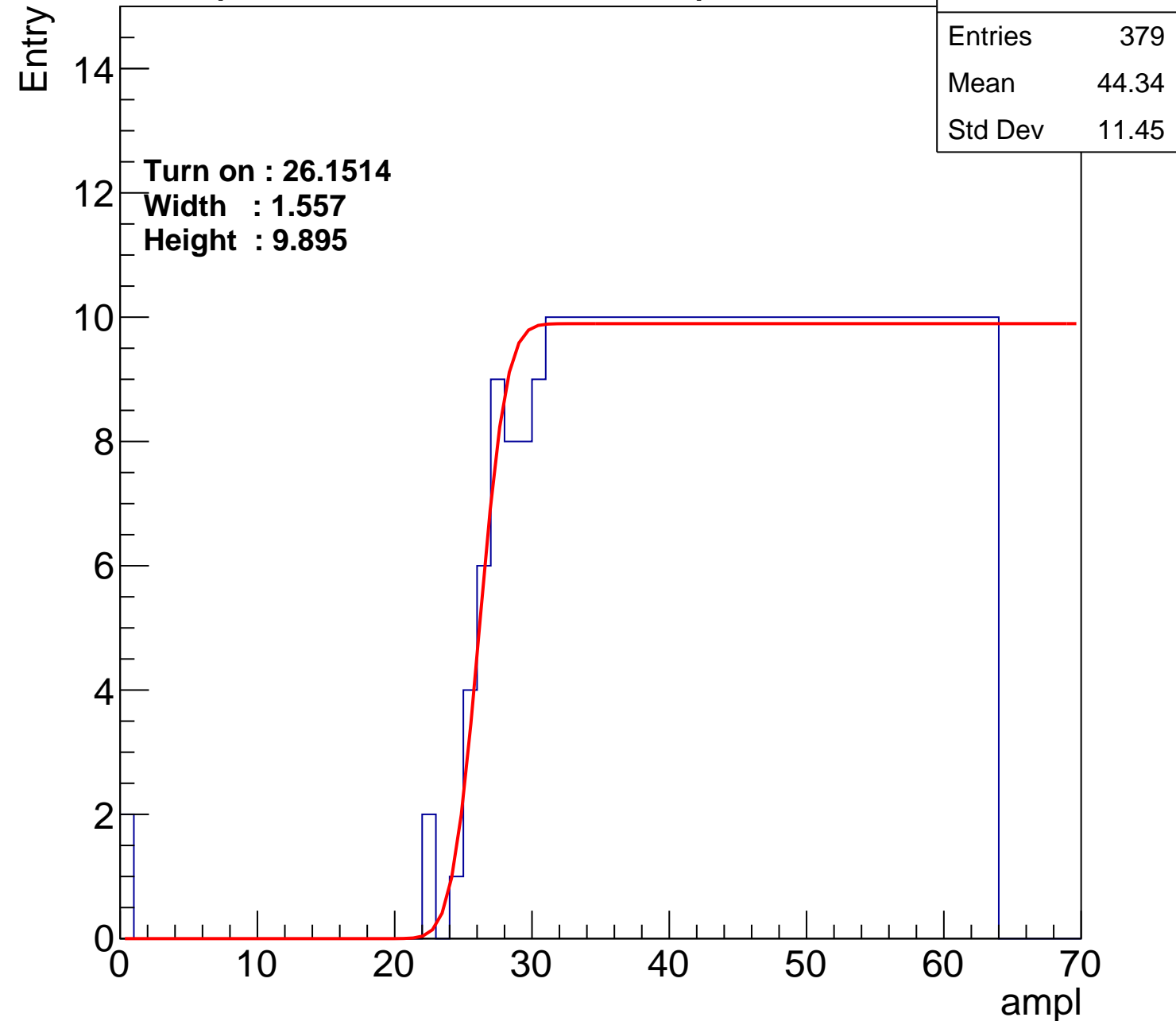
Width : 1.557

Height : 9.895

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch109

calib_packv5_042523_0143.root, FC#5, port B1

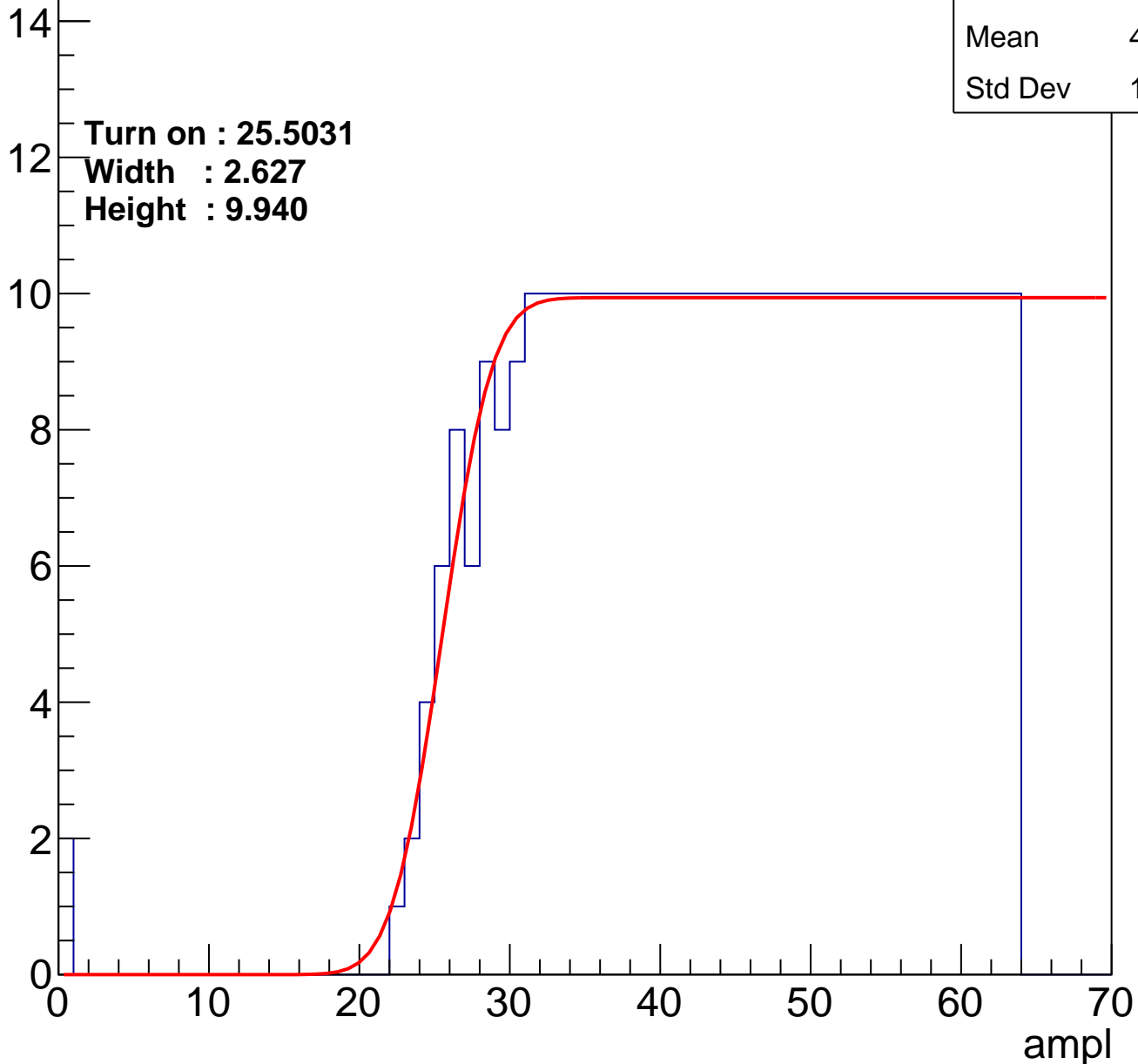
Entries	385
Mean	44.02
Std Dev	11.63

Turn on : 25.5031

Width : 2.627

Height : 9.940

Entry



B0L000S, U22-ch110

calib_packv5_042523_0143.root, FC#5, port B1

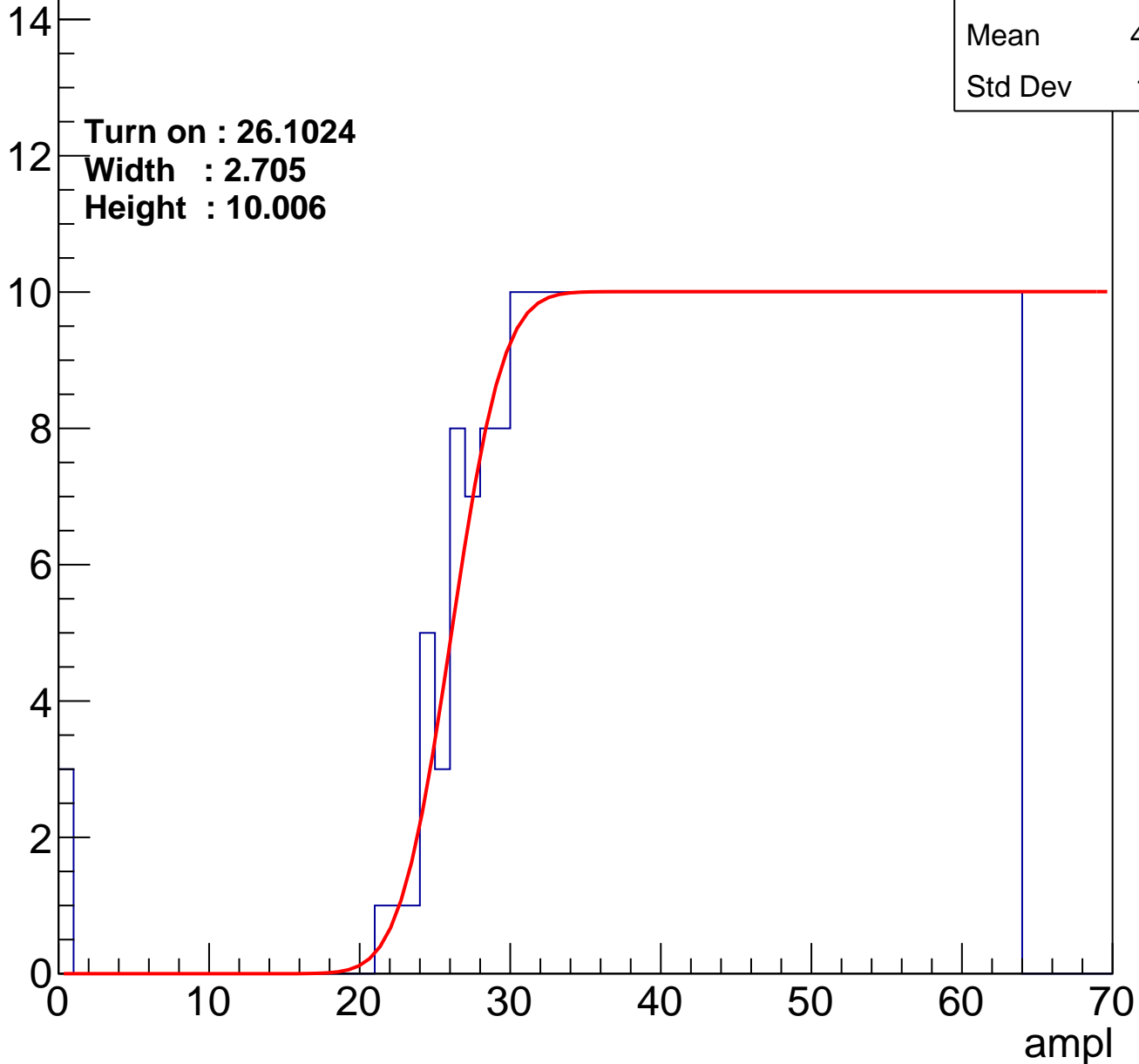
Entries	385
Mean	43.96
Std Dev	11.81

Turn on : 26.1024

Width : 2.705

Height : 10.006

Entry



B0L000S, U22-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.73
Std Dev	11.3

Turn on : 27.4482

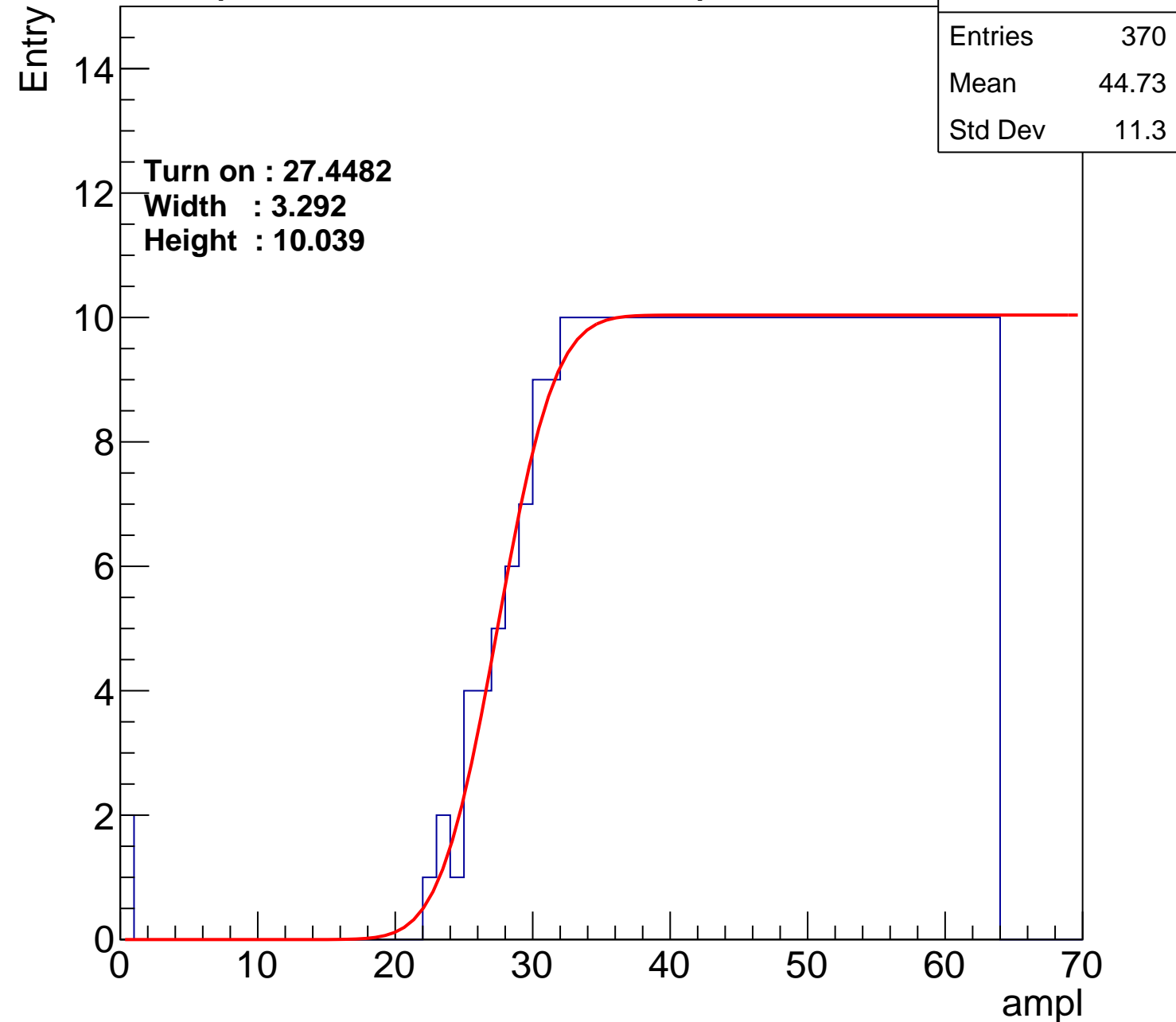
Width : 3.292

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch112

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.9568

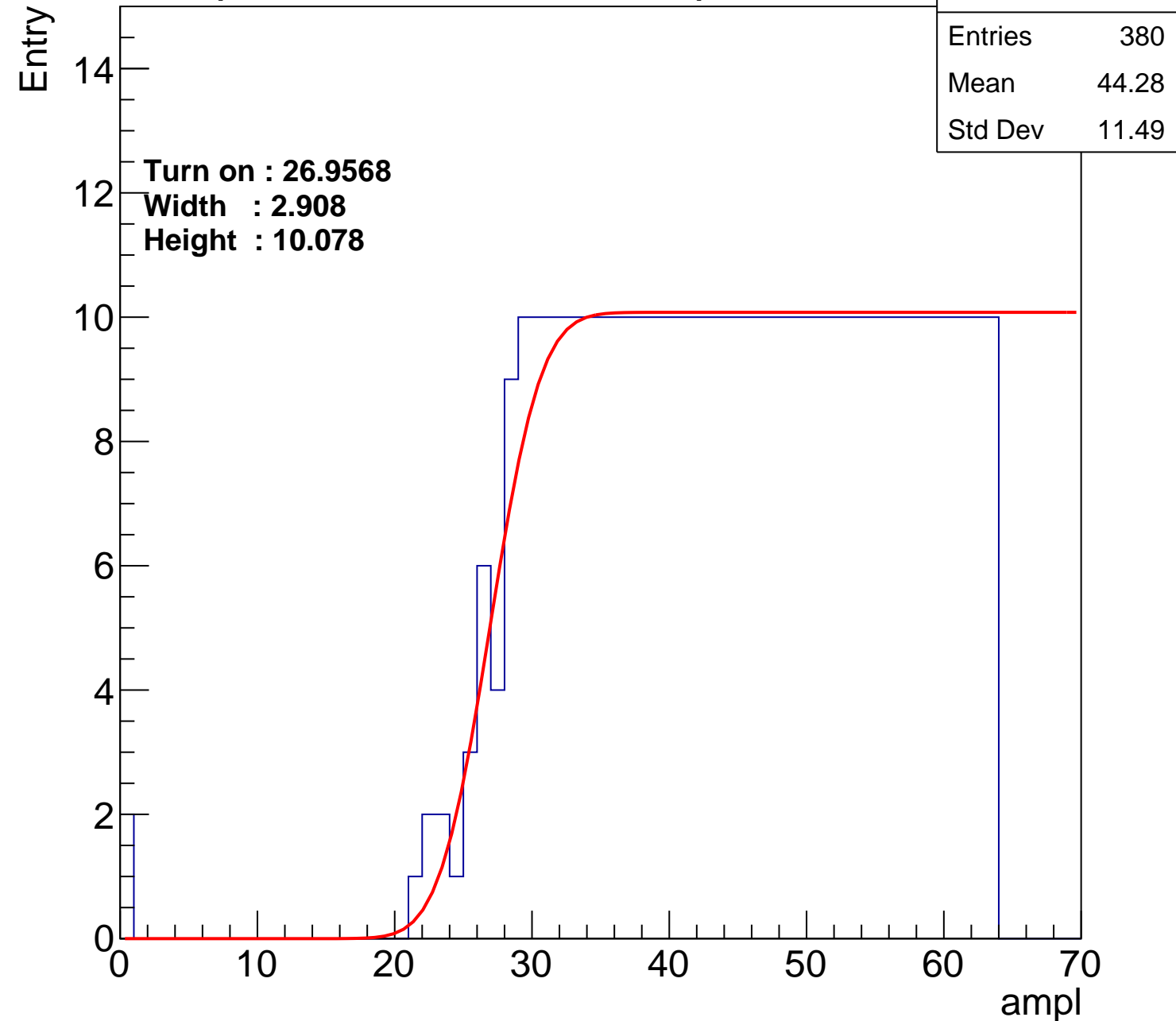
Width : 2.908

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.42
Std Dev	11.56

Turn on : 27.1188

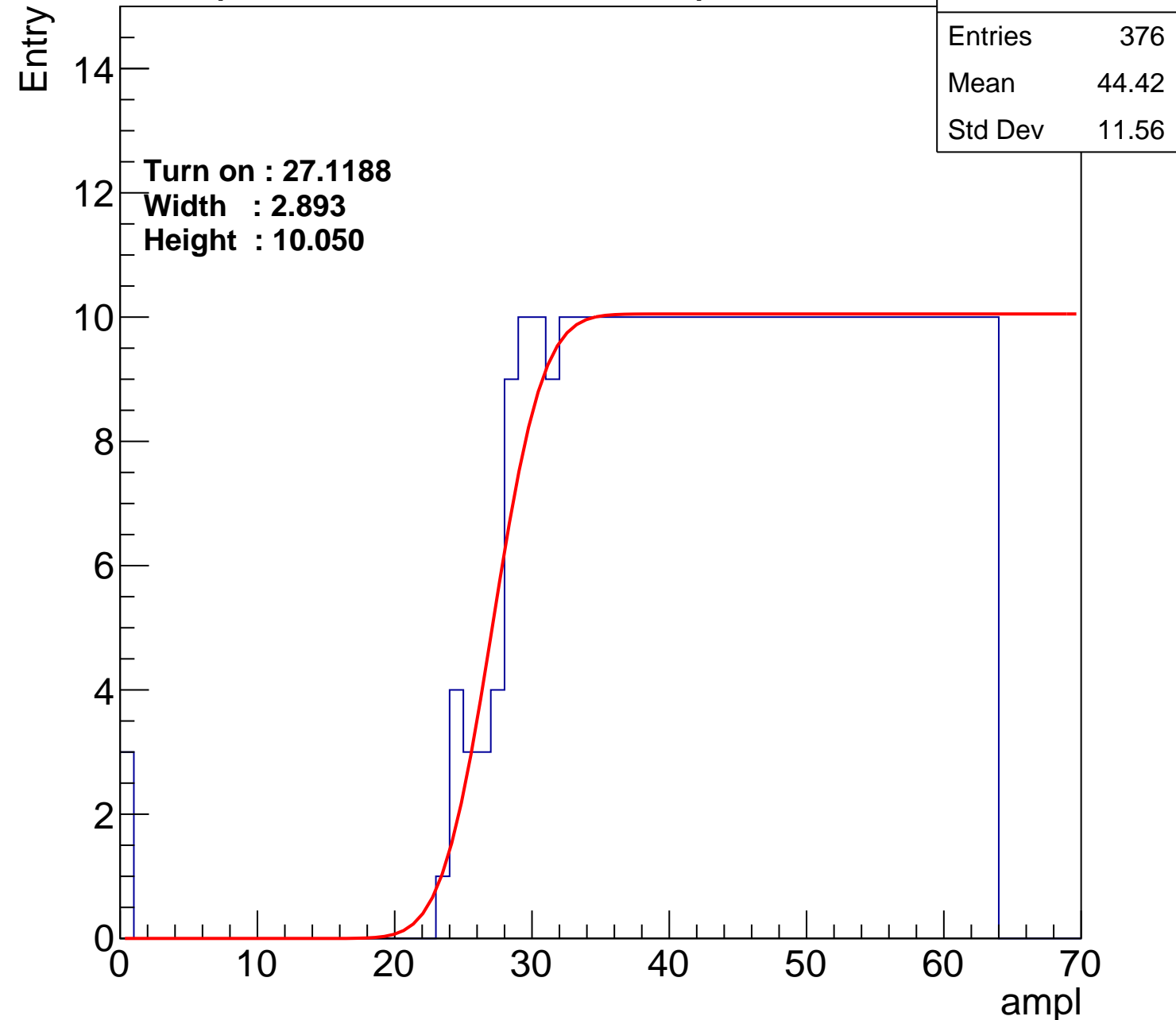
Width : 2.893

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.37
Std Dev	10.77

Turn on : 28.4738

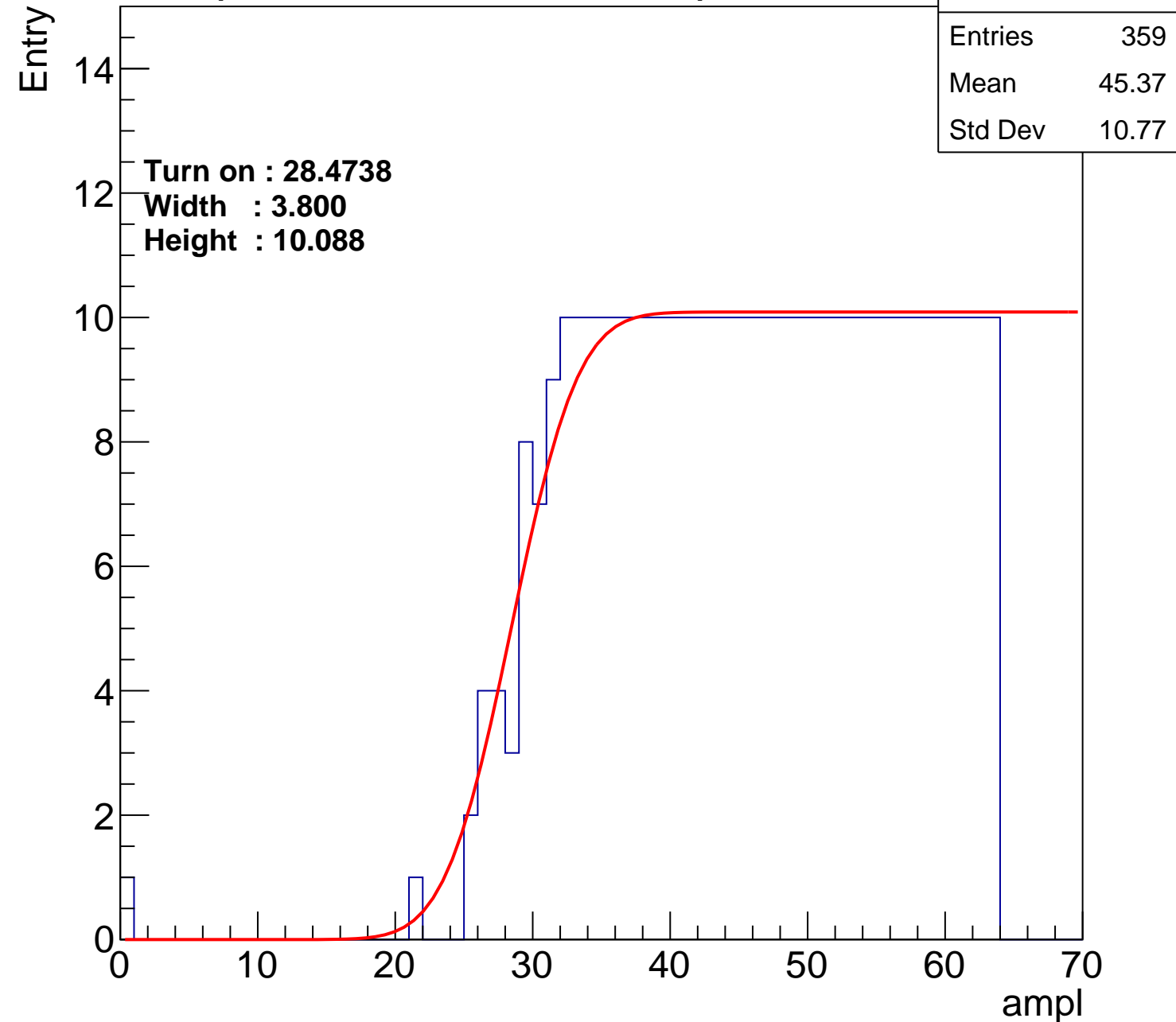
Width : 3.800

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch115

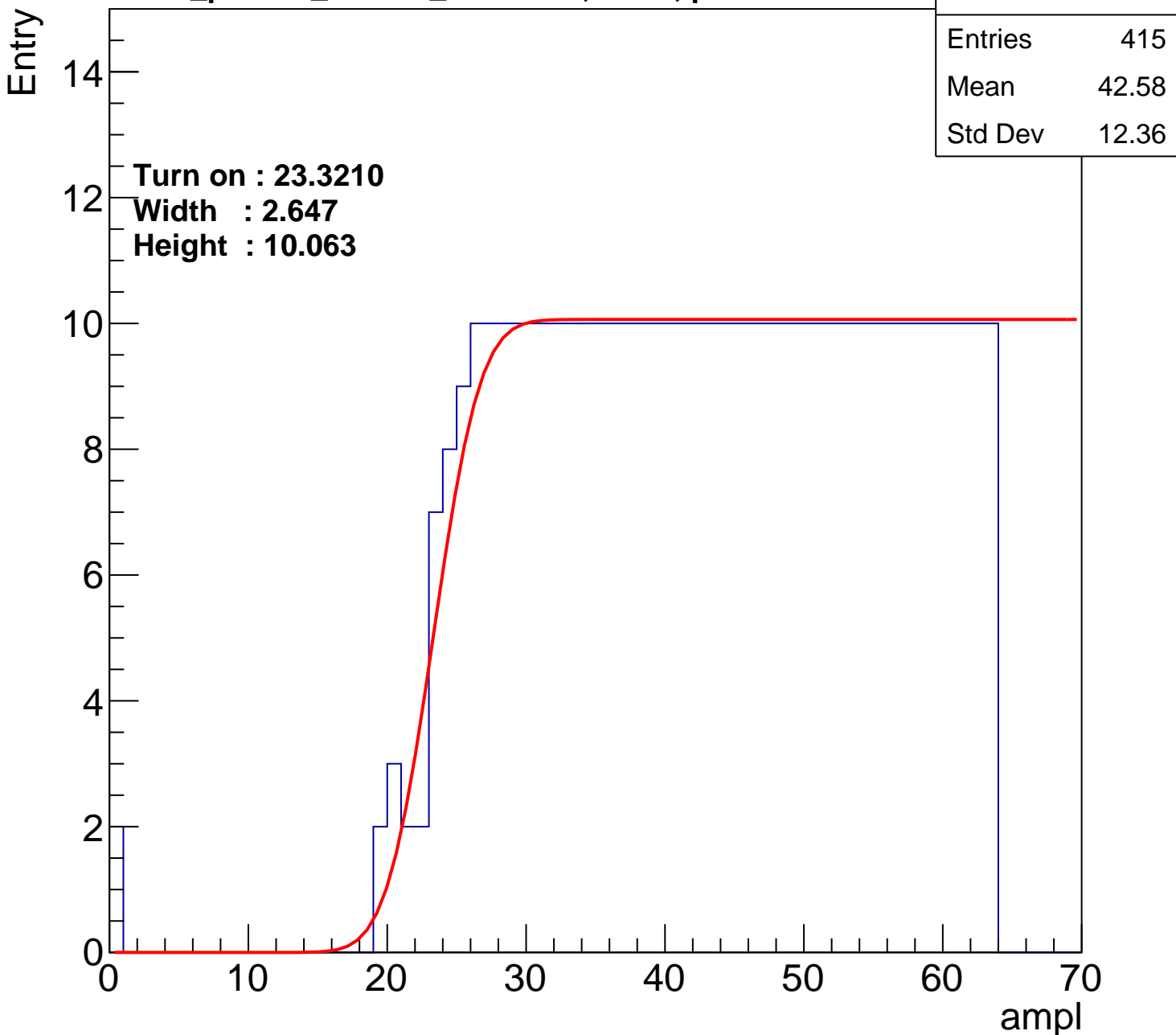
calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 23.3210

Width : 2.647

Height : 10.063

Entries	415
Mean	42.58
Std Dev	12.36



B0L000S, U22-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.86
Std Dev	11.39

Turn on : 27.8535

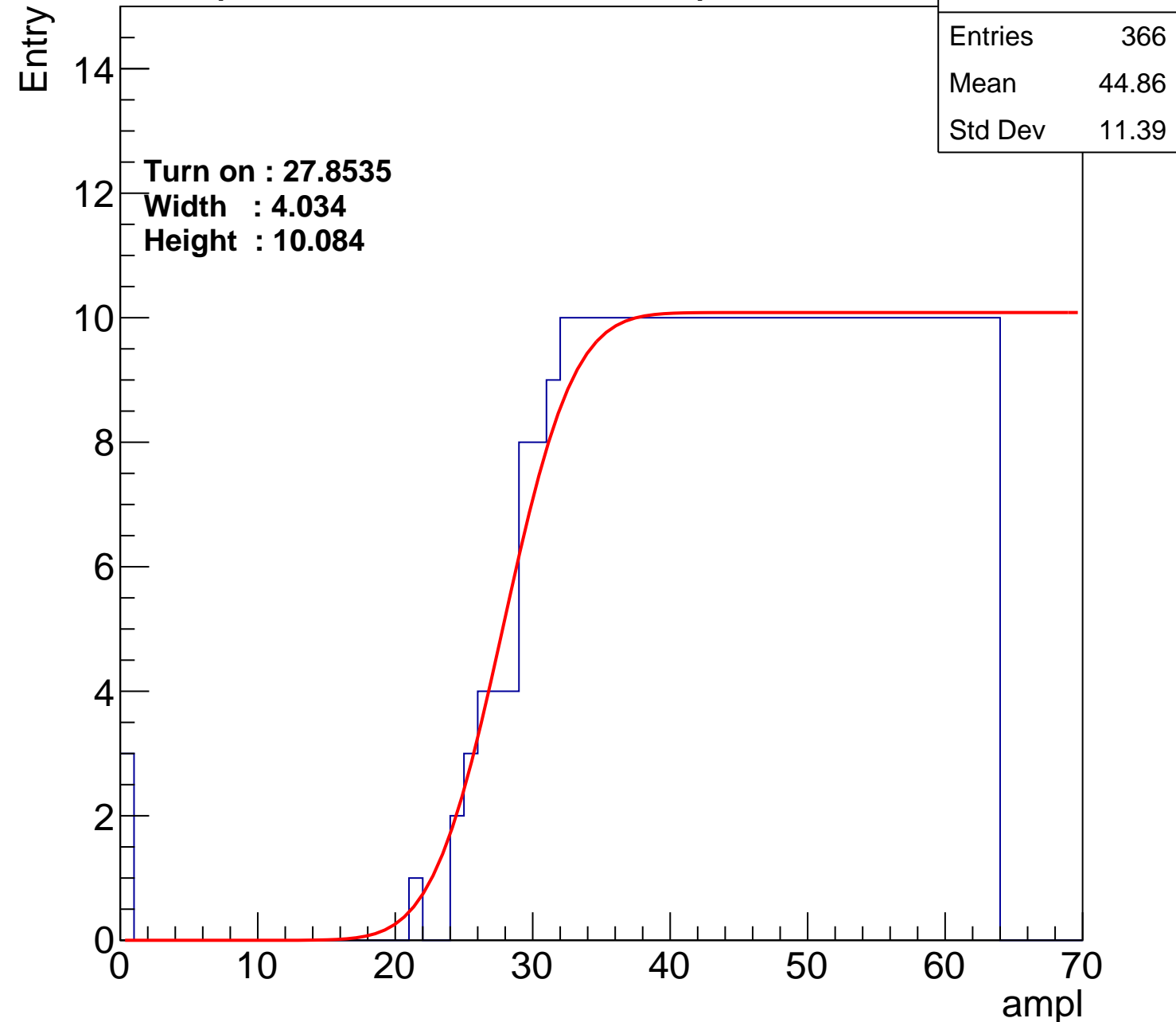
Width : 4.034

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch117

calib_packv5_042523_0143.root, FC#5, port B1

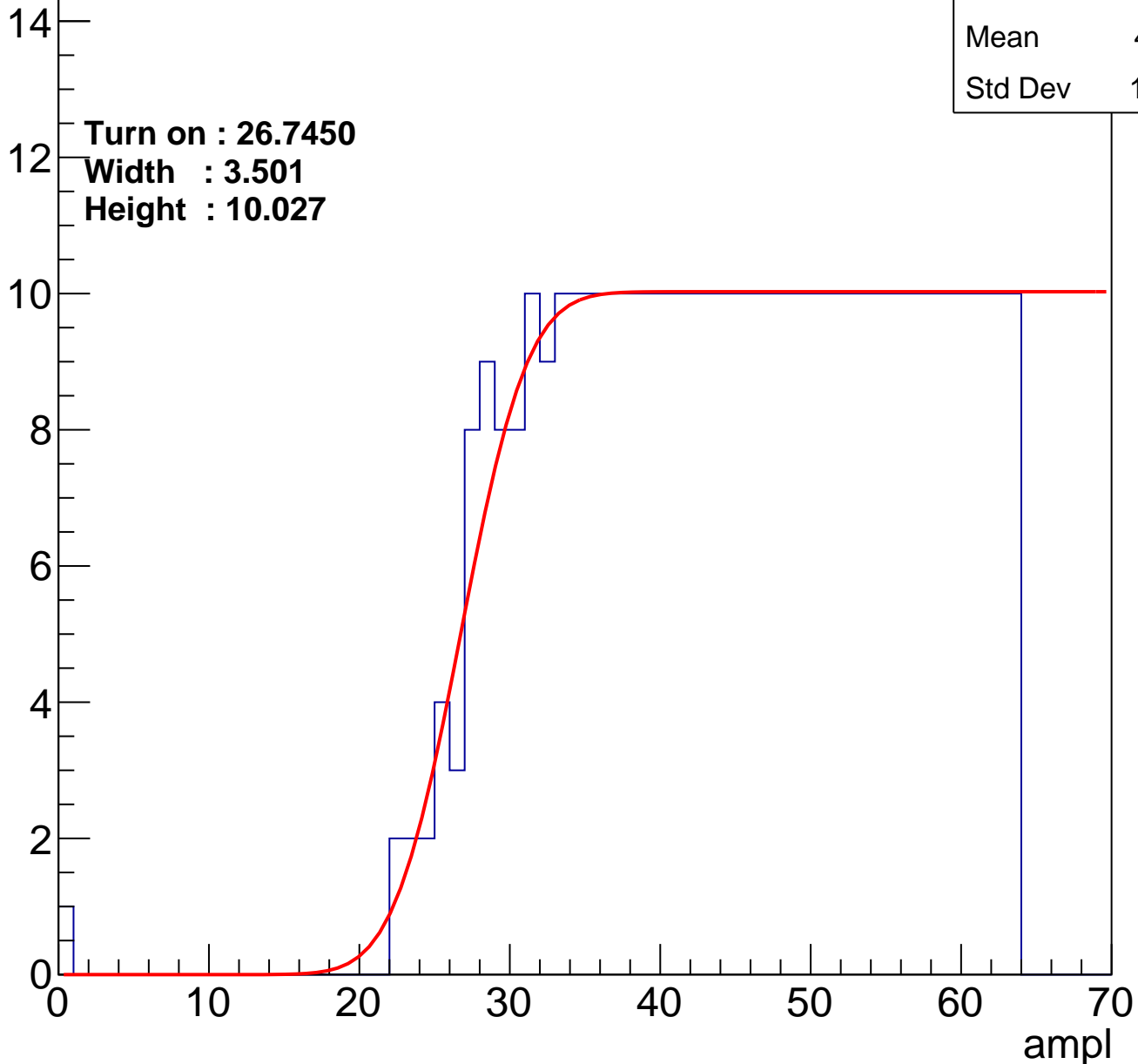
Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 26.7450

Width : 3.501

Height : 10.027

Entry



B0L000S, U22-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.67
Std Dev	11.27

Turn on : 26.8318

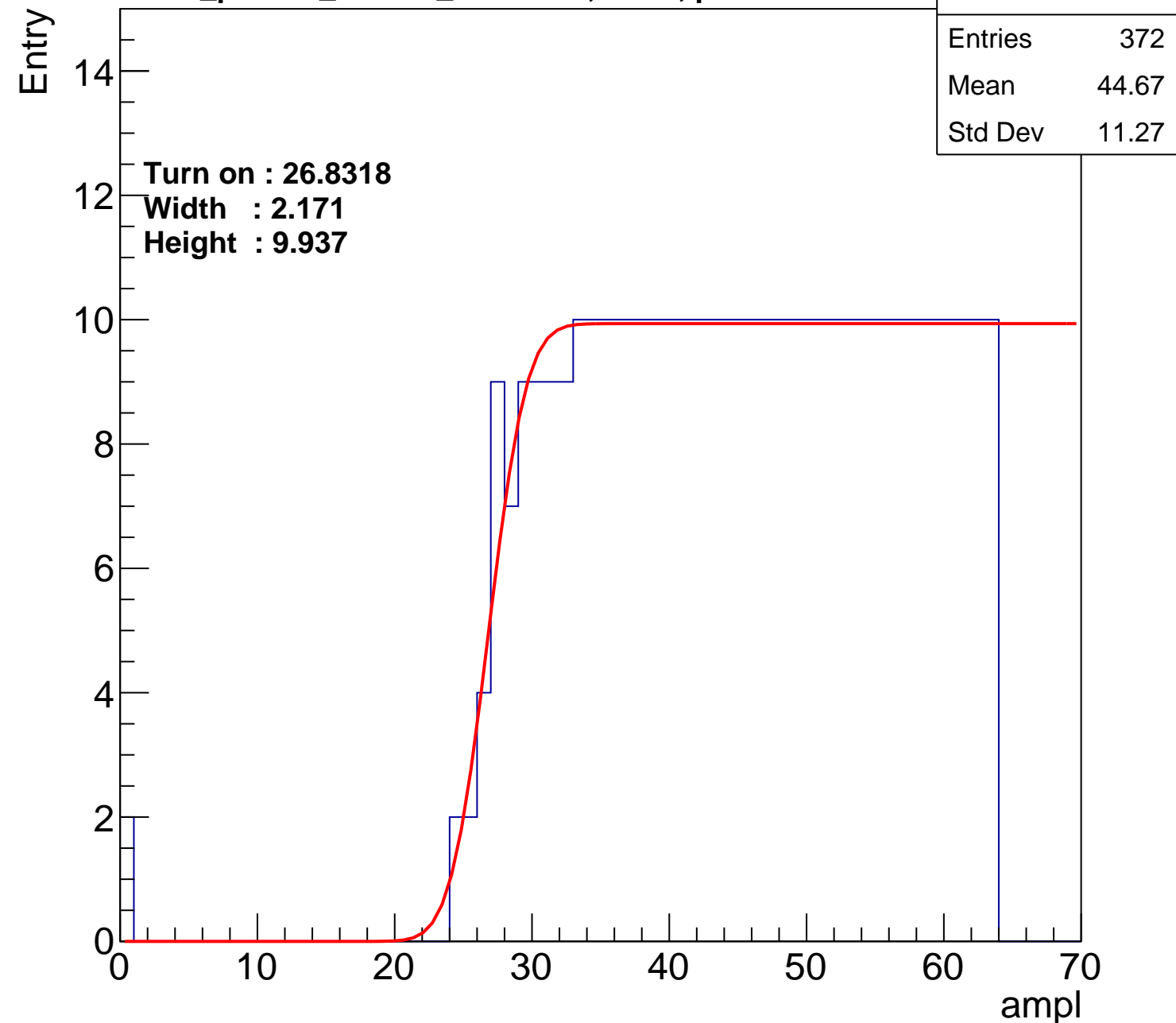
Width : 2.171

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch119

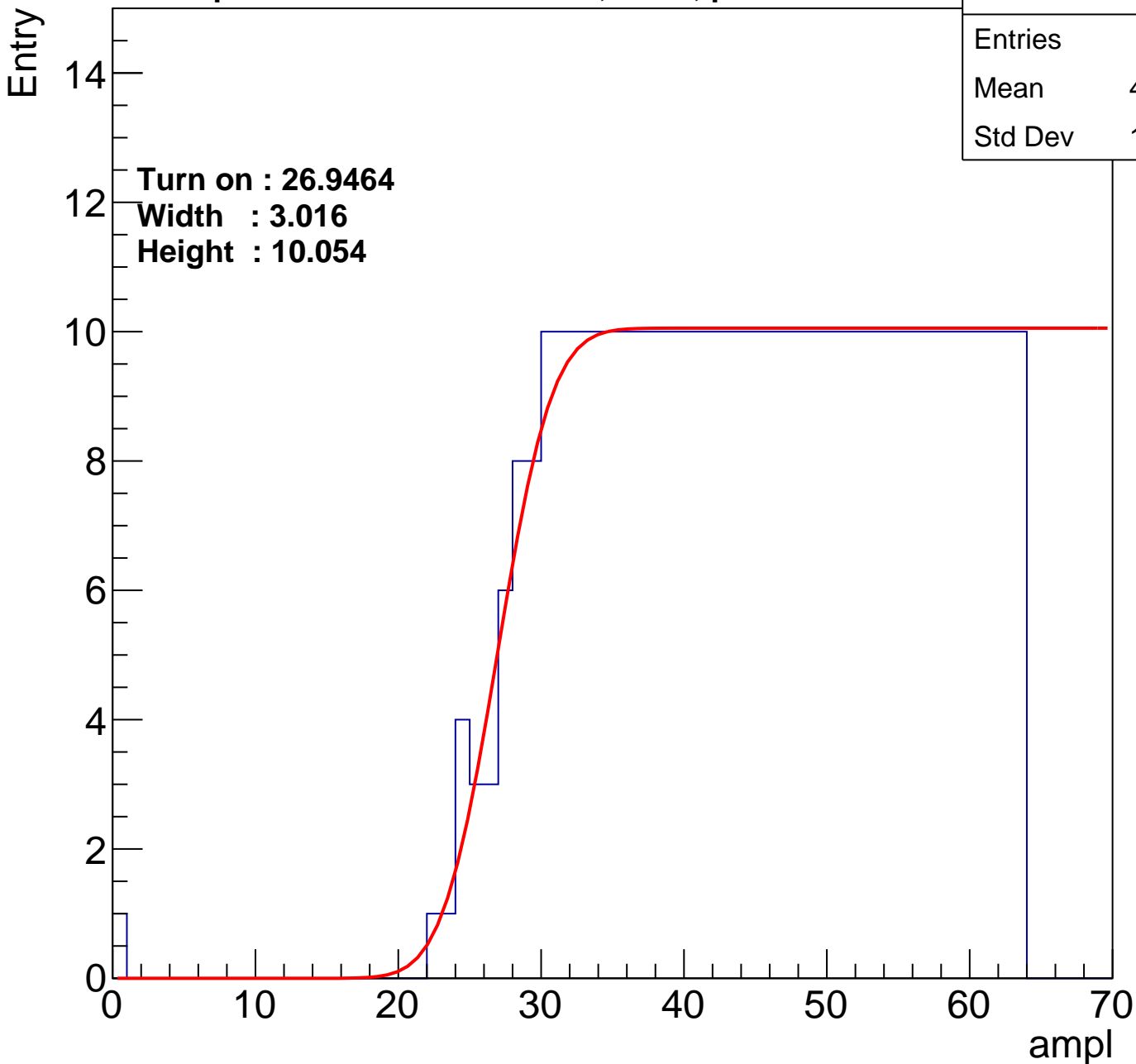
calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.9464

Width : 3.016

Height : 10.054



B0L000S, U22-ch120

calib_packv5_042523_0143.root, FC#5, port B1

Entries	396
Mean	43.49
Std Dev	11.91

Turn on : 24.4941

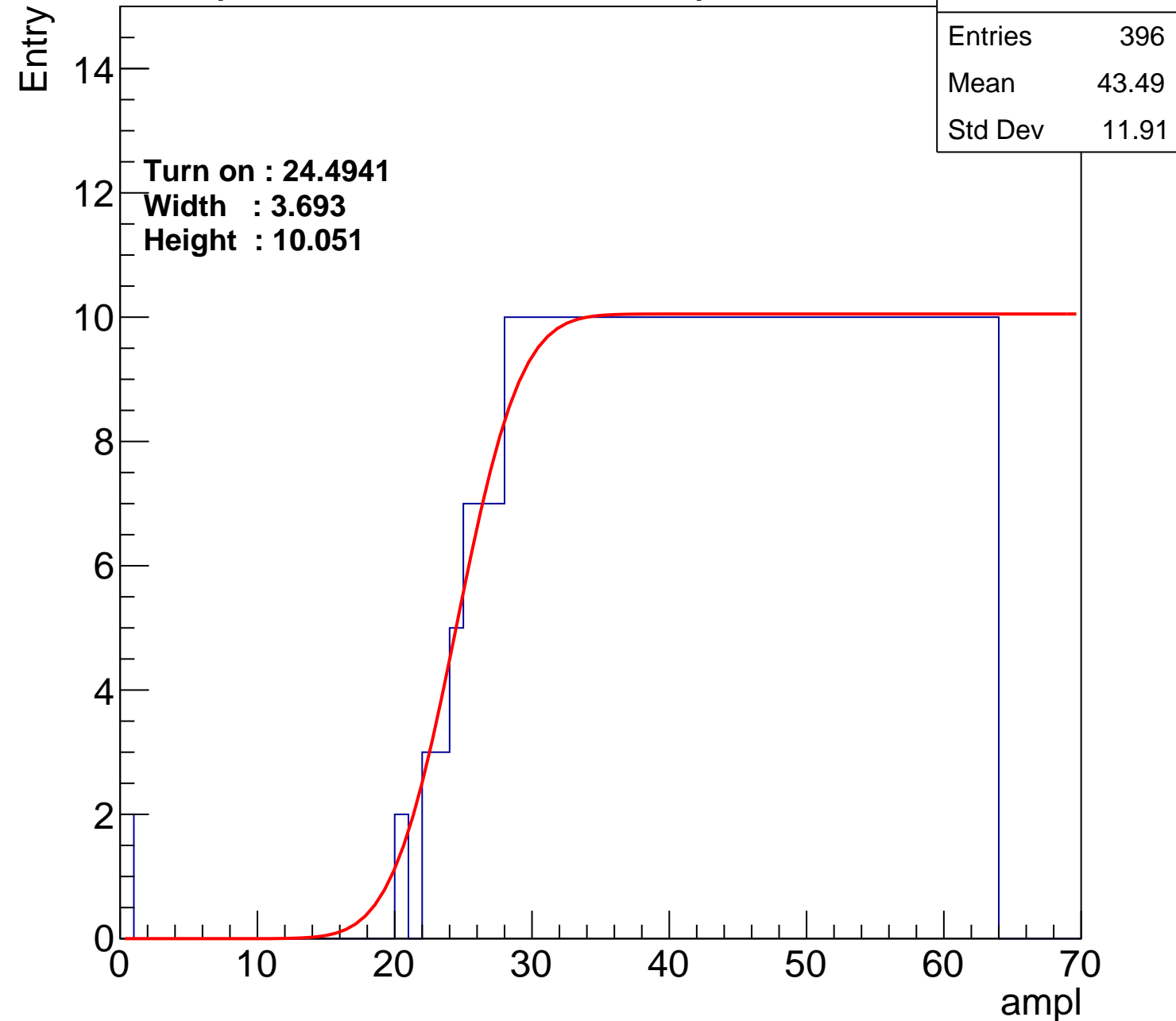
Width : 3.693

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.62
Std Dev	11.63

Turn on : 27.1129

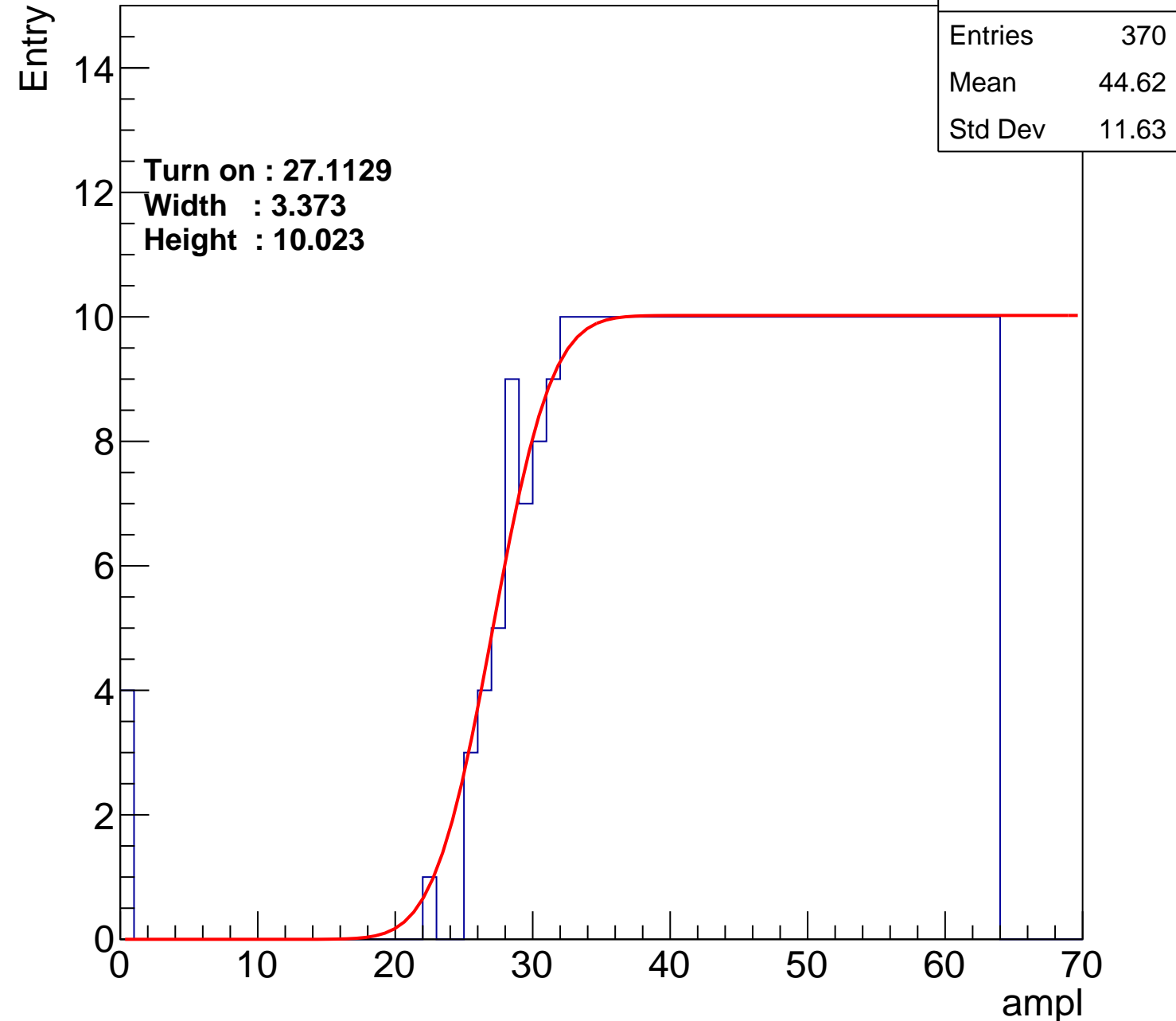
Width : 3.373

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.5422

Width : 3.498

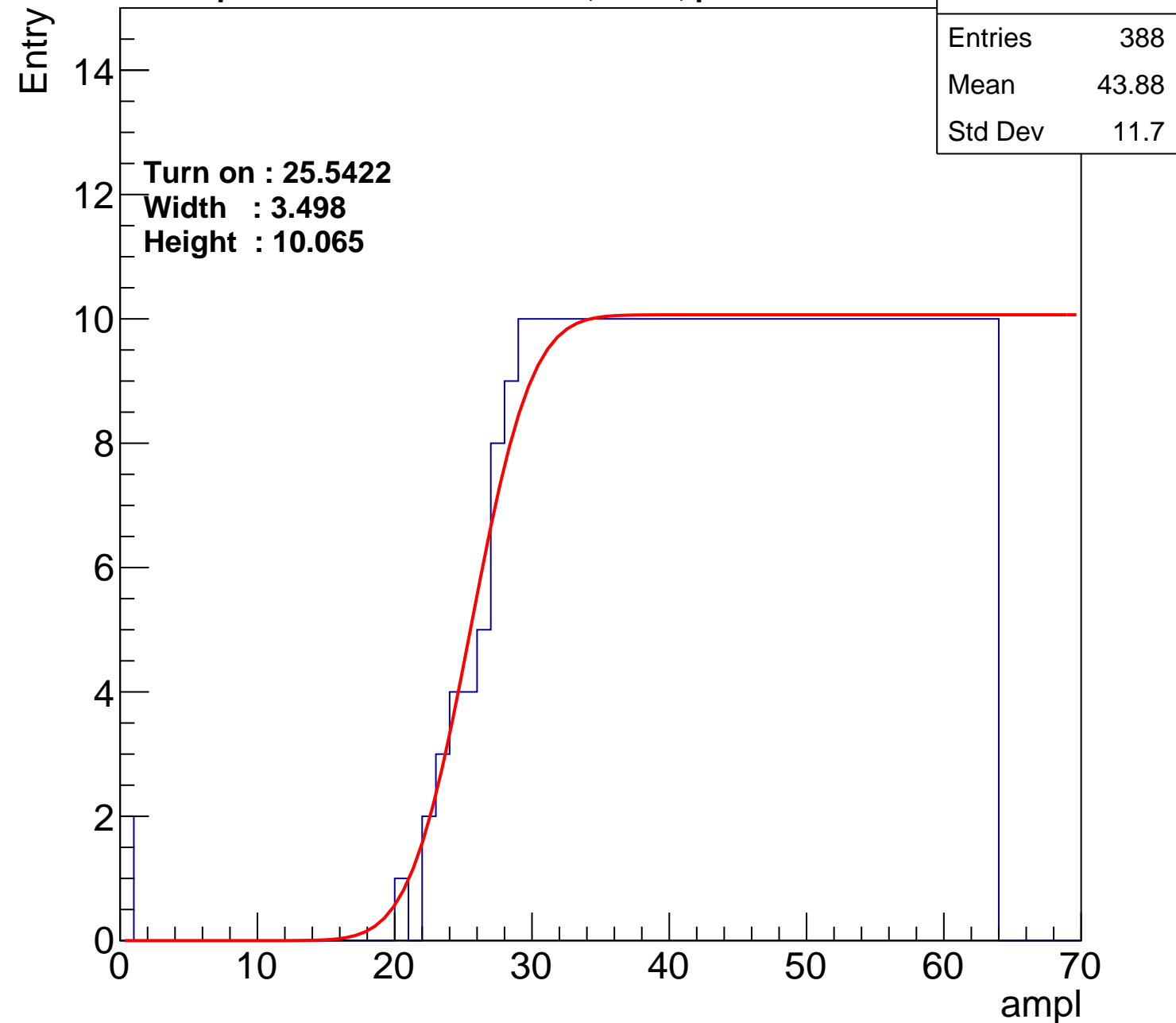
Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U22-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 26.6356

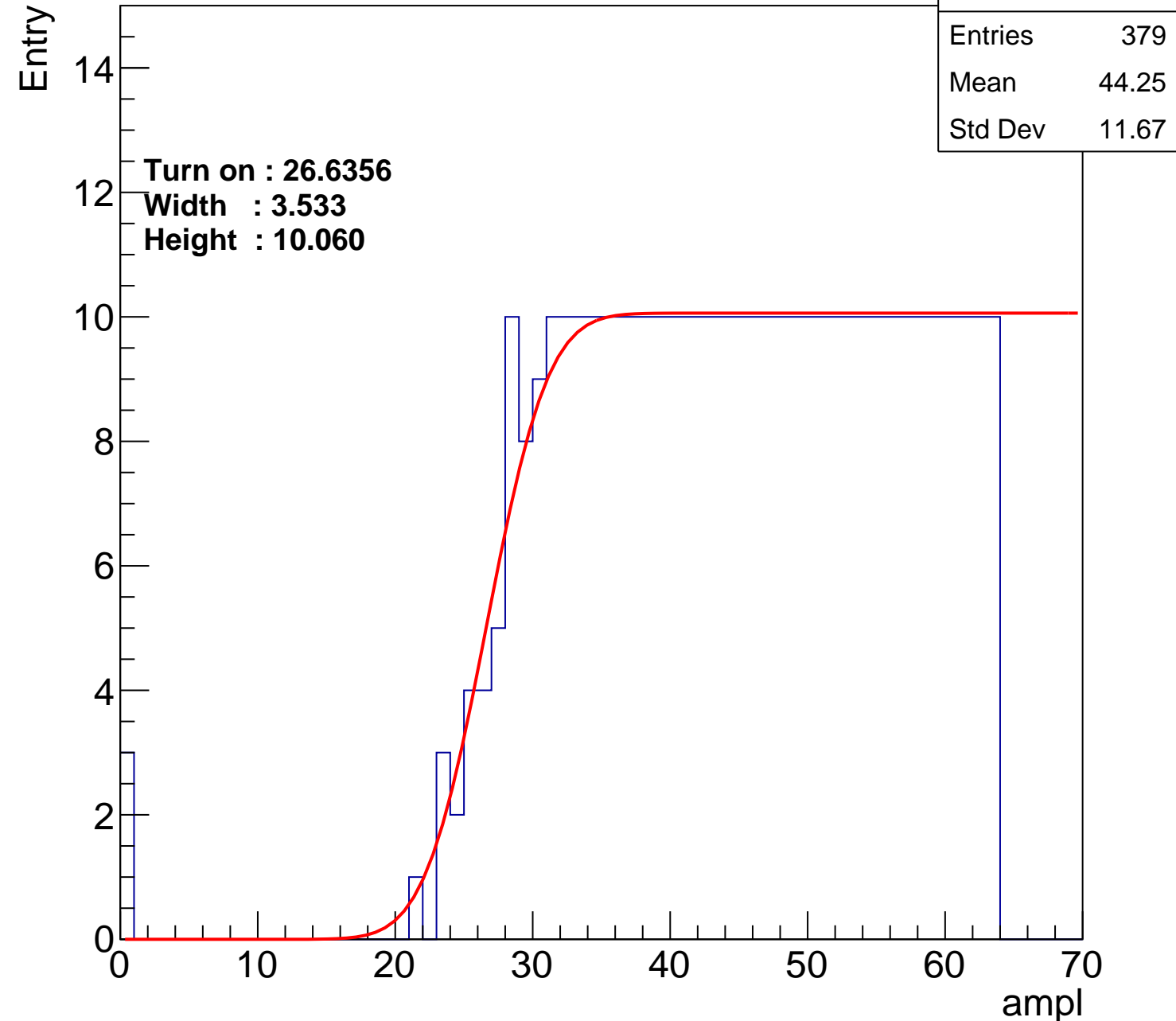
Width : 3.533

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch124

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.1
Std Dev	11.92

Turn on : 26.9734

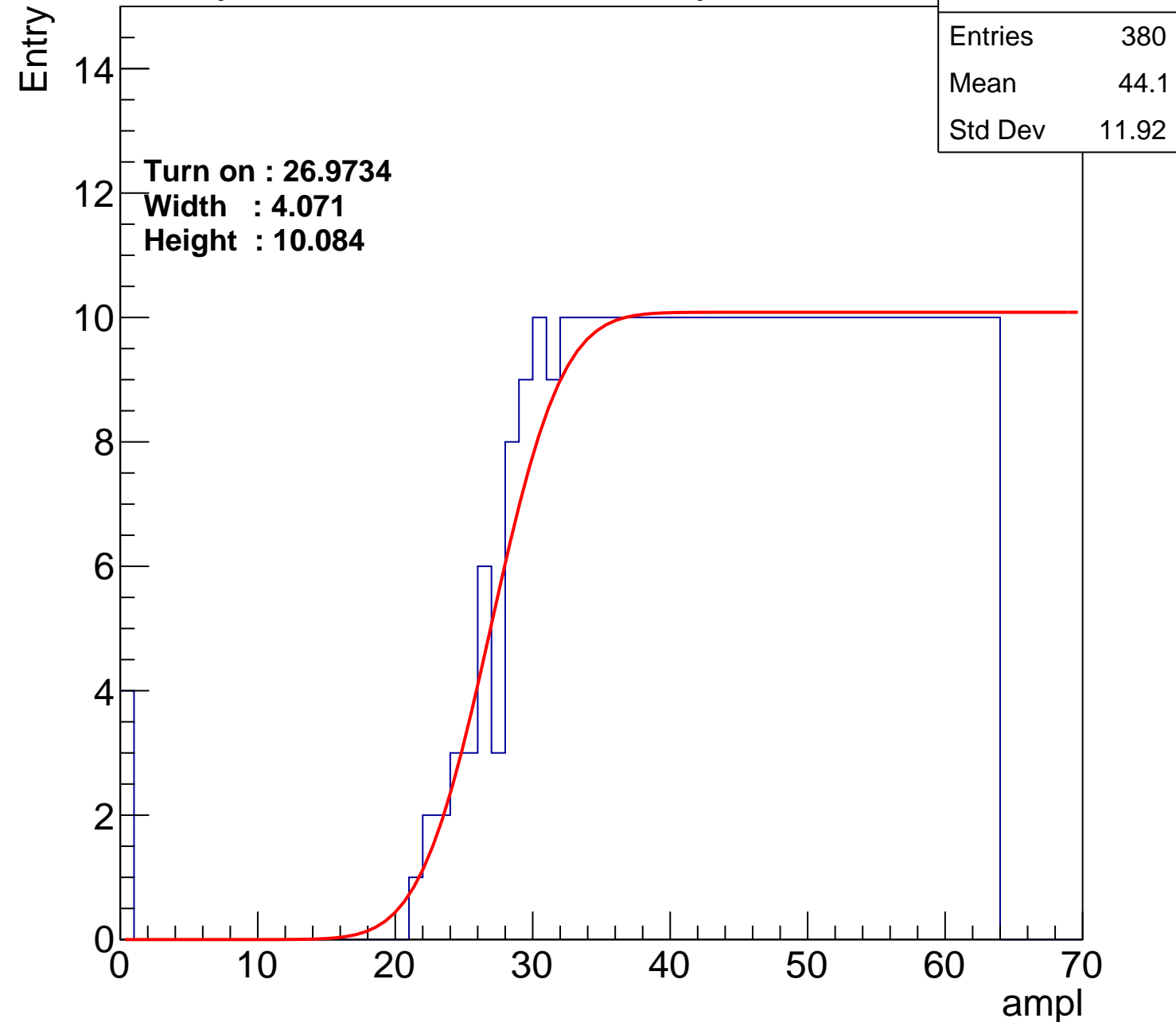
Width : 4.071

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	394
Mean	43.56
Std Dev	12.13

Turn on : 25.0925

Width : 2.808

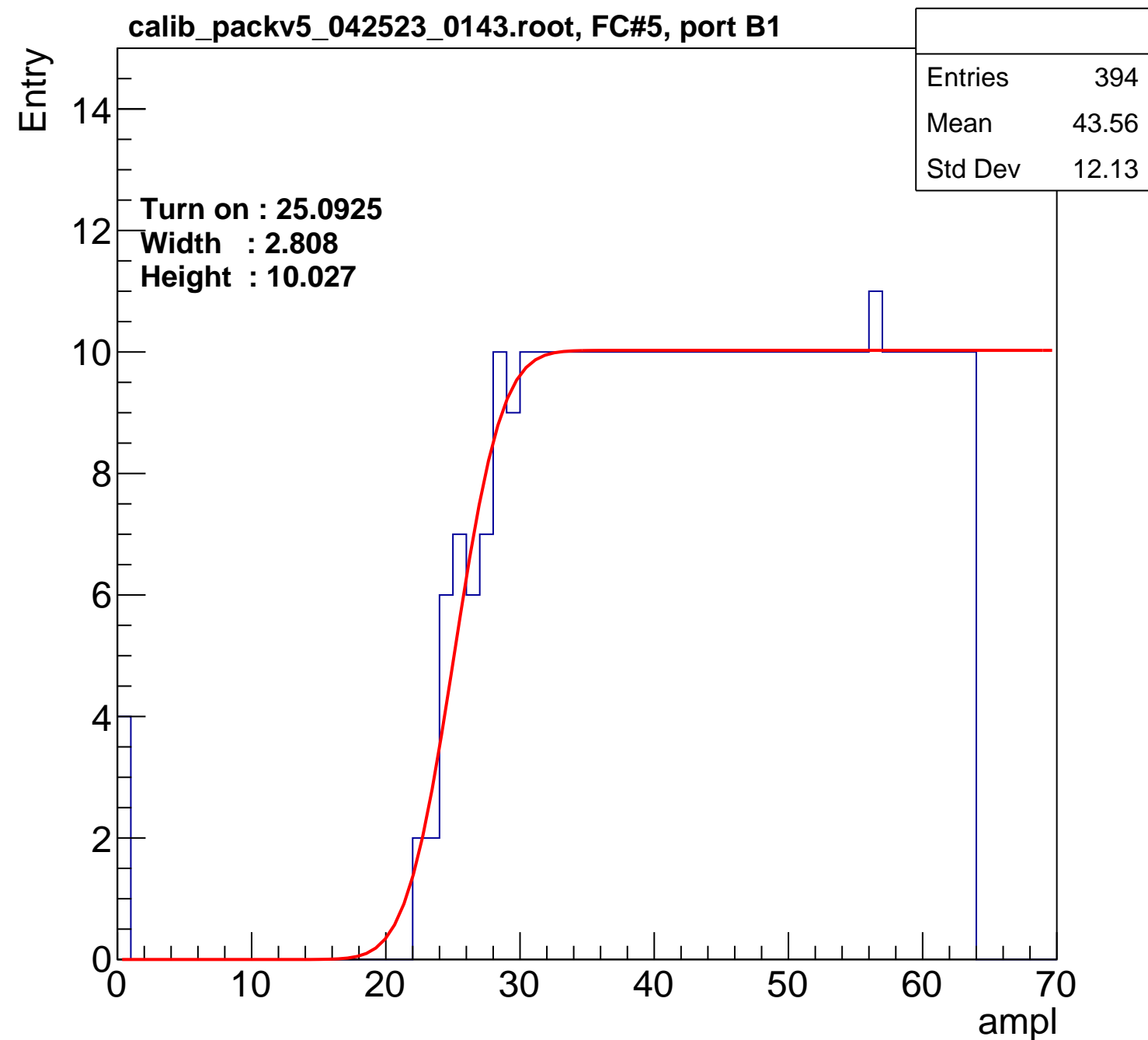
Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U22-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	353
Mean	45.76
Std Dev	10.57

Turn on : 29.2845

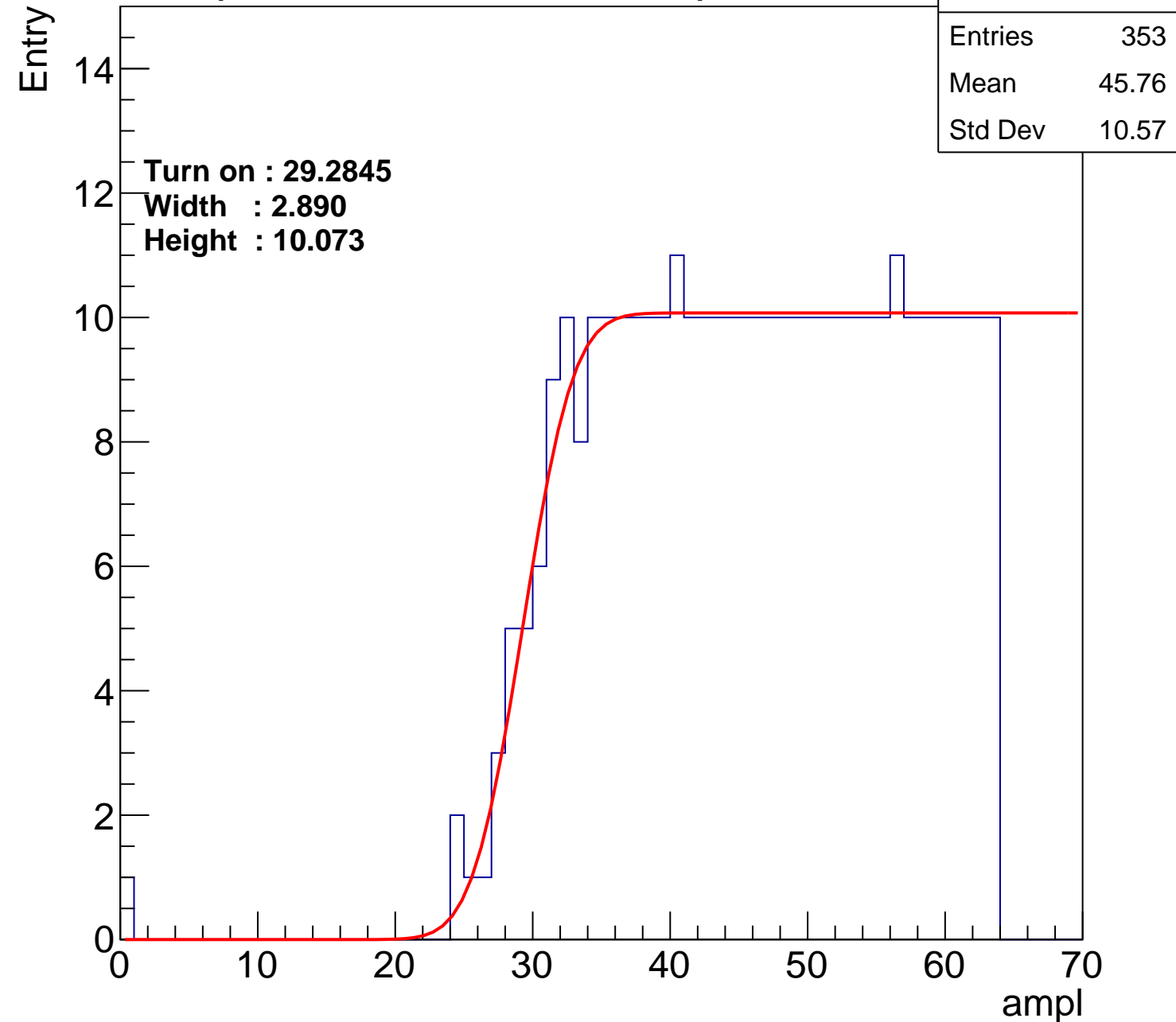
Width : 2.890

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	43.93
Std Dev	12.43

Turn on : 26.9891

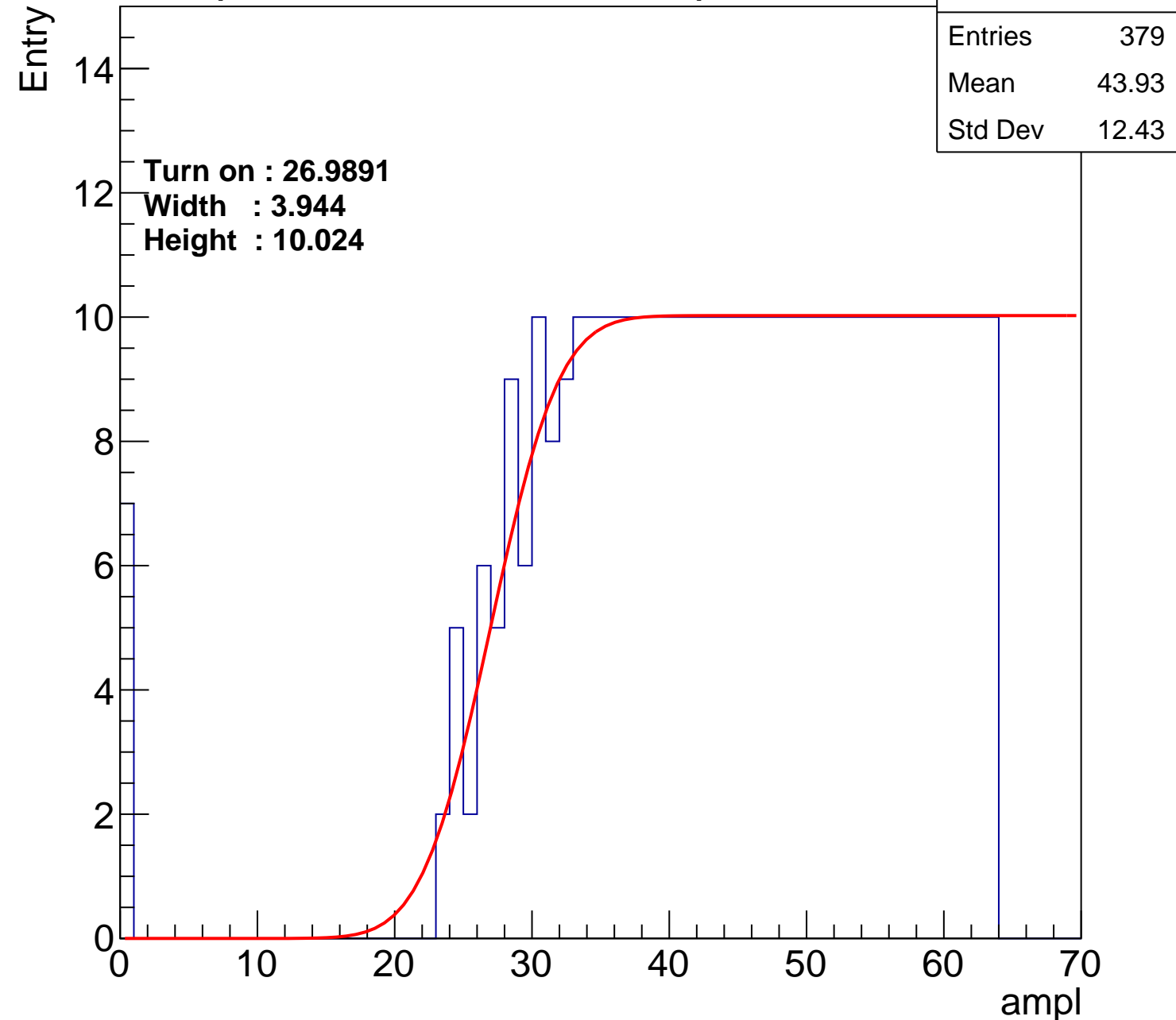
Width : 3.944

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U22-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	43.93
Std Dev	12.43

Turn on : 26.9891

Width : 3.944

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl

