

B1L001S, U20-ch0, adc0

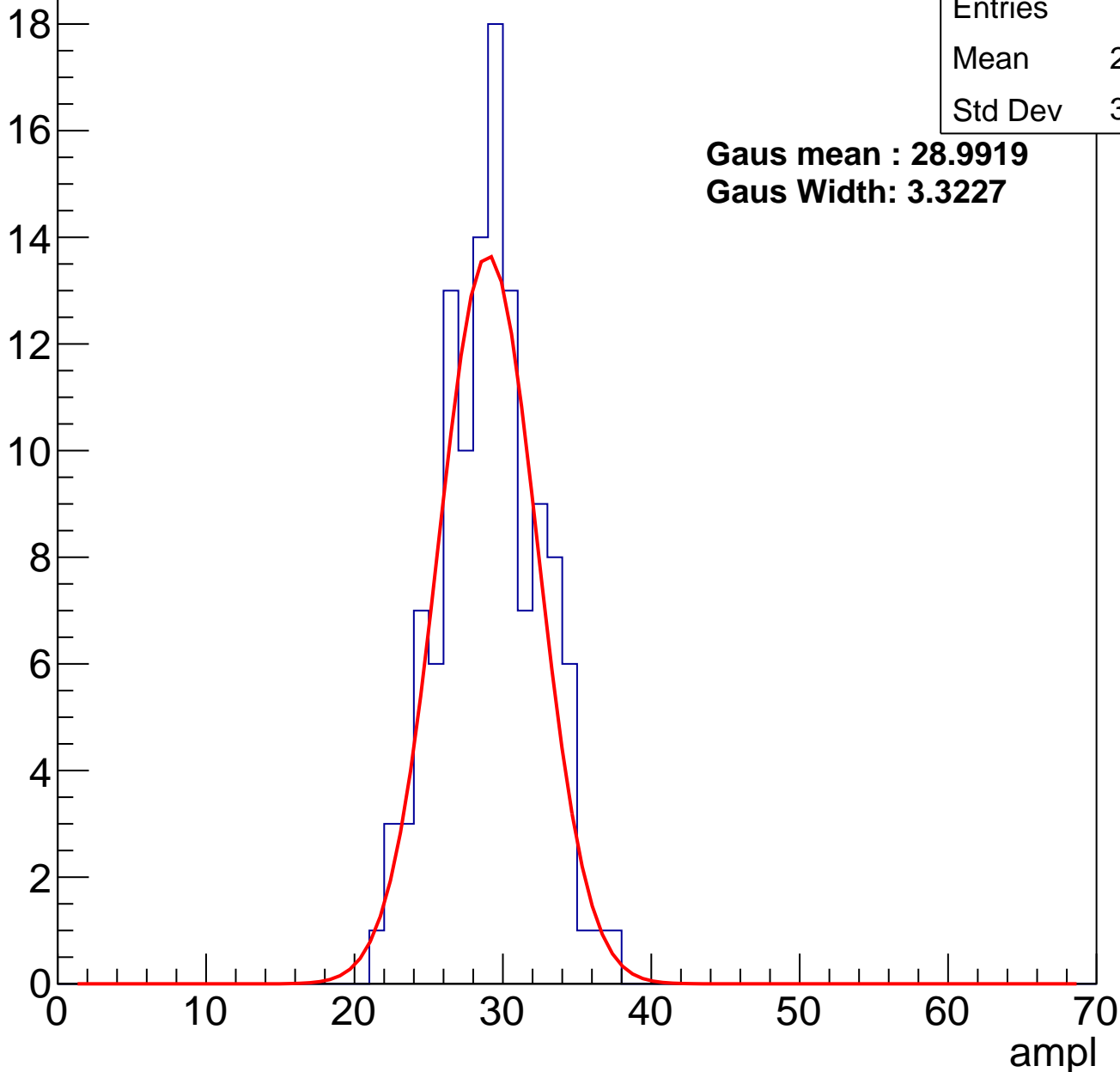
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	28.65
Std Dev	3.272

Gaus mean : 28.9919

Gaus Width: 3.3227

Entry



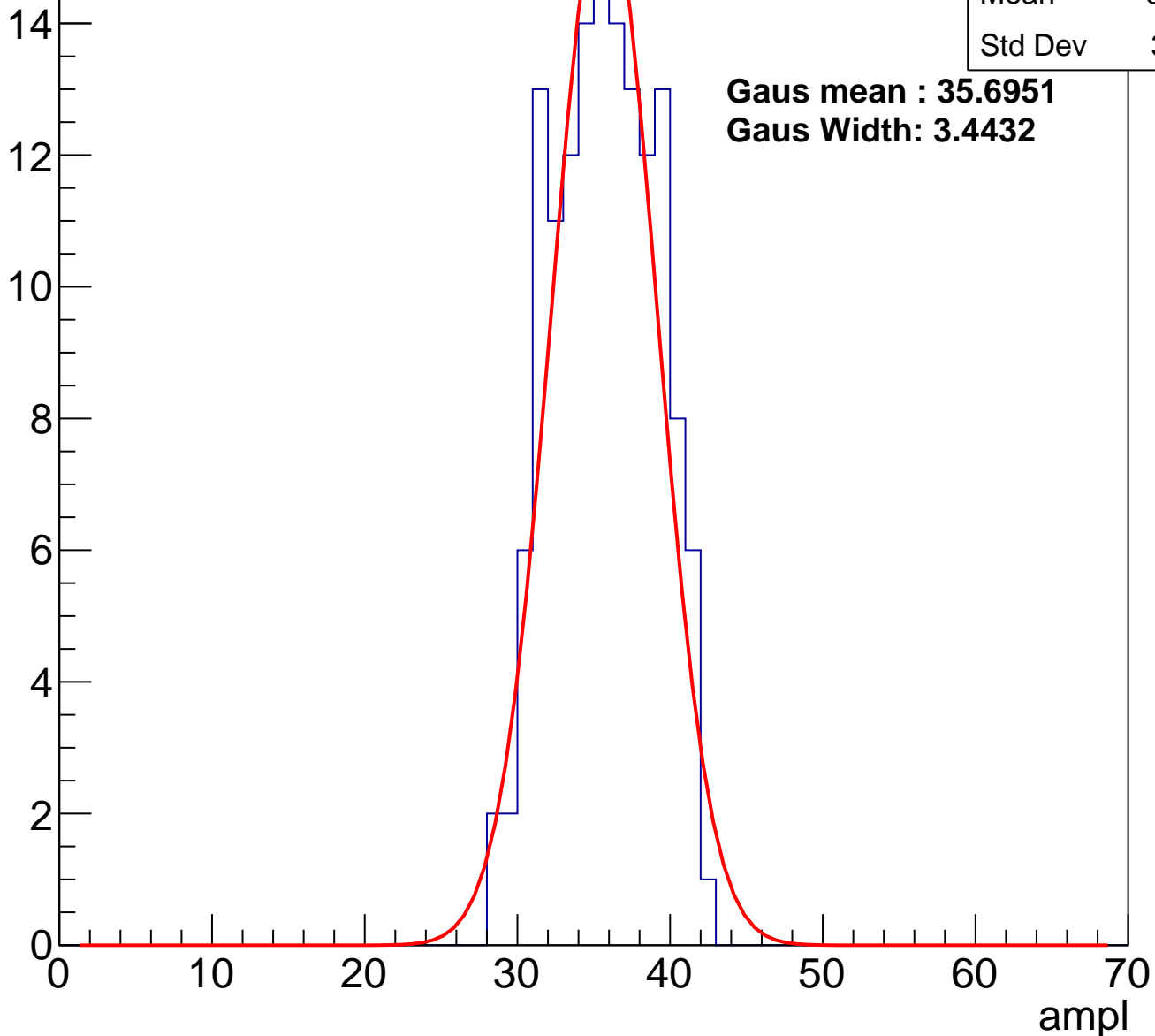
B1L001S, U20-ch0, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	35.23
Std Dev	3.281

Gaus mean : 35.6951
Gaus Width: 3.4432

Entry



B1L001S, U20-ch0, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

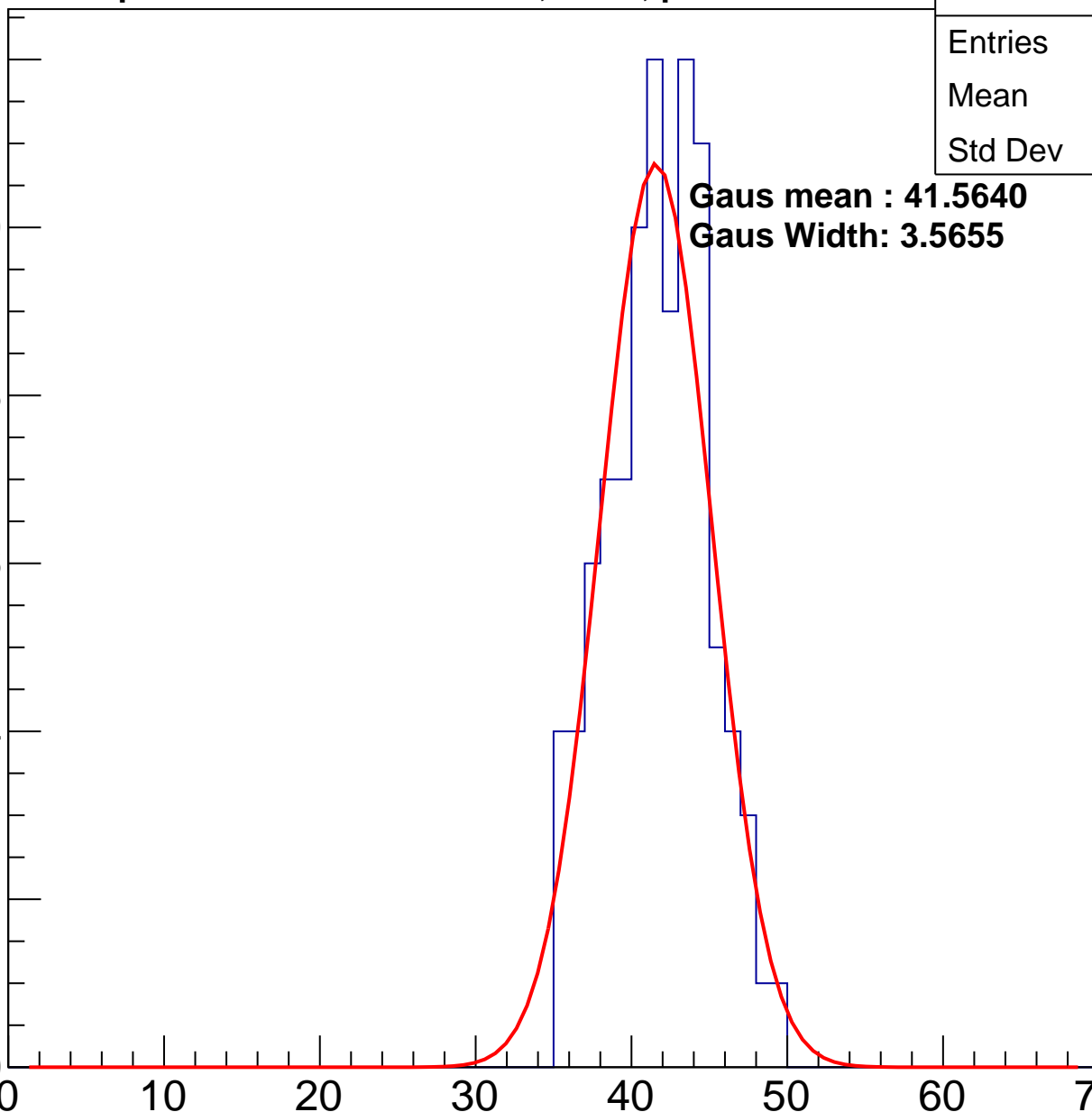
70

ampl

Entries	96
Mean	41.27
Std Dev	3.213

Gaus mean : 41.5640

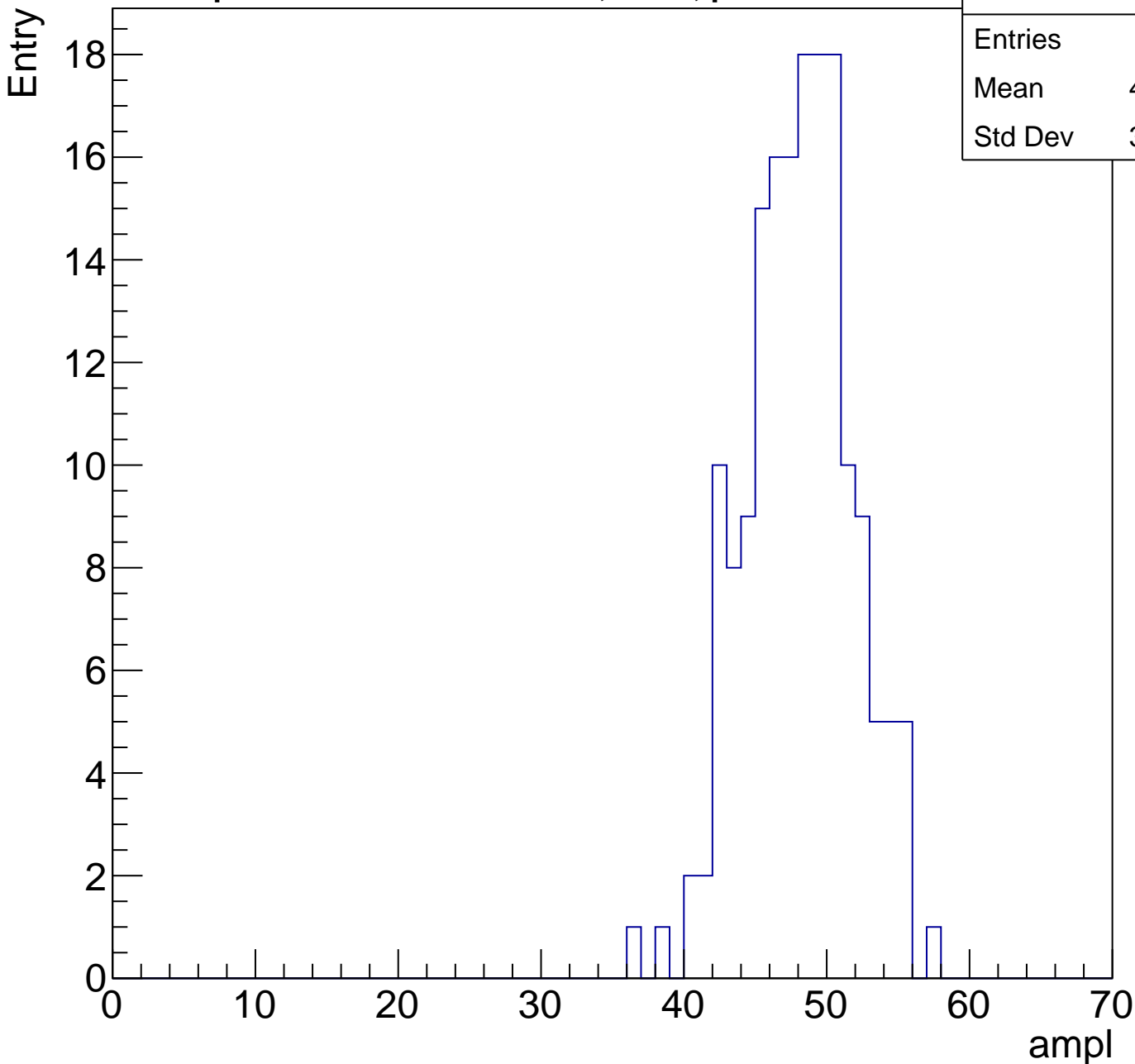
Gaus Width: 3.5655



B1L001S, U20-ch0, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	47.63
Std Dev	3.706



B1L001S, U20-ch0, adc4

calib_packv5_042523_0143.root, FC#2, port C2

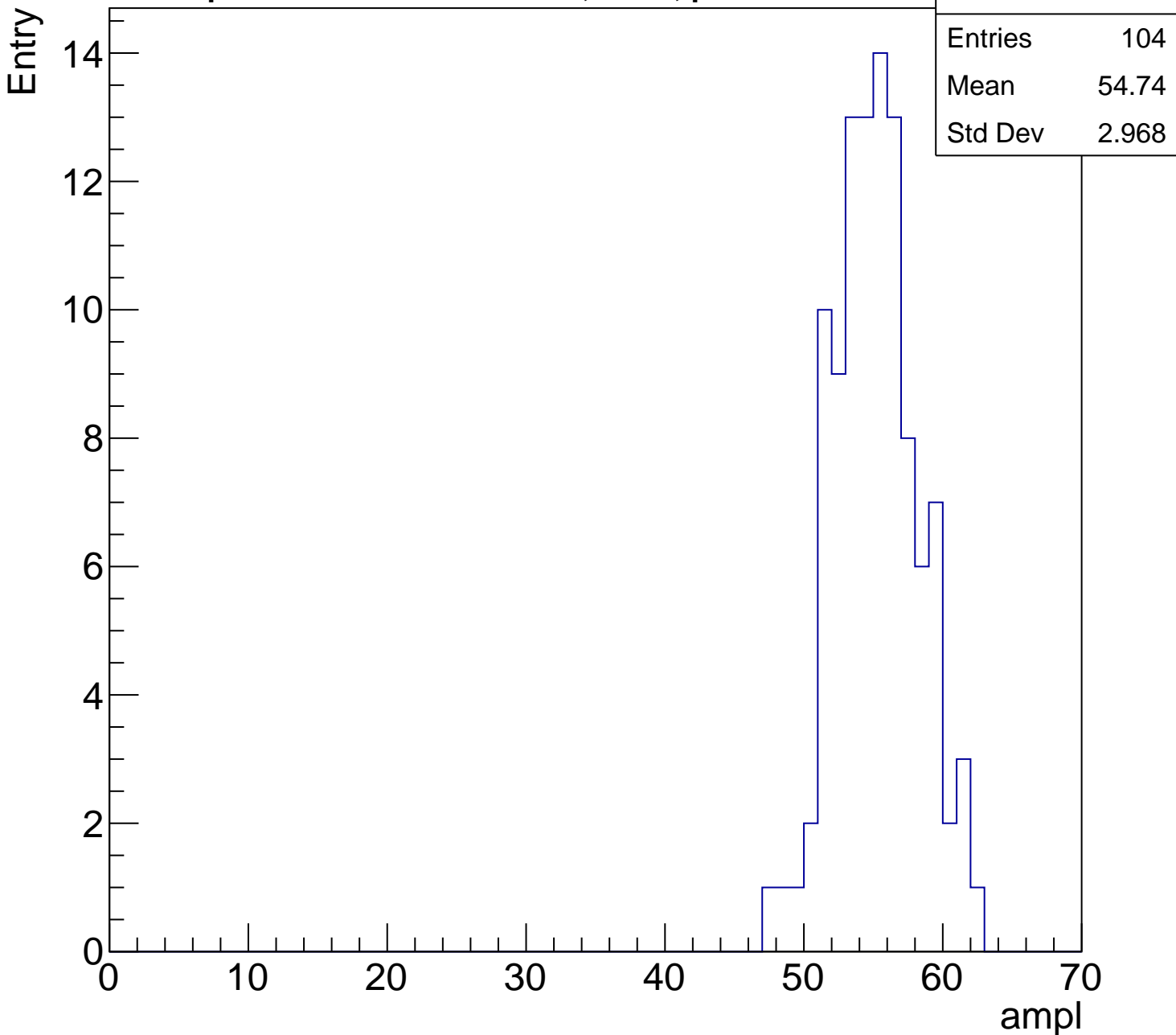
Entries	104
Mean	54.74
Std Dev	2.968

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch0, adc5

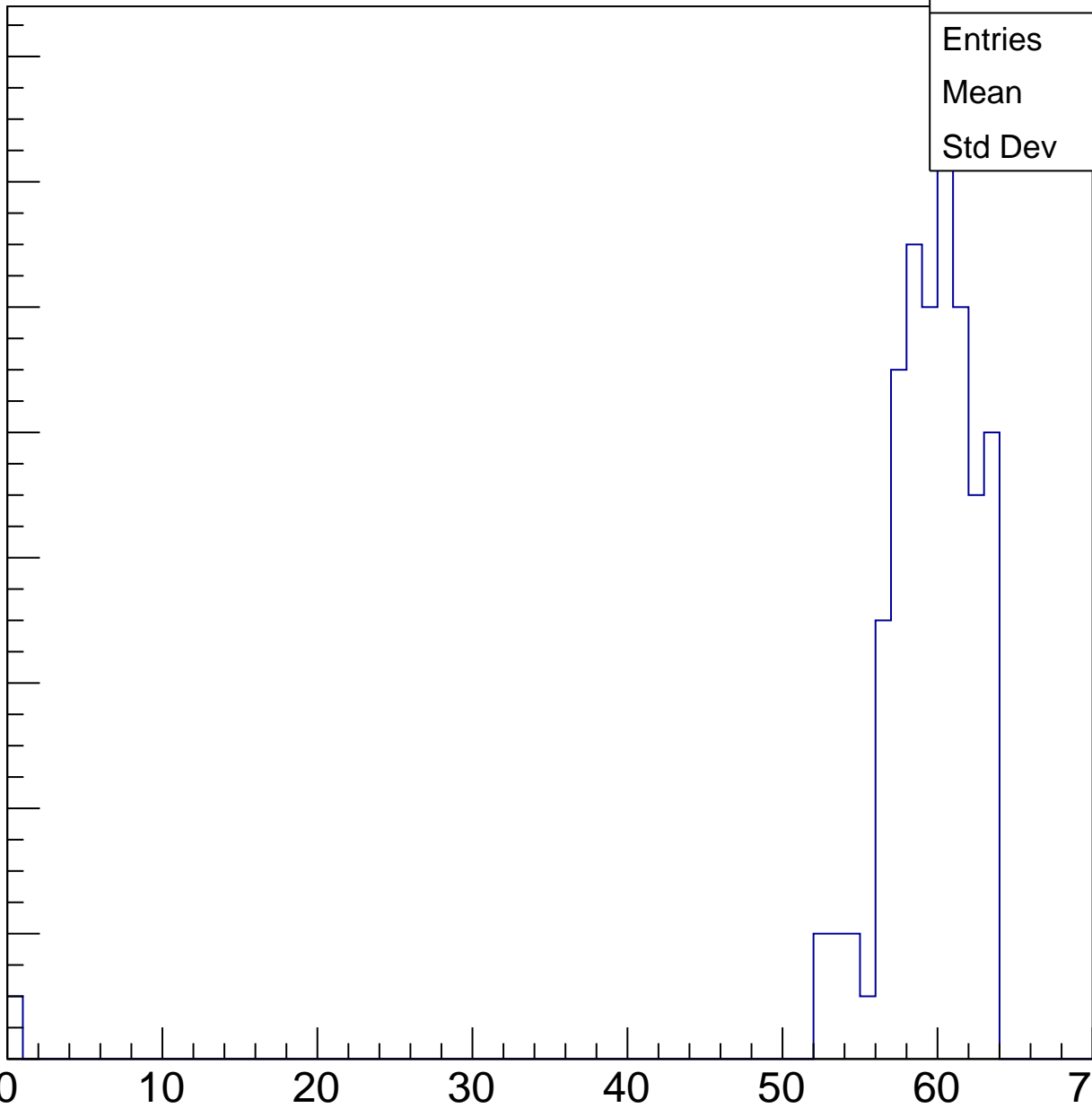
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.51
Std Dev	6.483

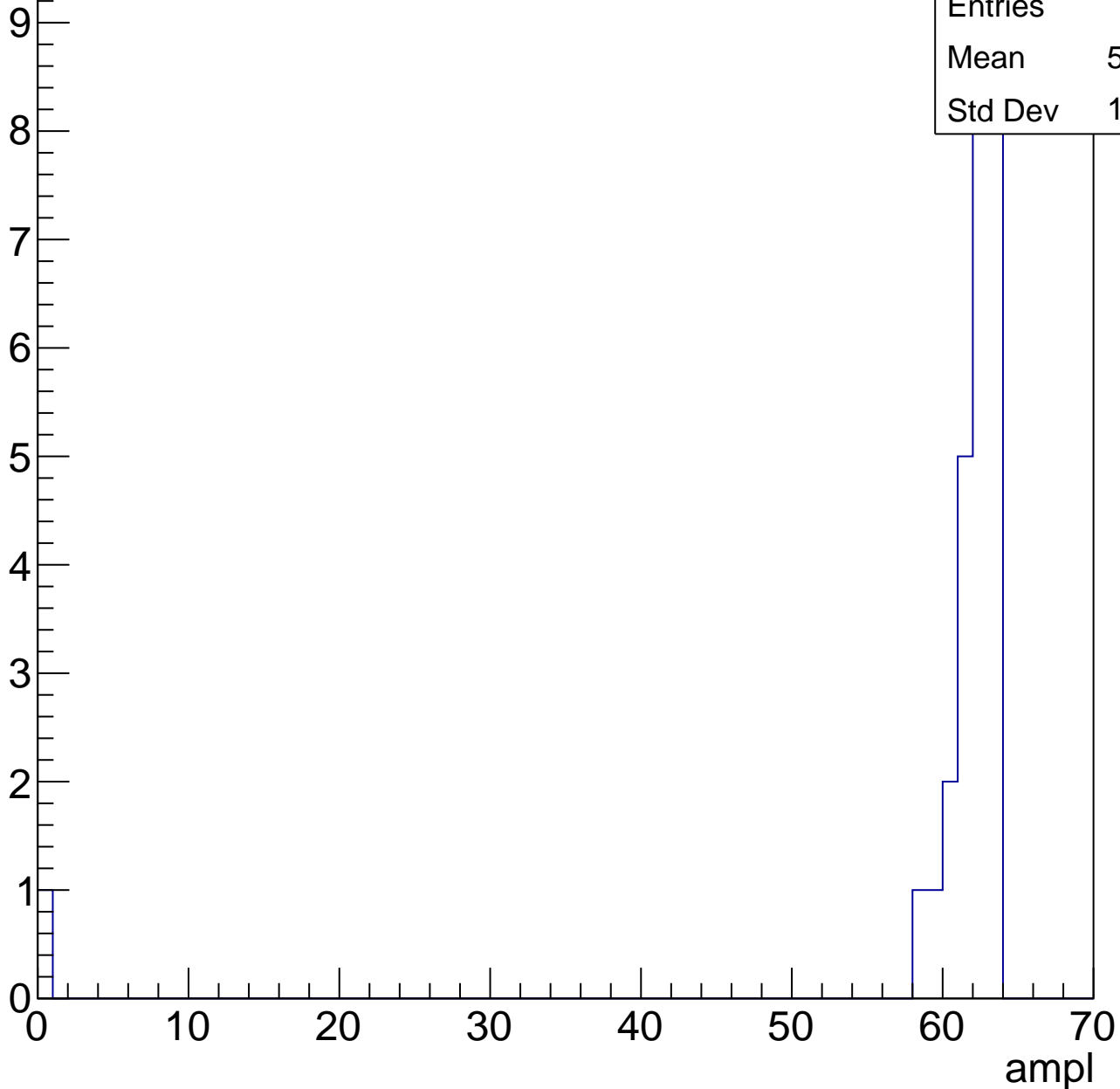
ampl



B1L001S, U20-ch0, adc6

calib_packv5_042523_0143.root, FC#2, port C2

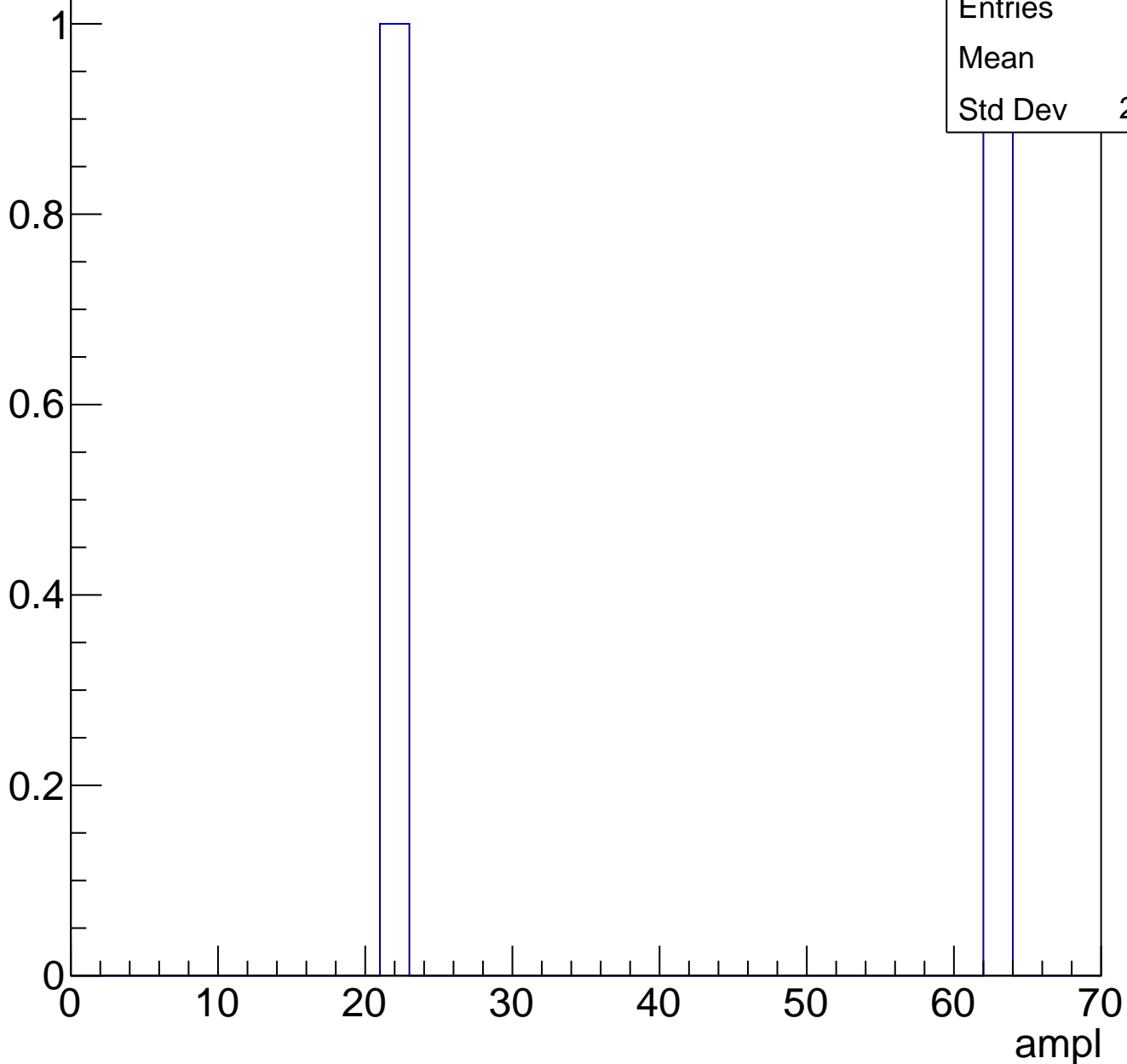
Entry



B1L001S, U20-ch0, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	42
Std Dev	20.51

B1L001S, U20-ch1, adc0

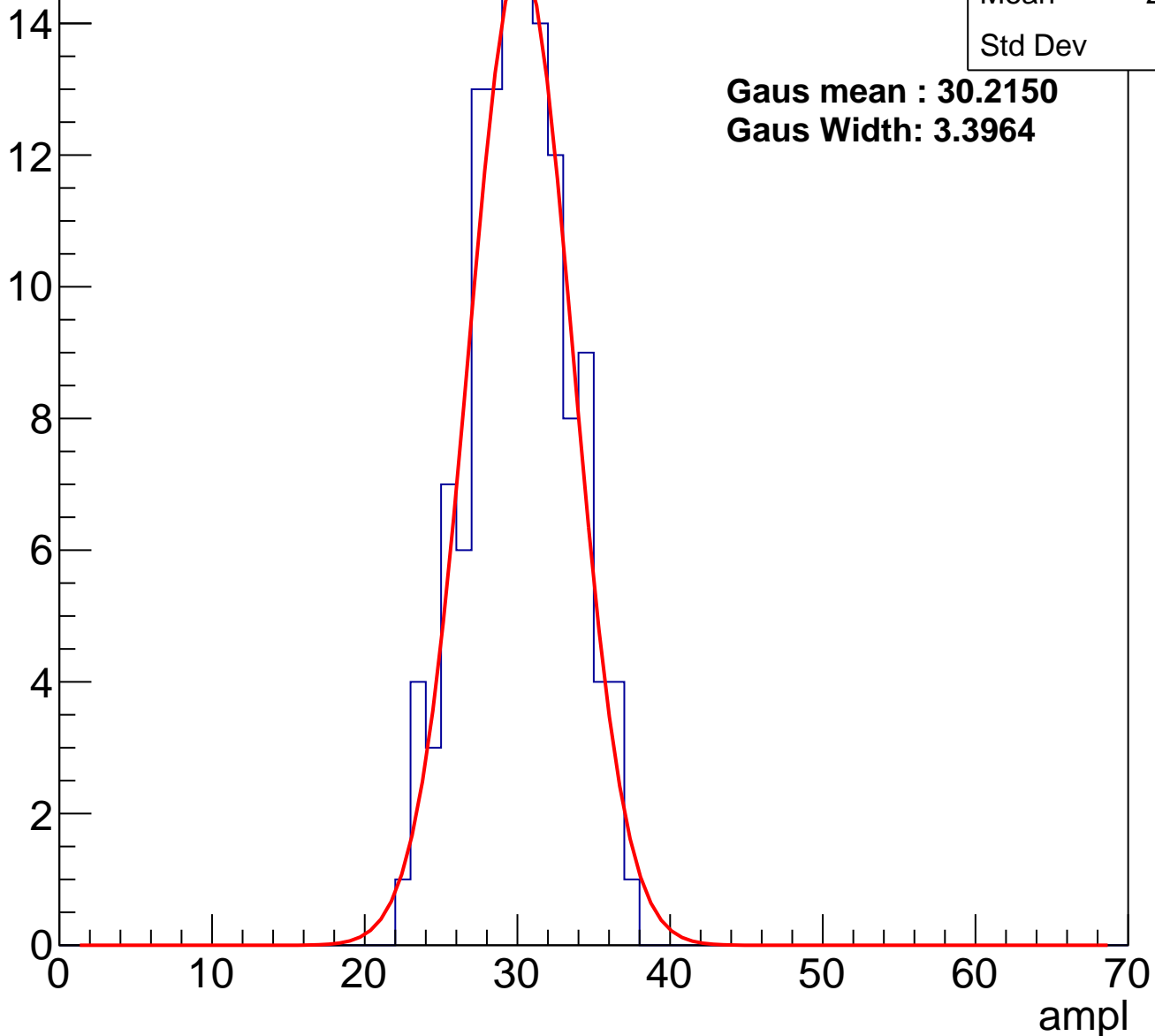
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	29.66
Std Dev	3.27

Gaus mean : 30.2150

Gaus Width: 3.3964

Entry



B1L001S, U20-ch1, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	36
Std Dev	3.208

Gaus mean : 36.0903

Gaus Width: 2.9604

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

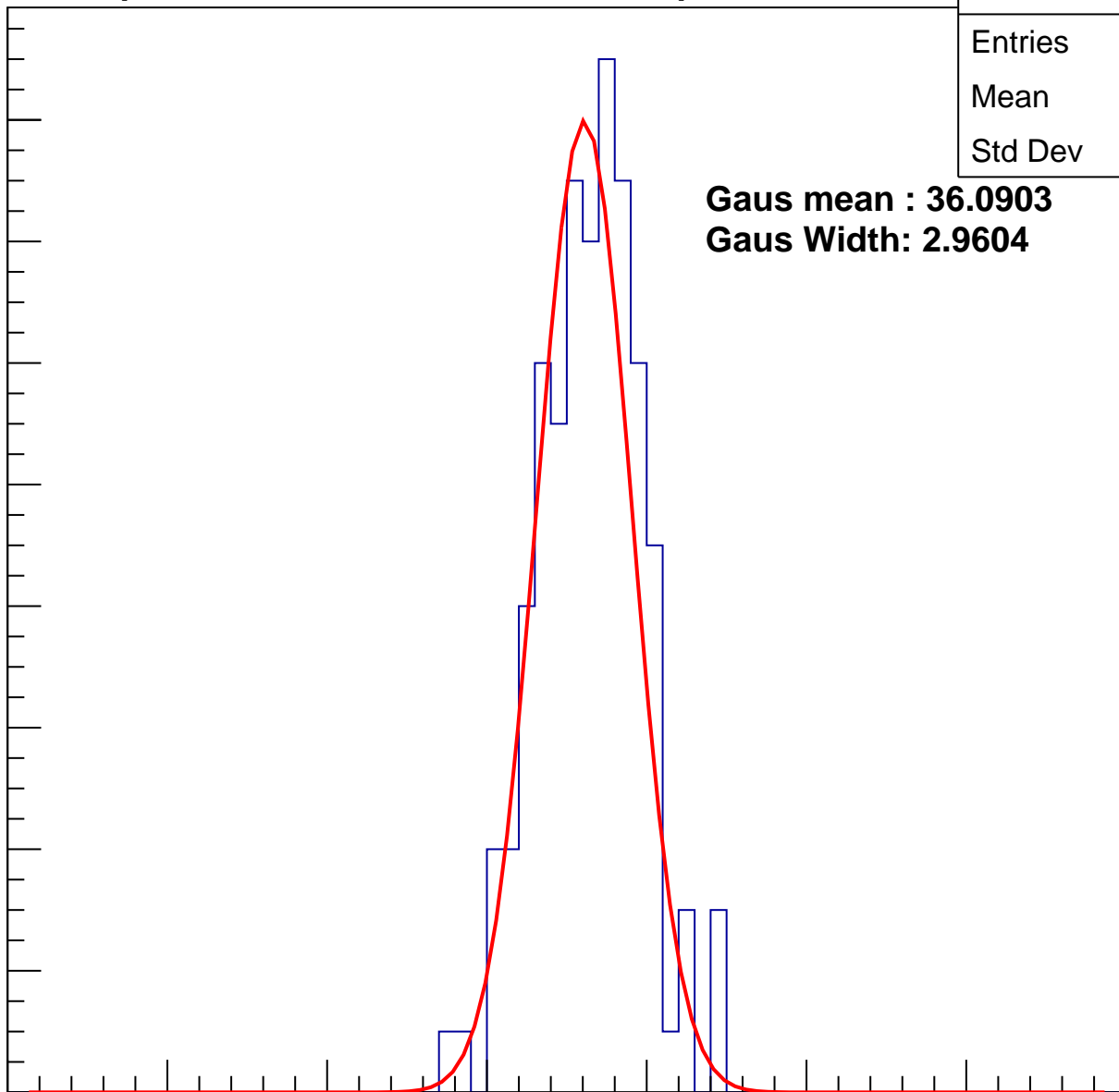
40

50

60

70

ampl



B1L001S, U20-ch1, adc2

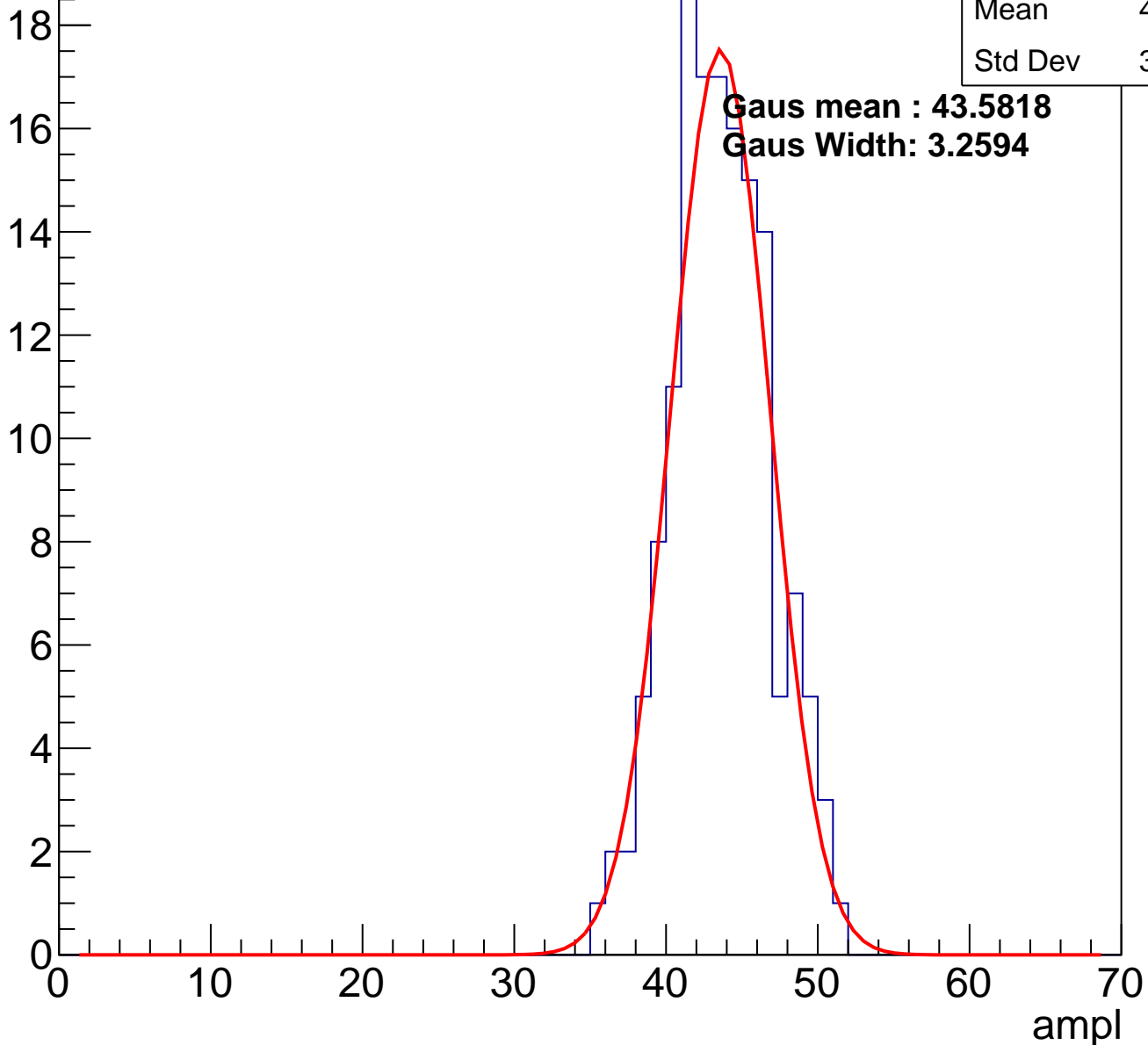
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	43.16
Std Dev	3.208

Gaus mean : 43.5818

Gaus Width: 3.2594

Entry



B1L001S, U20-ch1, adc3

calib_packv5_042523_0143.root, FC#2, port C2

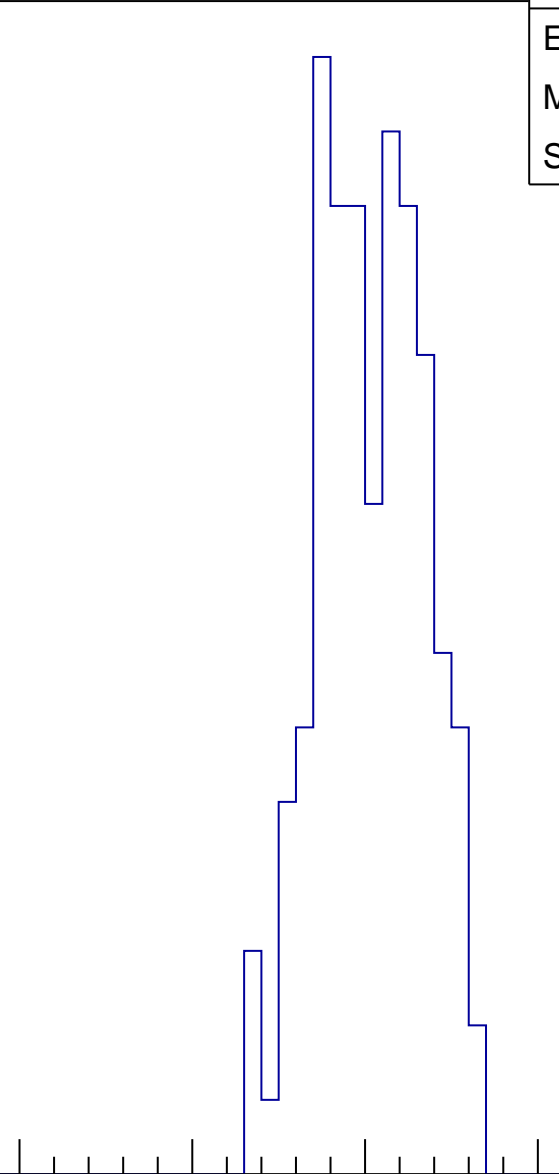
Entries	118
Mean	49.86
Std Dev	3.054

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch1, adc4

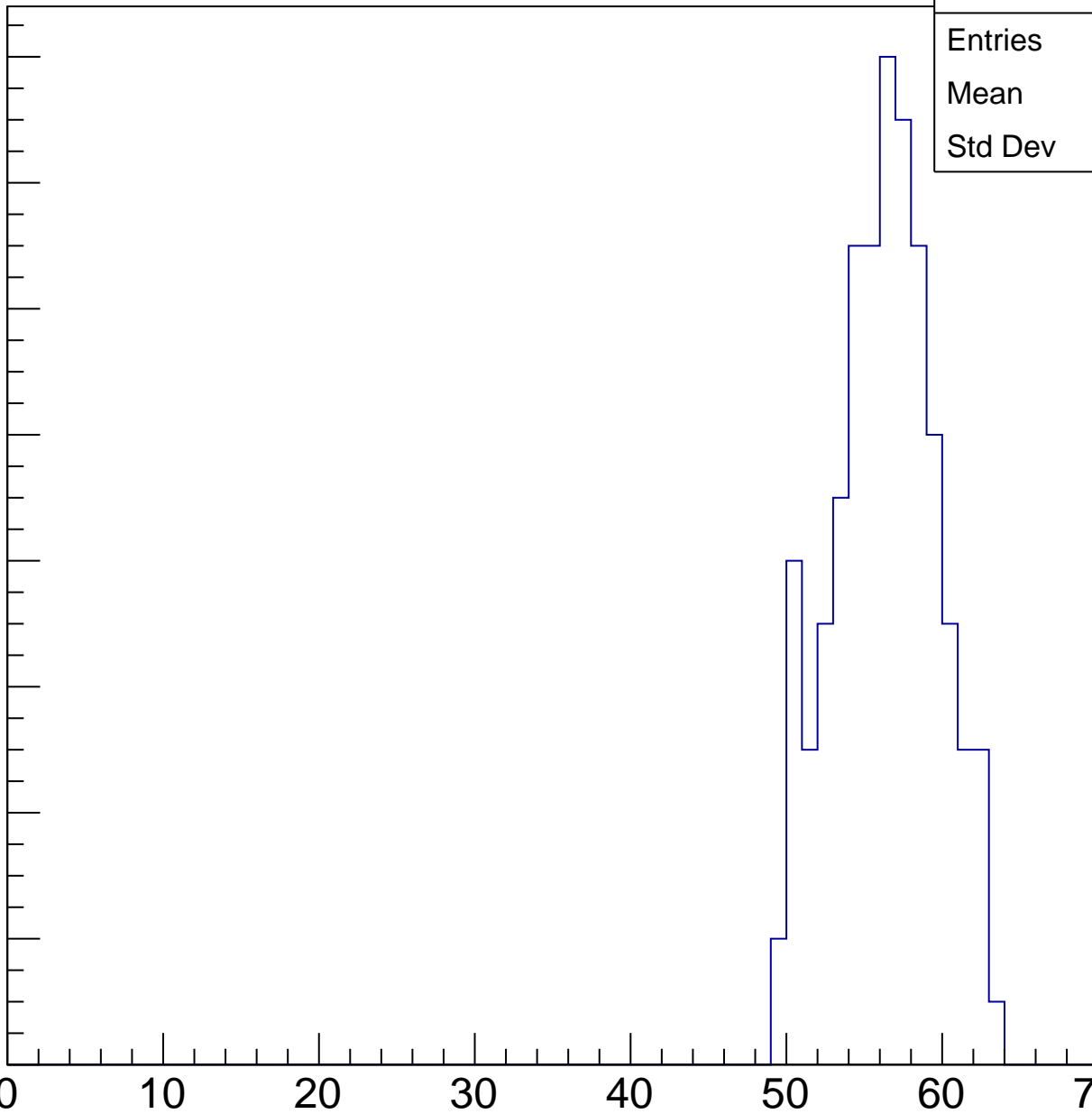
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	55.84
Std Dev	3.281

ampl



B1L001S, U20-ch1, adc5

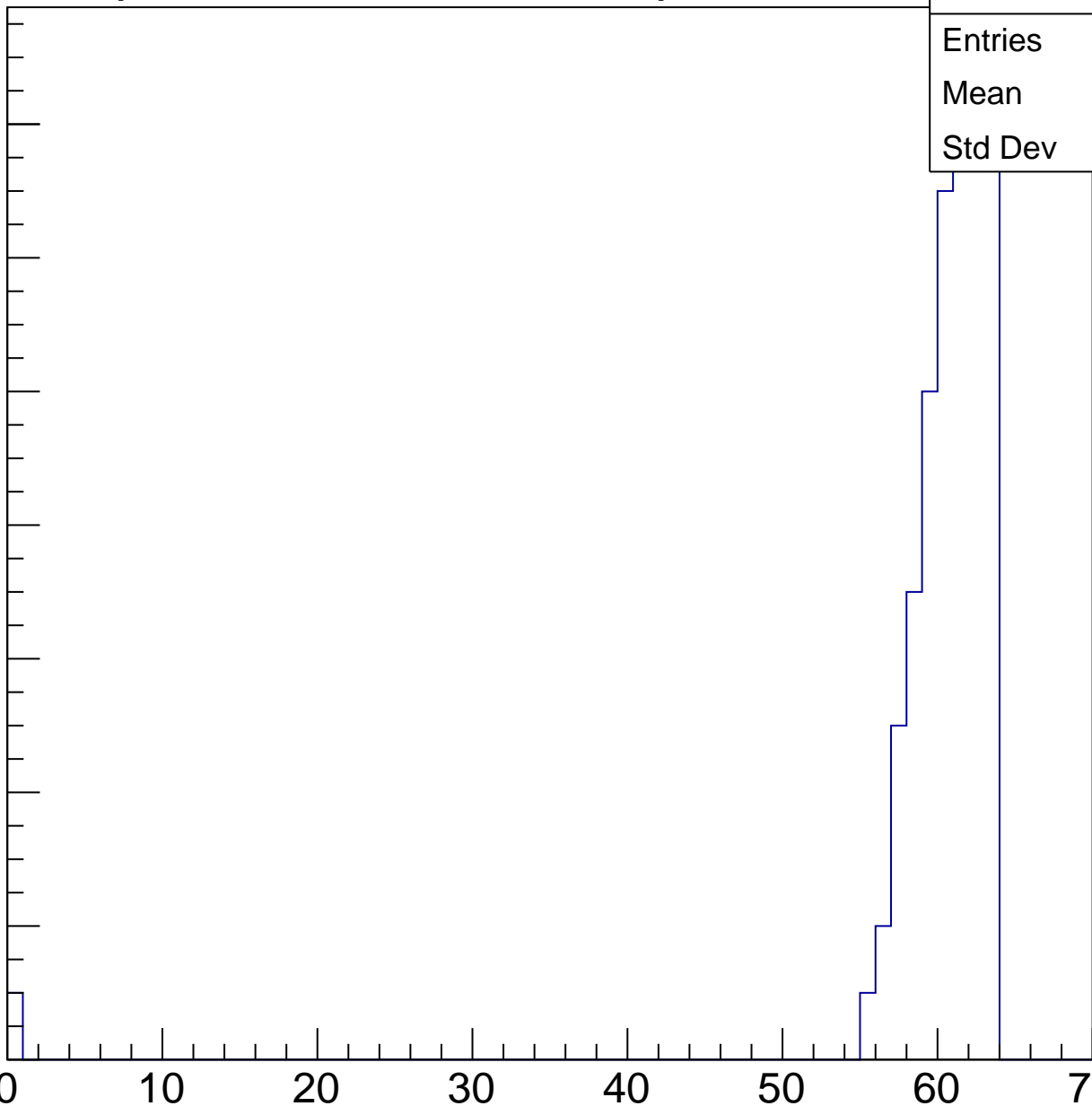
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	82
Mean	59.7
Std Dev	6.929

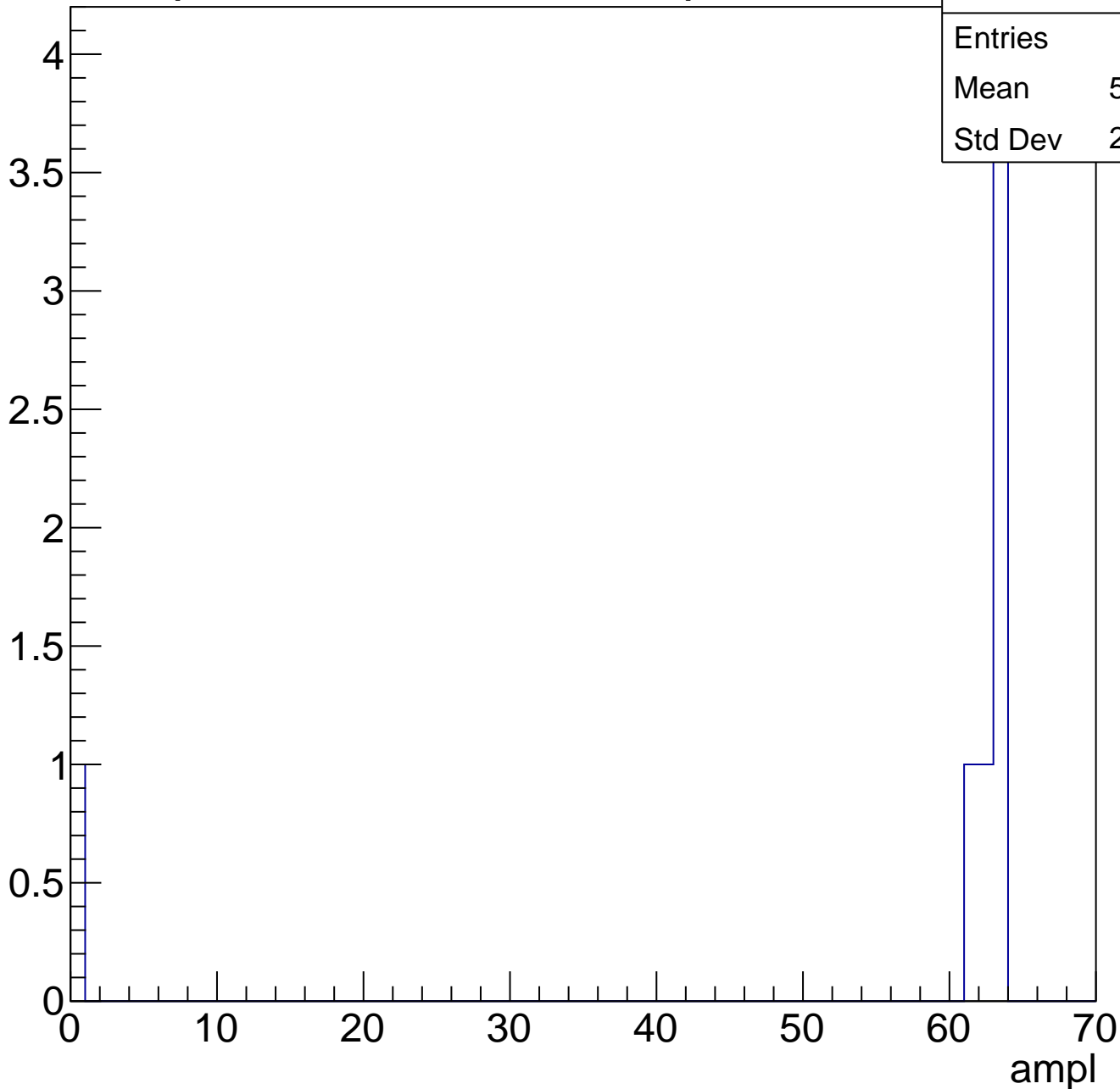
ampl



B1L001S, U20-ch1, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch1, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U20-ch2, adc0

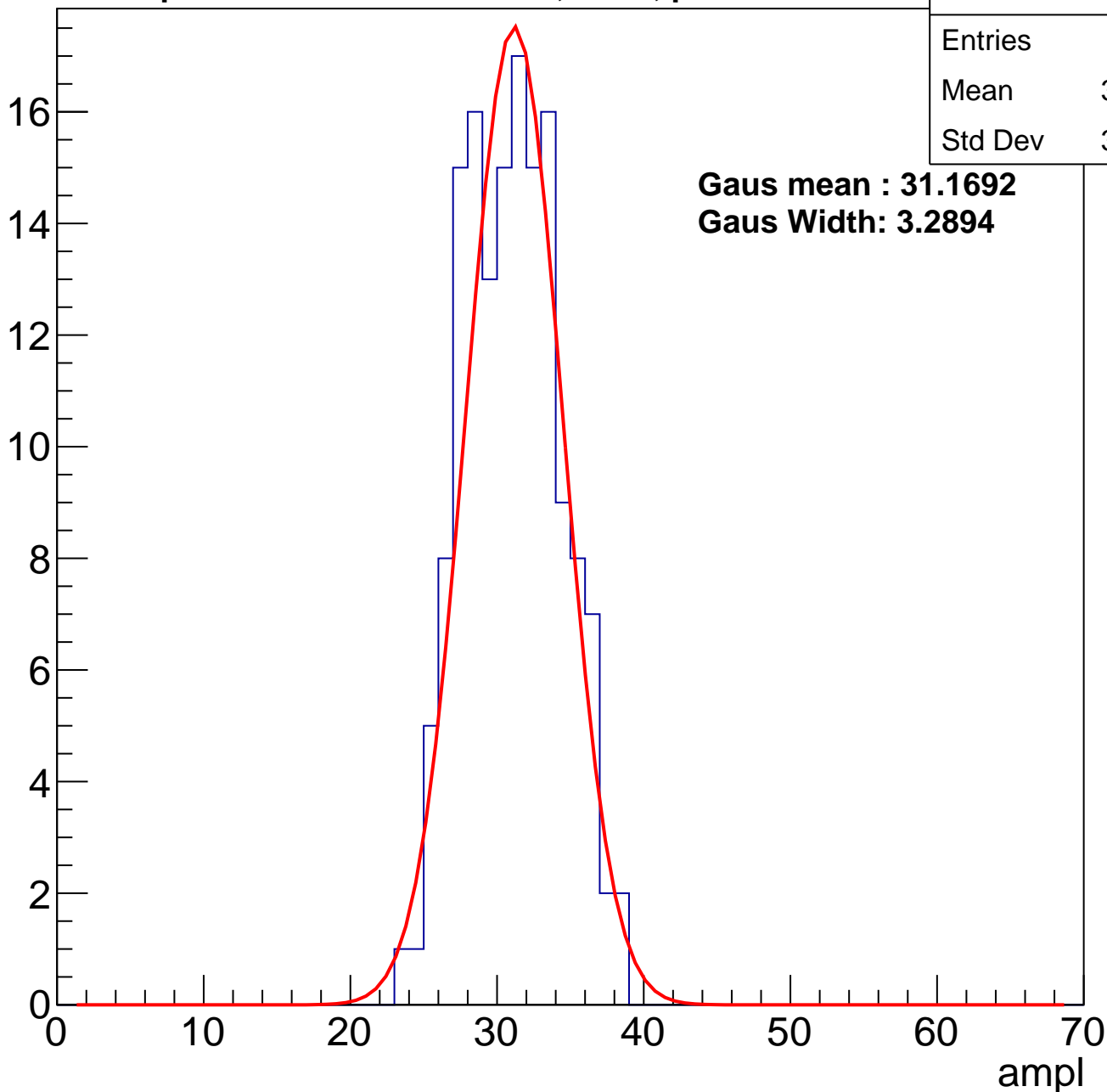
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	30.55
Std Dev	3.212

Gaus mean : 31.1692

Gaus Width: 3.2894

Entry



B1L001S, U20-ch2, adc1

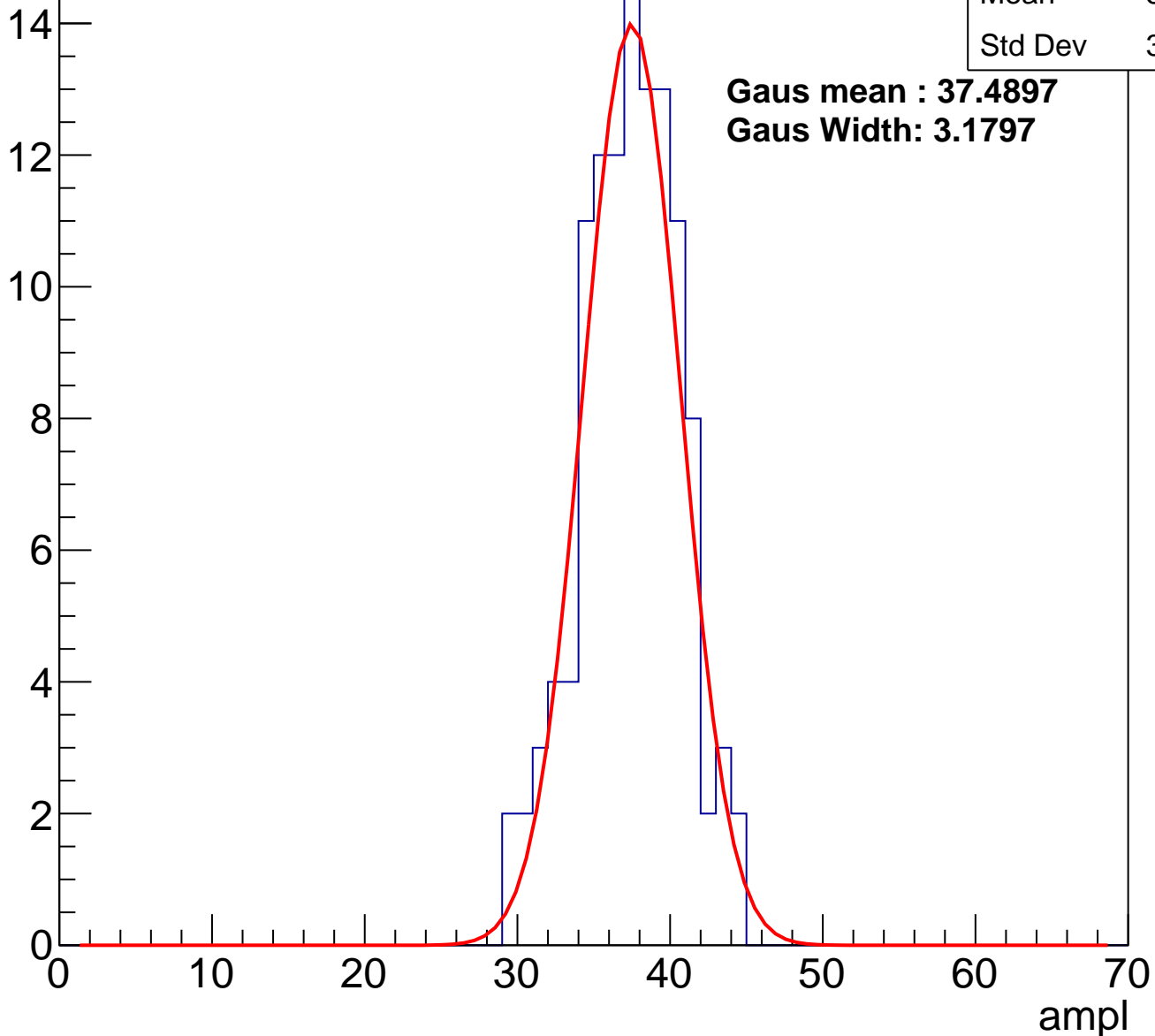
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	36.94
Std Dev	3.209

Gaus mean : 37.4897

Gaus Width: 3.1797

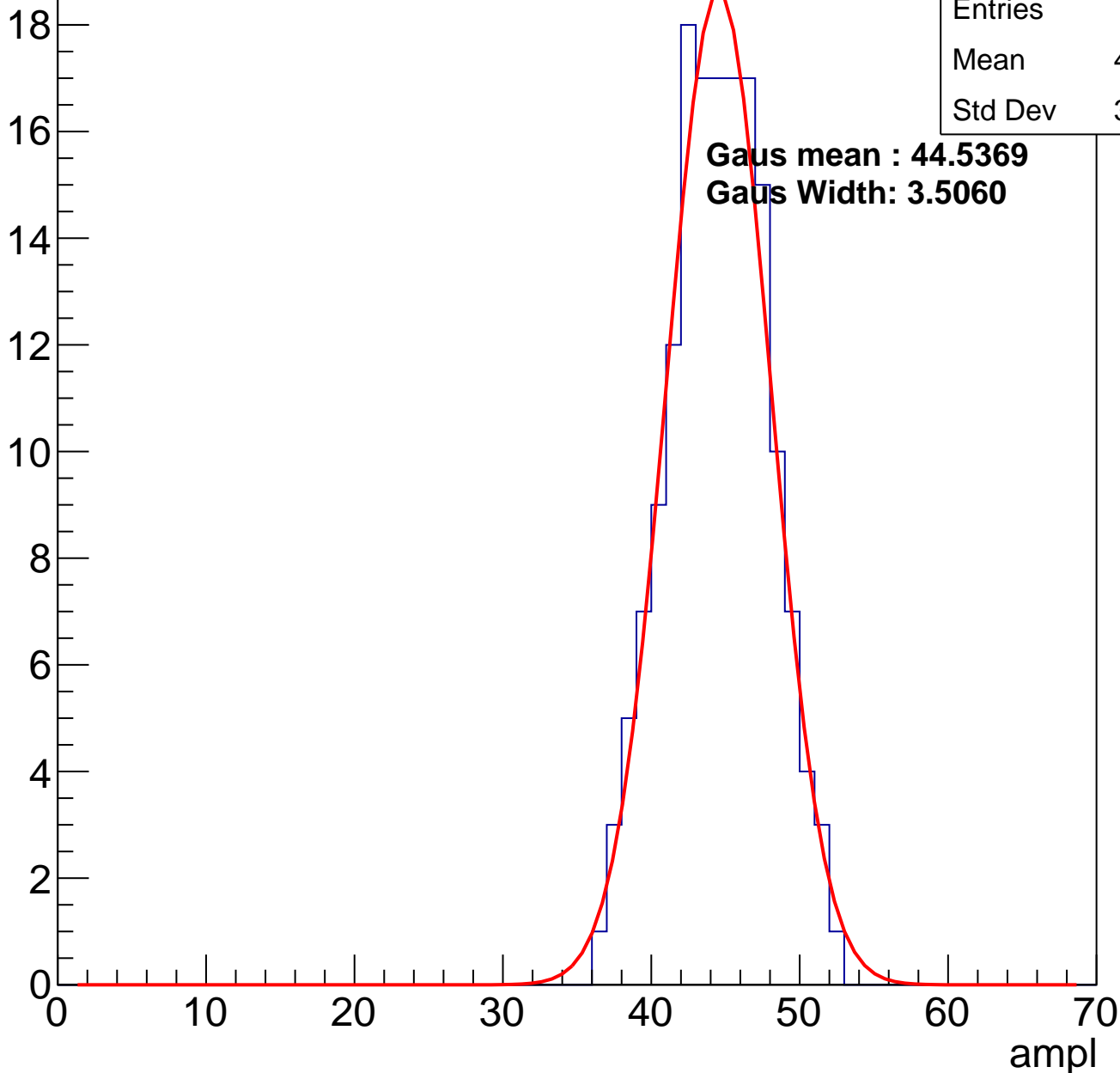
Entry



B1L001S, U20-ch2, adc2

calib_packv5_042523_0143.root, FC#2, port C2

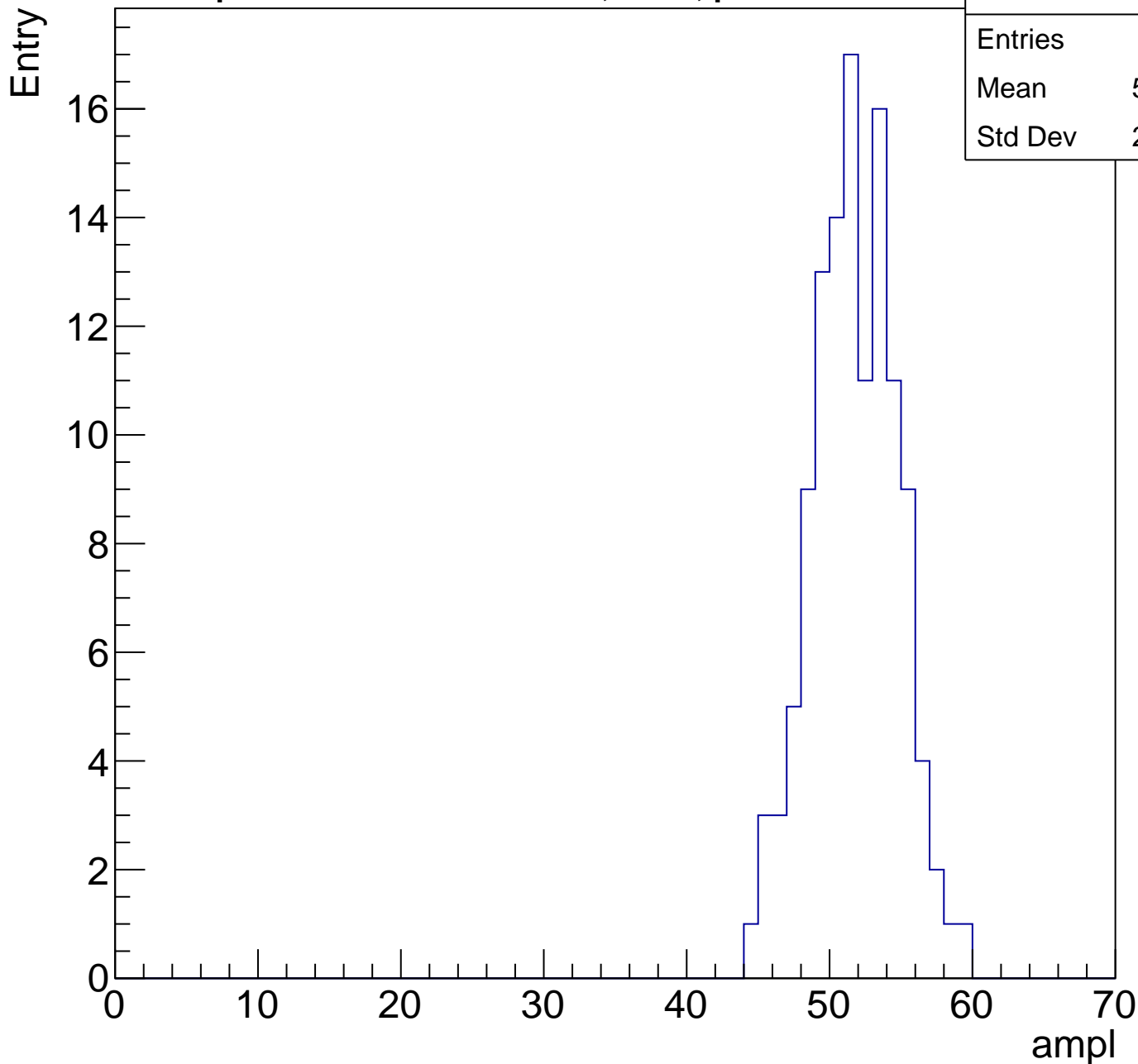
Entry



B1L001S, U20-ch2, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	51.27
Std Dev	2.963



B1L001S, U20-ch2, adc4

calib_packv5_042523_0143.root, FC#2, port C2

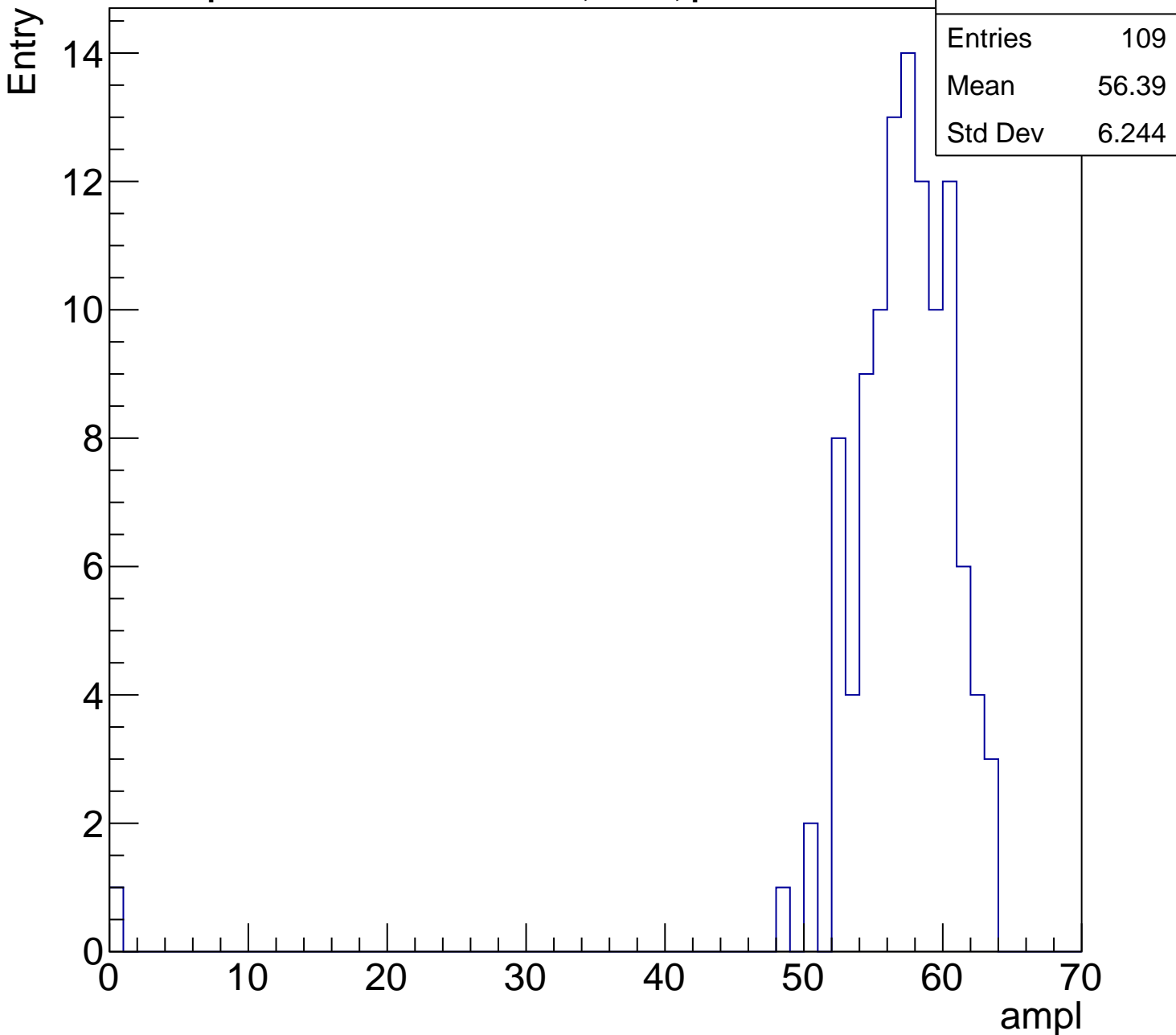
Entries	109
Mean	56.39
Std Dev	6.244

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch2, adc5

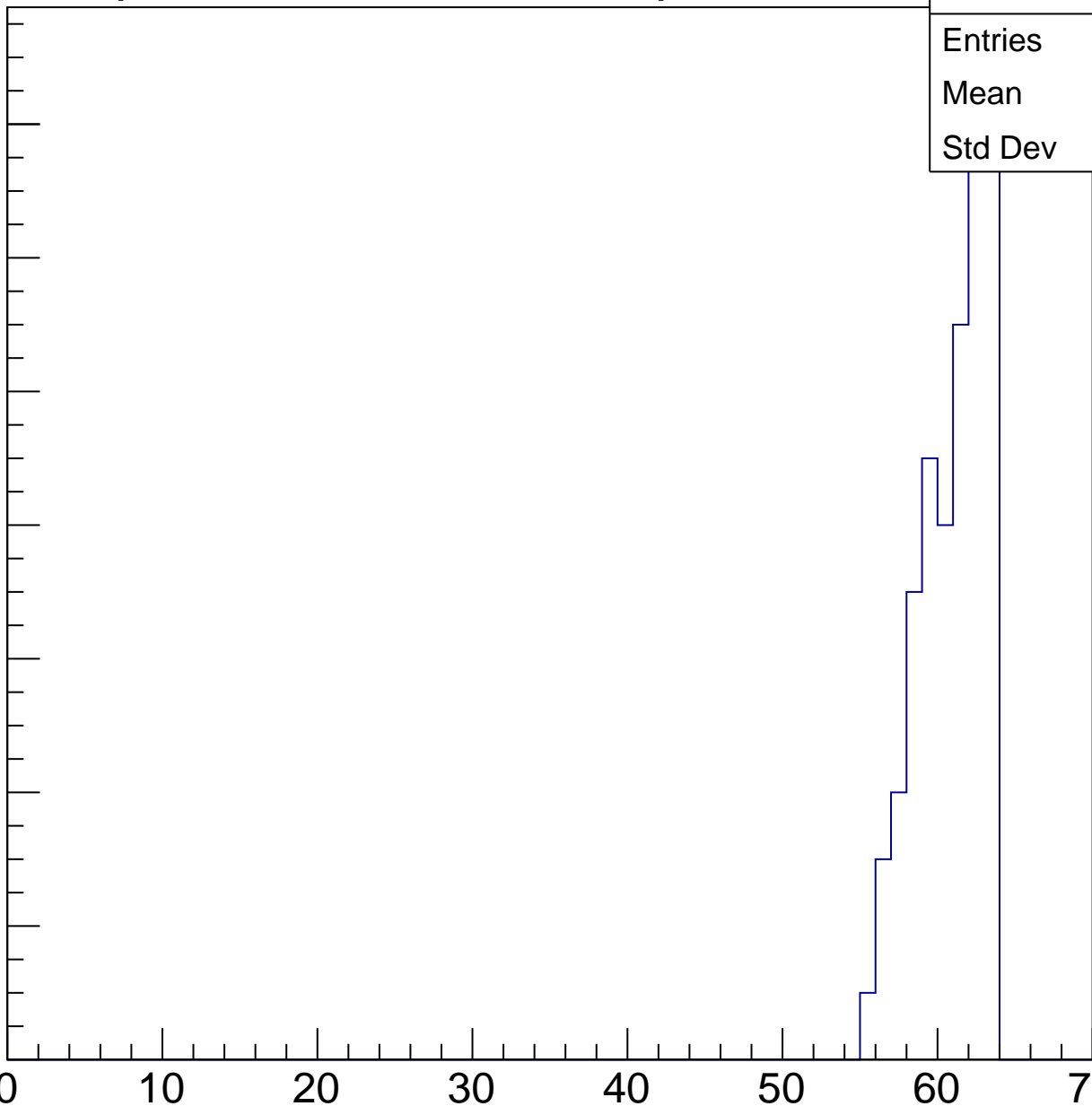
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	60.43
Std Dev	2.14

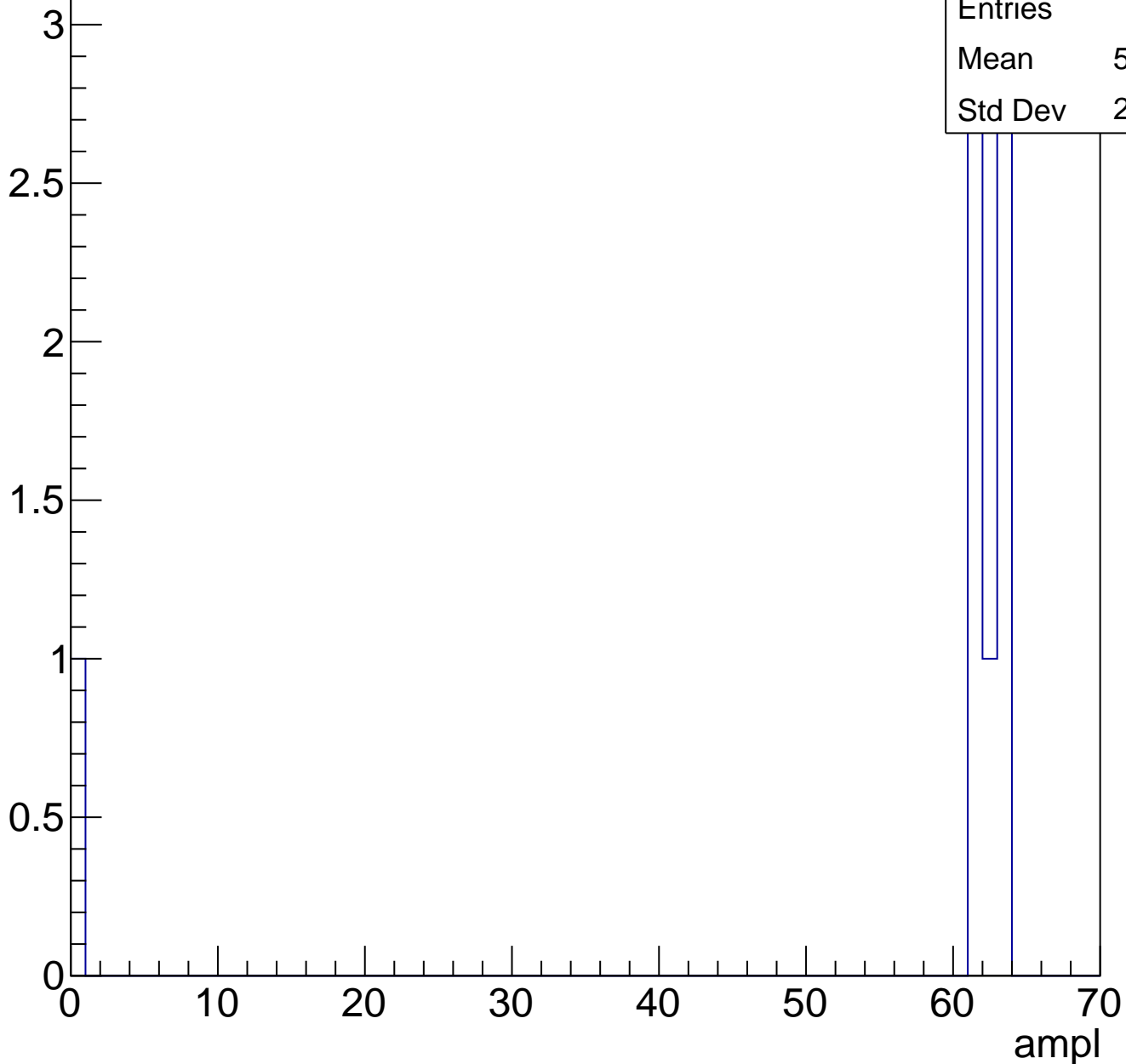
ampl



B1L001S, U20-ch2, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch2, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	25
Std Dev	0

B1L001S, U20-ch3, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	30.57
Std Dev	2.611

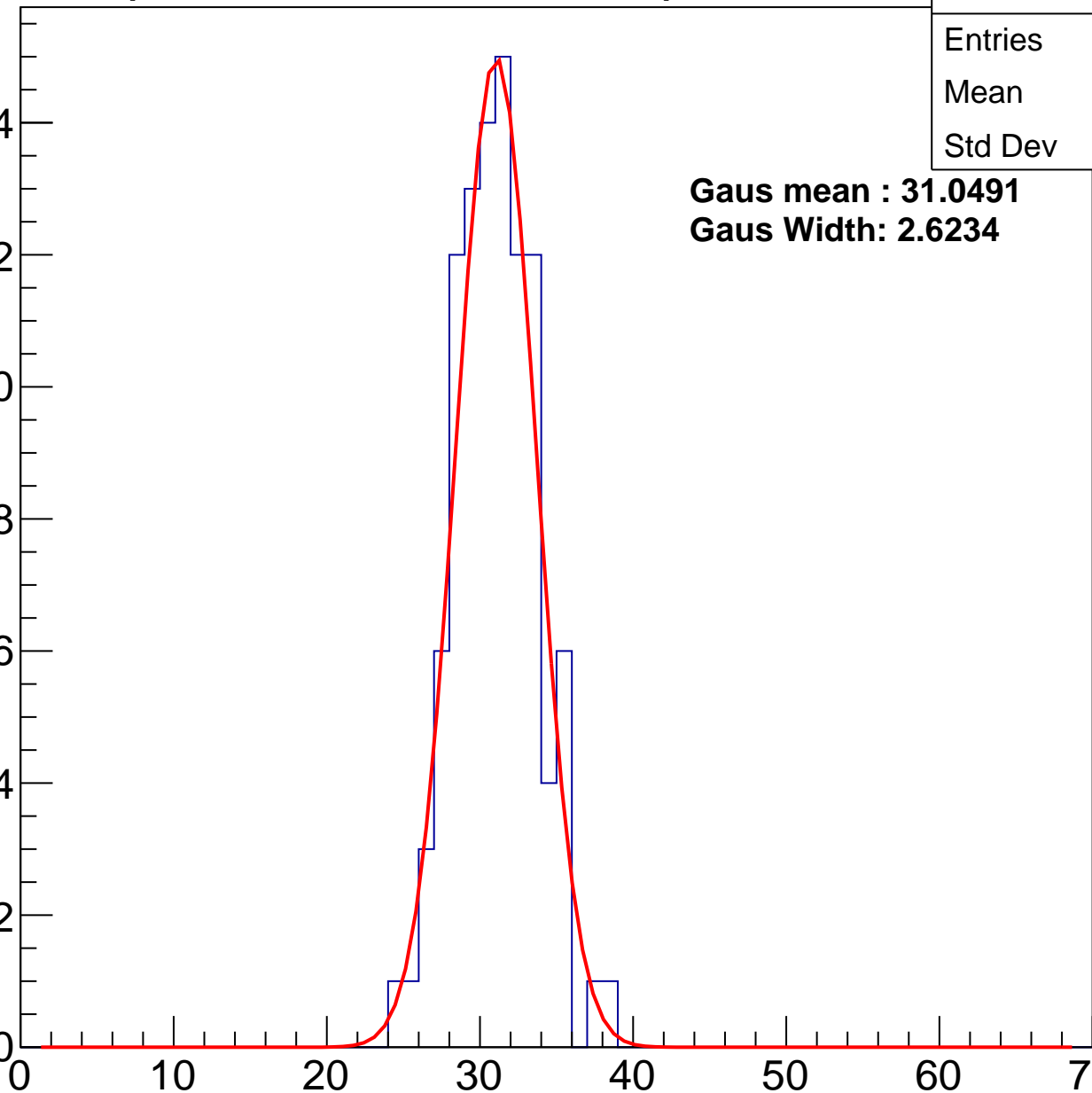
Gaus mean : 31.0491

Gaus Width: 2.6234

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch3, adc1

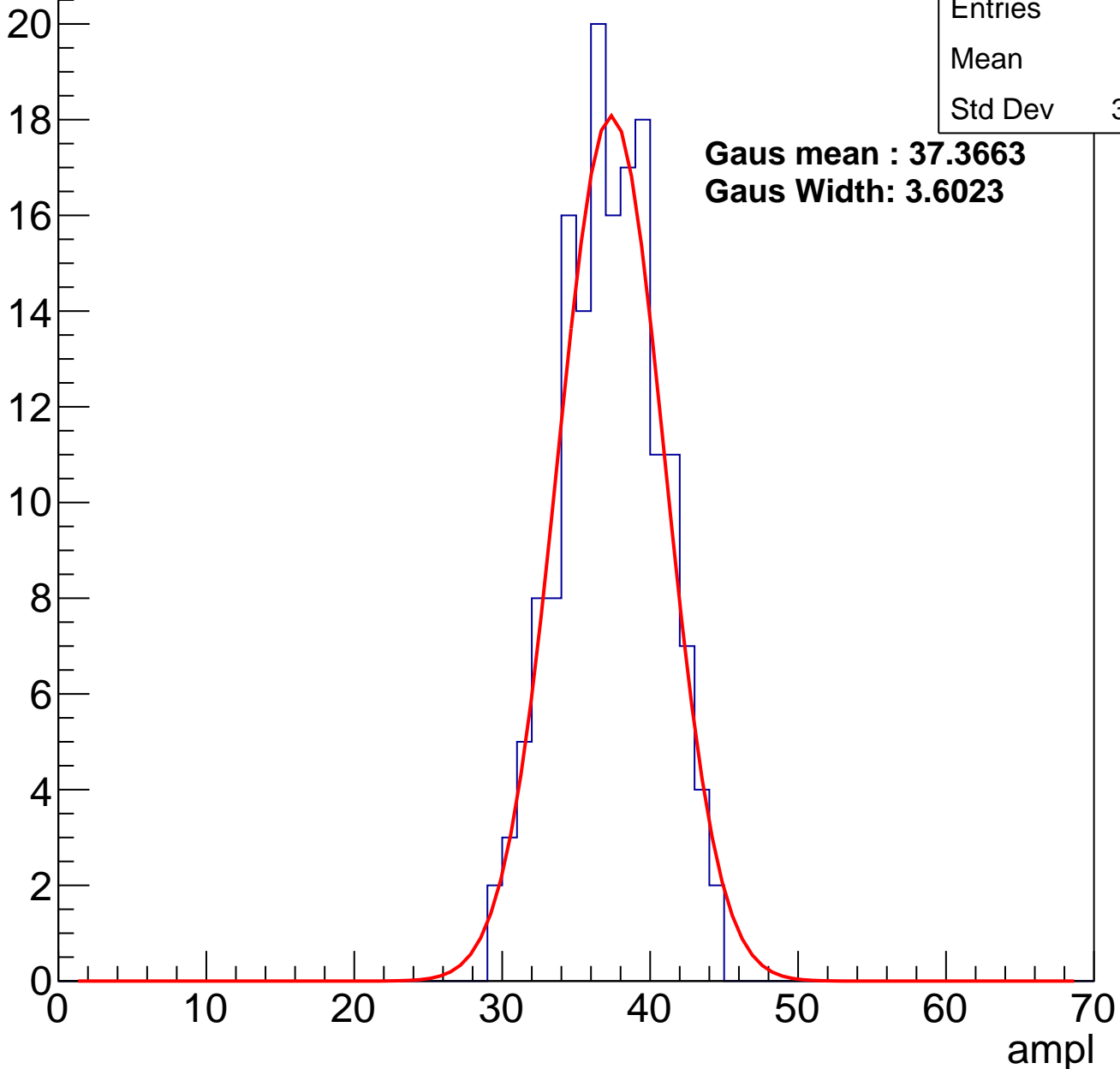
calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	36.8
Std Dev	3.313

Gaus mean : 37.3663

Gaus Width: 3.6023

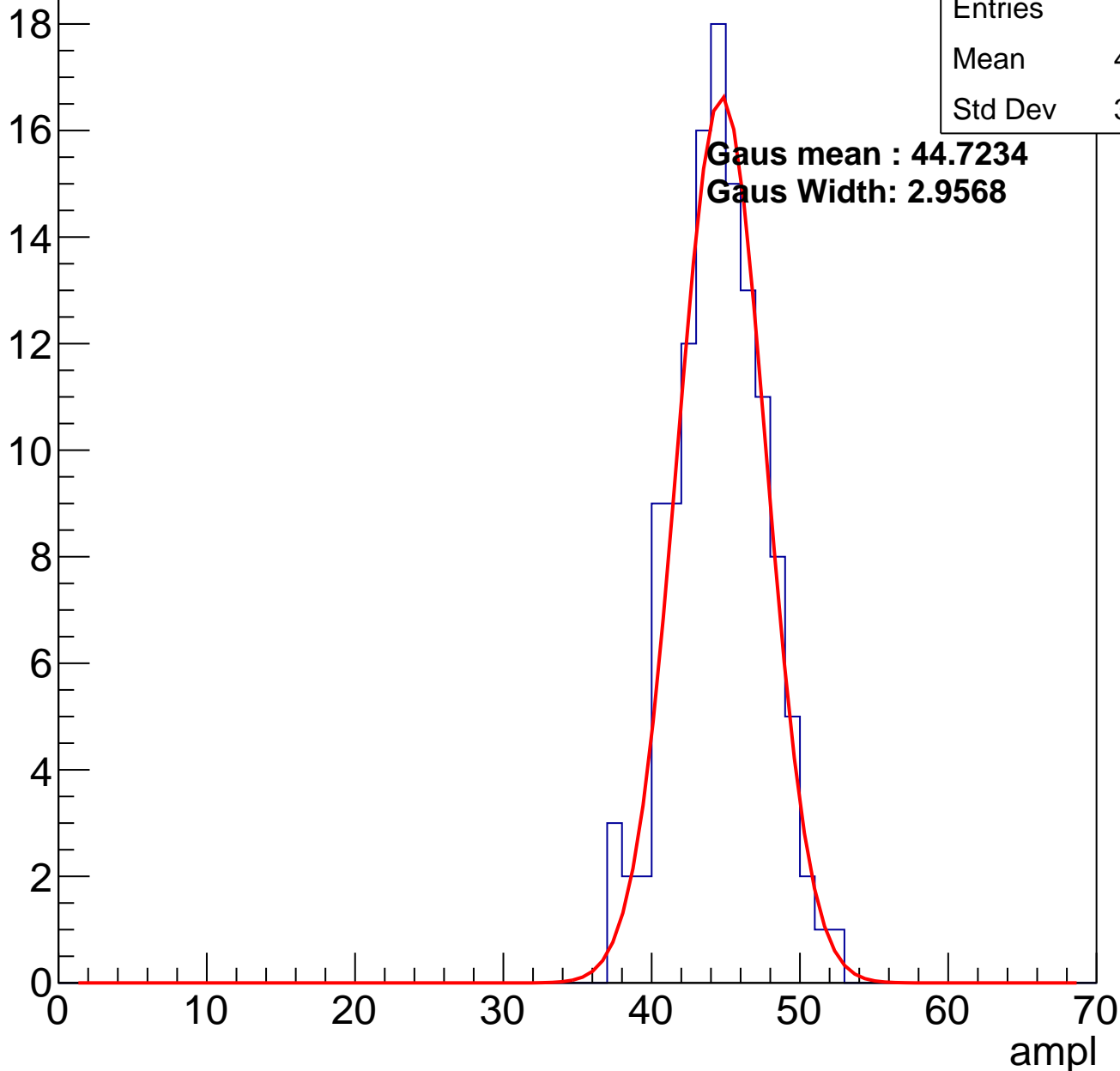
Entry



B1L001S, U20-ch3, adc2

calib_packv5_042523_0143.root, FC#2, port C2

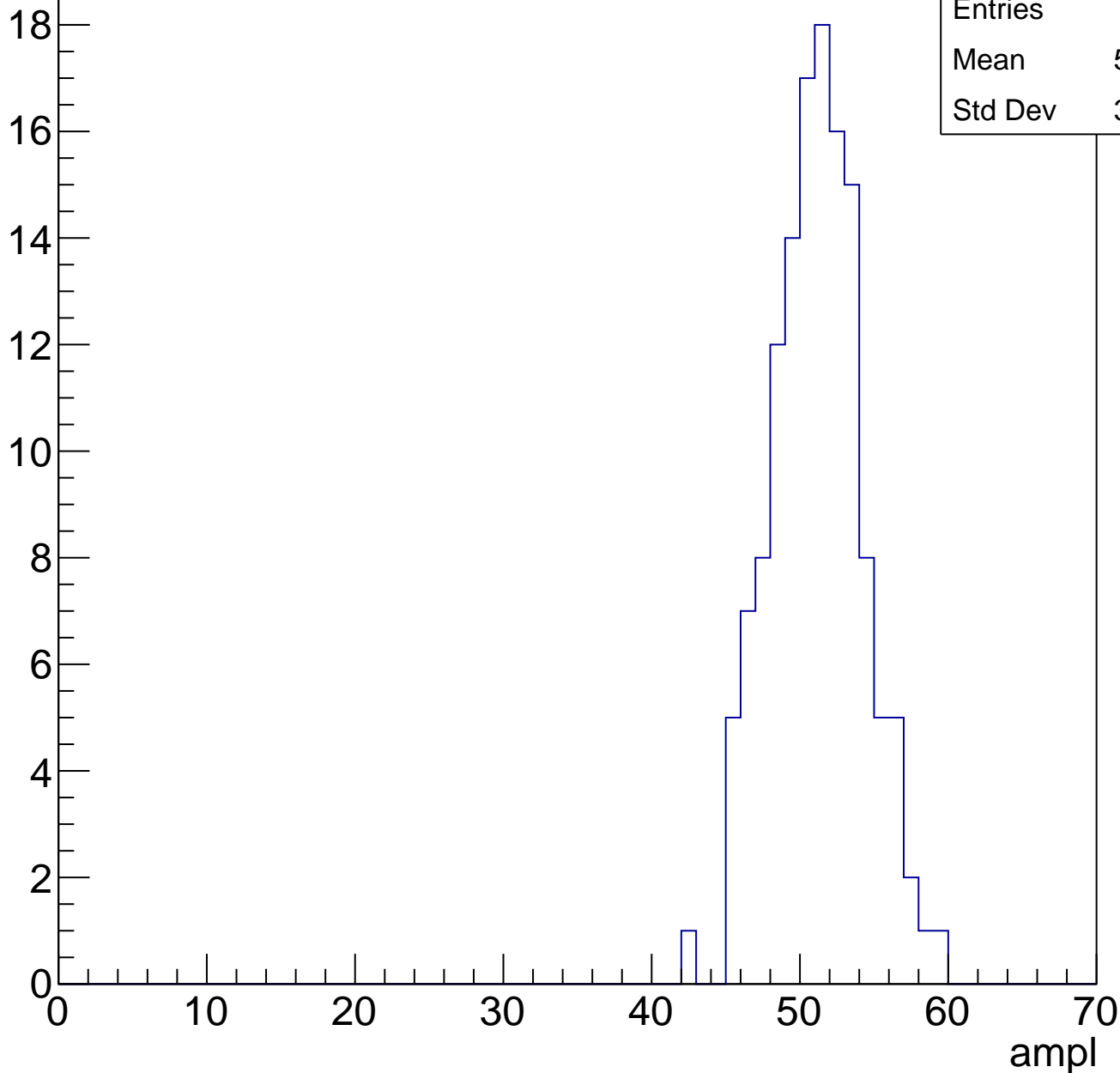
Entry



B1L001S, U20-ch3, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	135
Mean	50.67
Std Dev	3.065

B1L001S, U20-ch3, adc4

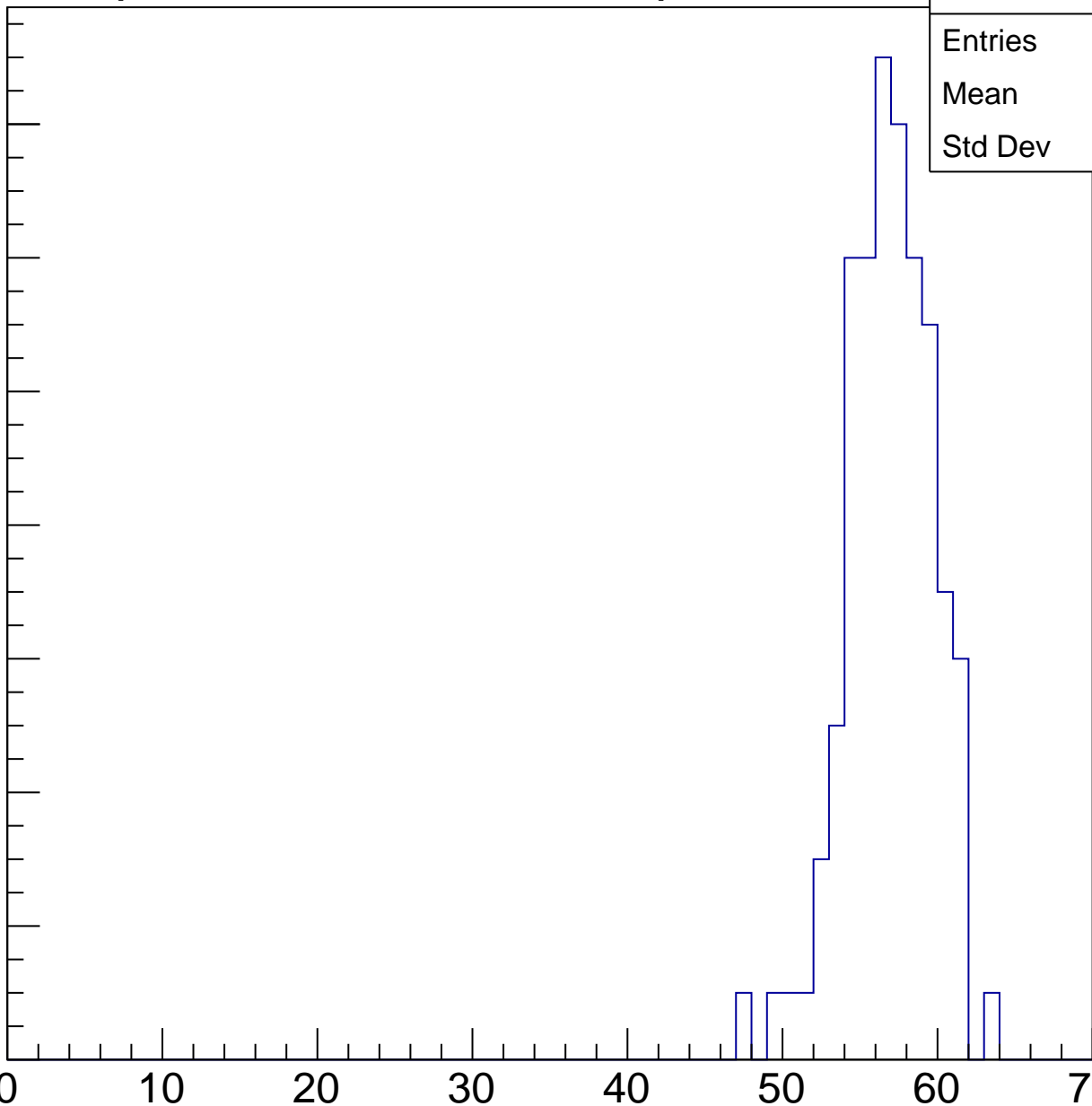
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	102
Mean	56.45
Std Dev	2.792

ampl



B1L001S, U20-ch3, adc5

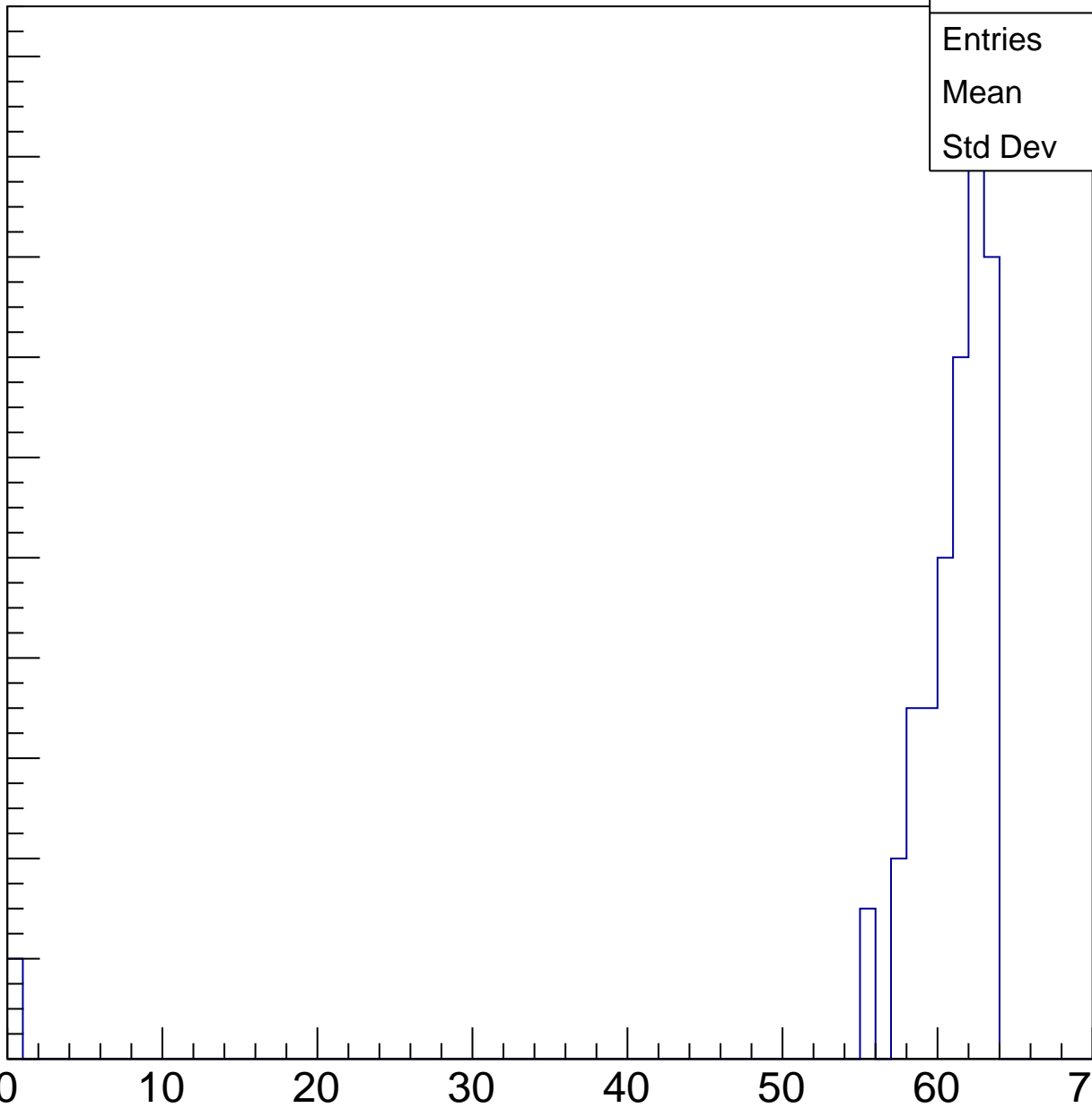
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.2
Std Dev	9.526

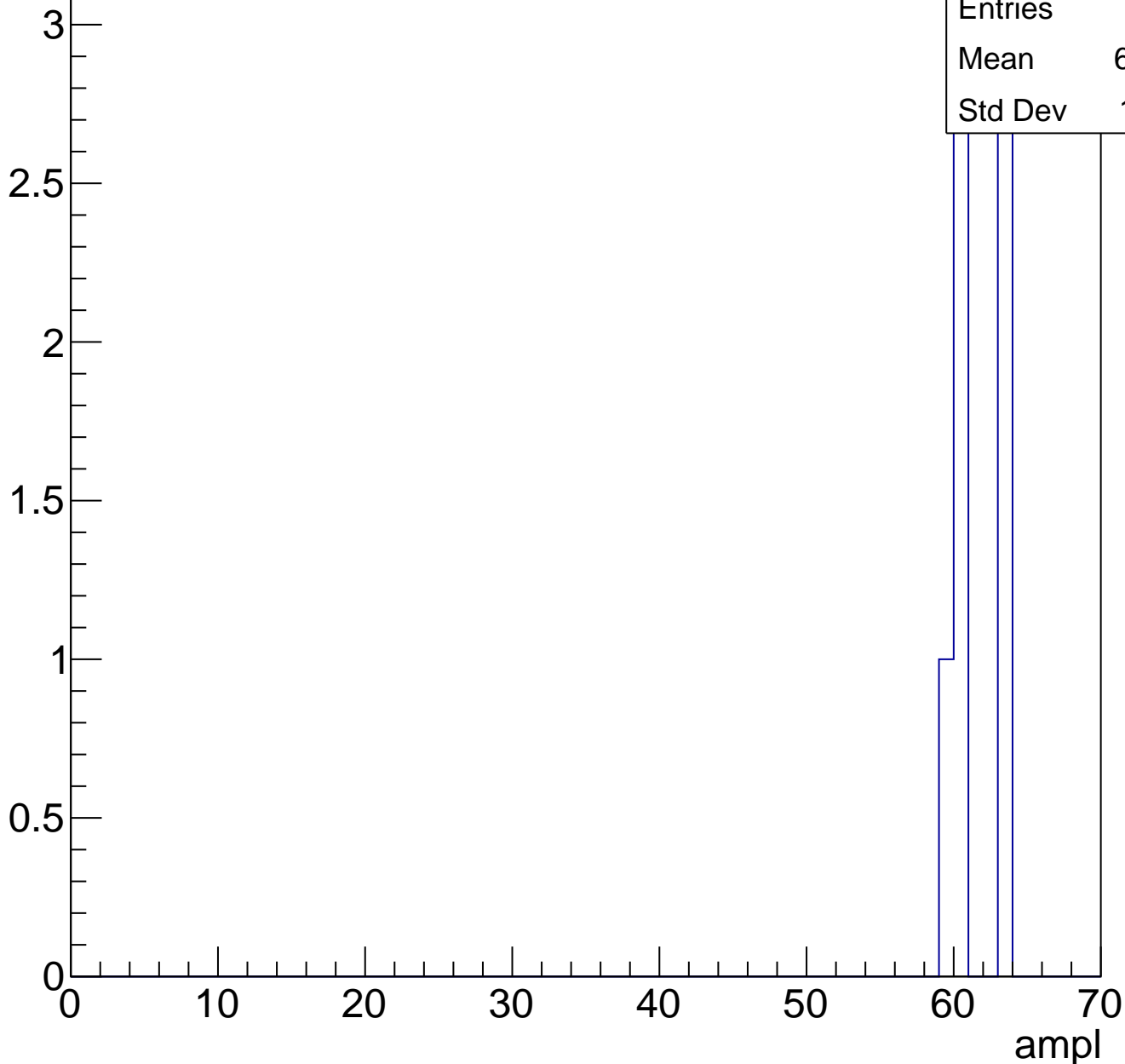
ampl



B1L001S, U20-ch3, adc6

calib_packv5_042523_0143.root, FC#2, port C2

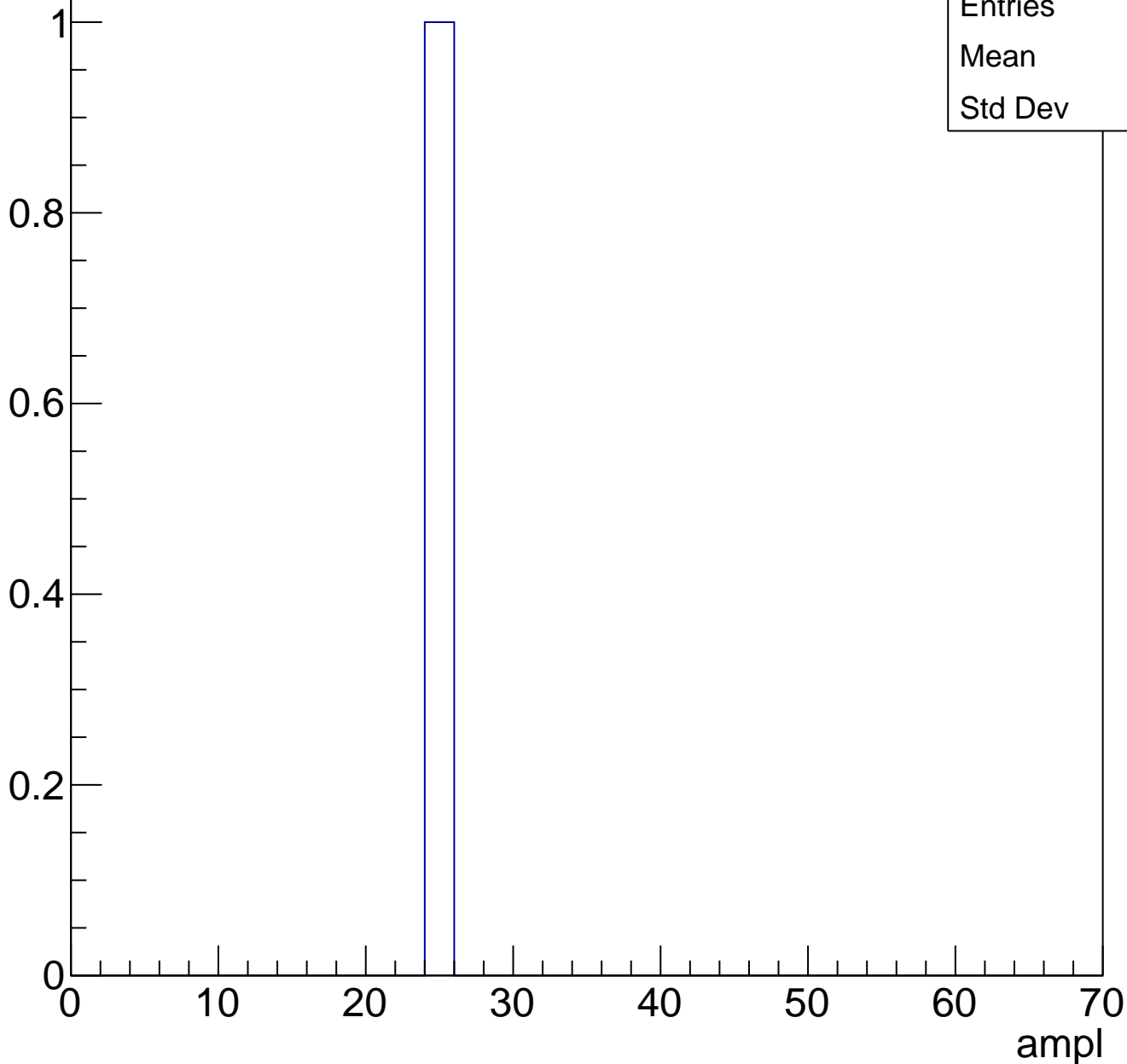
Entry



B1L001S, U20-ch3, adc7

calib_packv5_042523_0143.root, FC#2, port C2

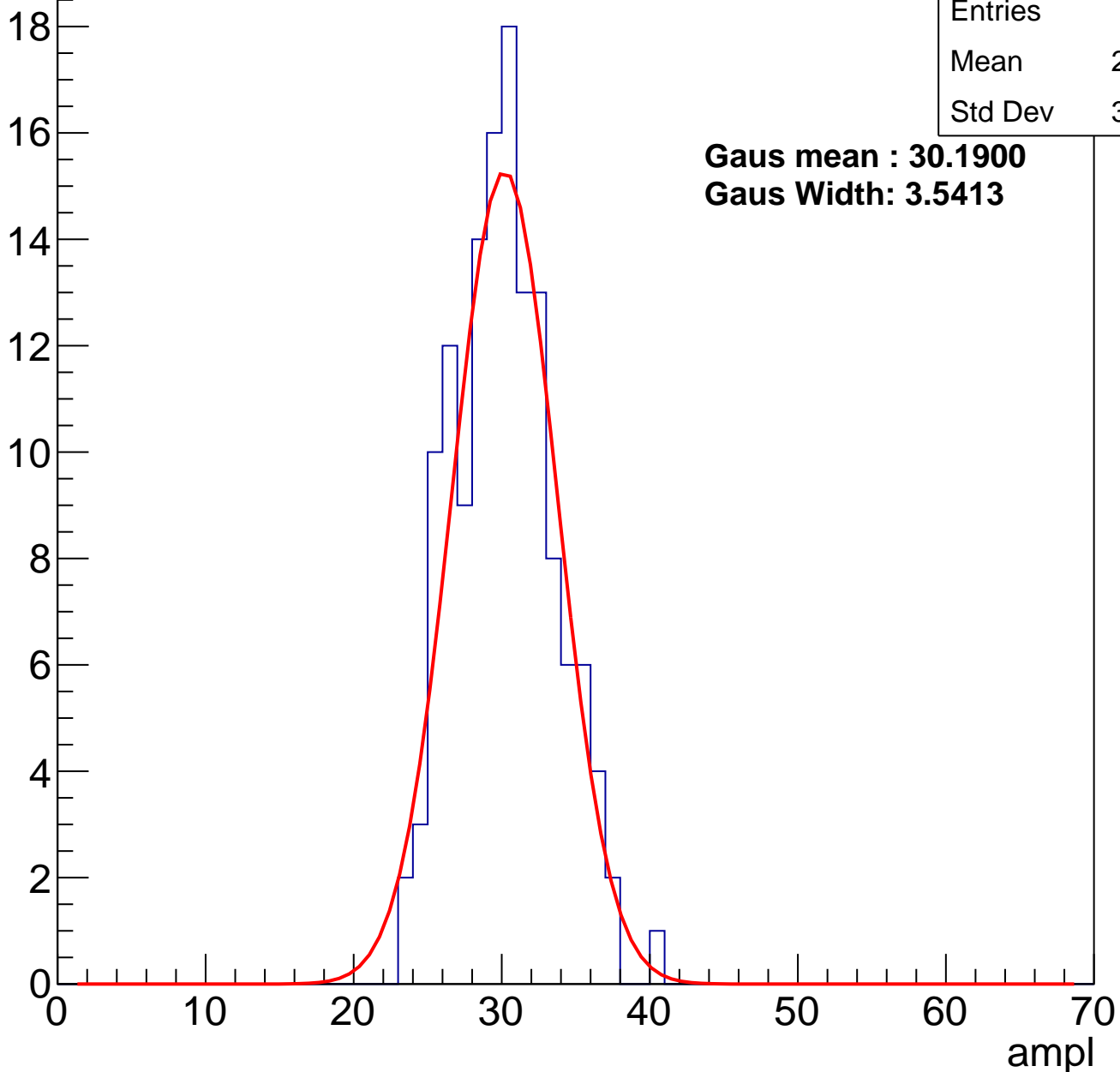
Entry



B1L001S, U20-ch4, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	137
Mean	29.74
Std Dev	3.327

B1L001S, U20-ch4, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	35.71
Std Dev	2.916

Gaus mean : 36.0609

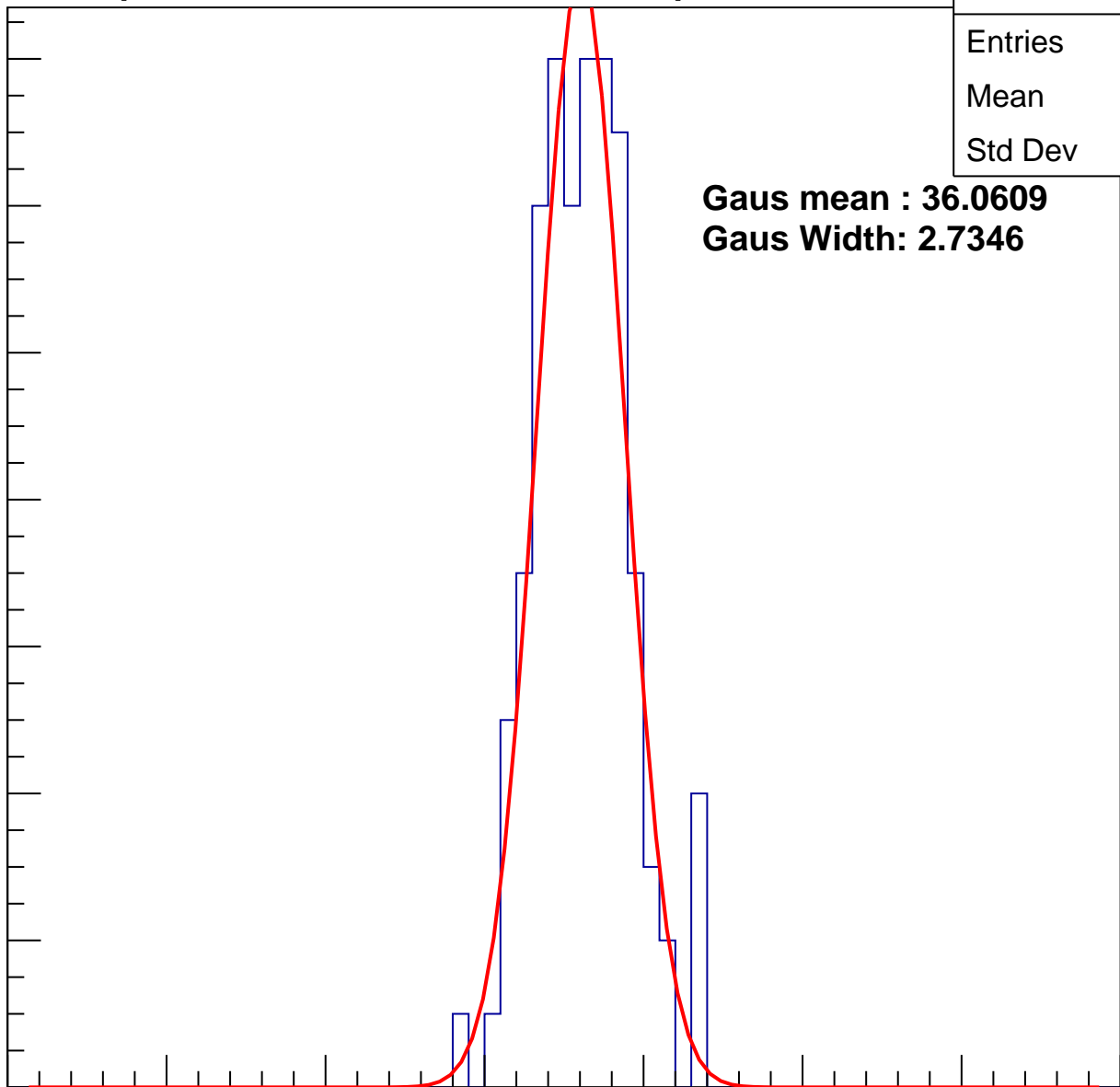
Gaus Width: 2.7346

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch4, adc2

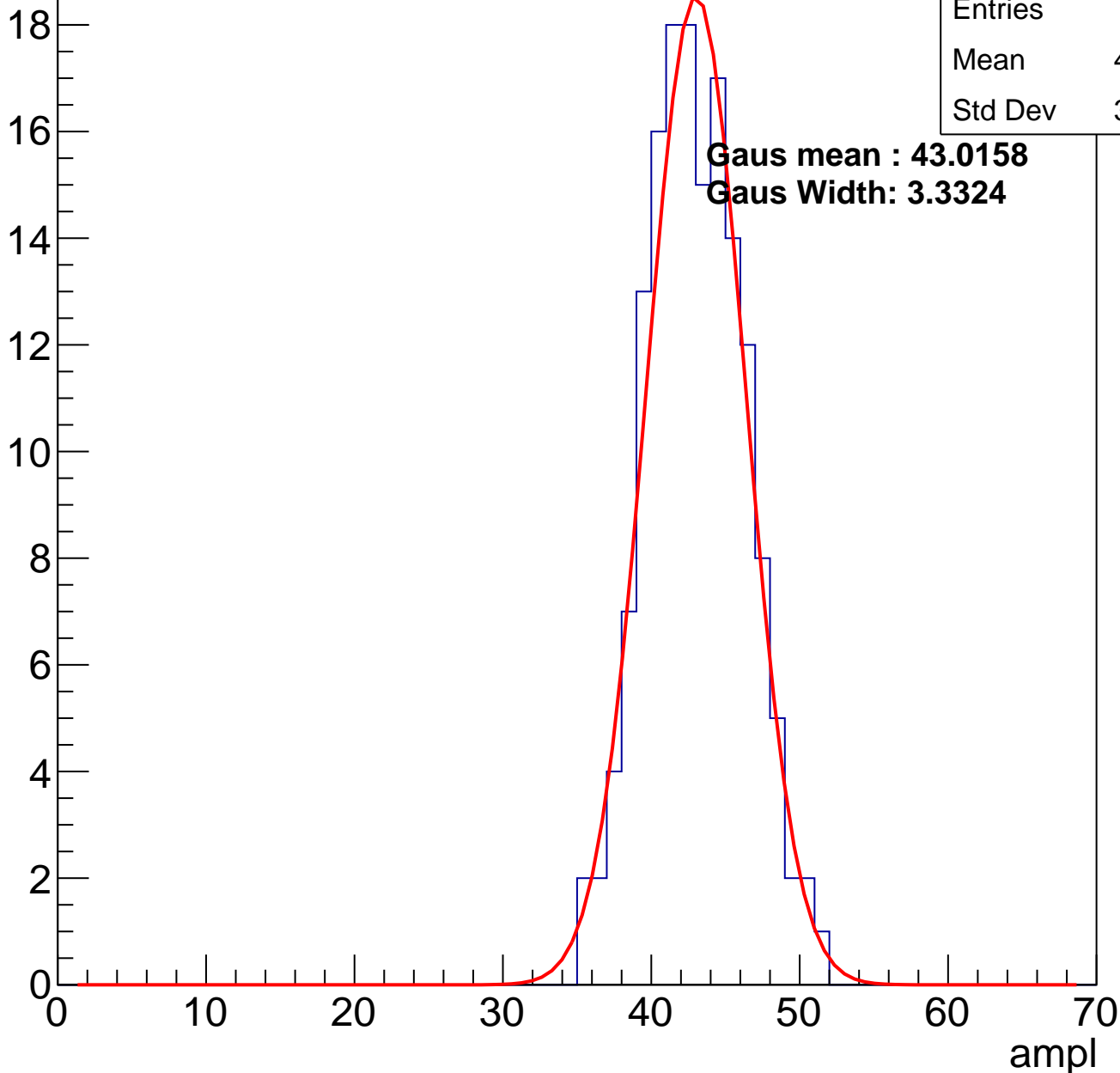
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	42.54
Std Dev	3.225

Gaus mean : 43.0158

Gaus Width: 3.3324

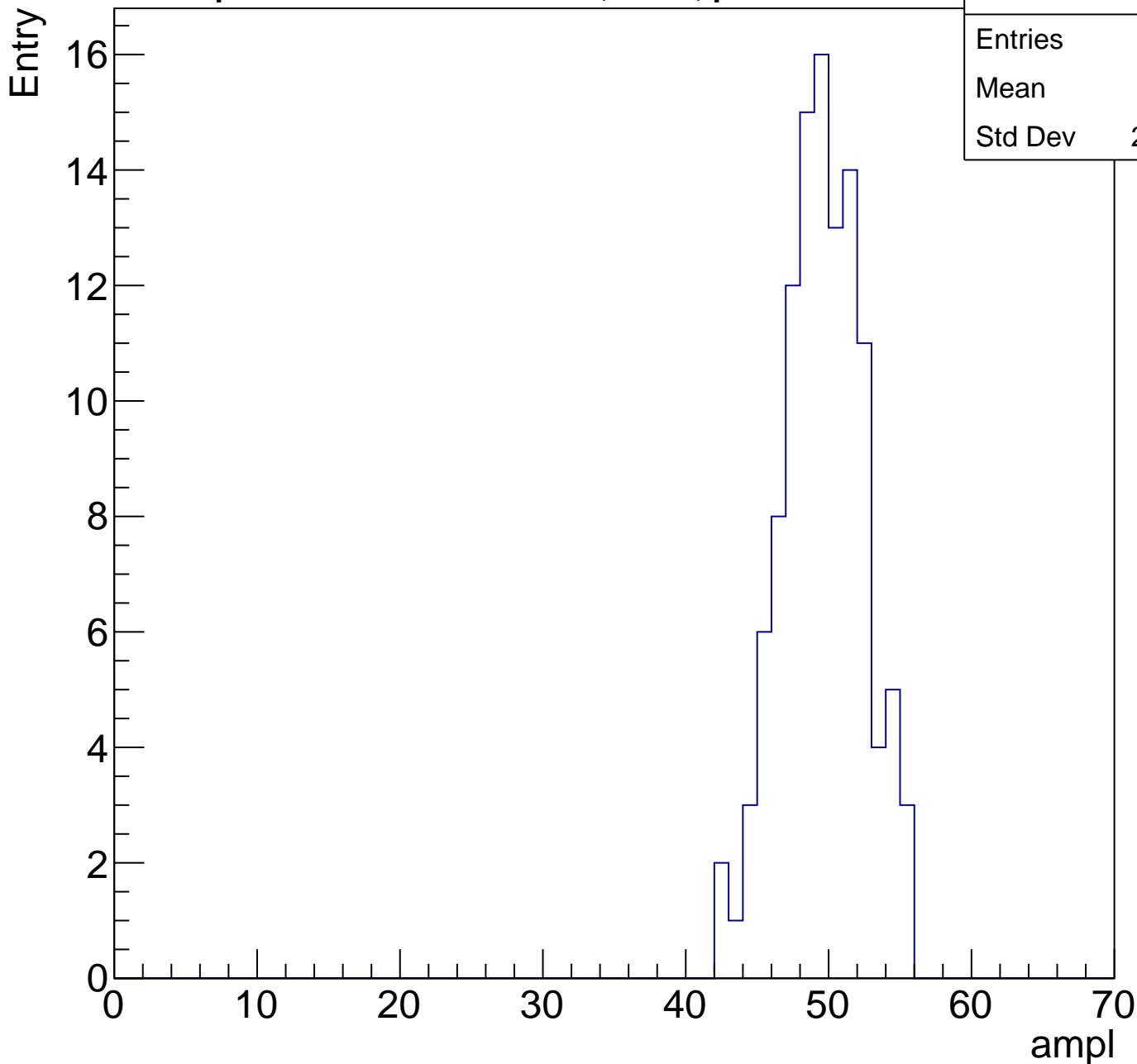
Entry



B1L001S, U20-ch4, adc3

calib_packv5_042523_0143.root, FC#2, port C2

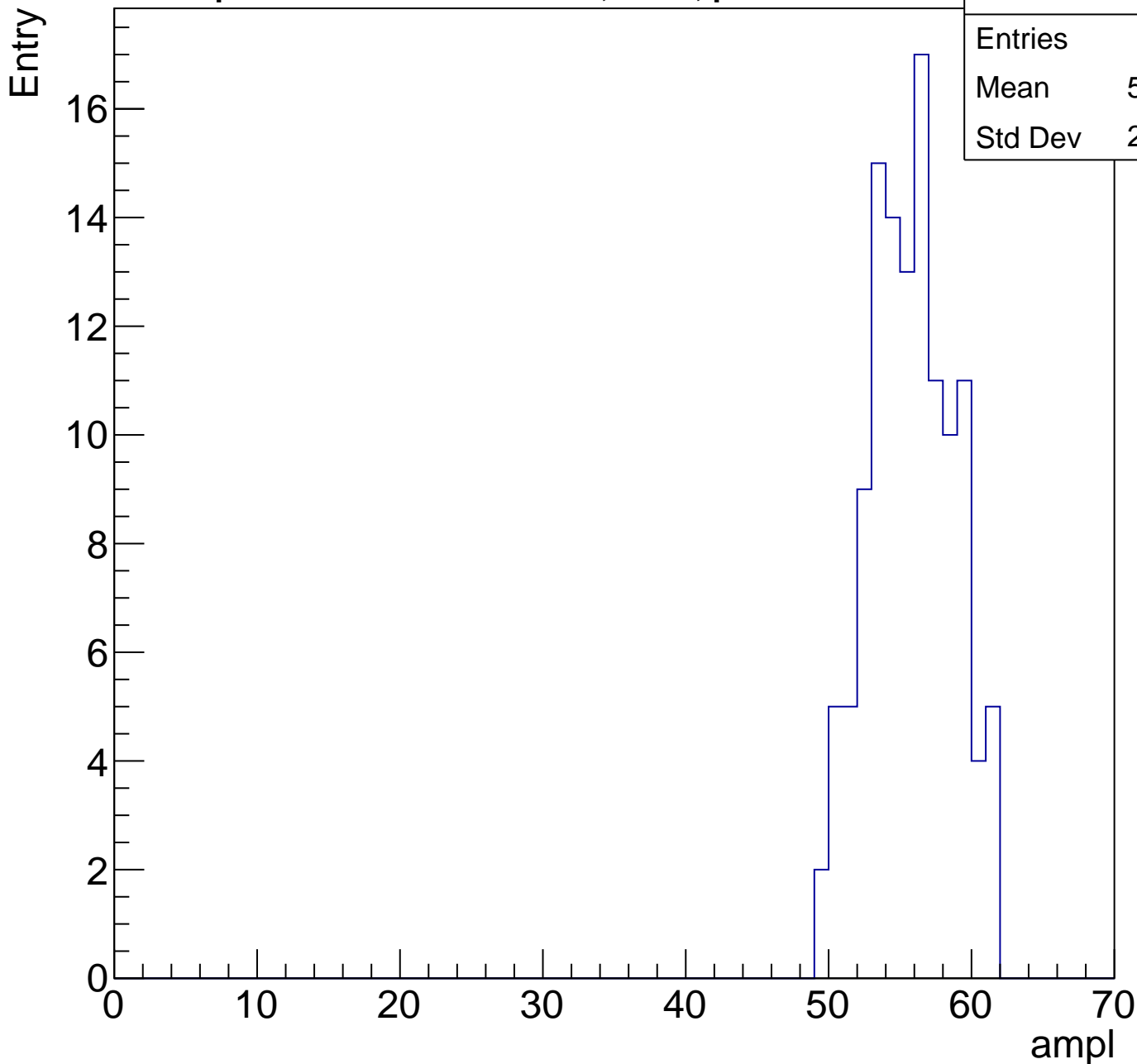
Entries	113
Mean	49.1
Std Dev	2.828



B1L001S, U20-ch4, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	55.29
Std Dev	2.916



B1L001S, U20-ch4, adc5

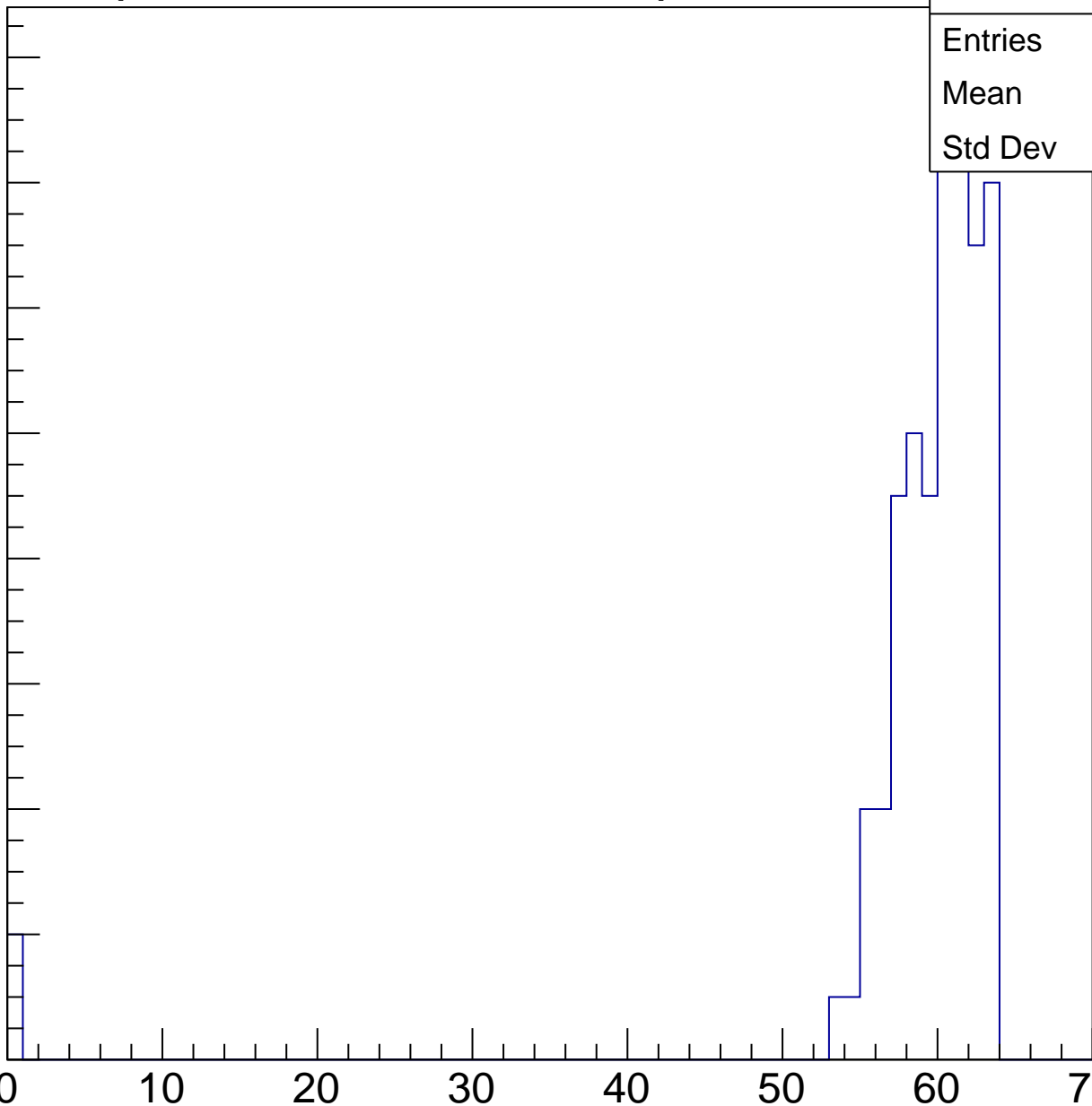
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.55
Std Dev	8.787

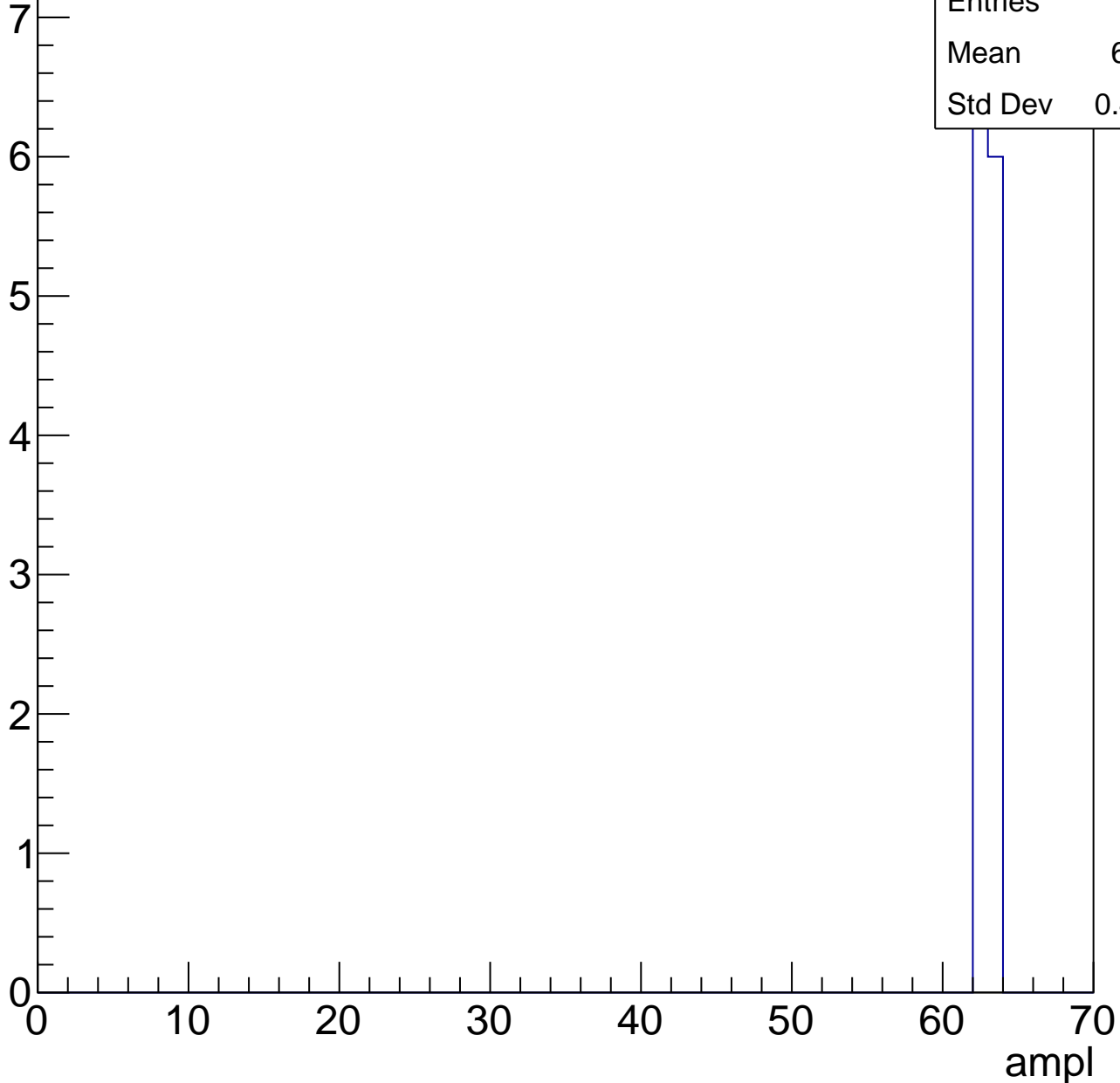
ampl



B1L001S, U20-ch4, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch4, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	2
Mean	23
Std Dev	0

B1L001S, U20-ch5, adc0

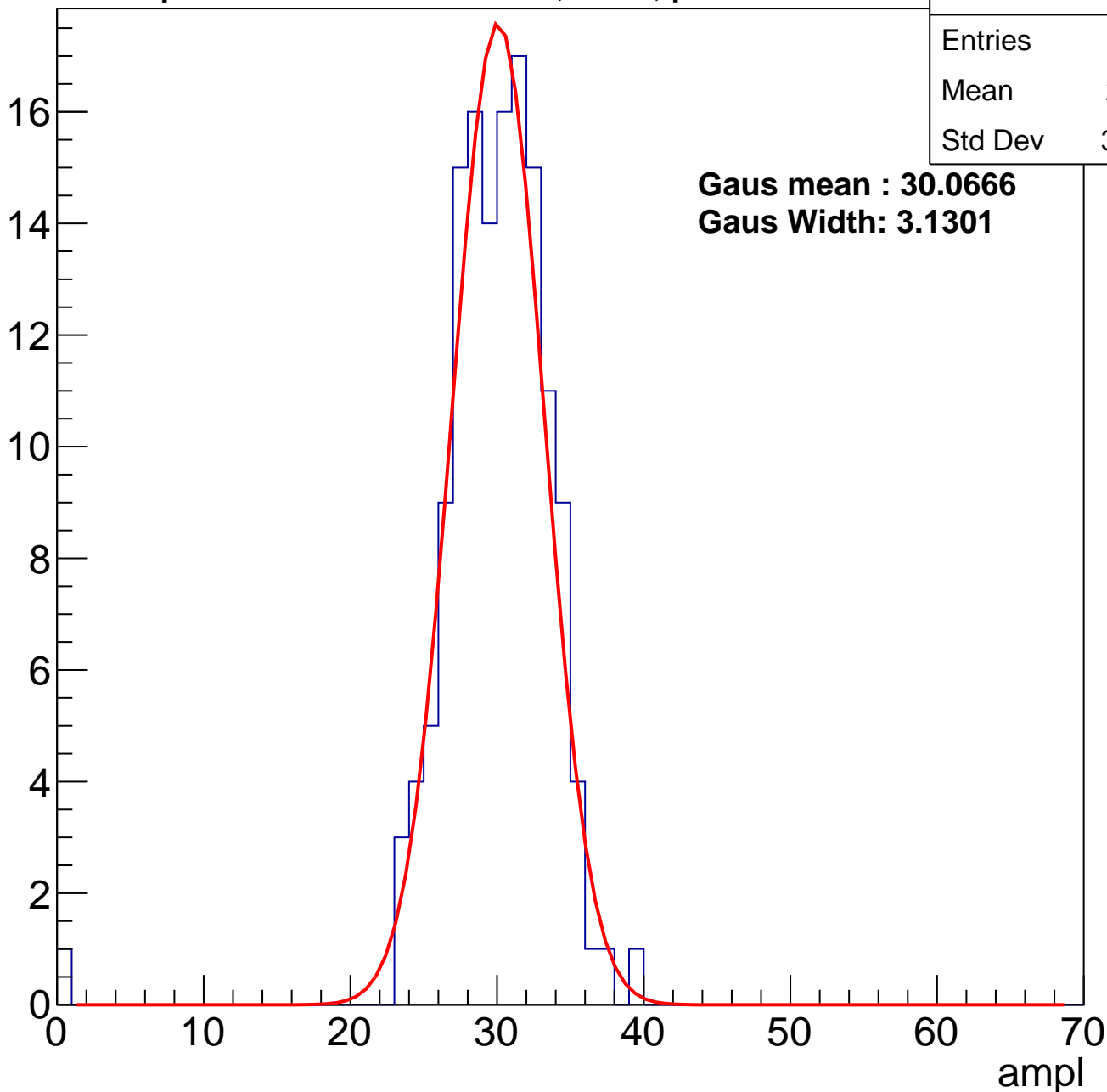
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	29.51
Std Dev	3.959

Gaus mean : 30.0666

Gaus Width: 3.1301

Entry



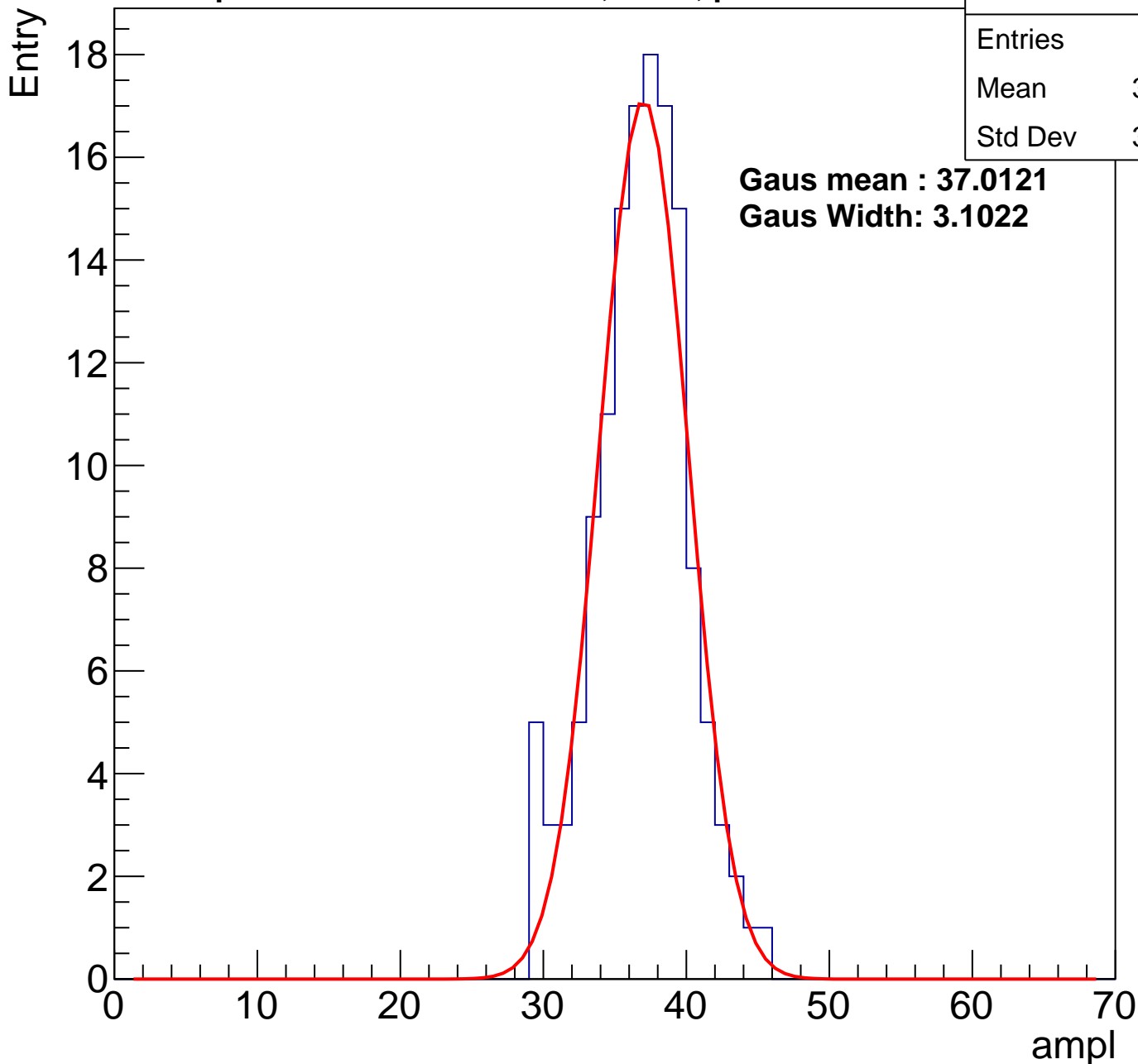
B1L001S, U20-ch5, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	36.37
Std Dev	3.246

Gaus mean : 37.0121

Gaus Width: 3.1022



B1L001S, U20-ch5, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	43.51
Std Dev	3.245

Gaus mean : 44.2438

Gaus Width: 3.3149

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

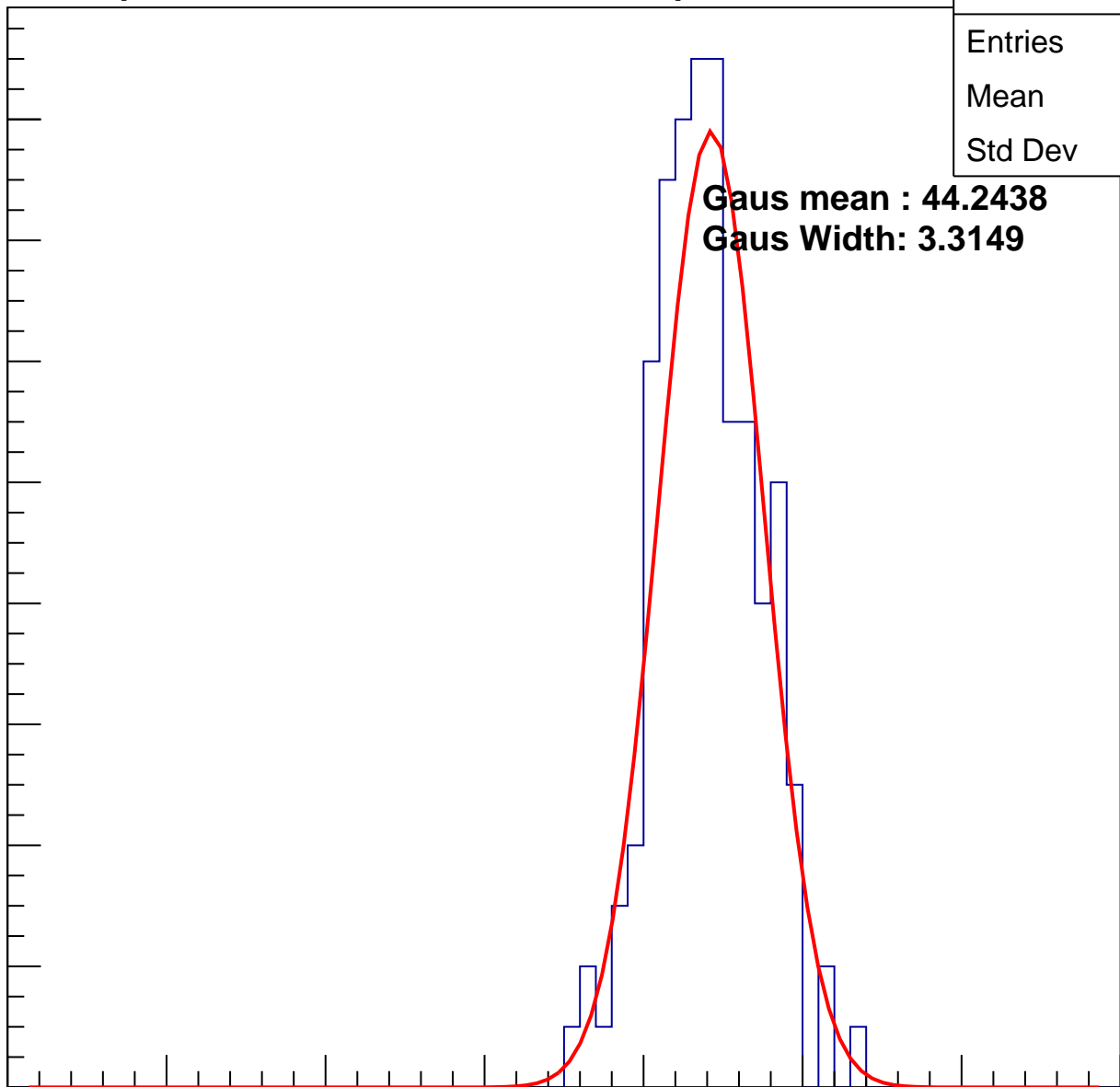
40

50

60

70

ampl

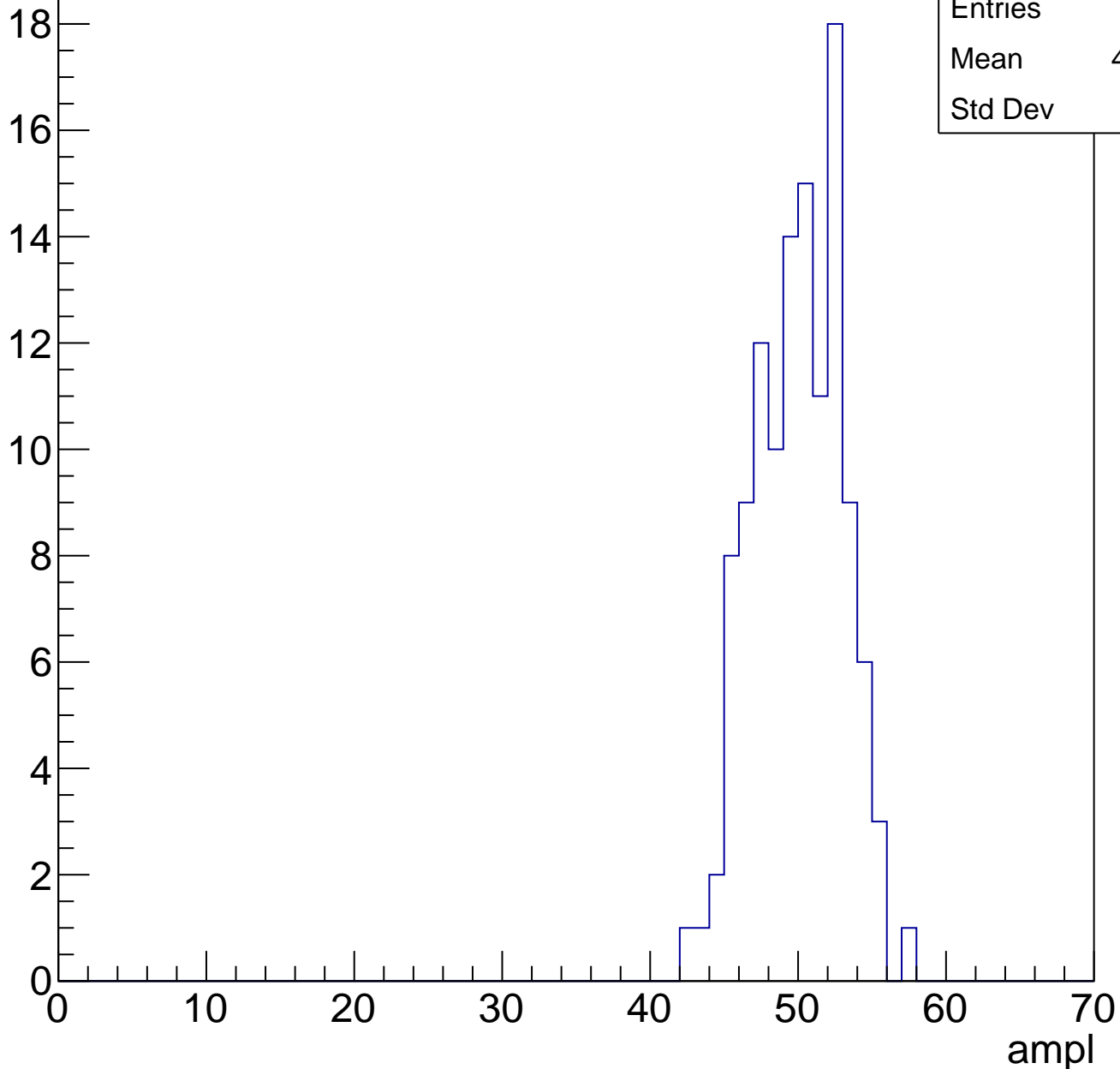


B1L001S, U20-ch5, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	49.56
Std Dev	2.96

Entry

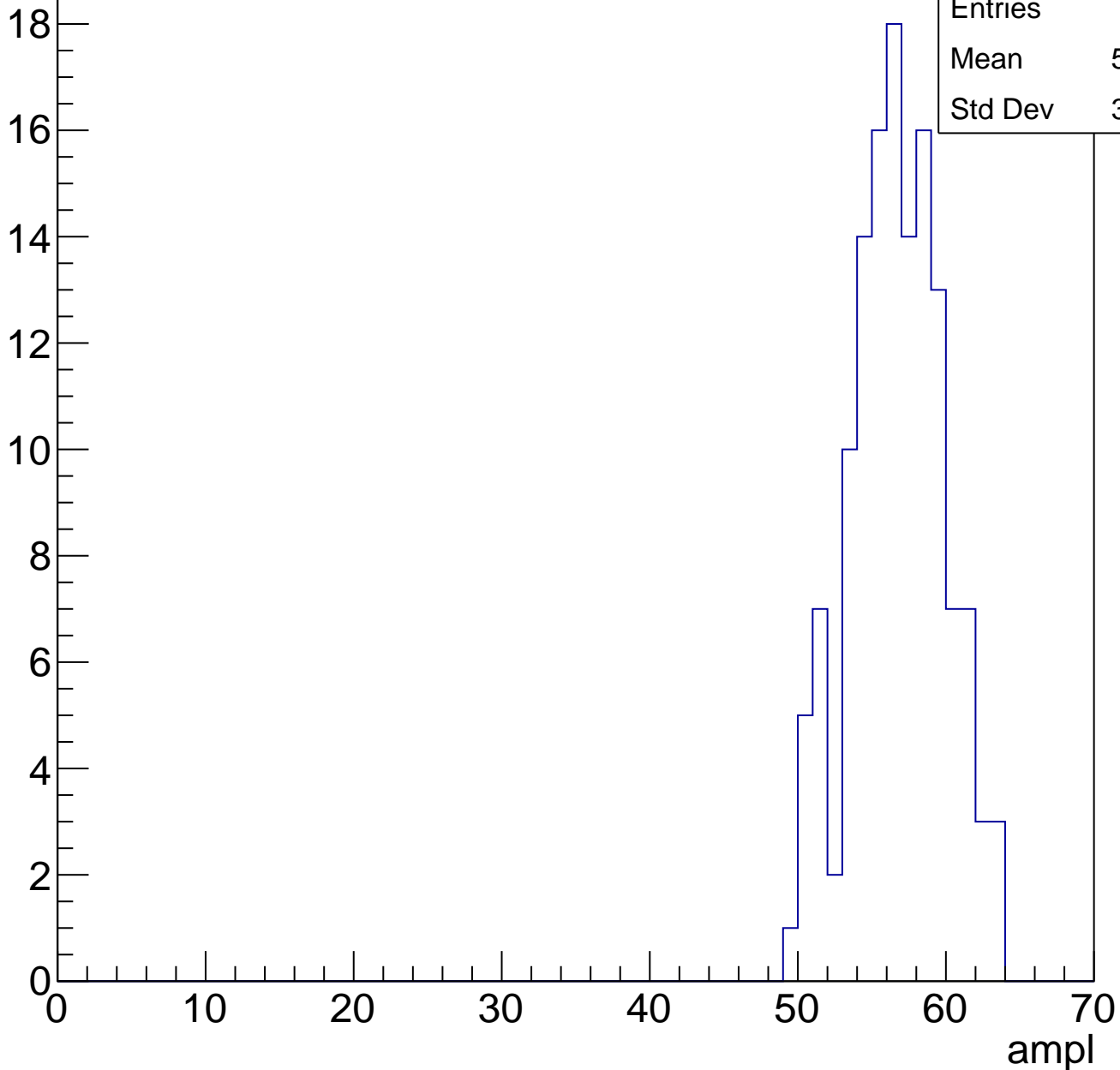


B1L001S, U20-ch5, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	56.24
Std Dev	3.124

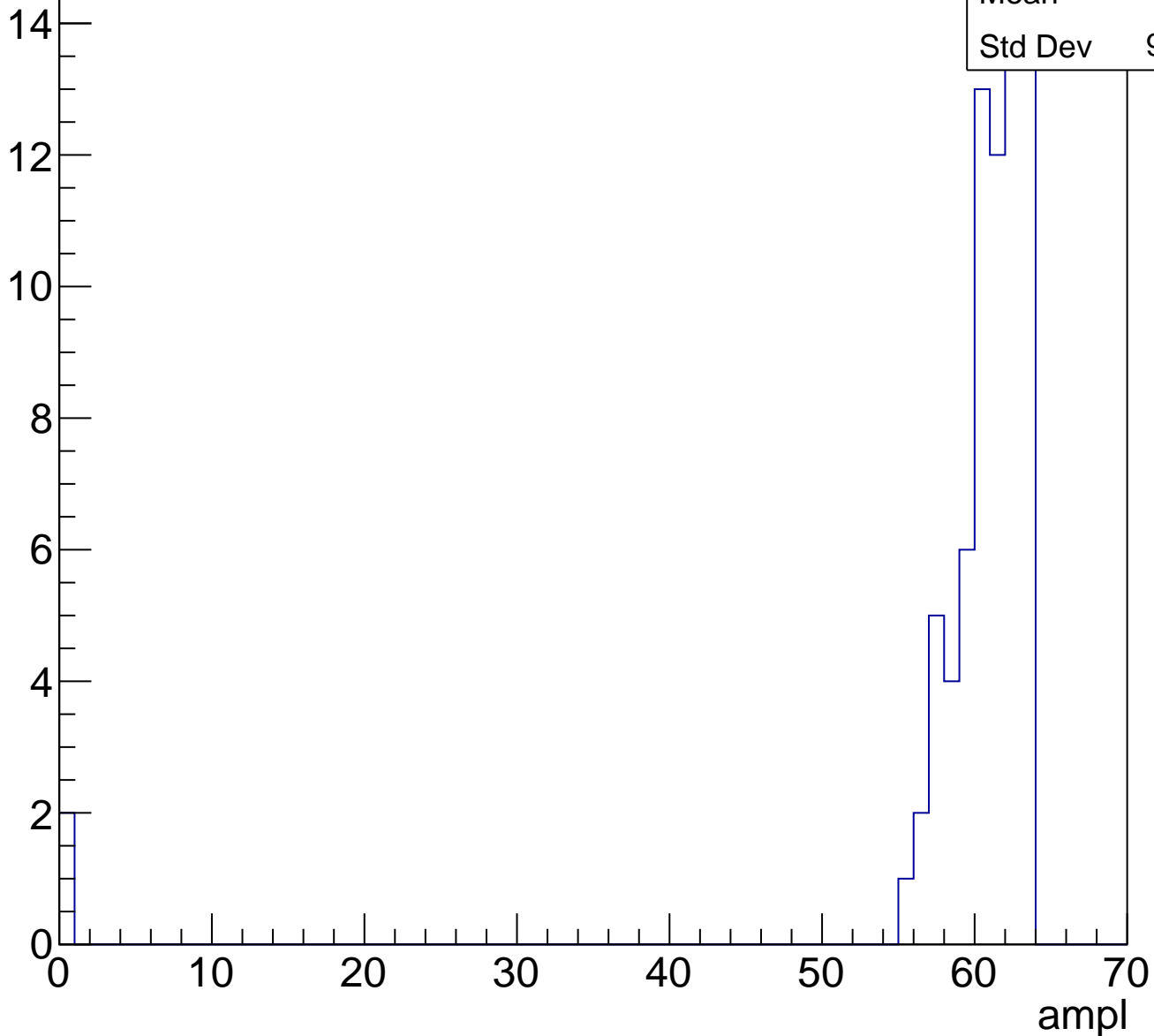
Entry



B1L001S, U20-ch5, adc5

calib_packv5_042523_0143.root, FC#2, port C2

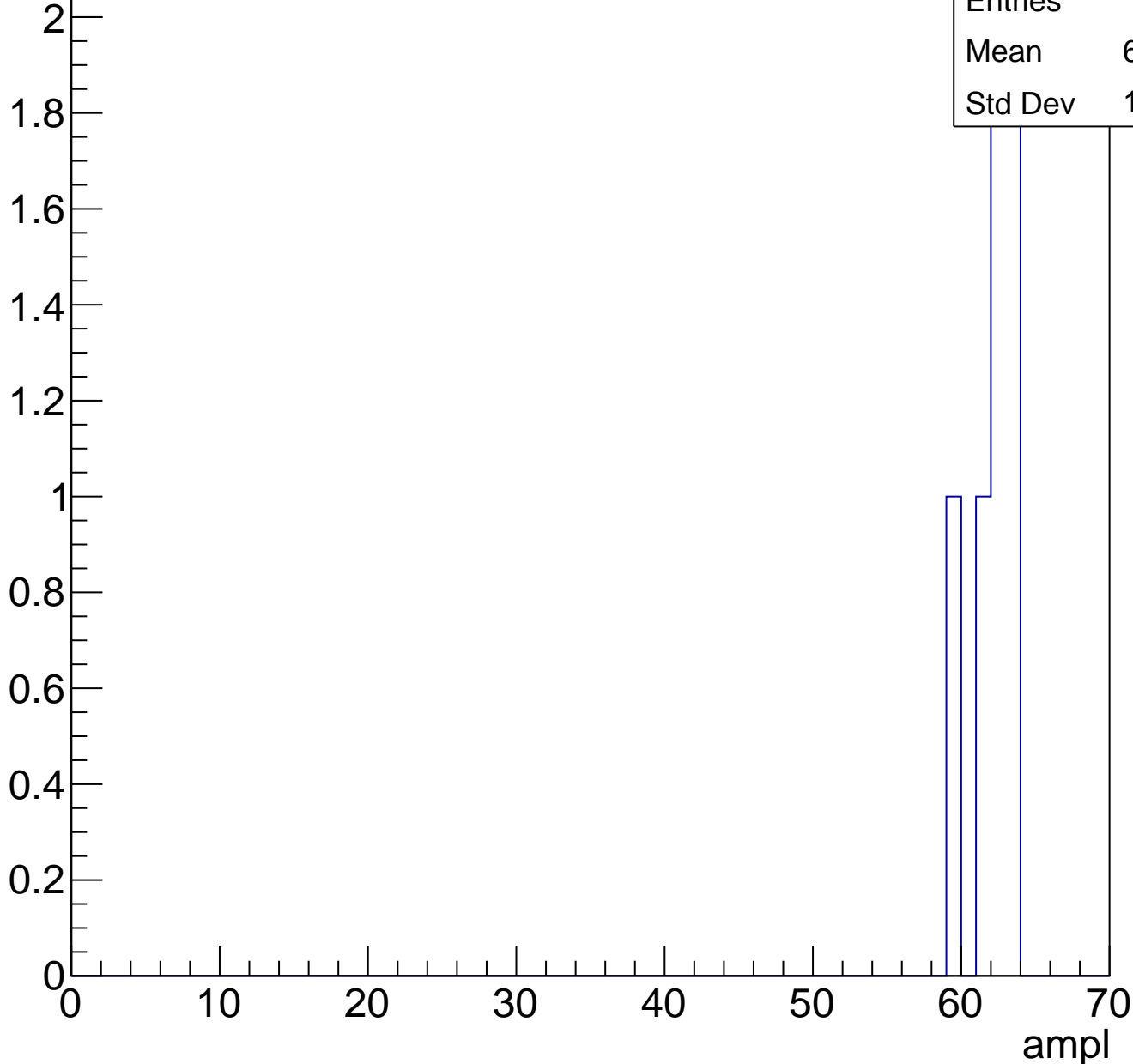
Entry



B1L001S, U20-ch5, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch5, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



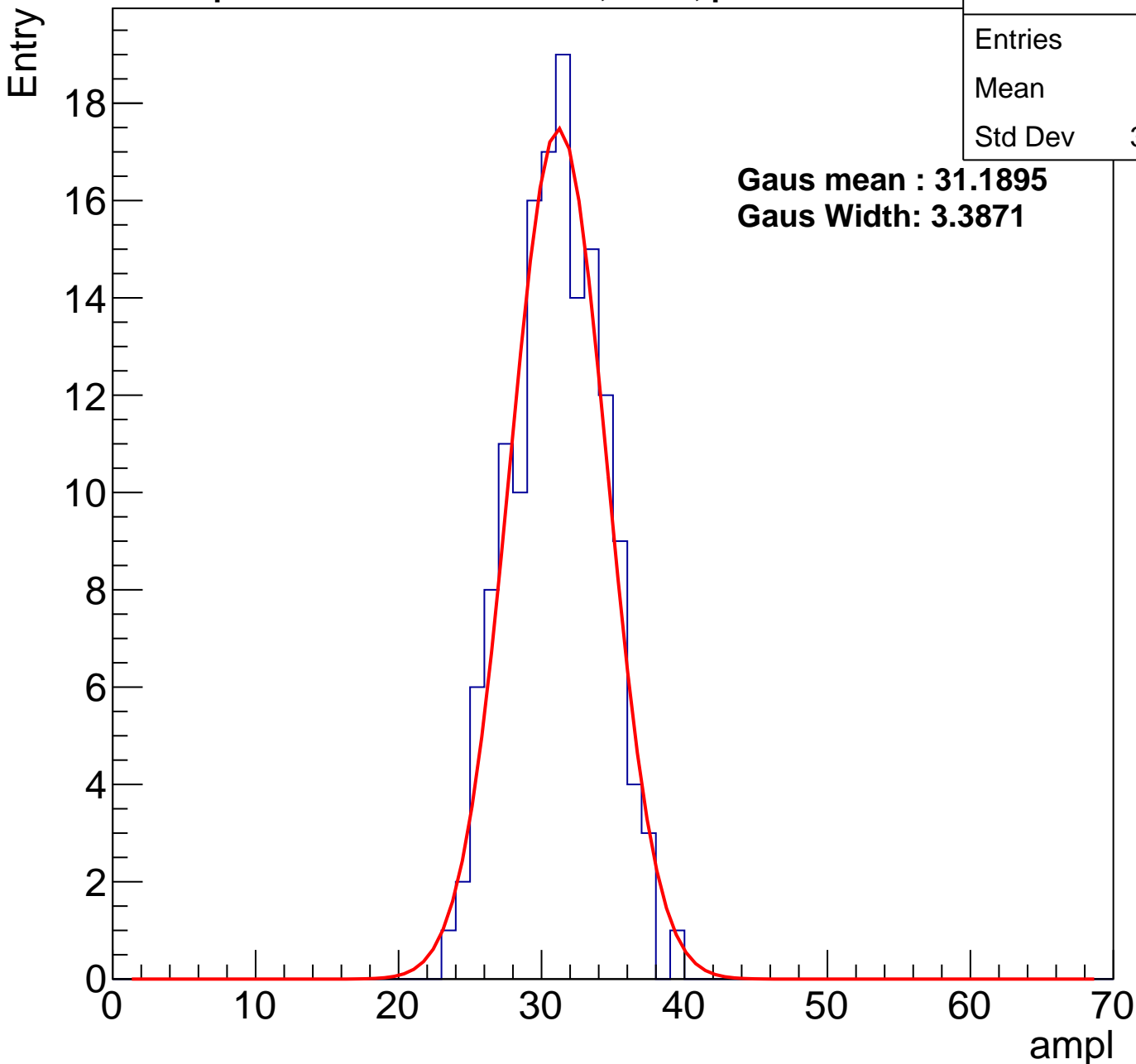
Entries	0
Mean	0
Std Dev	0

B1L001S, U20-ch6, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	30.6
Std Dev	3.183

Gaus mean : 31.1895
Gaus Width: 3.3871



B1L001S, U20-ch6, adc1

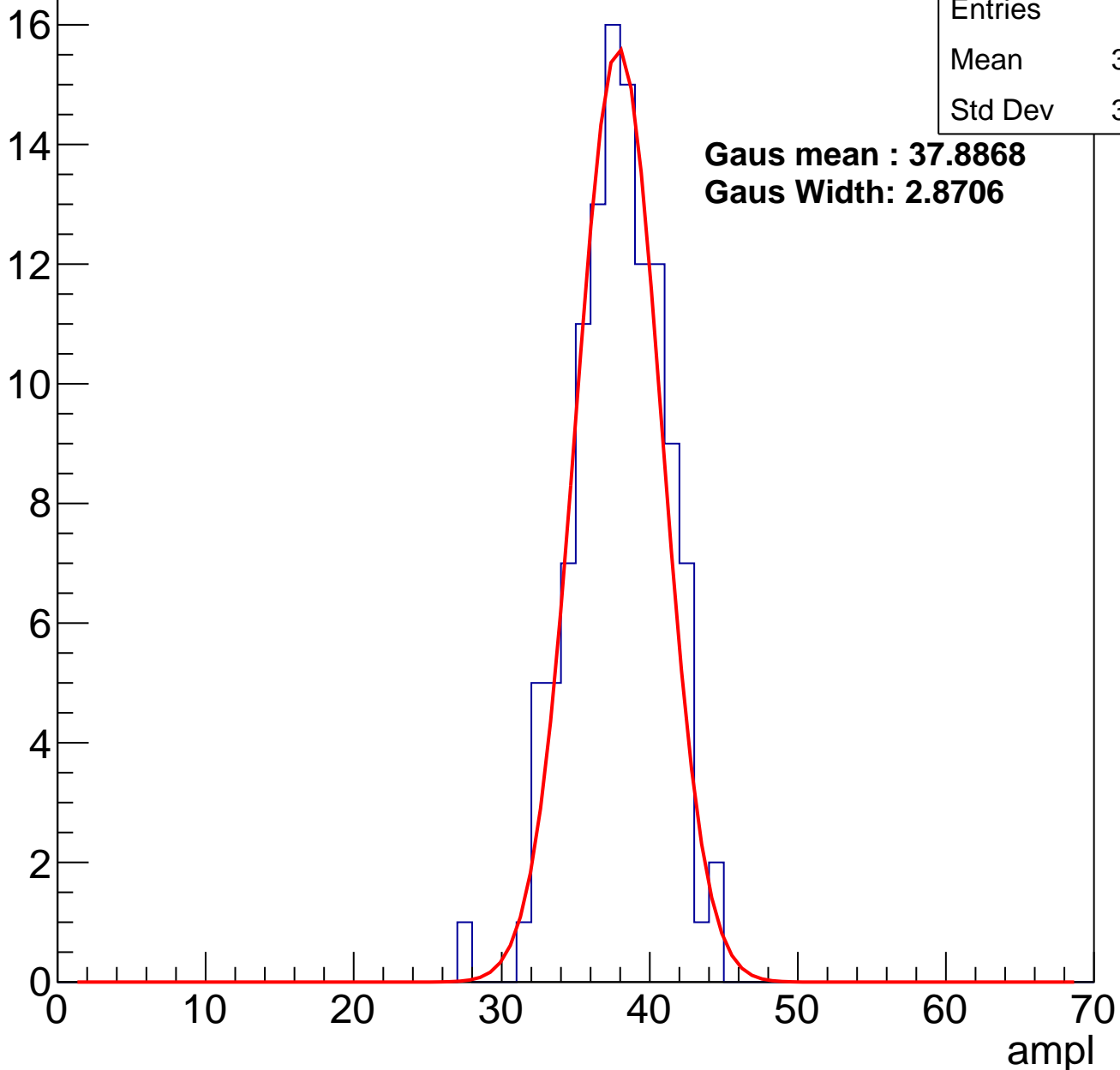
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	37.42
Std Dev	3.016

Gaus mean : 37.8868

Gaus Width: 2.8706

Entry



B1L001S, U20-ch6, adc2

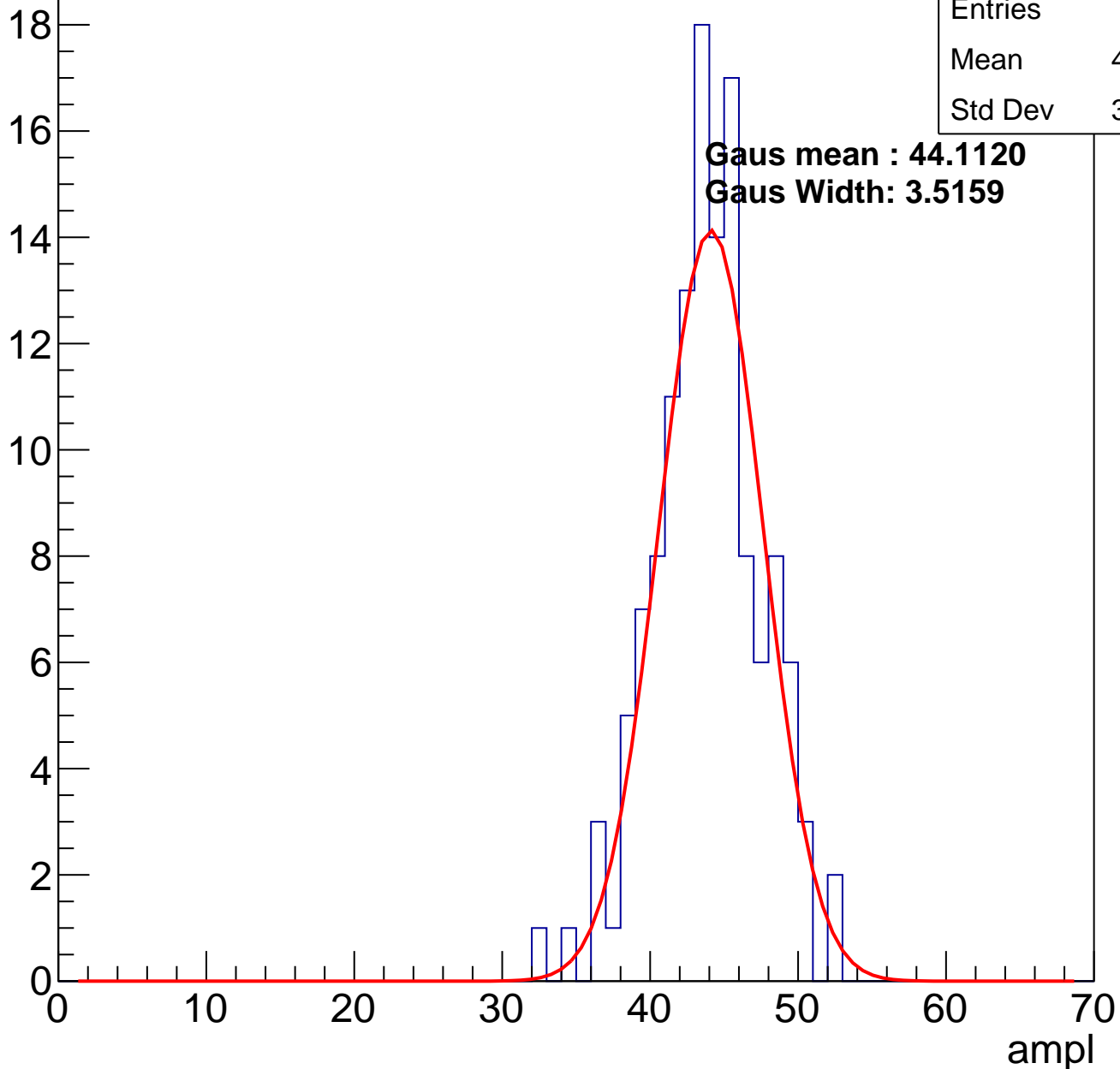
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	43.39
Std Dev	3.595

Gaus mean : 44.1120

Gaus Width: 3.5159

Entry



B1L001S, U20-ch6, adc3

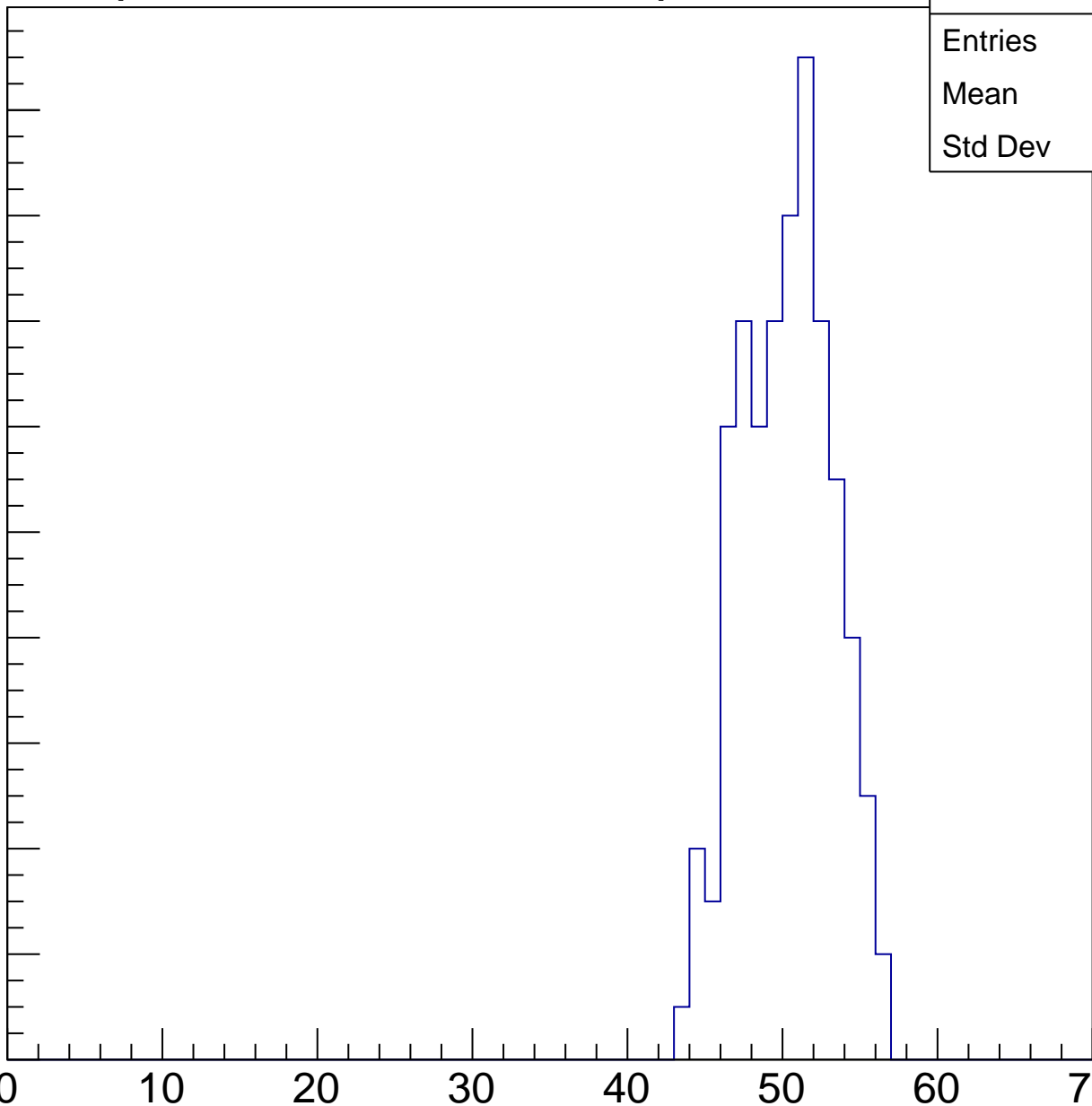
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	135
Mean	49.81
Std Dev	2.909

ampl



B1L001S, U20-ch6, adc4

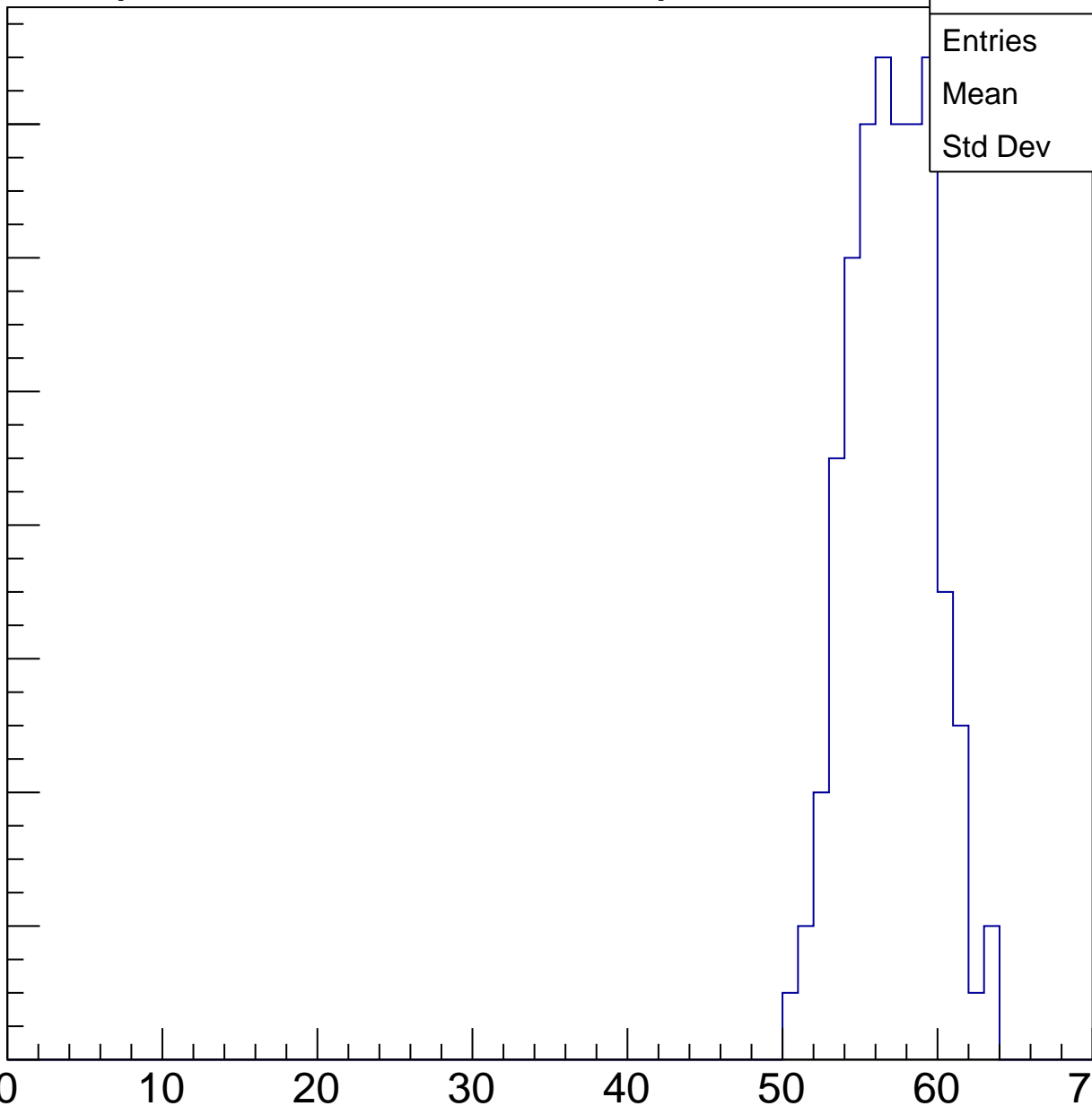
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	115
Mean	56.55
Std Dev	2.694

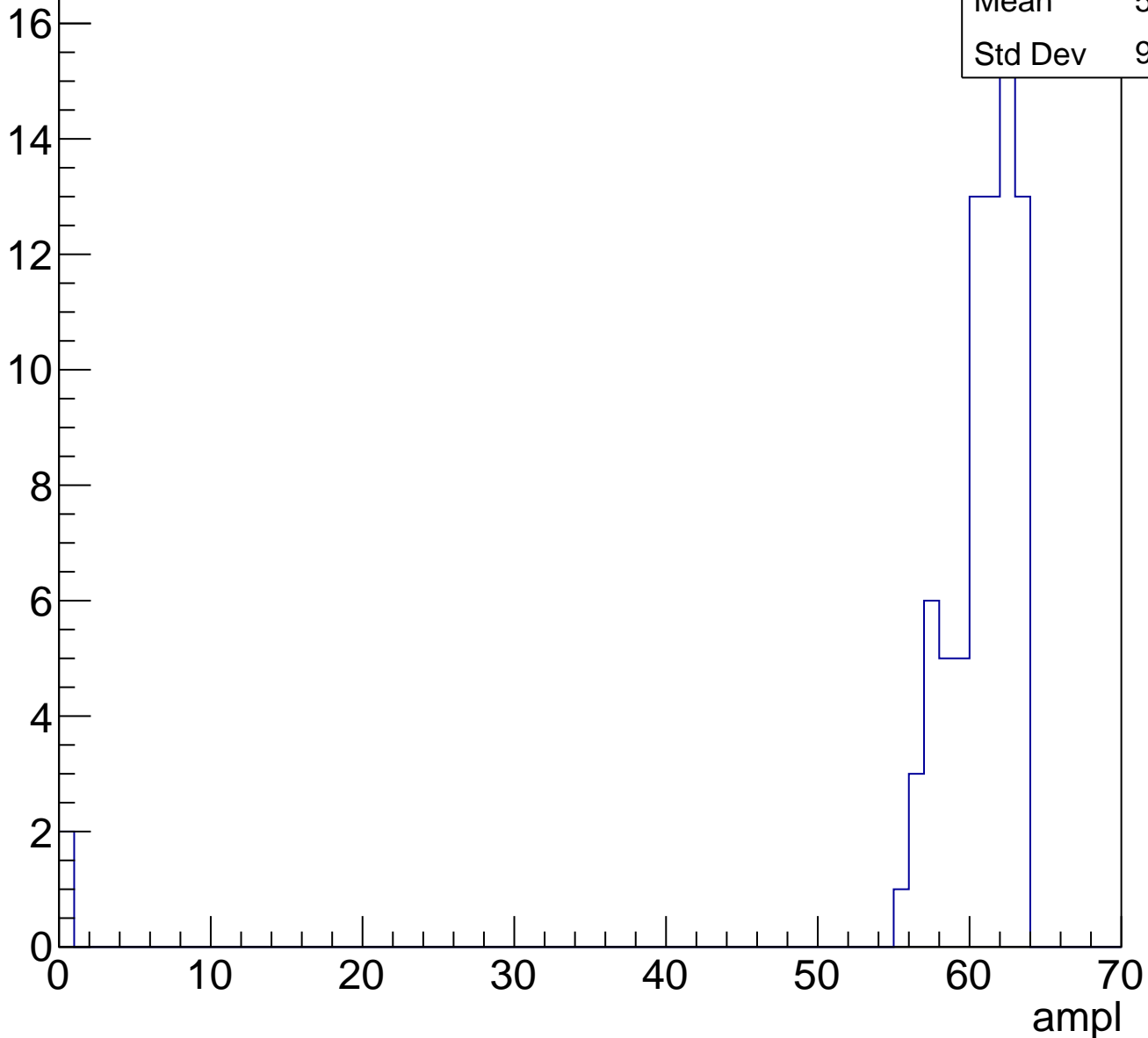
ampl



B1L001S, U20-ch6, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

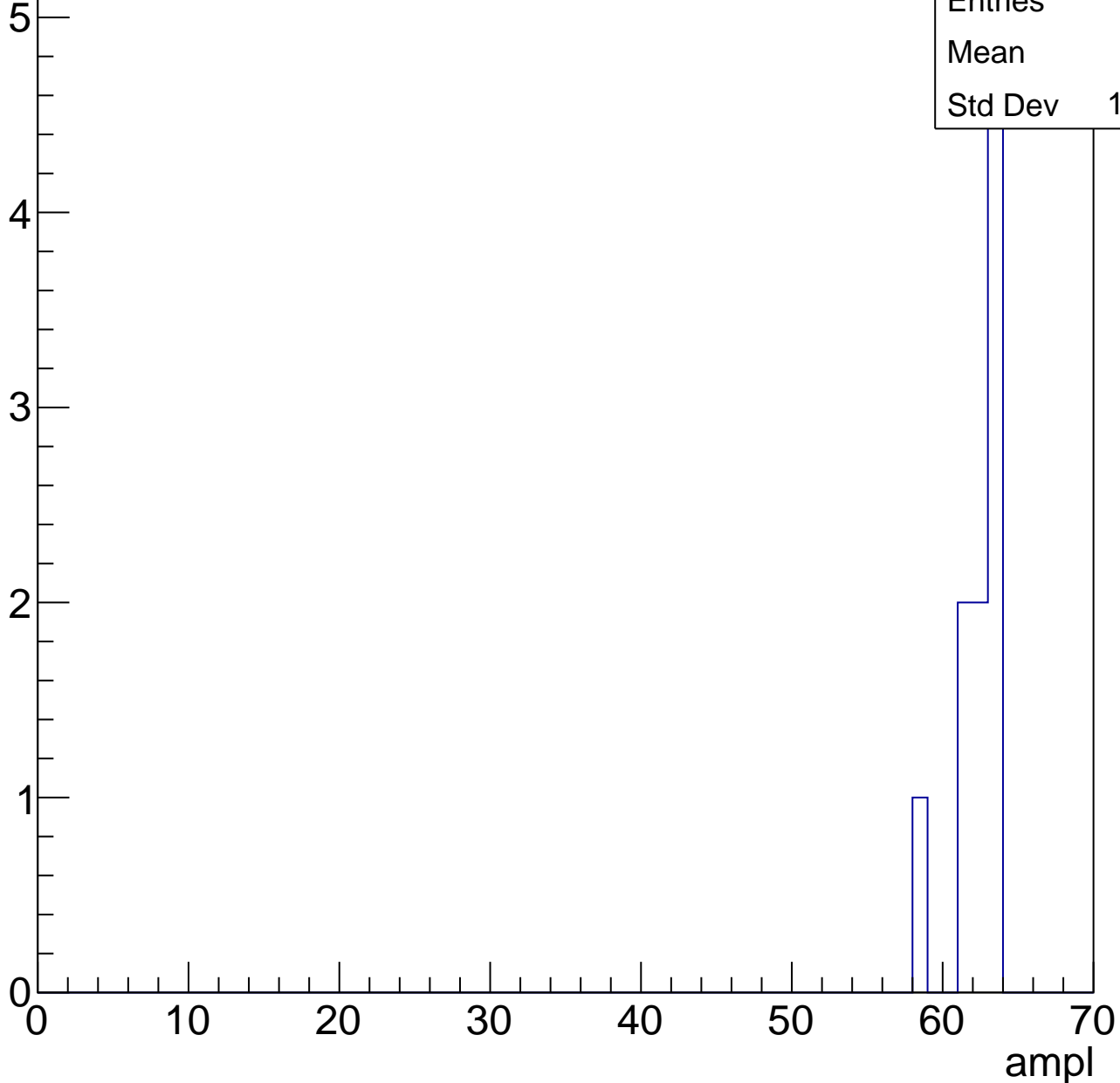


B1L001S, U20-ch6, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	61.9
Std Dev	1.513



B1L001S, U20-ch6, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23
Std Dev	0

0 10 20 30 40 50 60 70

ampl

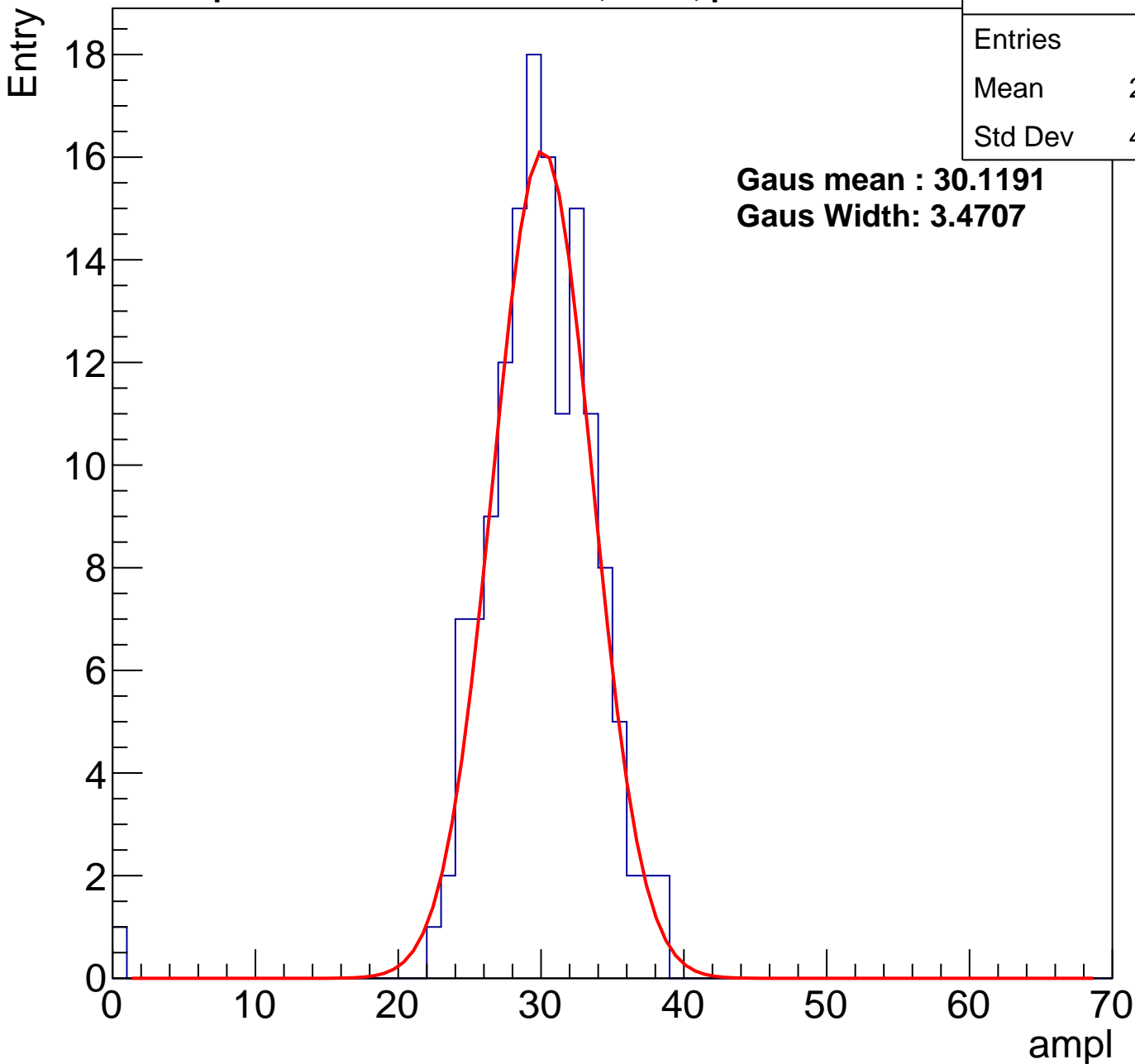


B1L001S, U20-ch7, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	29.47
Std Dev	4.167

Gaus mean : 30.1191
Gaus Width: 3.4707



B1L001S, U20-ch7, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.55
Std Dev	3.382

Gaus mean : 36.8385

Gaus Width: 3.4771

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

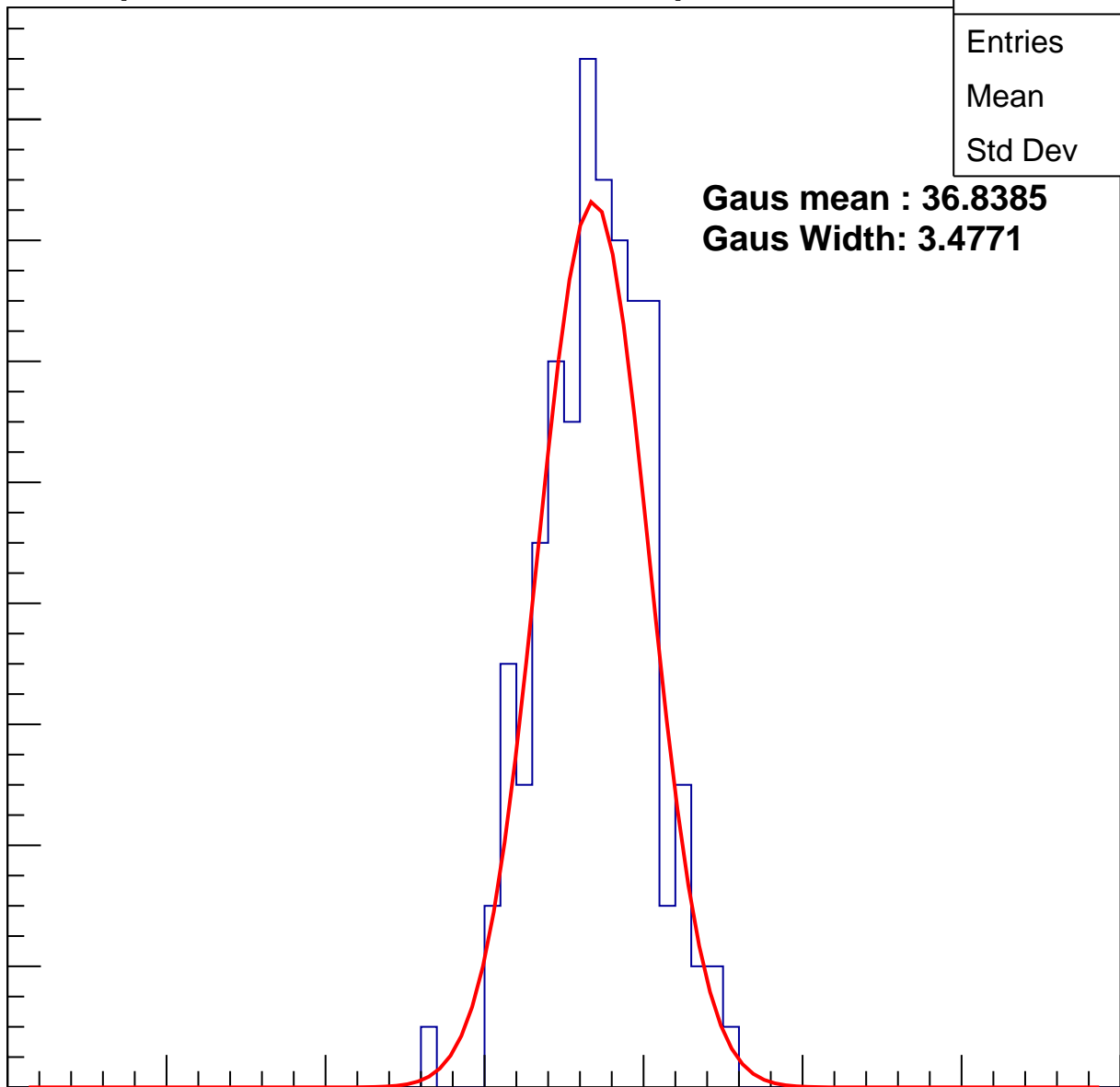
40

50

60

70

ampl



B1L001S, U20-ch7, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

149

Mean

43.48

Std Dev

3.463

Gaus mean : 44.2162

Gaus Width: 3.4676

0

10

20

30

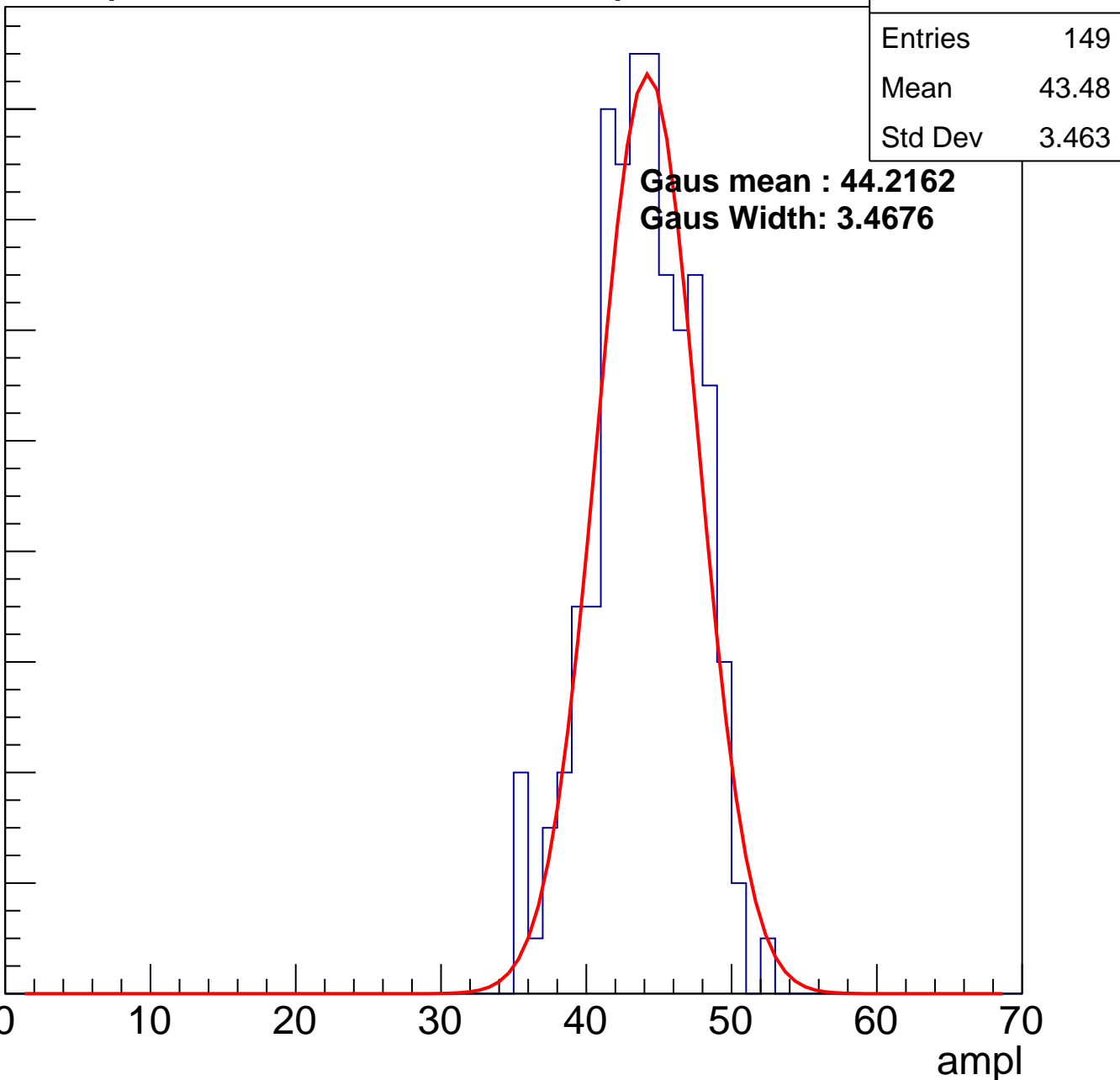
40

50

60

70

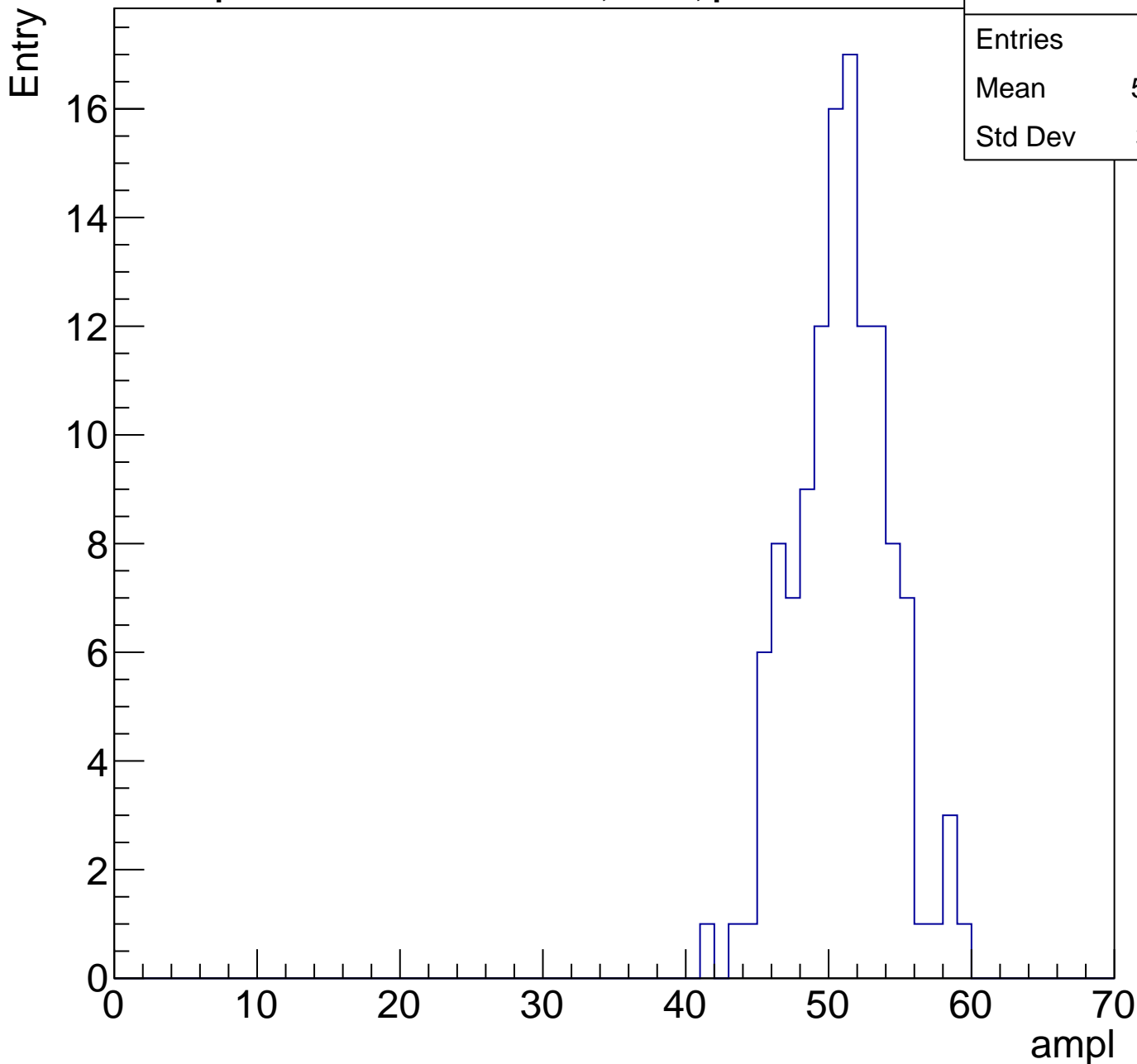
ampl



B1L001S, U20-ch7, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	50.45
Std Dev	3.331



B1L001S, U20-ch7, adc4

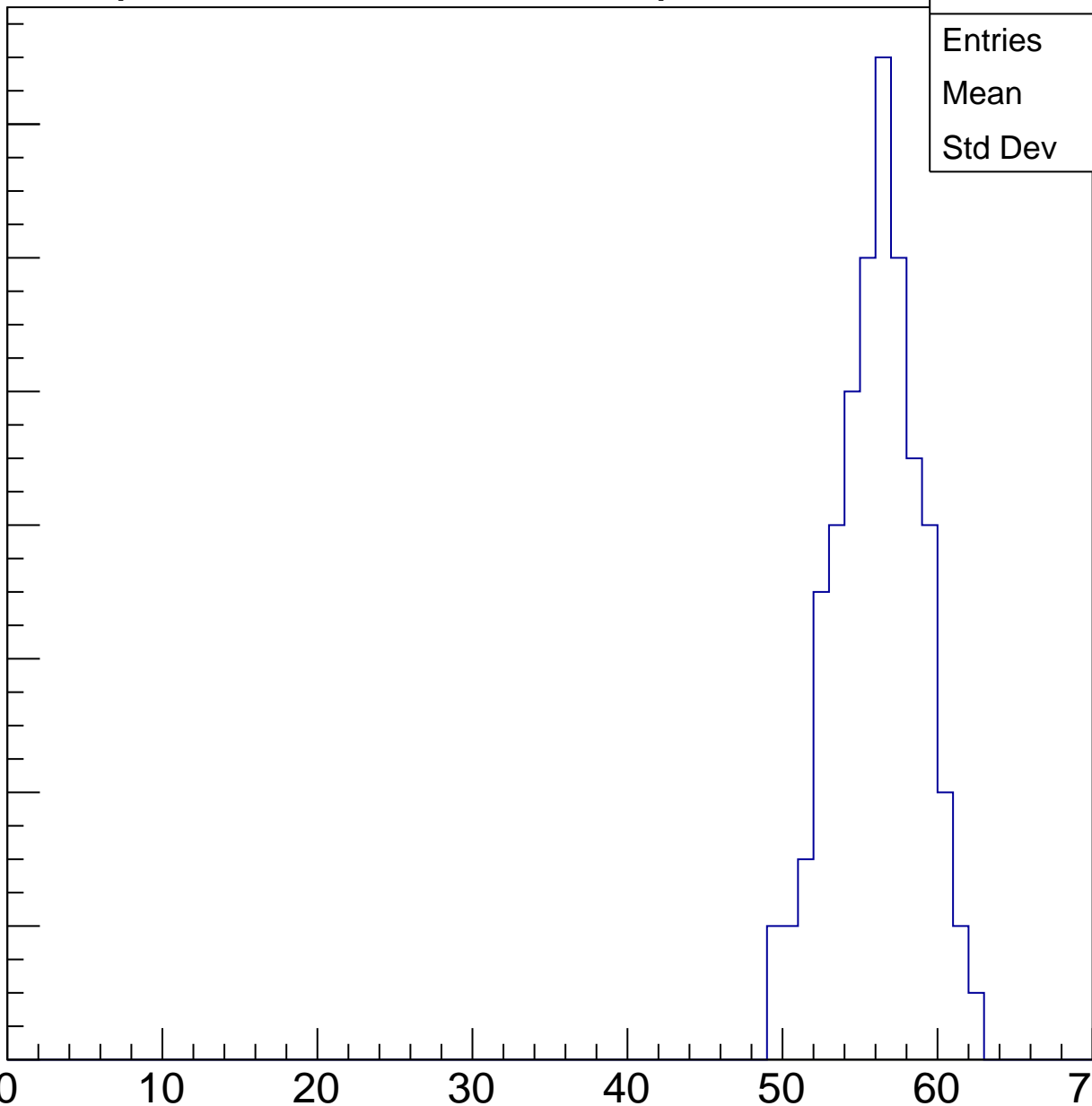
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	55.59
Std Dev	2.781

ampl



B1L001S, U20-ch7, adc5

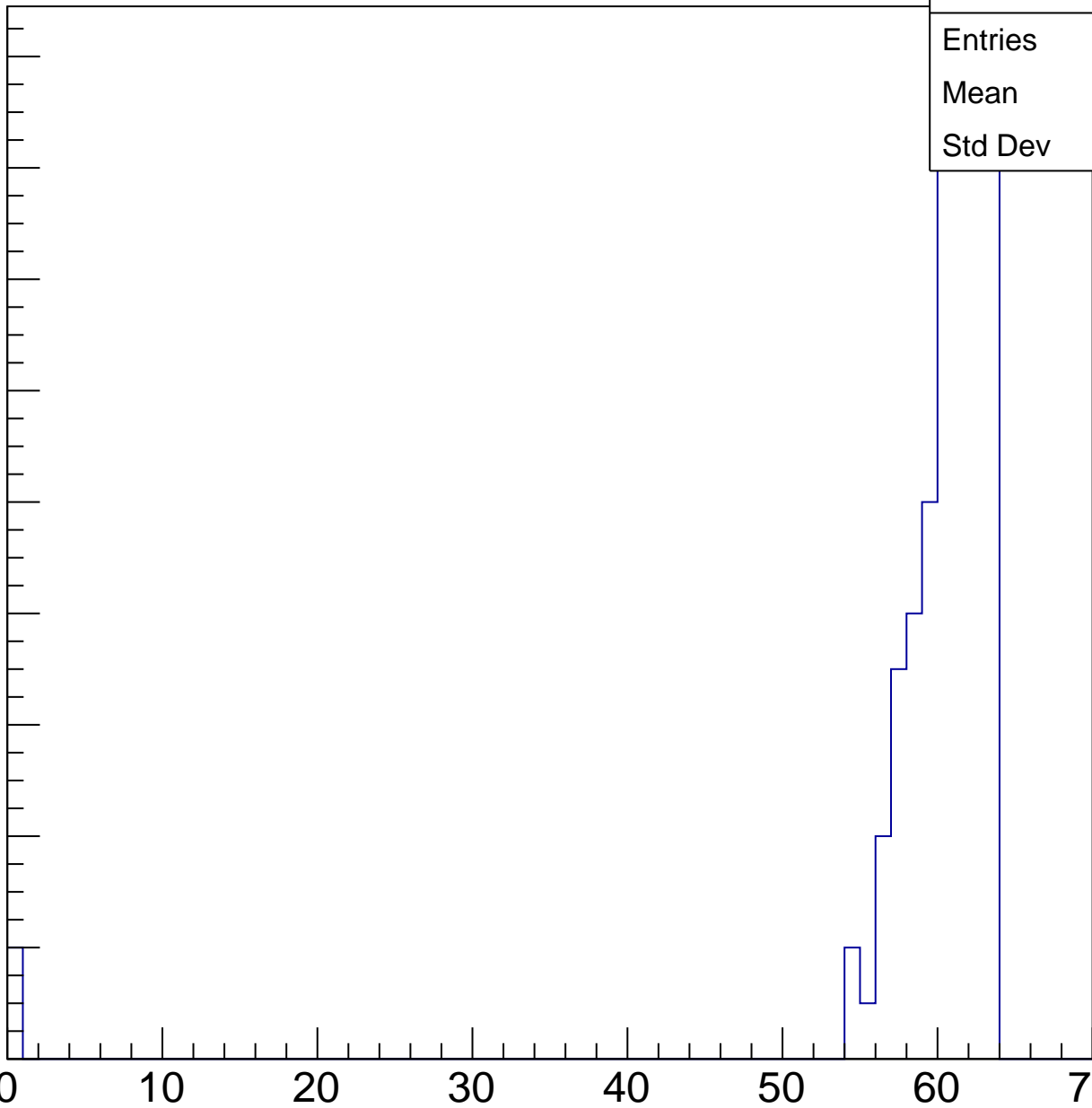
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	103
Mean	59.09
Std Dev	8.606

ampl



B1L001S, U20-ch7, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch7, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch8, adc0

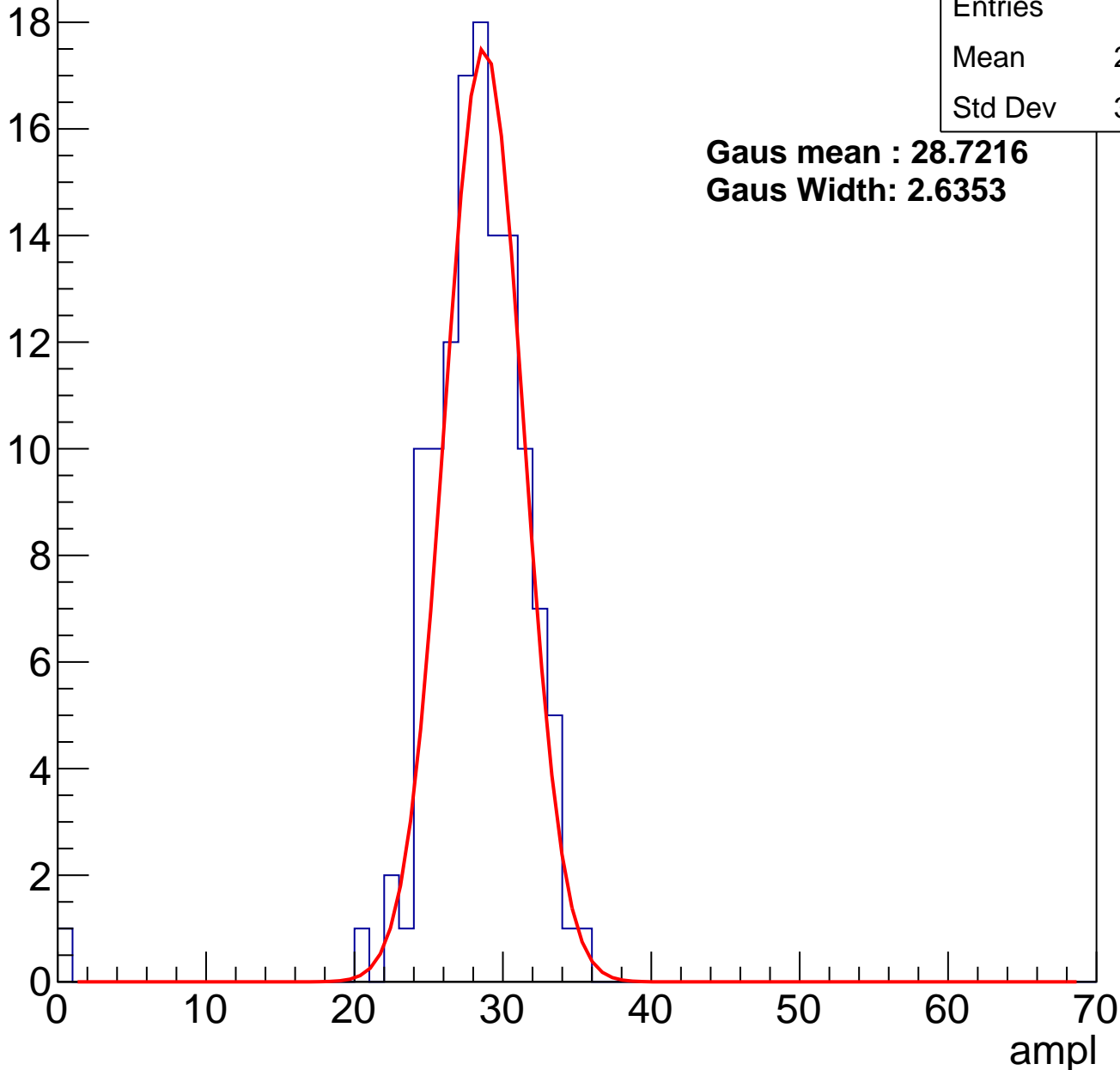
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	27.79
Std Dev	3.742

Gaus mean : 28.7216

Gaus Width: 2.6353

Entry



B1L001S, U20-ch8, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	34.8
Std Dev	3.294

Gaus mean : 35.3133

Gaus Width: 3.3825

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

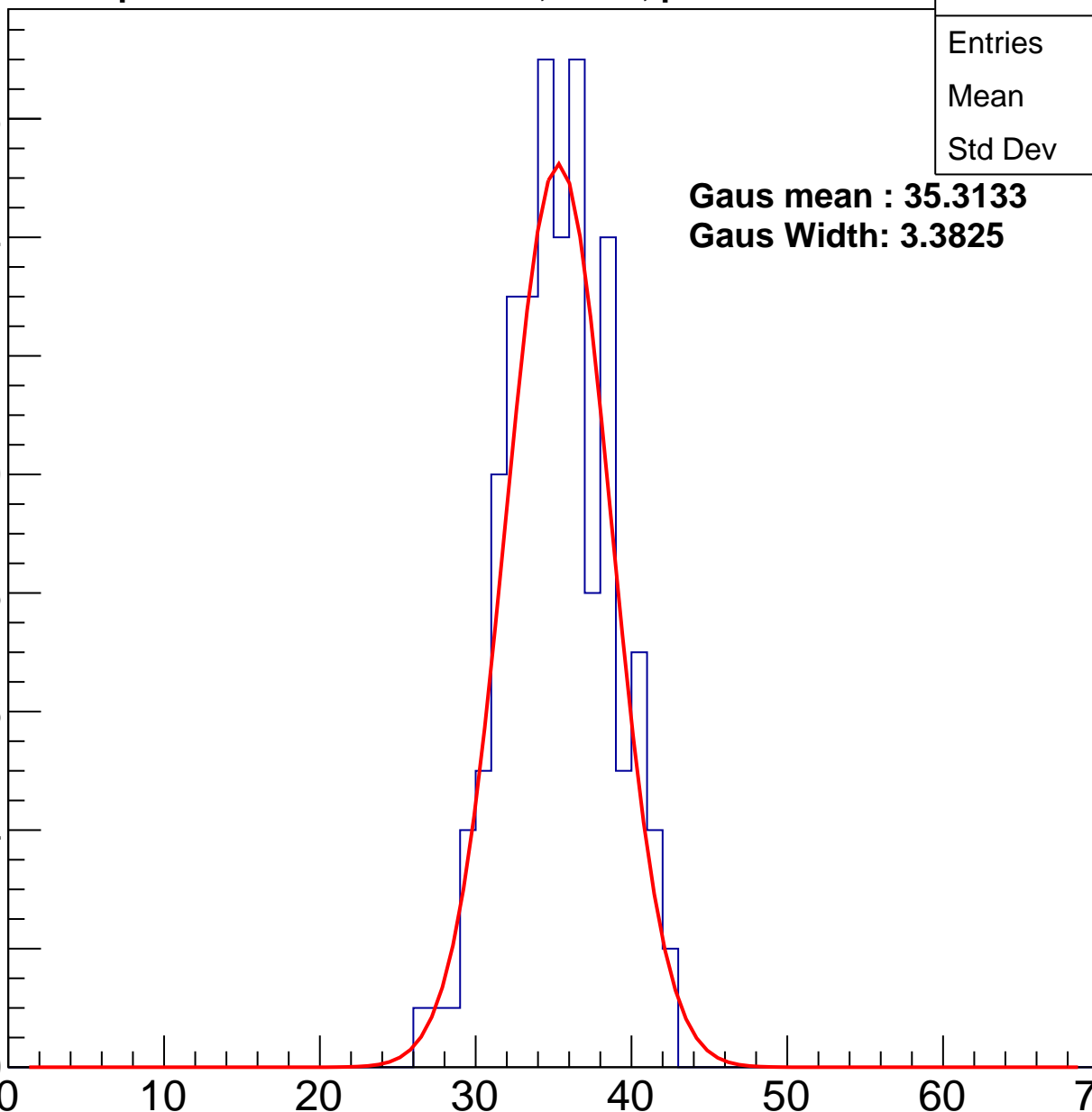
40

50

60

70

ampl



B1L001S, U20-ch8, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

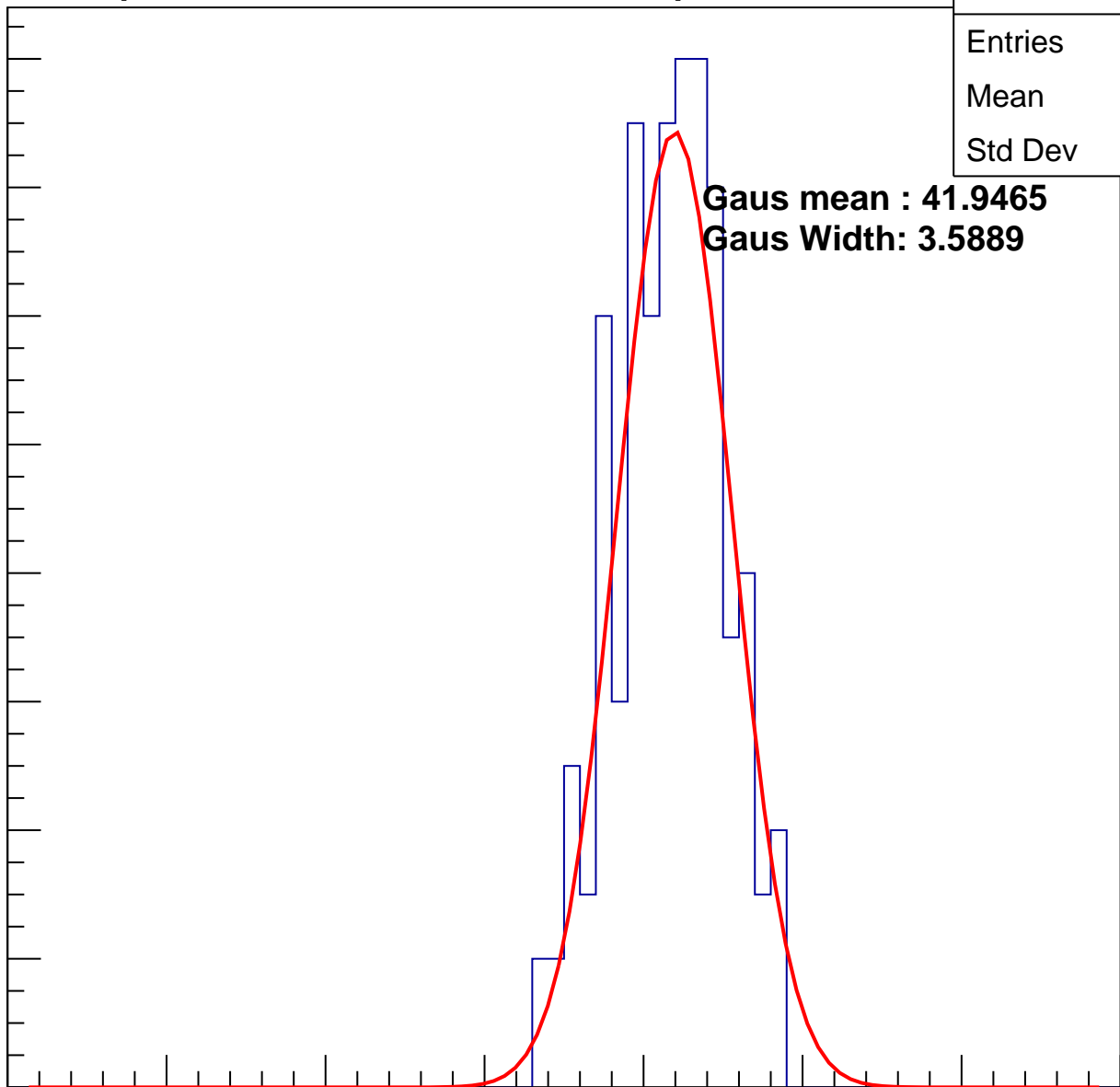
Entries	140
Mean	41.15
Std Dev	3.399

Gaus mean : 41.9465

Gaus Width: 3.5889

ampl

0 10 20 30 40 50 60 70

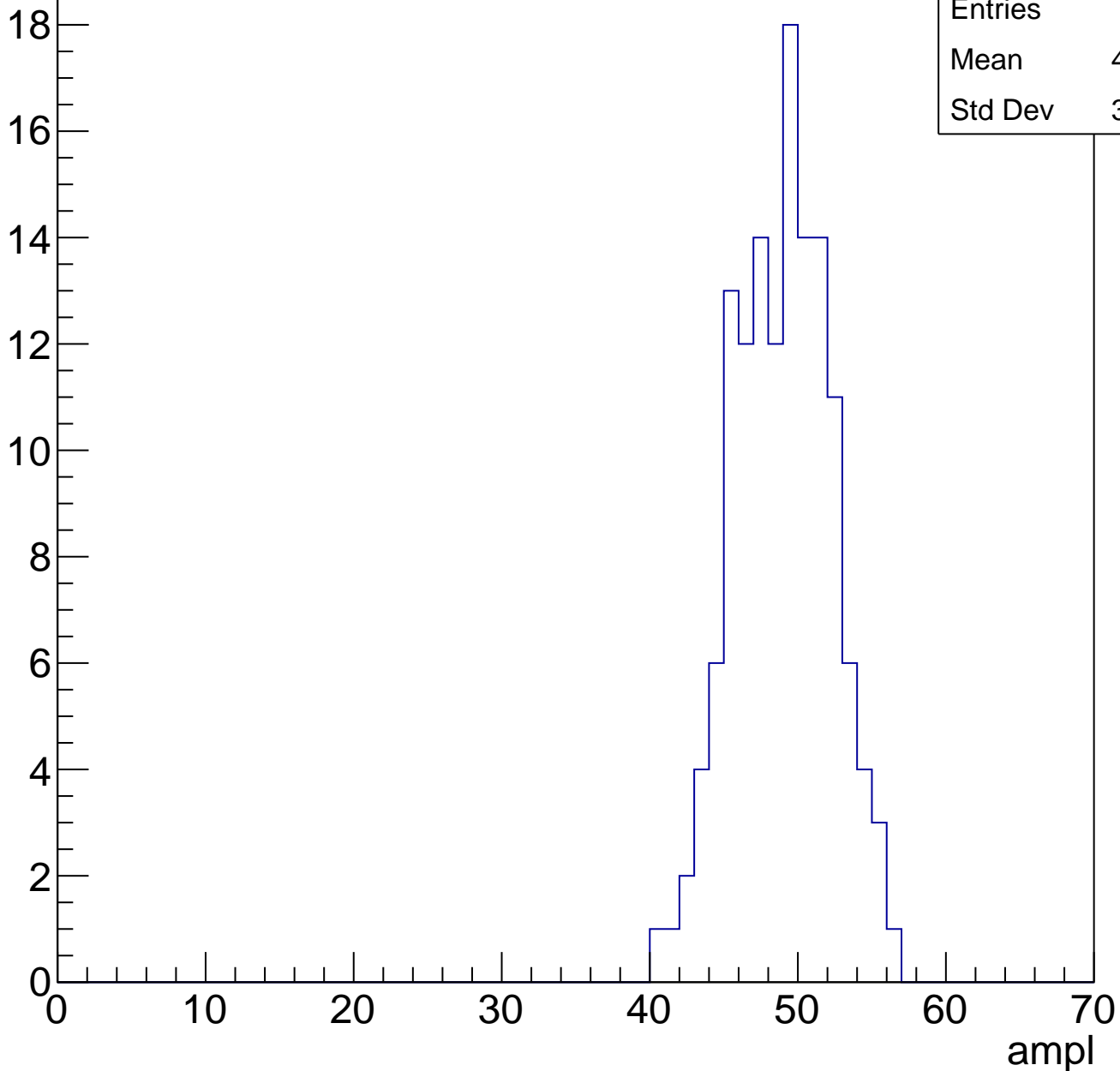


B1L001S, U20-ch8, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	48.49
Std Dev	3.218

Entry



B1L001S, U20-ch8, adc4

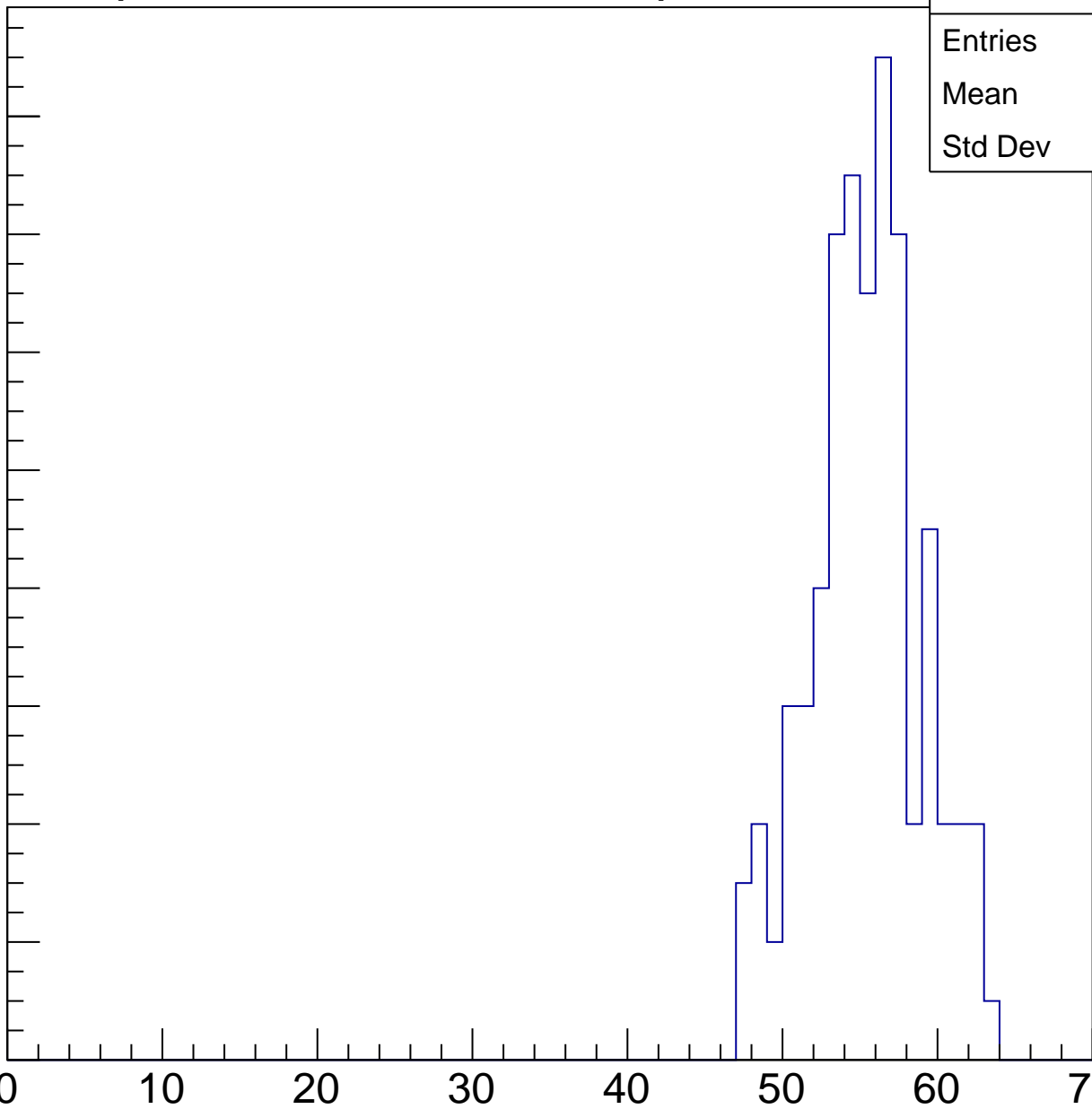
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	54.91
Std Dev	3.541

ampl



B1L001S, U20-ch8, adc5

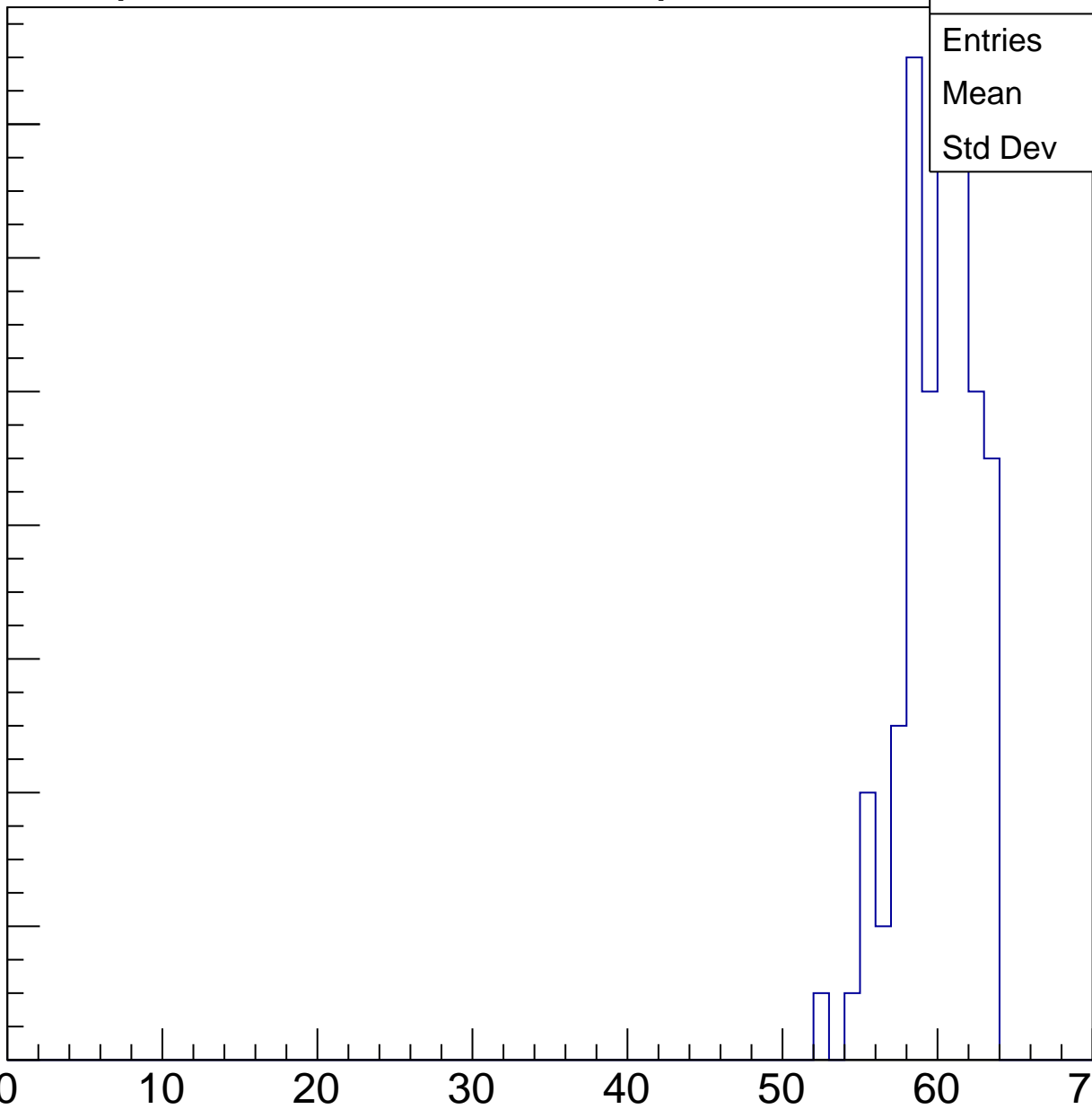
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	87
Mean	59.6
Std Dev	2.322

ampl

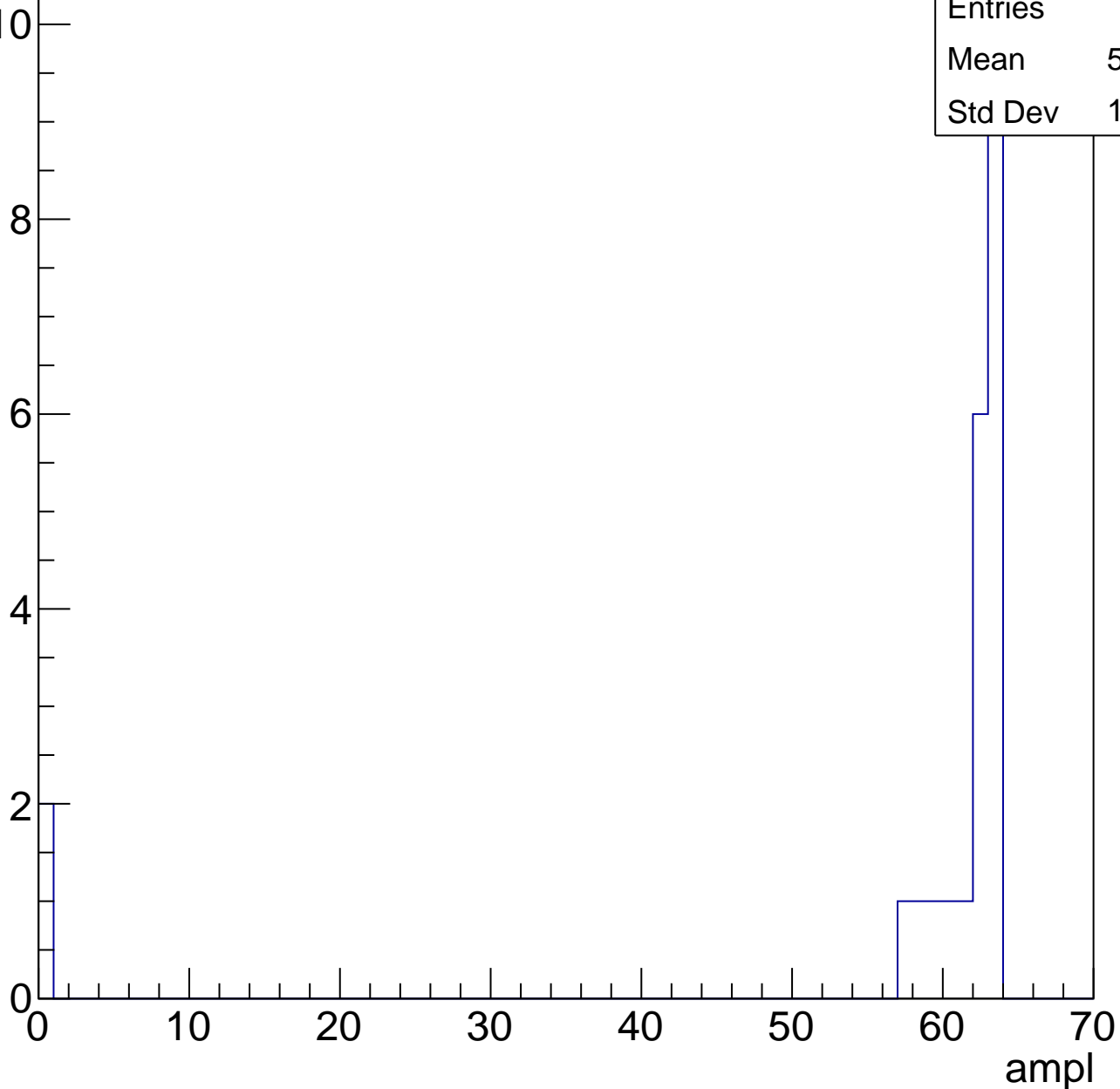


B1L001S, U20-ch8, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

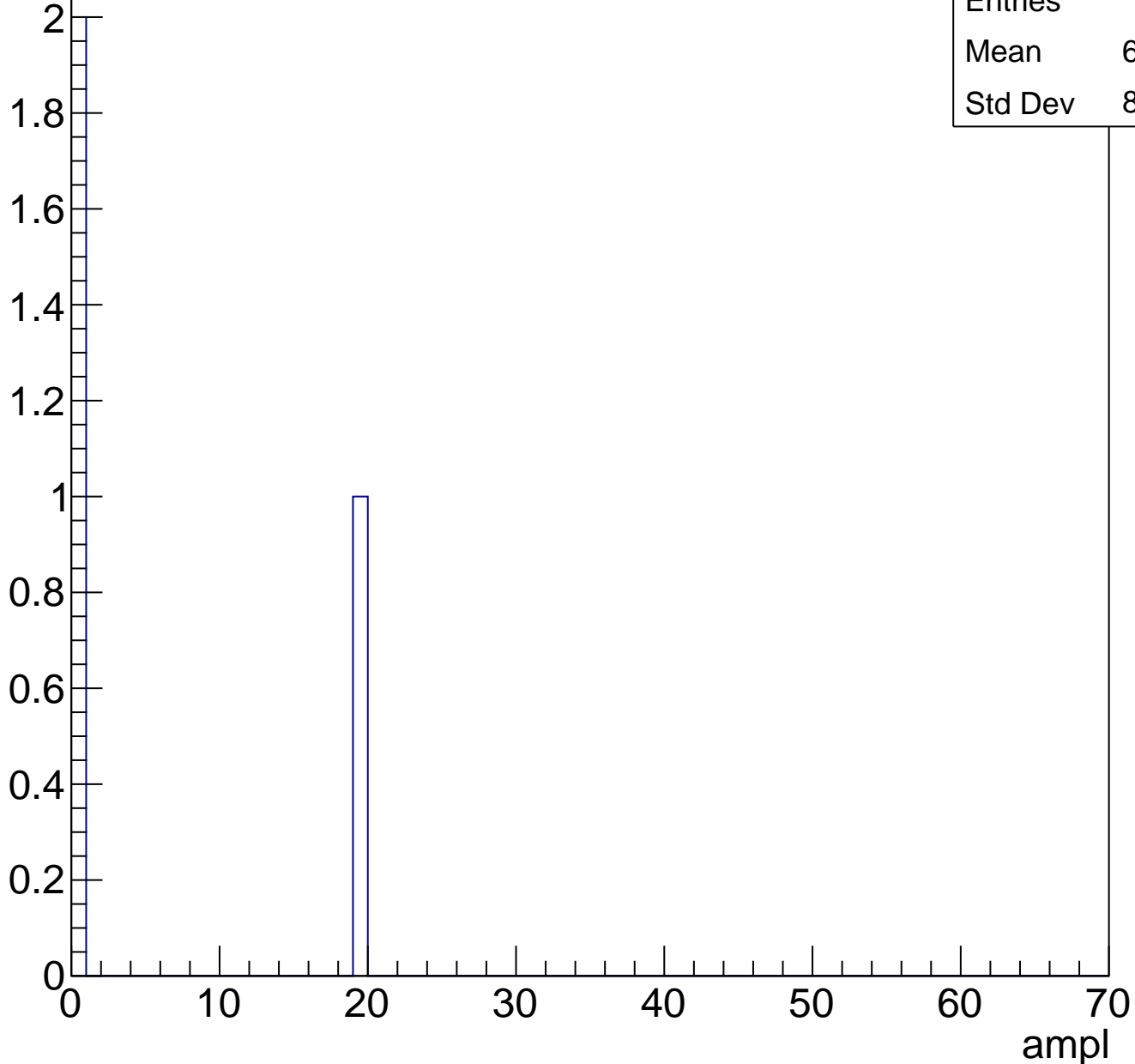
Entries	23
Mean	56.39
Std Dev	17.48



B1L001S, U20-ch8, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

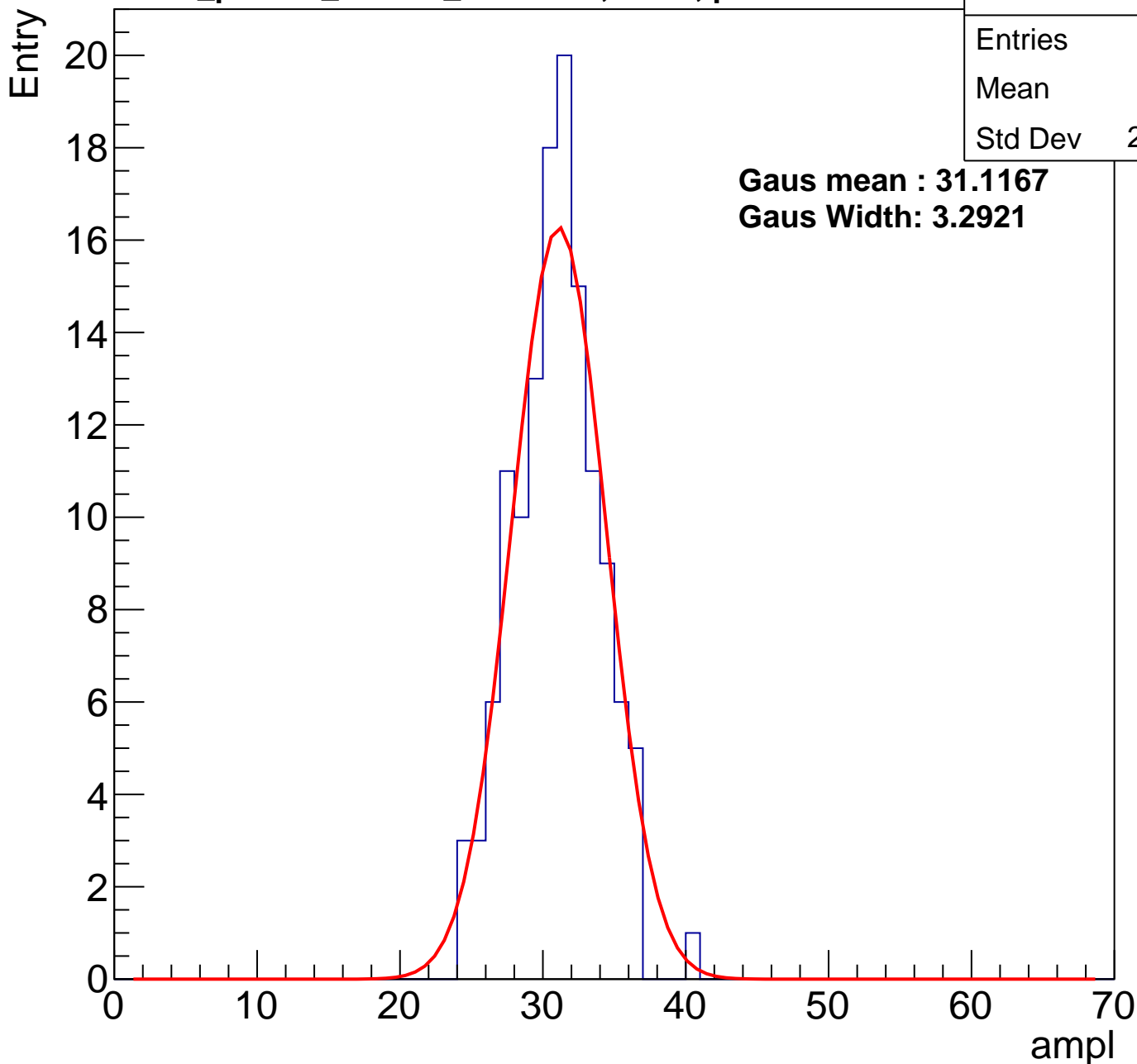
B1L001S, U20-ch9, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	30.5
Std Dev	2.964

Gaus mean : 31.1167

Gaus Width: 3.2921



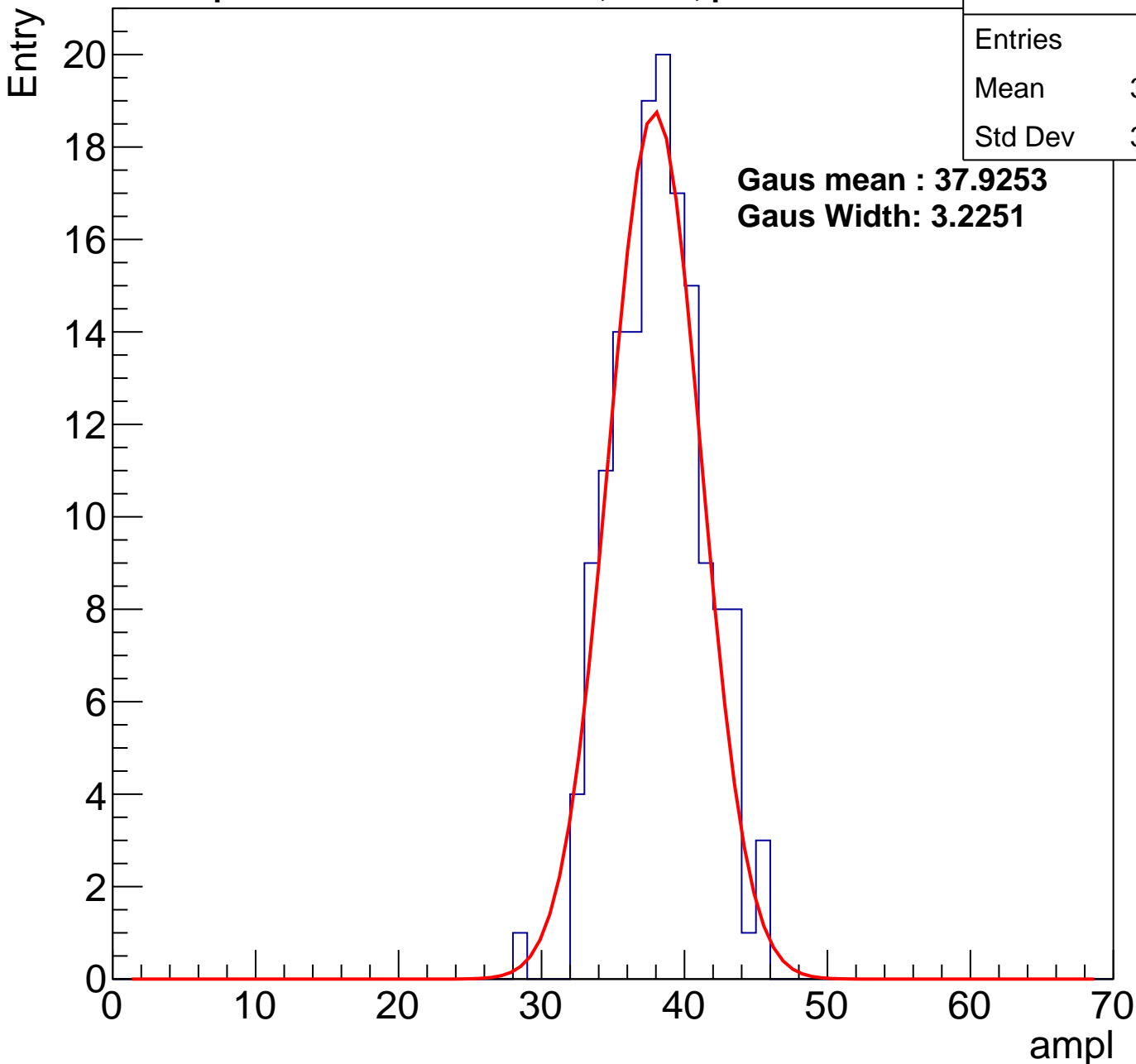
B1L001S, U20-ch9, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	37.75
Std Dev	3.136

Gaus mean : 37.9253

Gaus Width: 3.2251



B1L001S, U20-ch9, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	44.64
Std Dev	3.124

Gaus mean : 45.0931

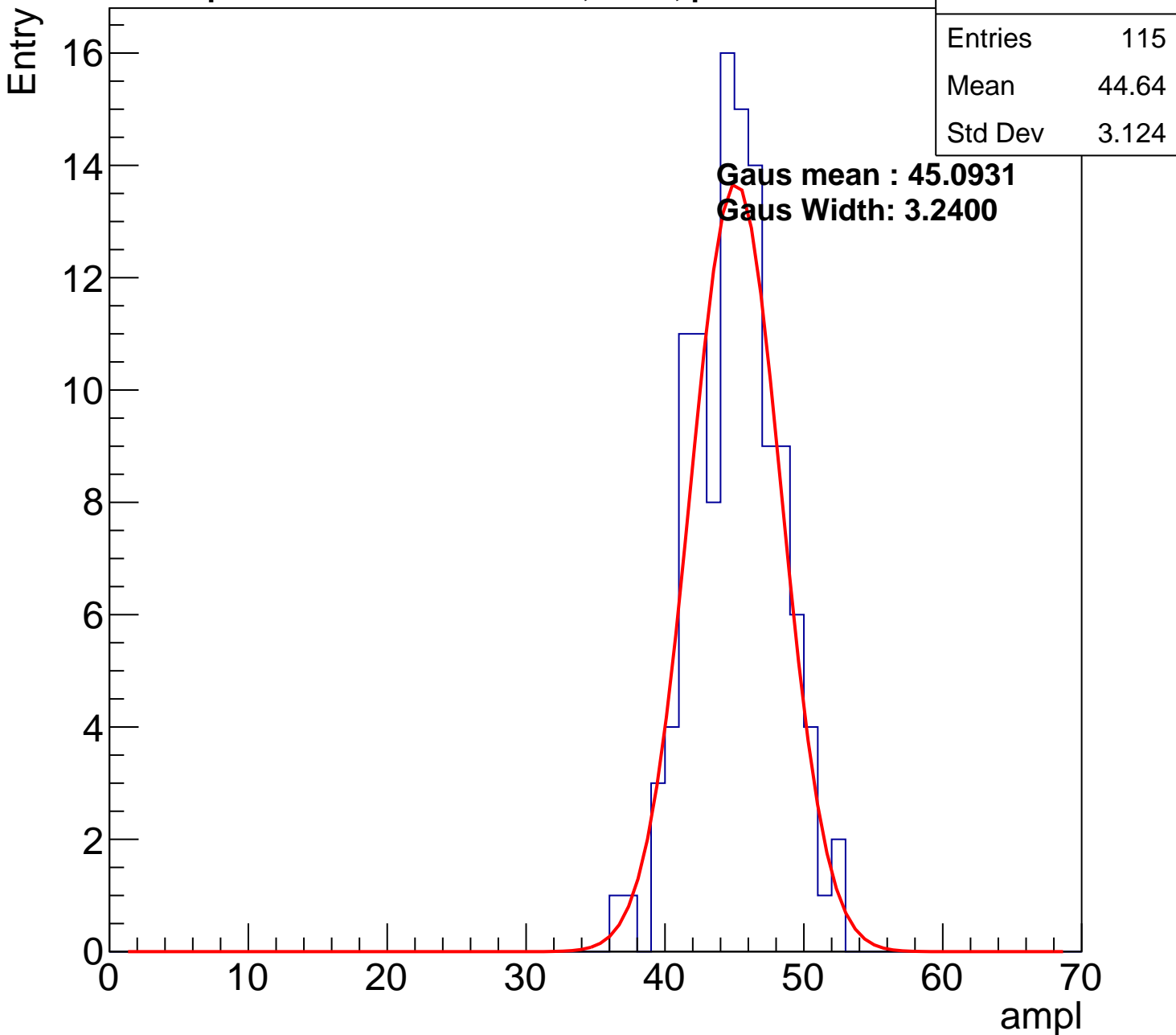
Gaus Width: 3.2400

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch9, adc3

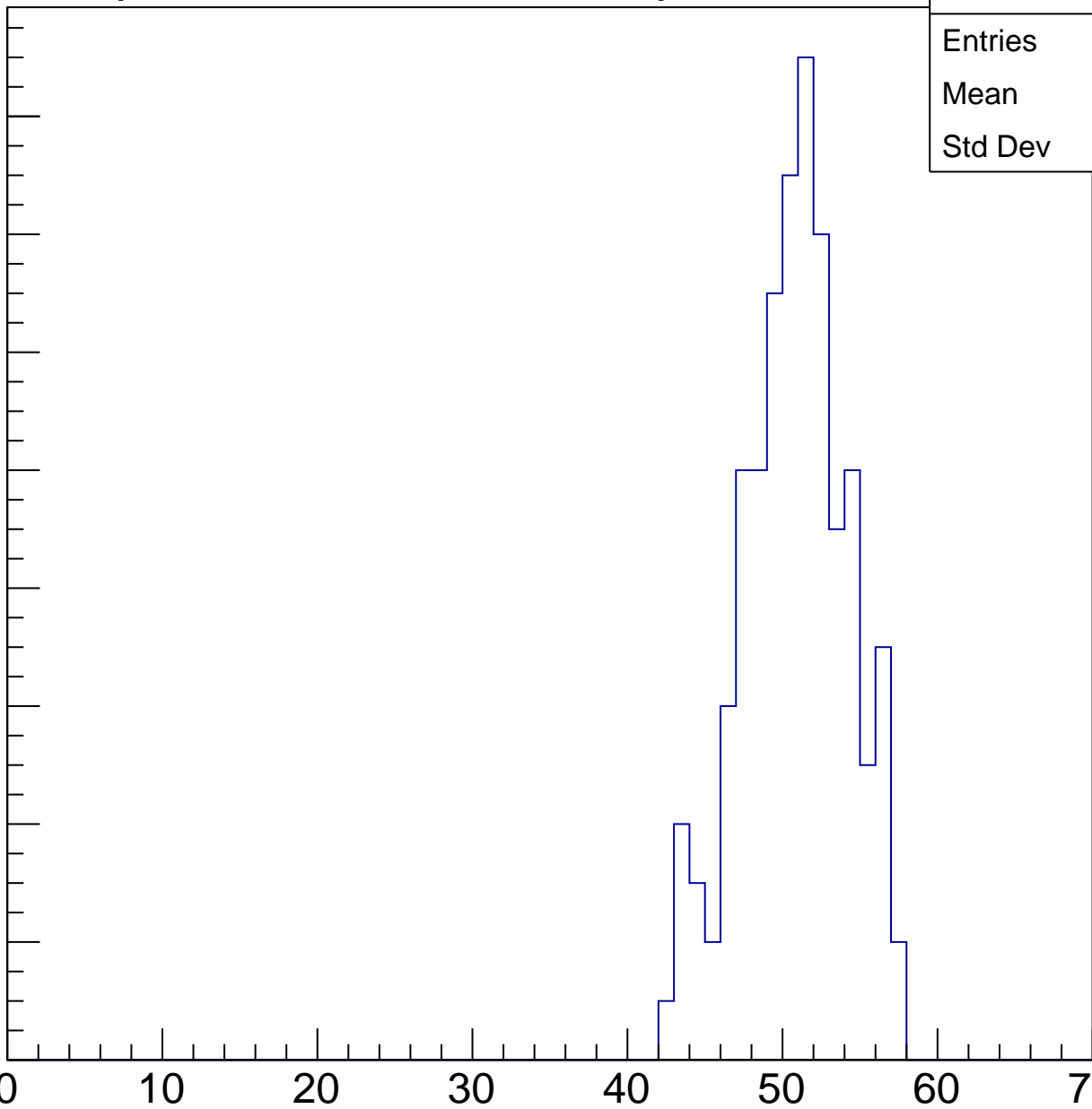
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	128
Mean	50.33
Std Dev	3.345

ampl



B1L001S, U20-ch9, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	55.96
Std Dev	5.964

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

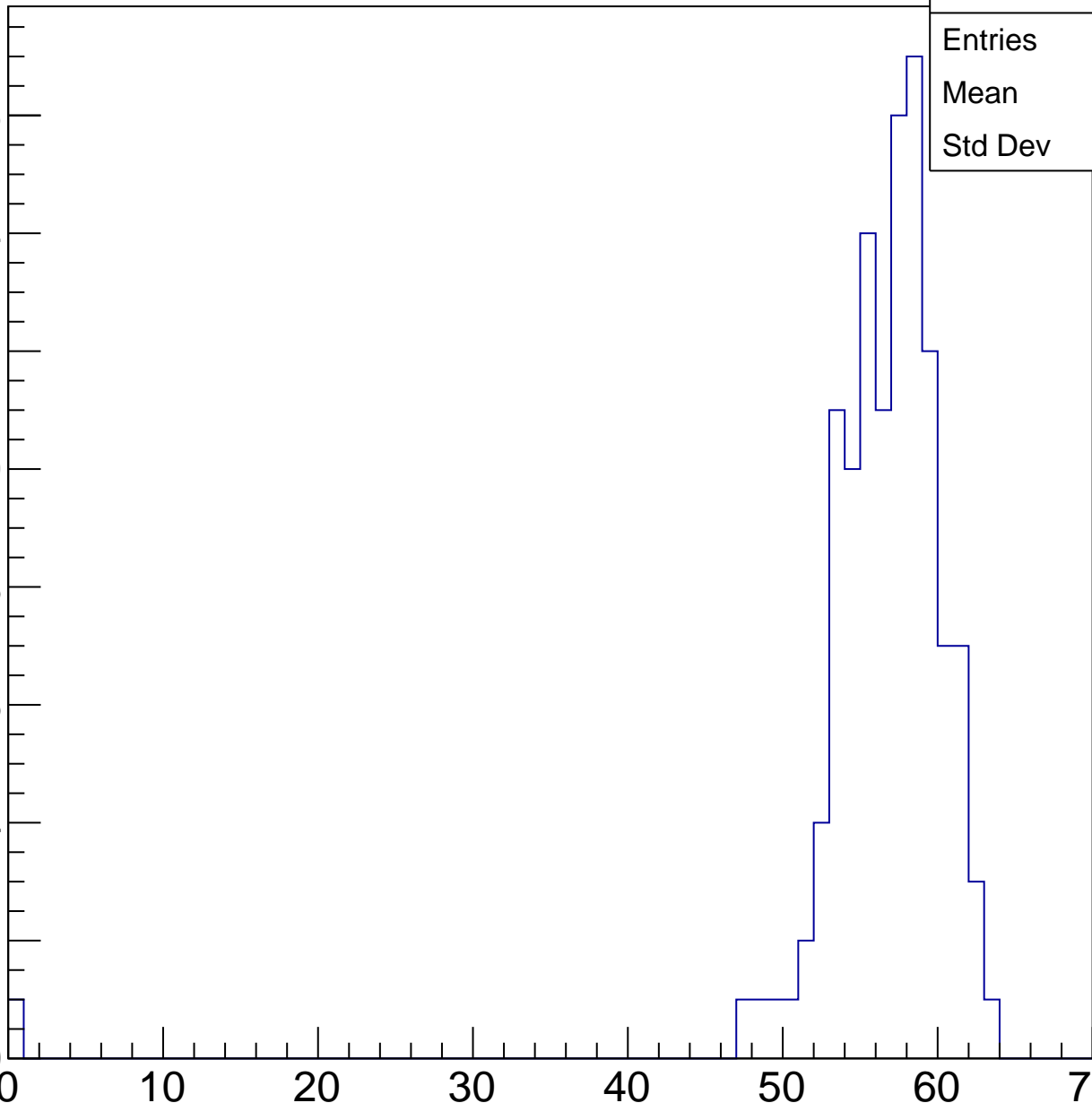
40

50

60

70

ampl



B1L001S, U20-ch9, adc5

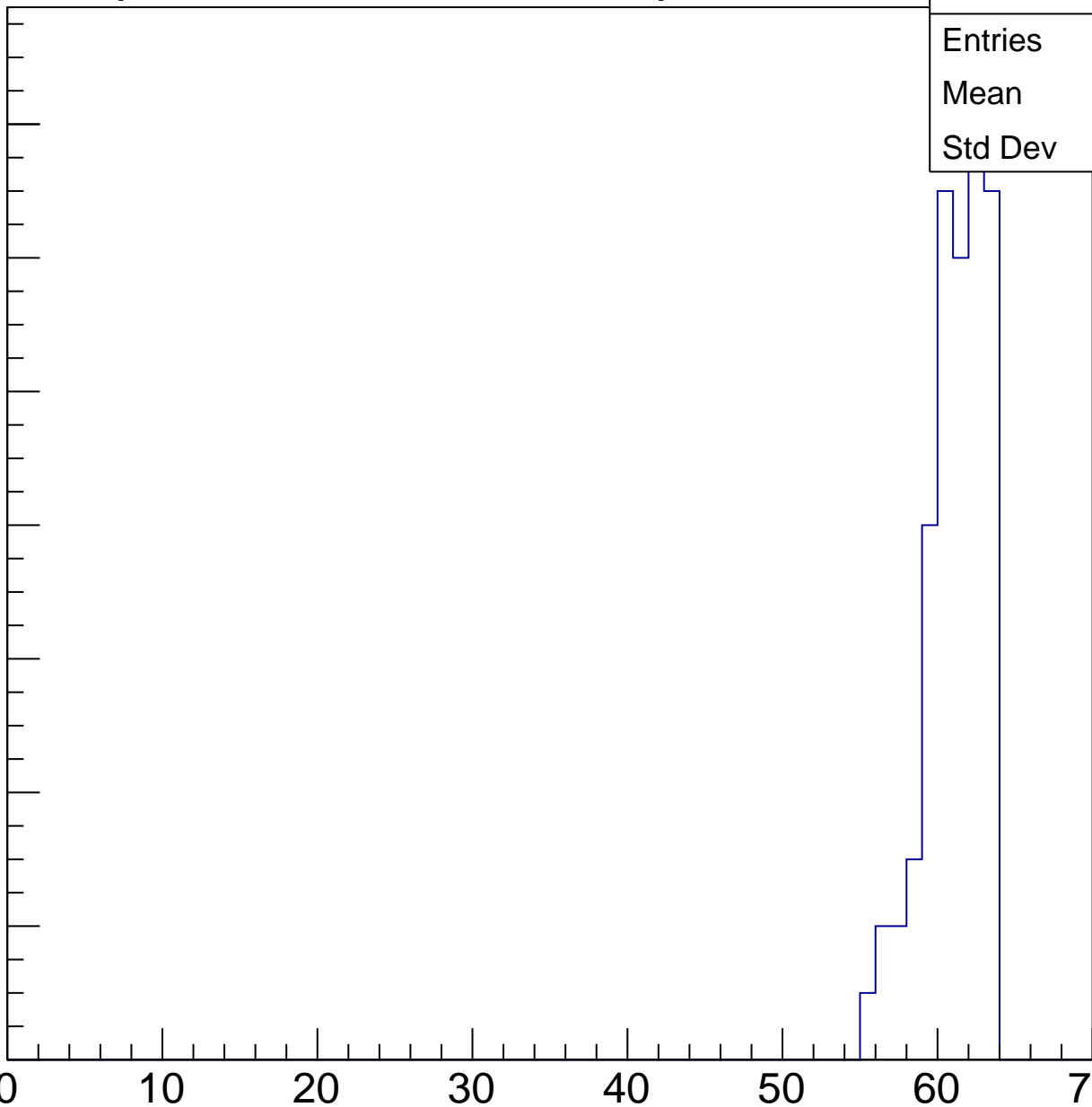
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	69
Mean	60.7
Std Dev	1.906

ampl

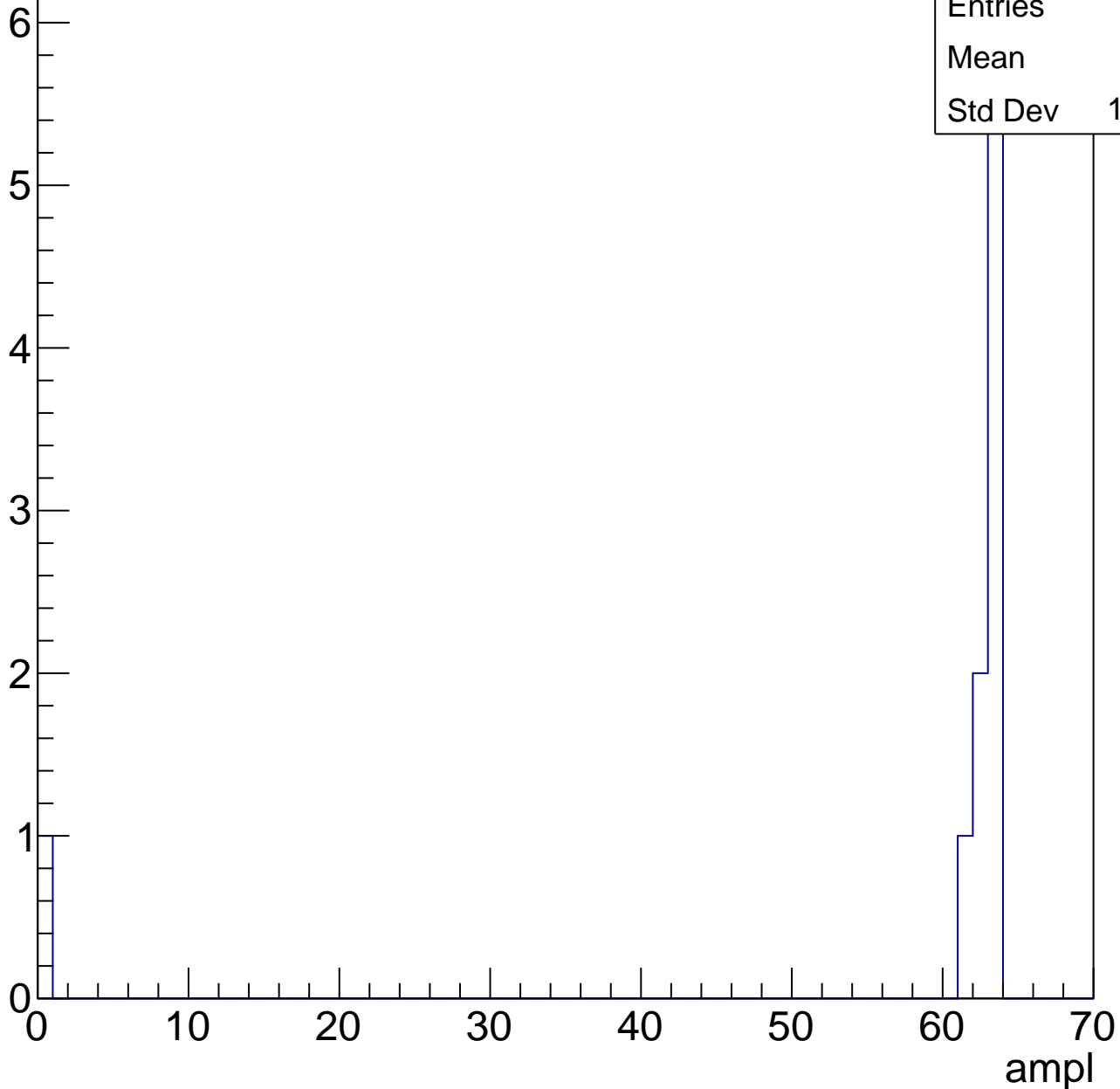


B1L001S, U20-ch9, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	56.3
Std Dev	18.78



B1L001S, U20-ch9, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

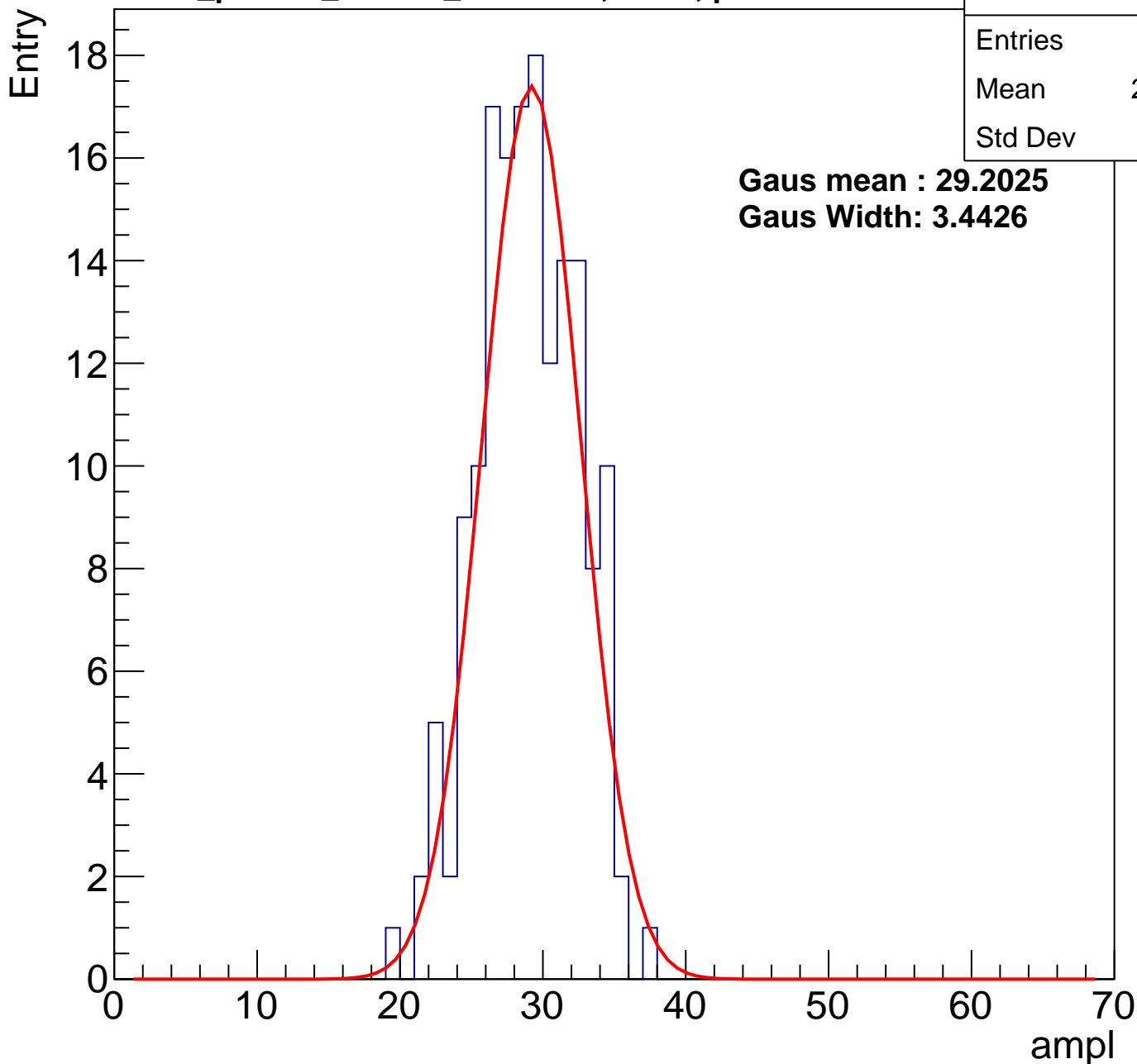
B1L001S, U20-ch10, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	28.53
Std Dev	3.41

Gaus mean : 29.2025

Gaus Width: 3.4426



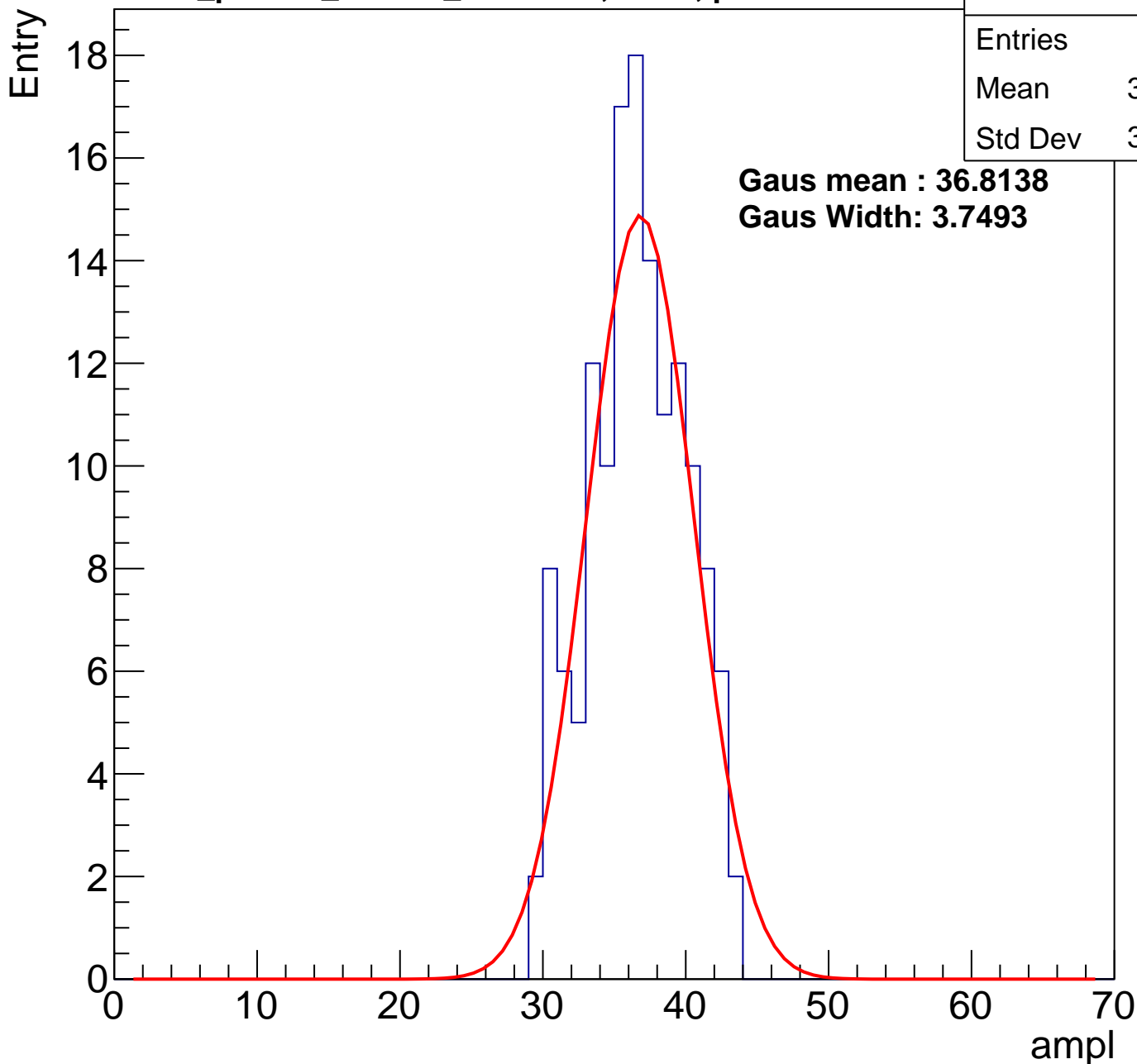
B1L001S, U20-ch10, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	36.12
Std Dev	3.388

Gaus mean : 36.8138

Gaus Width: 3.7493



B1L001S, U20-ch10, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	42.19
Std Dev	3.379

Gaus mean : 42.7611

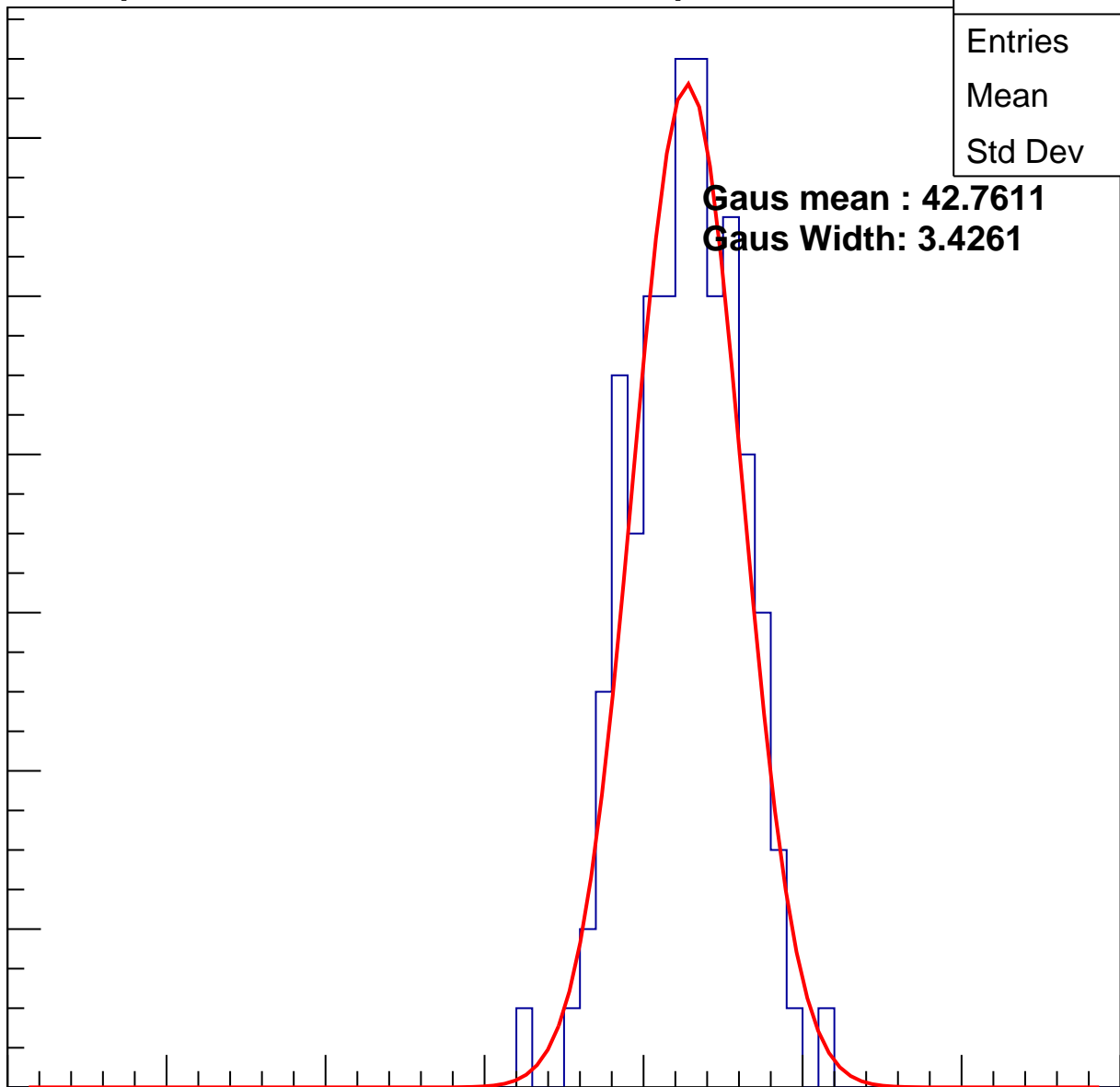
Gaus Width: 3.4261

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch10, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

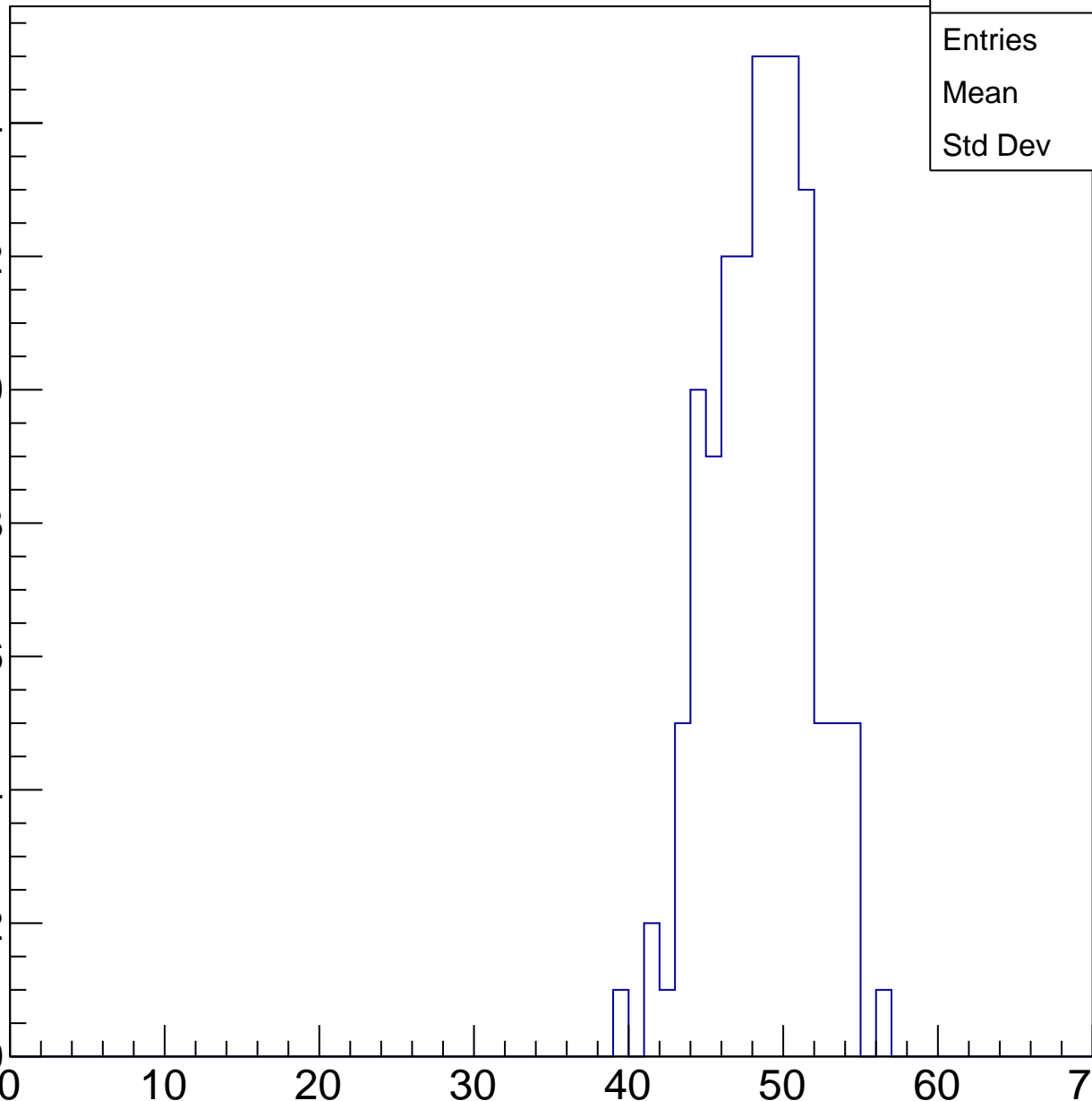
40

50

60

ampl

Entries	126
Mean	48.08
Std Dev	3.189



B1L001S, U20-ch10, adc4

calib_packv5_042523_0143.root, FC#2, port C2

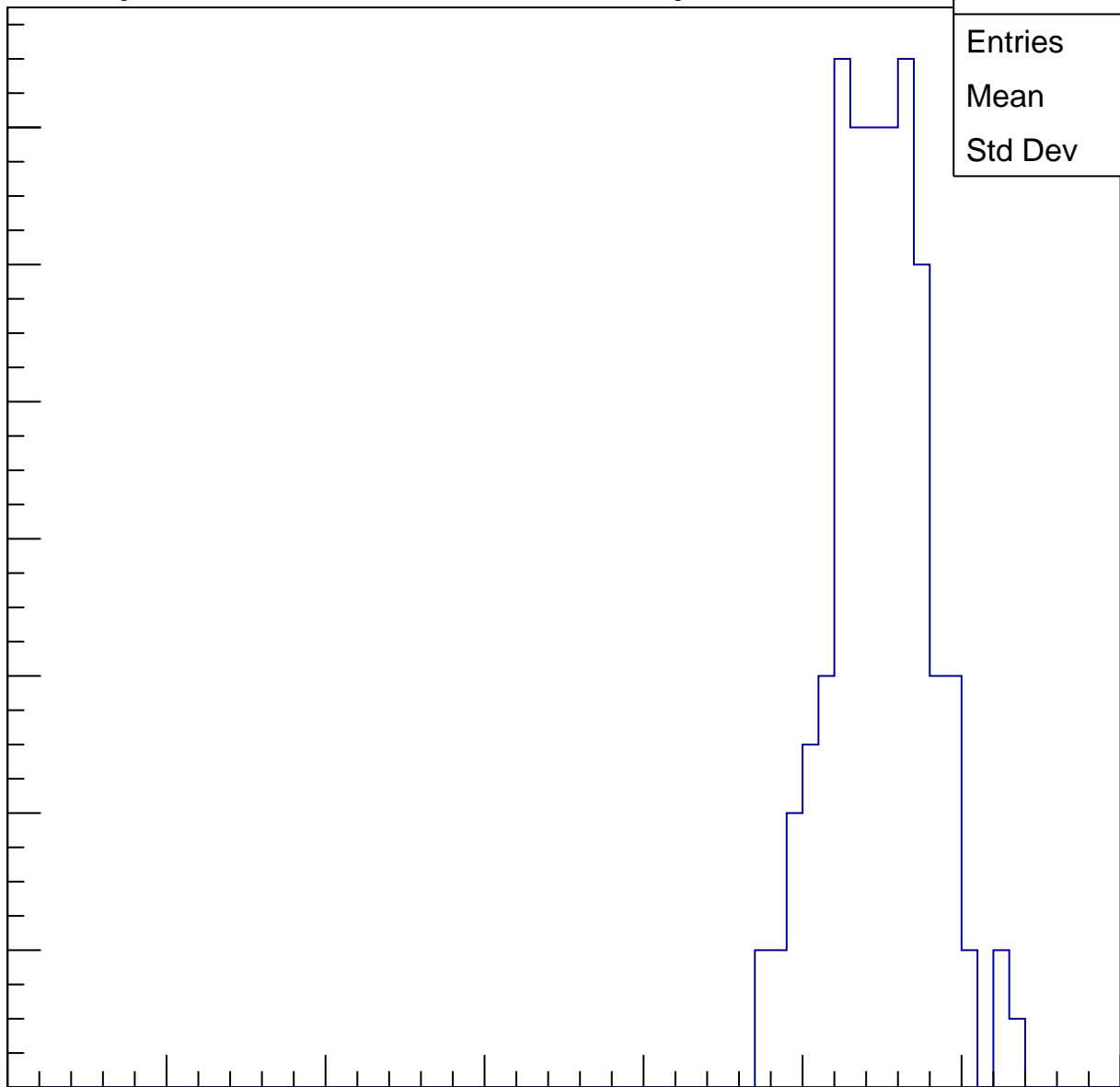
Entries	120
Mean	54.36
Std Dev	3.125

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch10, adc5

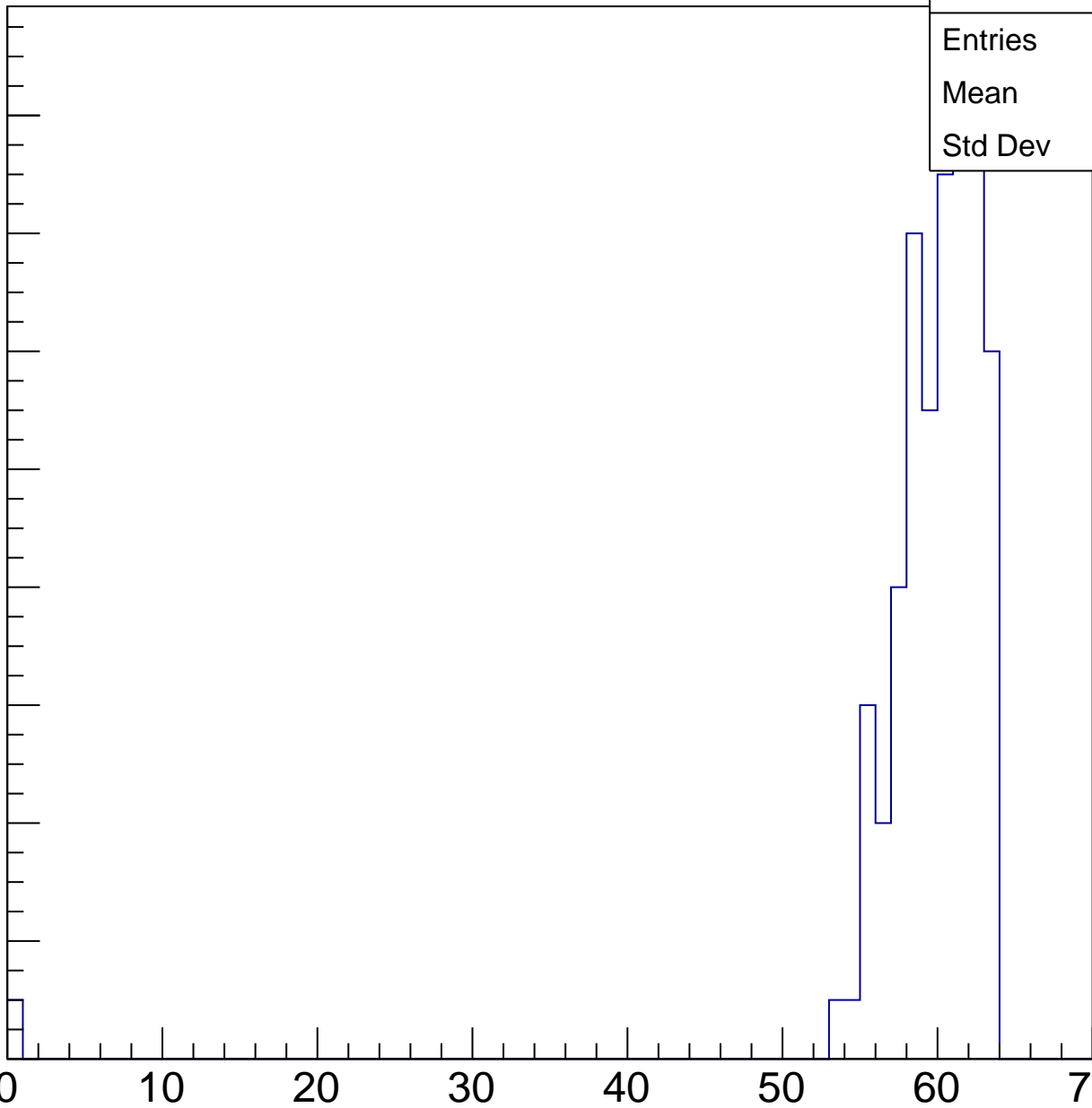
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	59.08
Std Dev	6.248

ampl

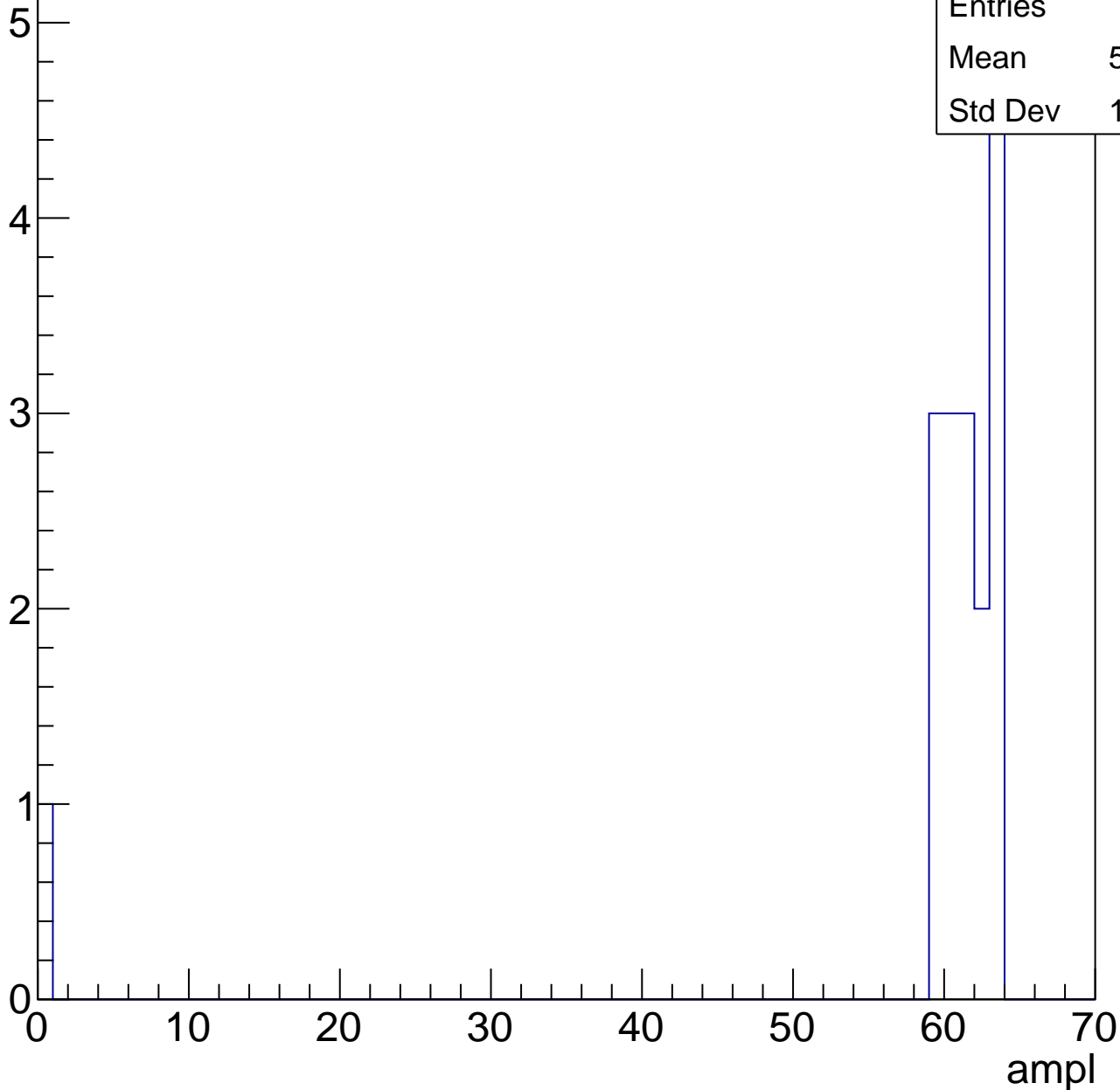


B1L001S, U20-ch10, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

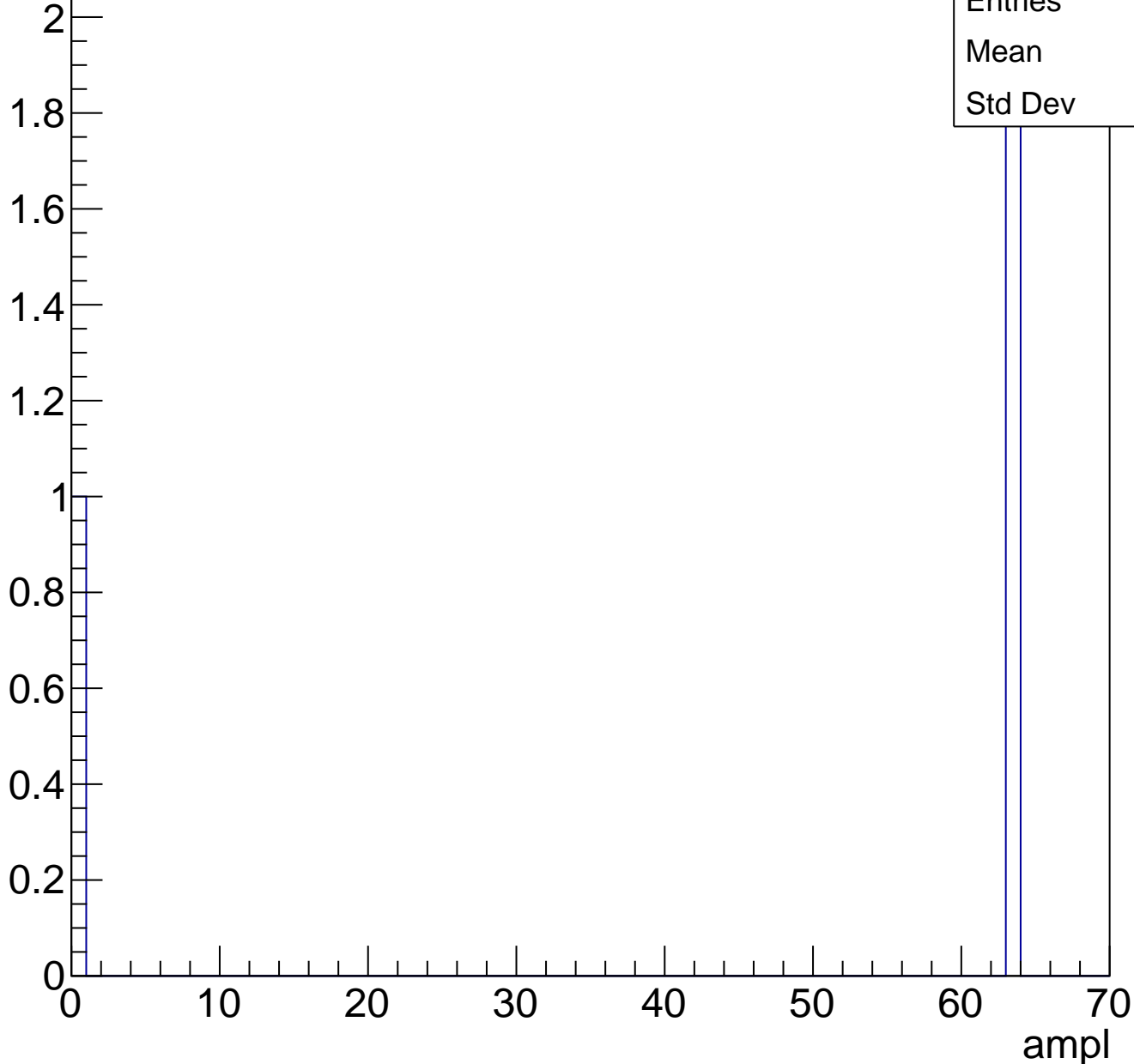
Entries	17
Mean	57.59
Std Dev	14.47



B1L001S, U20-ch10, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch11, adc0

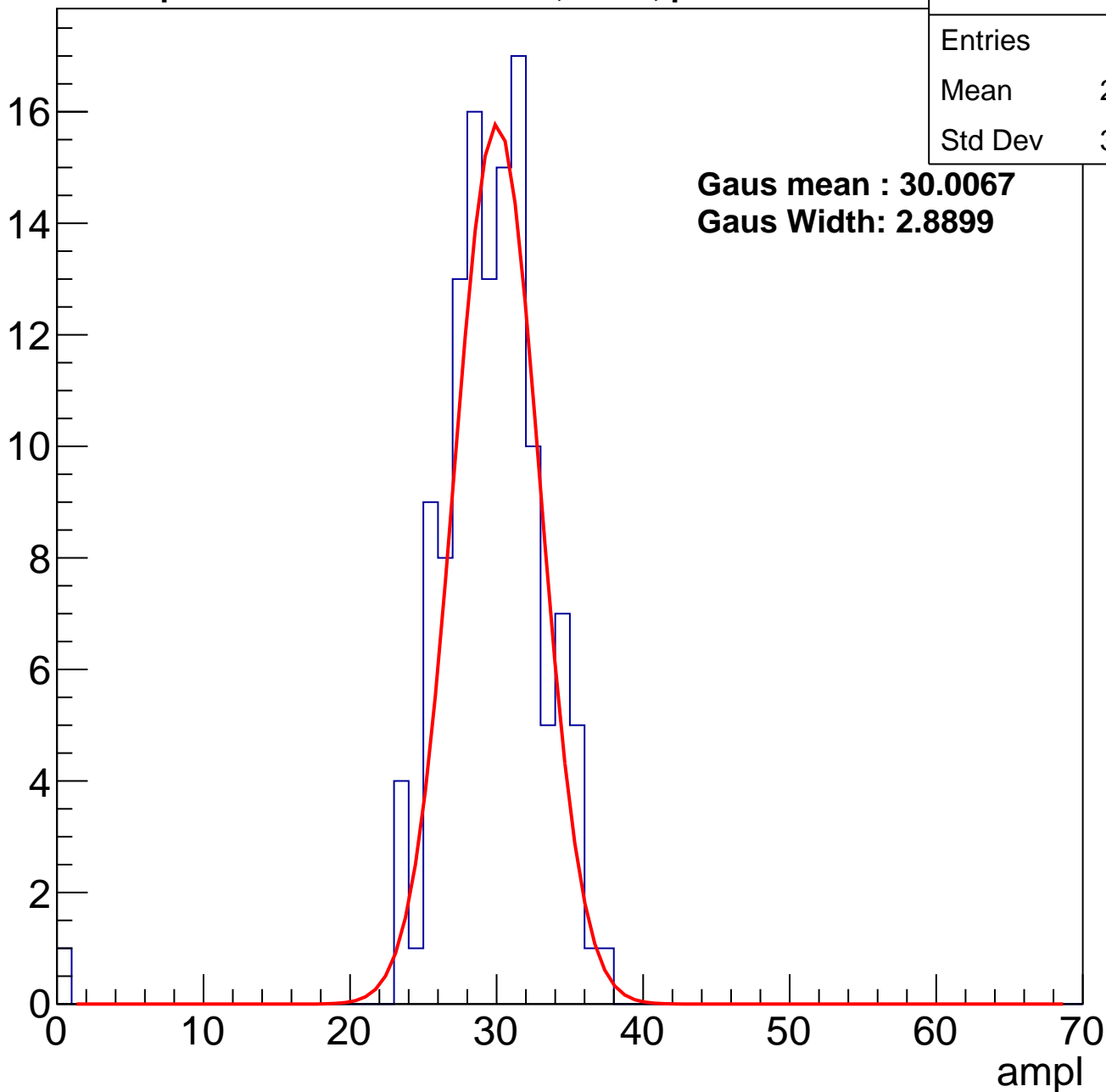
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	29.15
Std Dev	3.996

Gaus mean : 30.0067

Gaus Width: 2.8899

Entry



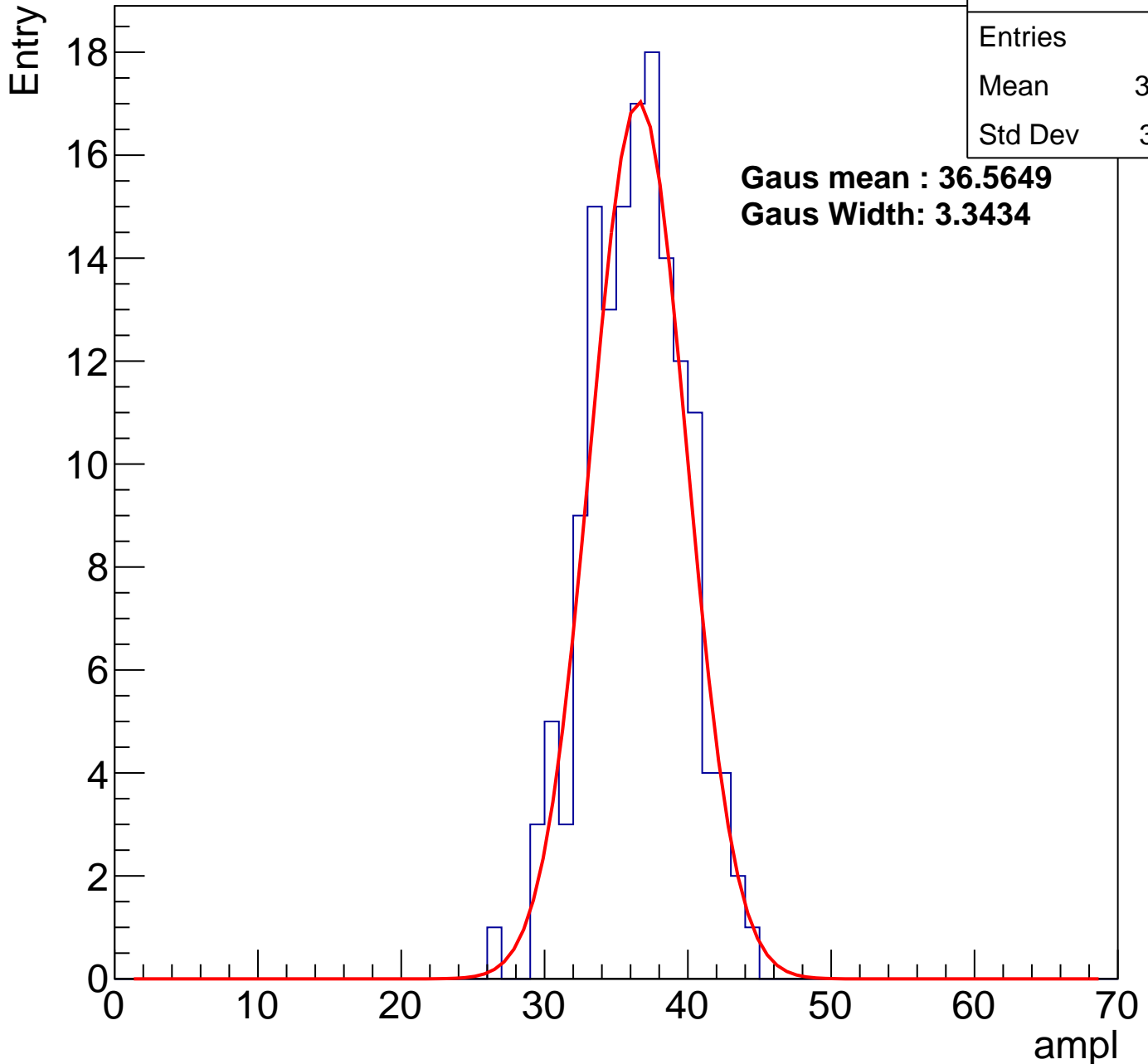
B1L001S, U20-ch11, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	35.96
Std Dev	3.311

Gaus mean : 36.5649

Gaus Width: 3.3434



B1L001S, U20-ch11, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	42.89
Std Dev	3.001

Gaus mean : 43.6515

Gaus Width: 3.2224

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

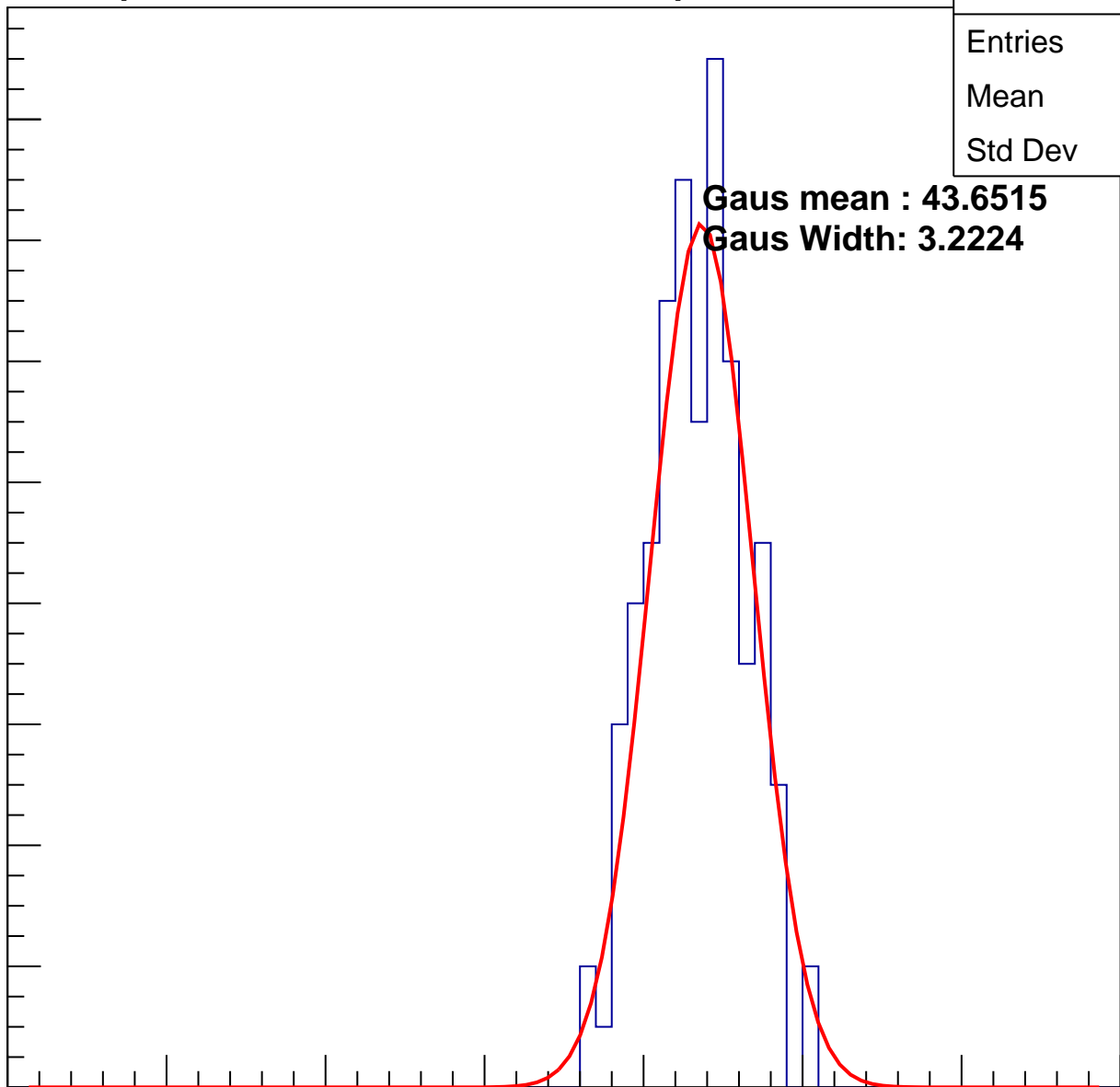
40

50

60

70

ampl



B1L001S, U20-ch11, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	48.92
Std Dev	3.386

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

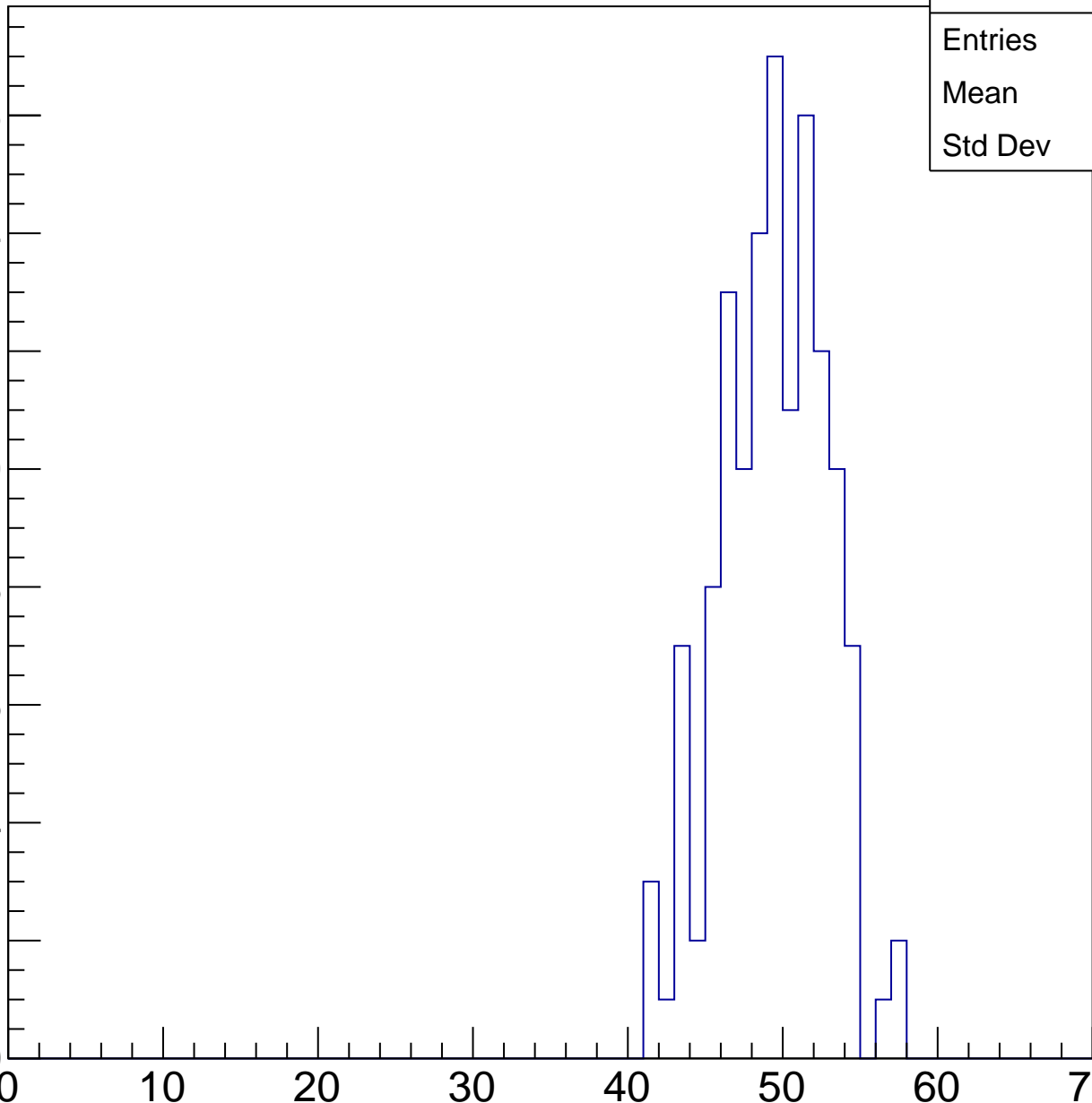
40

50

60

70

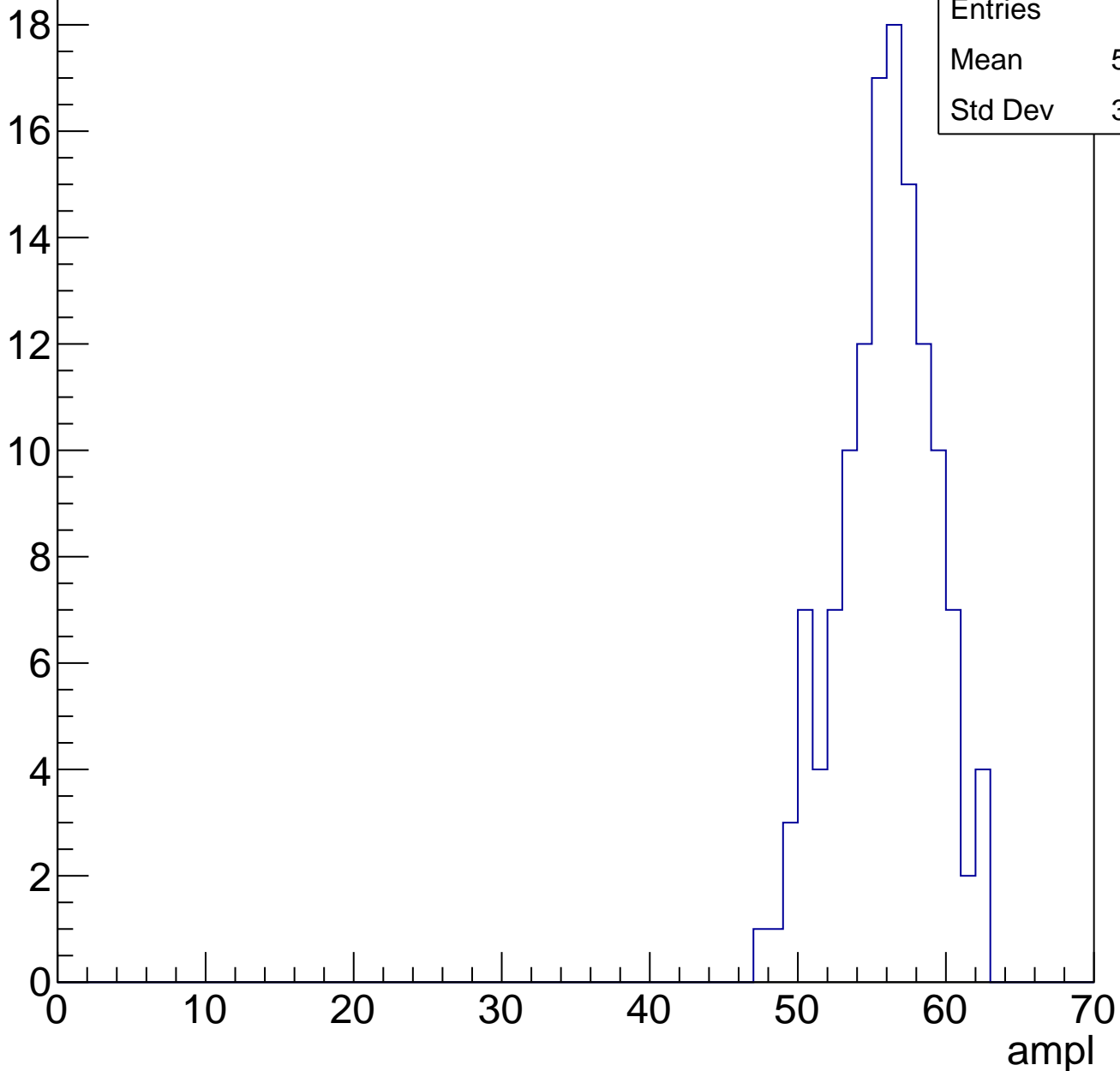
ampl



B1L001S, U20-ch11, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch11, adc5

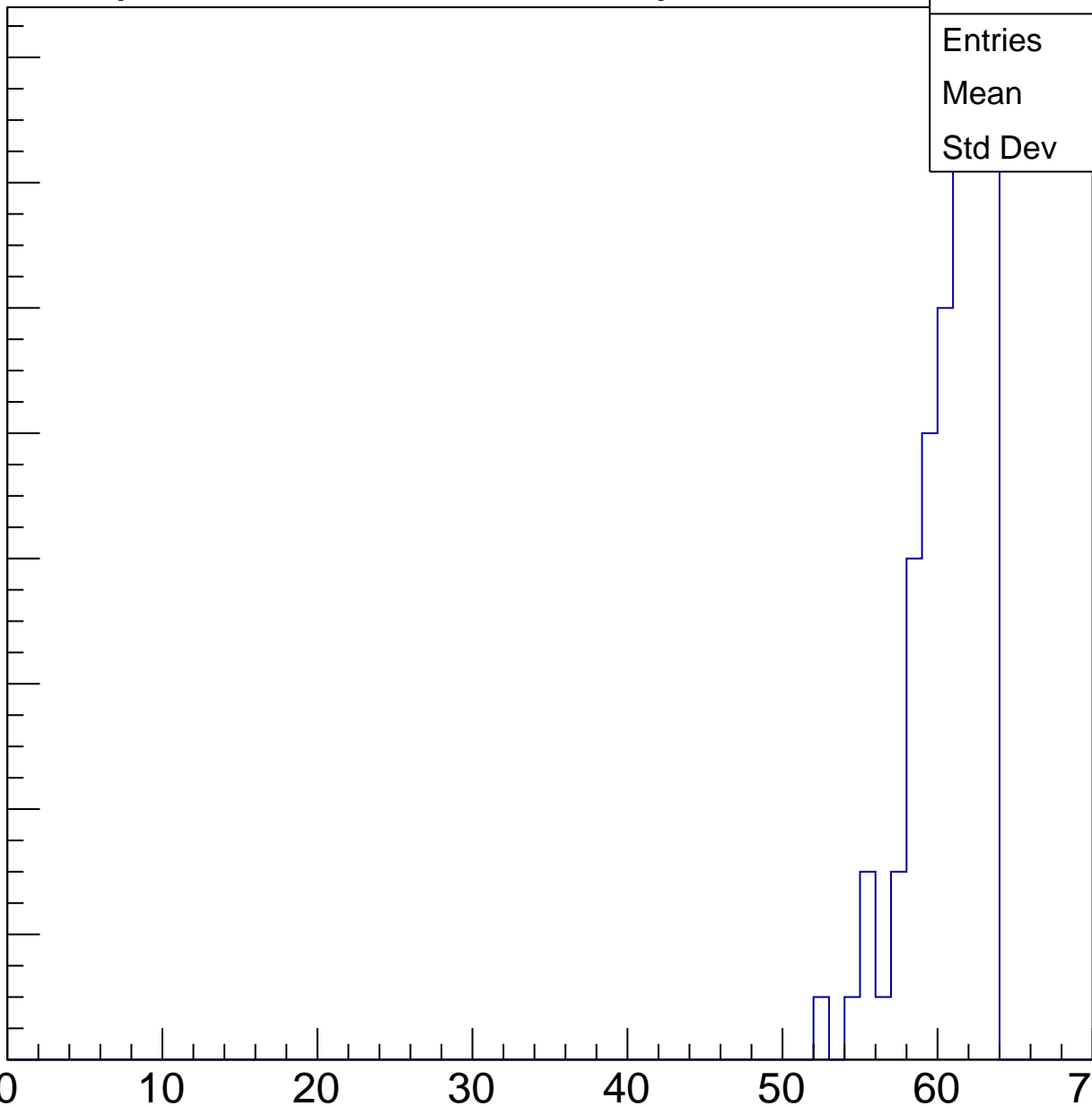
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	60.27
Std Dev	2.338

ampl

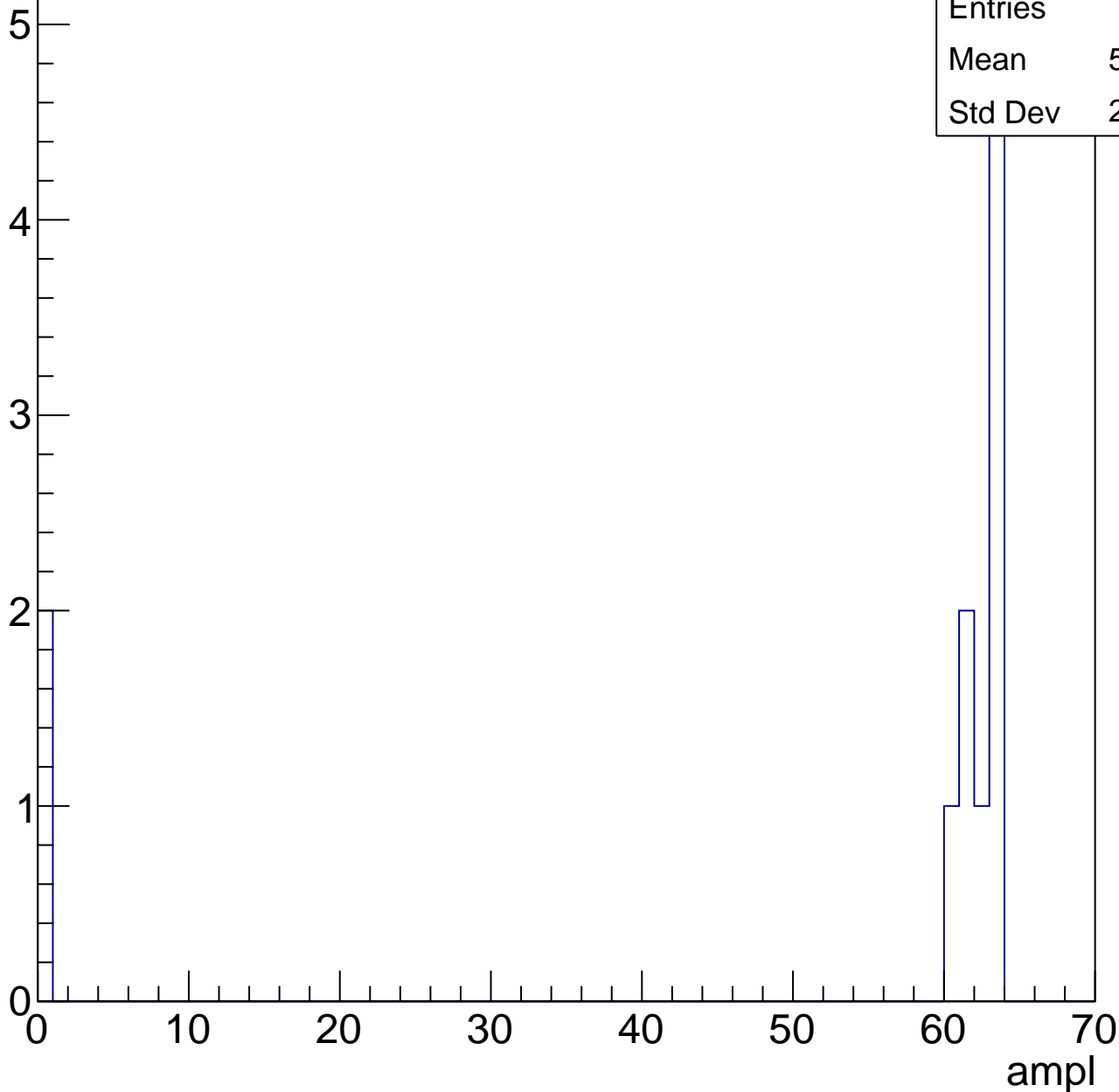


B1L001S, U20-ch11, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	50.82
Std Dev	23.98



B1L001S, U20-ch11, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



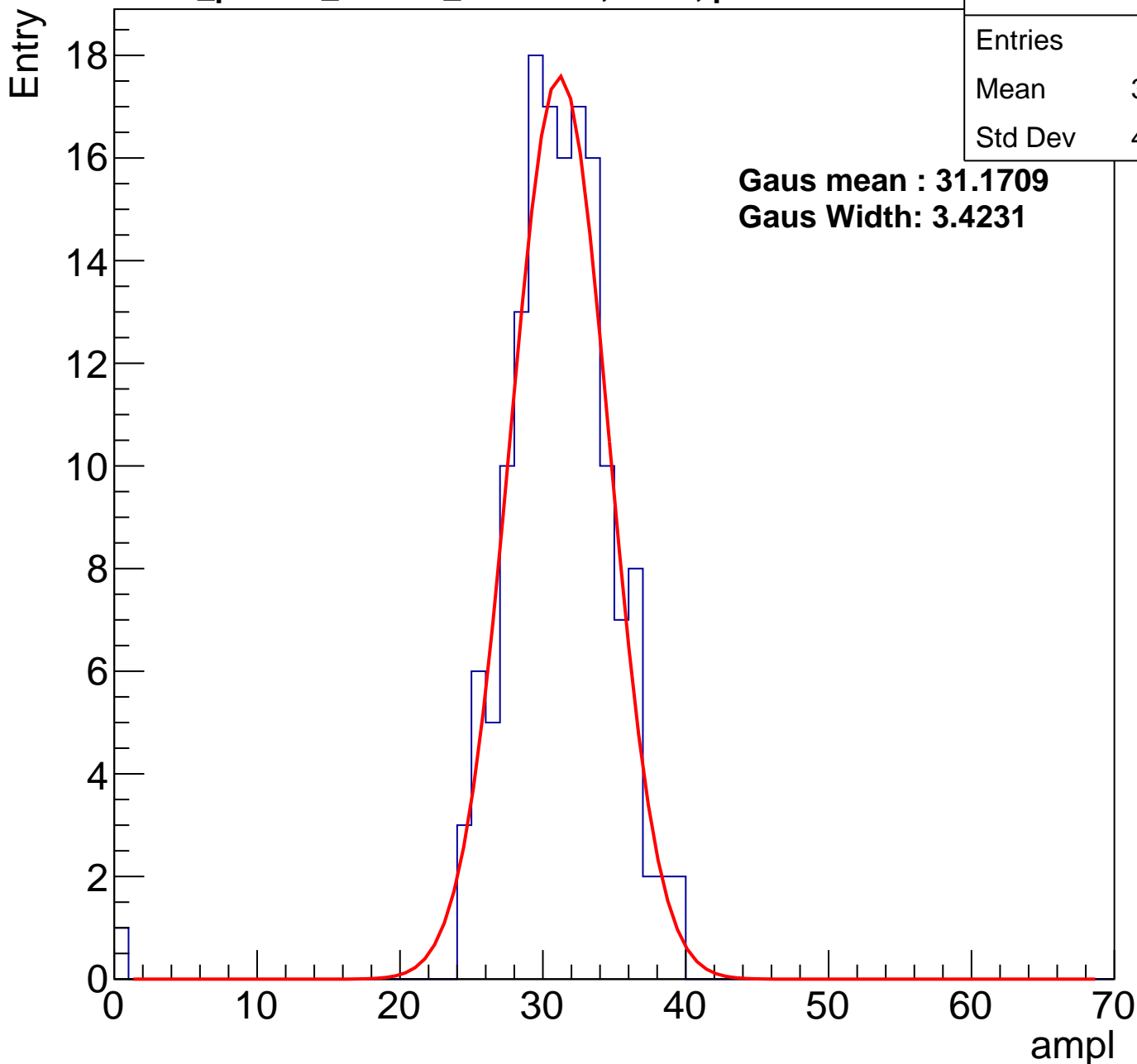
B1L001S, U20-ch12, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	30.63
Std Dev	4.103

Gaus mean : 31.1709

Gaus Width: 3.4231



B1L001S, U20-ch12, adc1

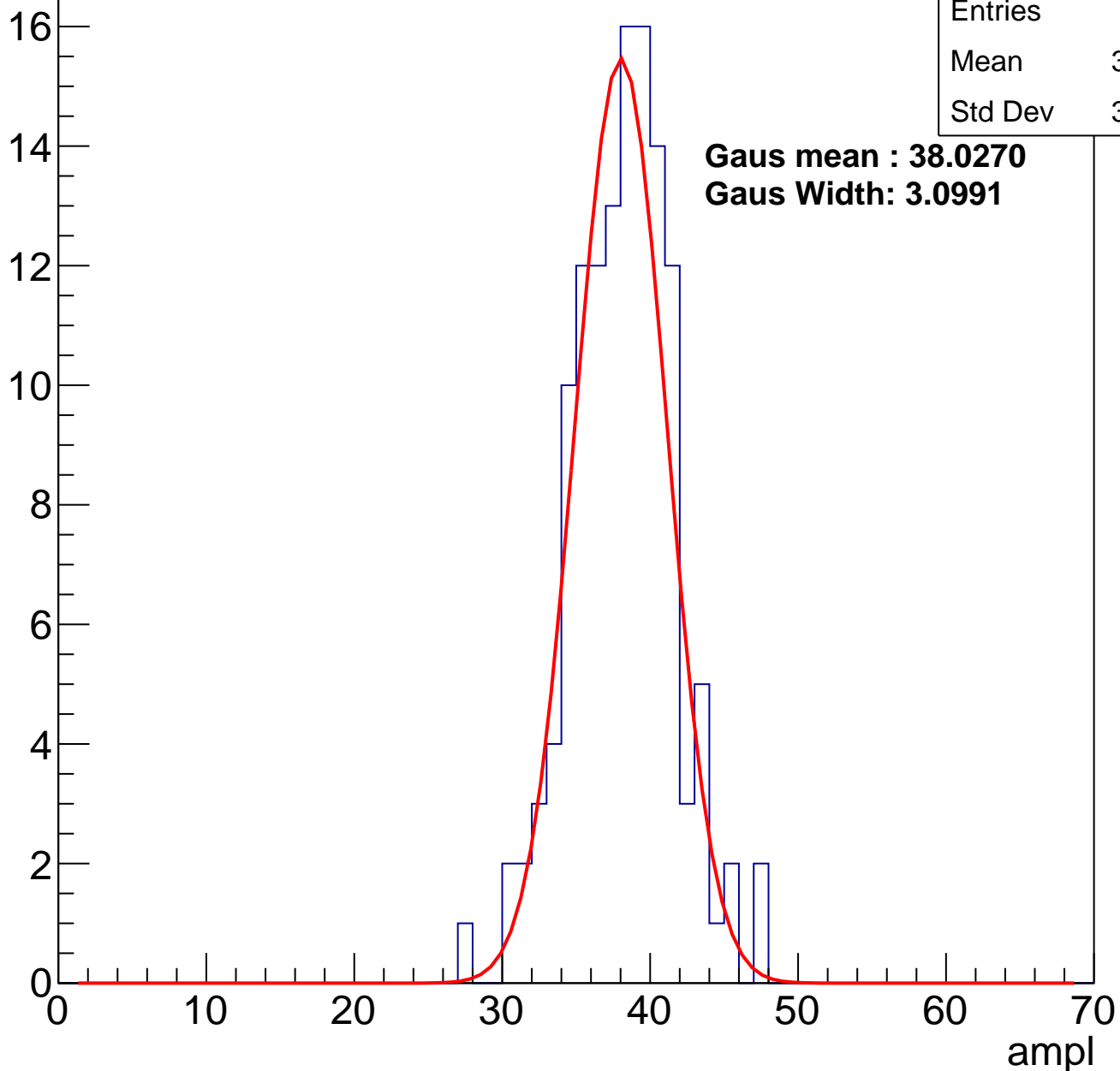
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	37.72
Std Dev	3.422

Gaus mean : 38.0270

Gaus Width: 3.0991

Entry



B1L001S, U20-ch12, adc2

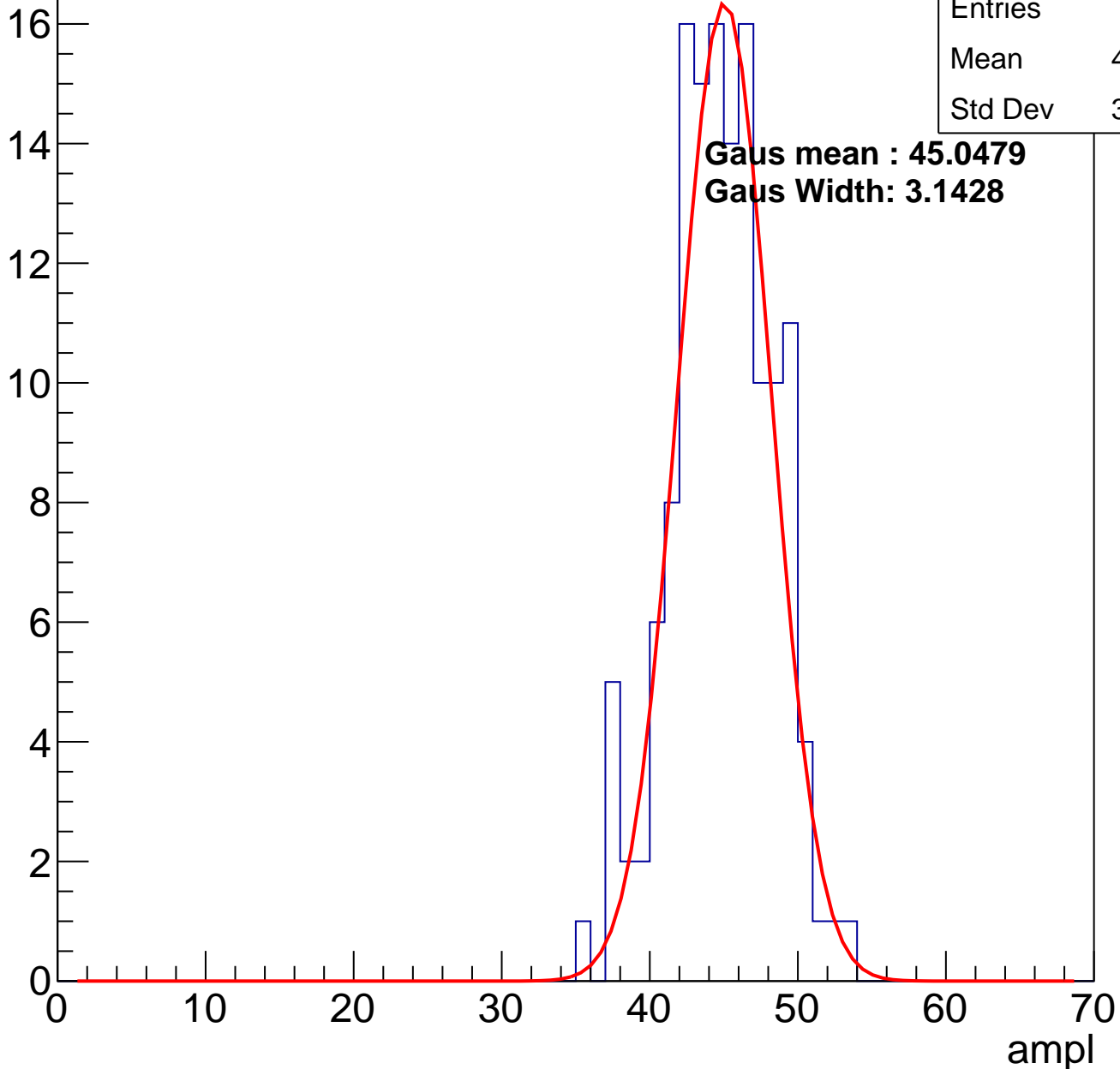
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	44.42
Std Dev	3.405

Gaus mean : 45.0479

Gaus Width: 3.1428

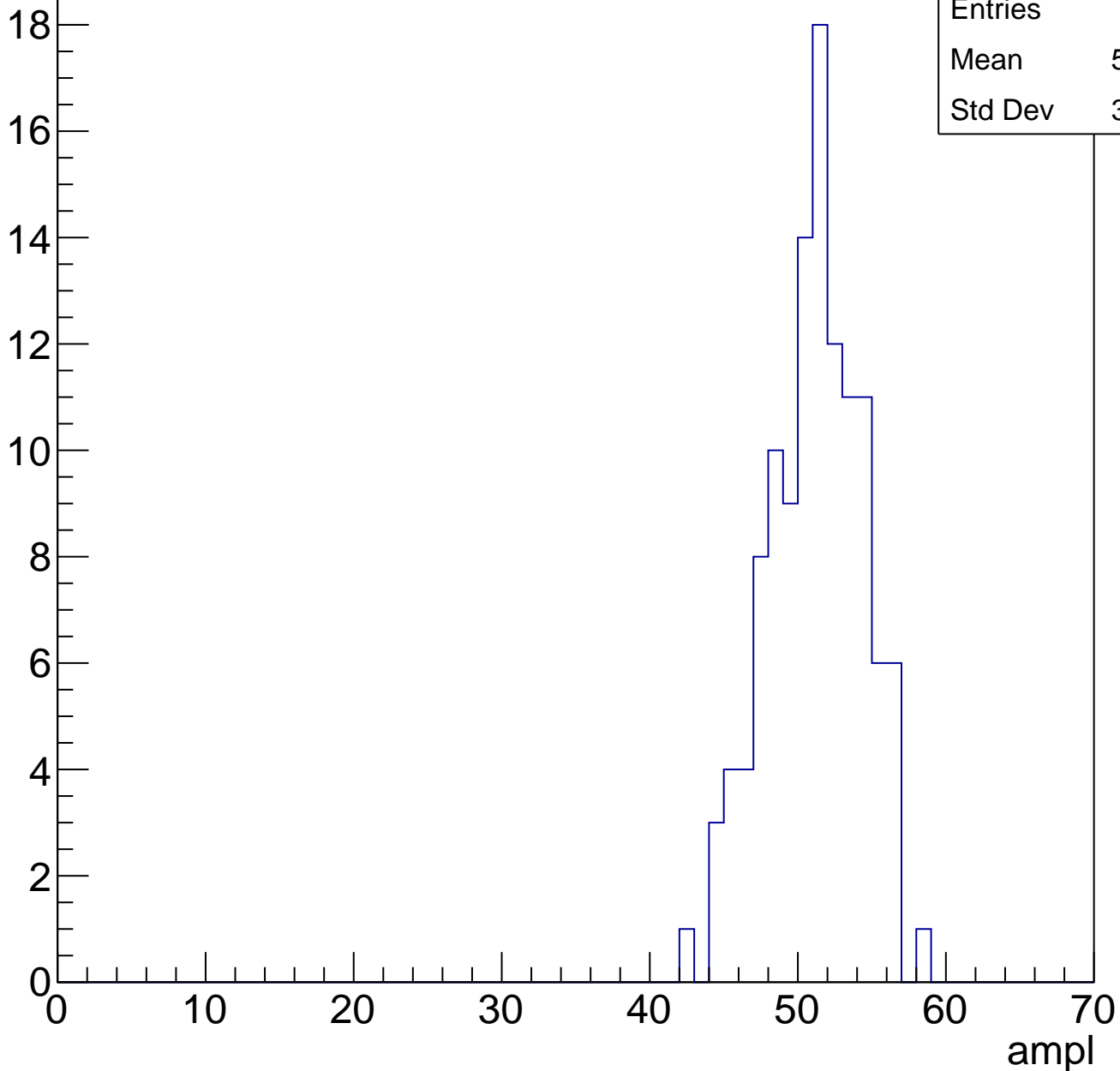
Entry



B1L001S, U20-ch12, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	118
Mean	50.66
Std Dev	3.158

B1L001S, U20-ch12, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

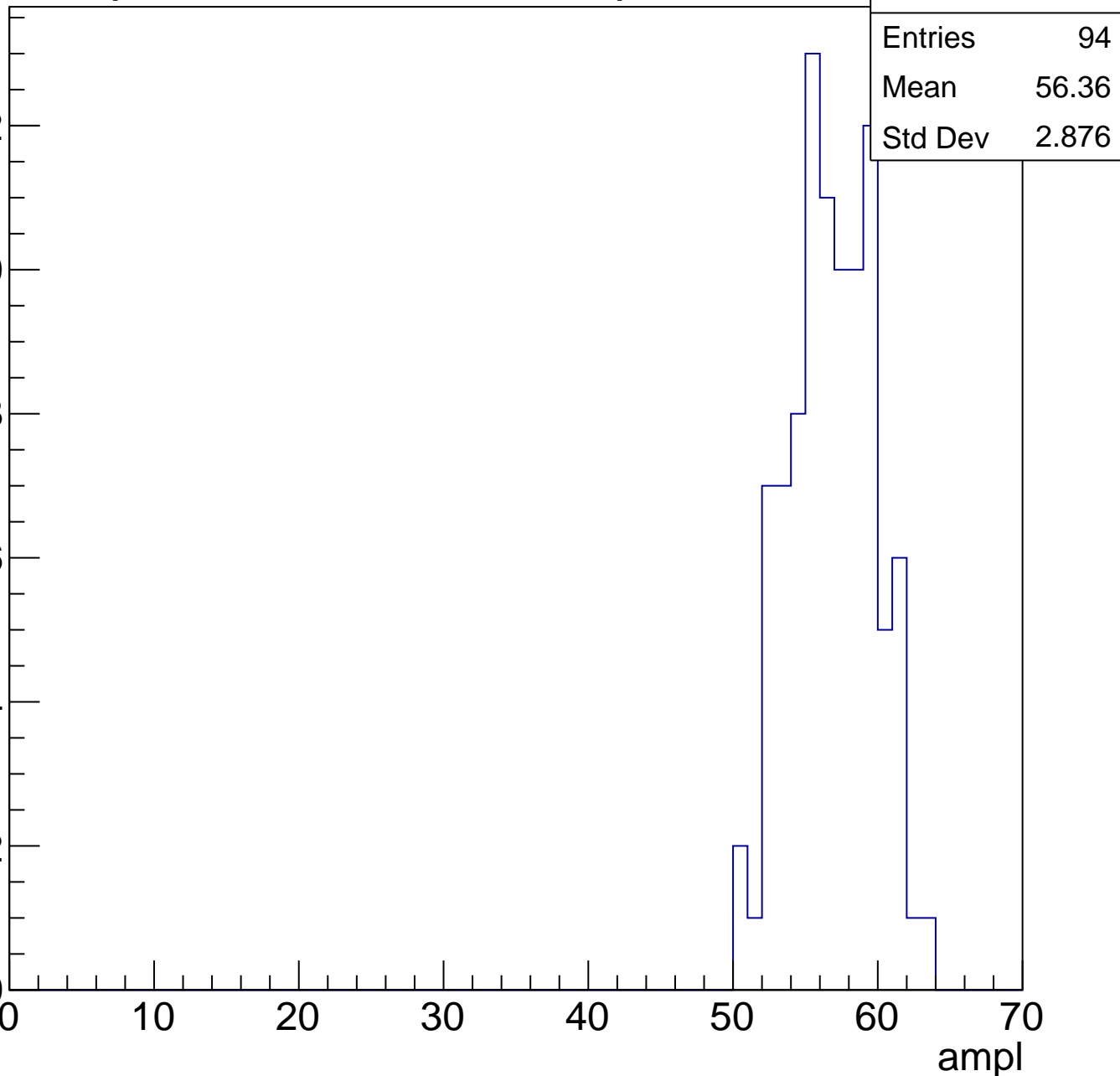
50

60

70

ampl

Entries	94
Mean	56.36
Std Dev	2.876



B1L001S, U20-ch12, adc5

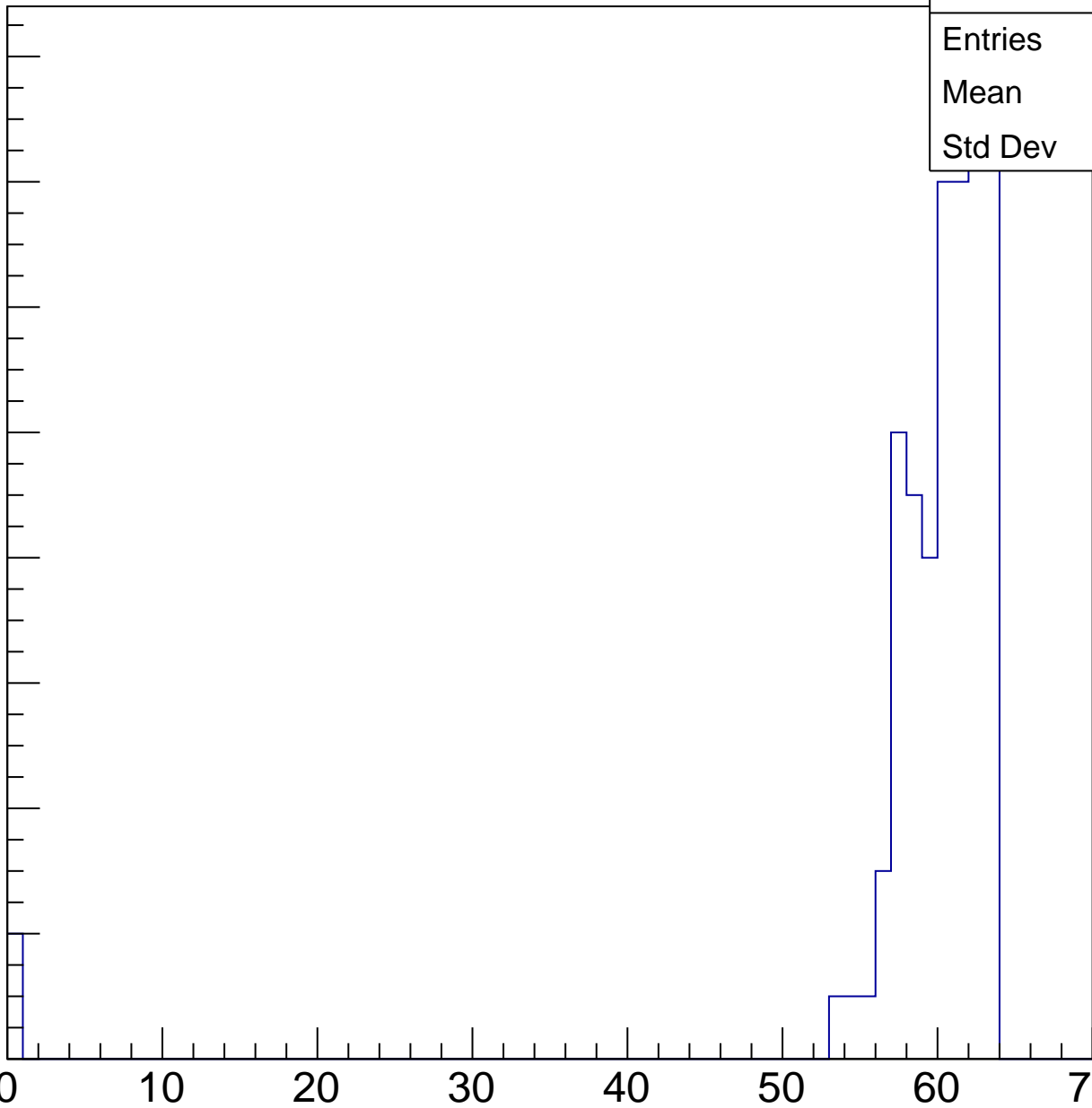
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	58.82
Std Dev	8.934

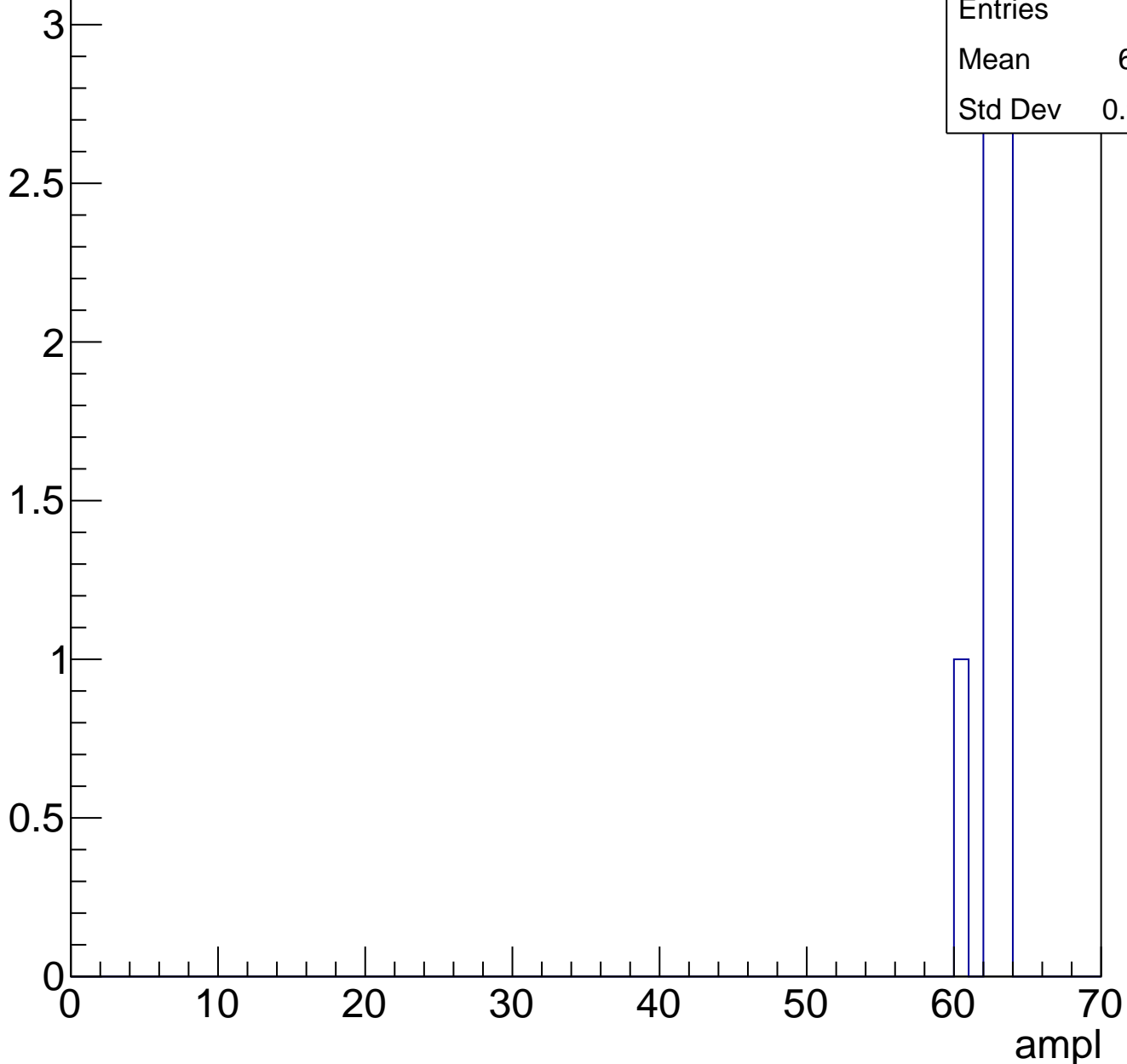
ampl



B1L001S, U20-ch12, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch12, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch13, adc0

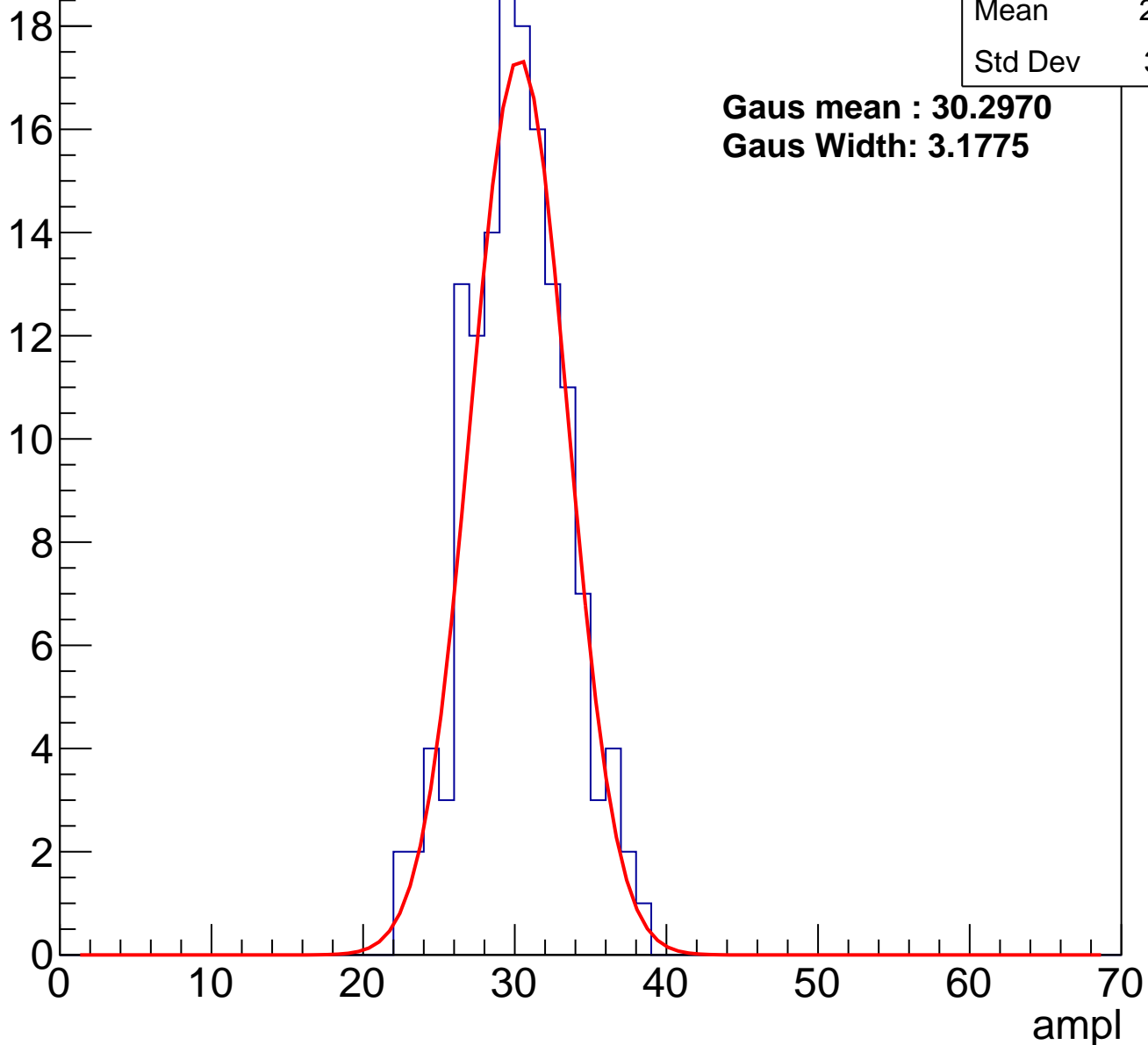
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	29.72
Std Dev	3.211

Gaus mean : 30.2970

Gaus Width: 3.1775

Entry



B1L001S, U20-ch13, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	36.65
Std Dev	3.047

Gaus mean : 37.1213

Gaus Width: 3.0653

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

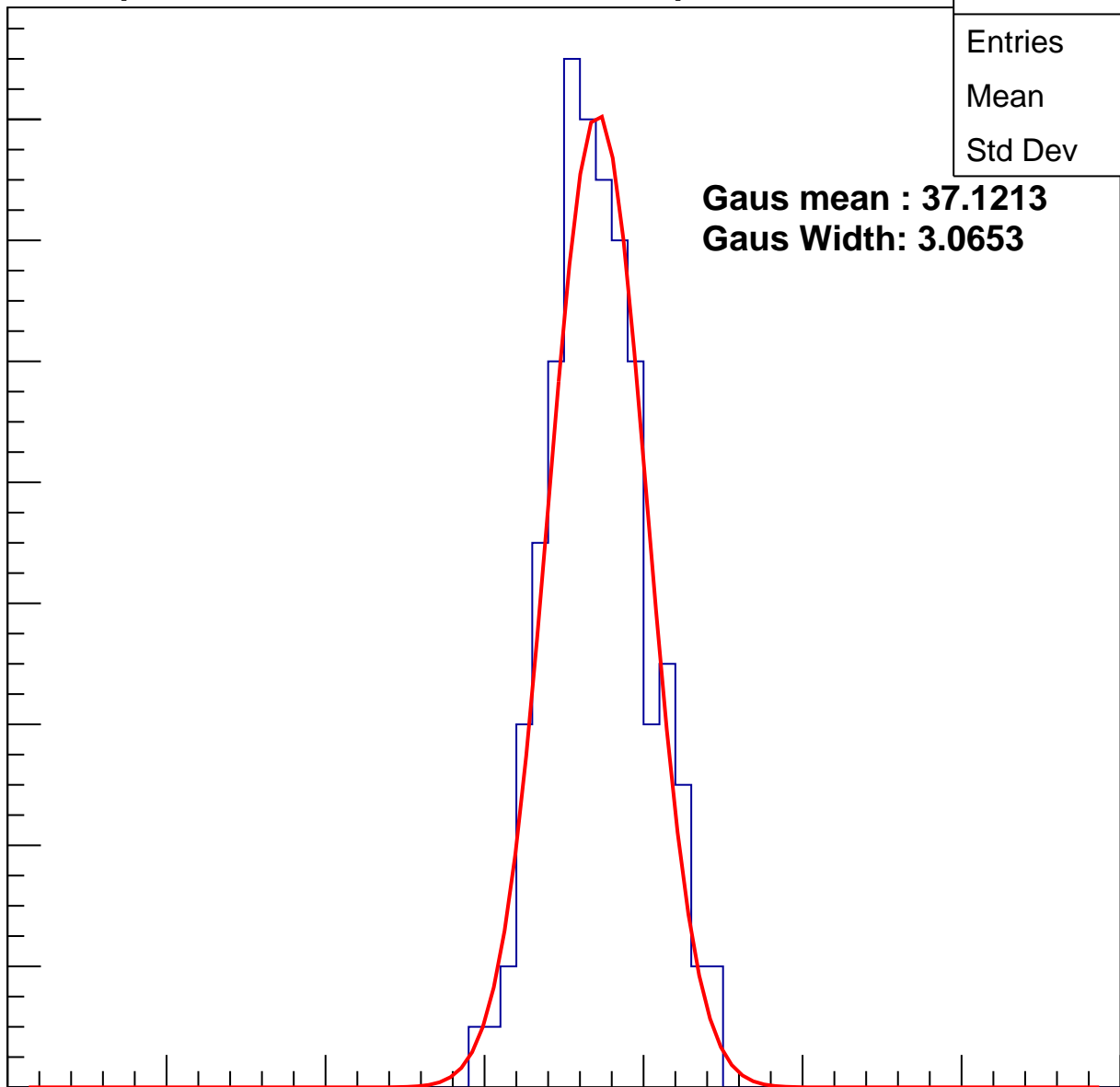
40

50

60

70

ampl



B1L001S, U20-ch13, adc2

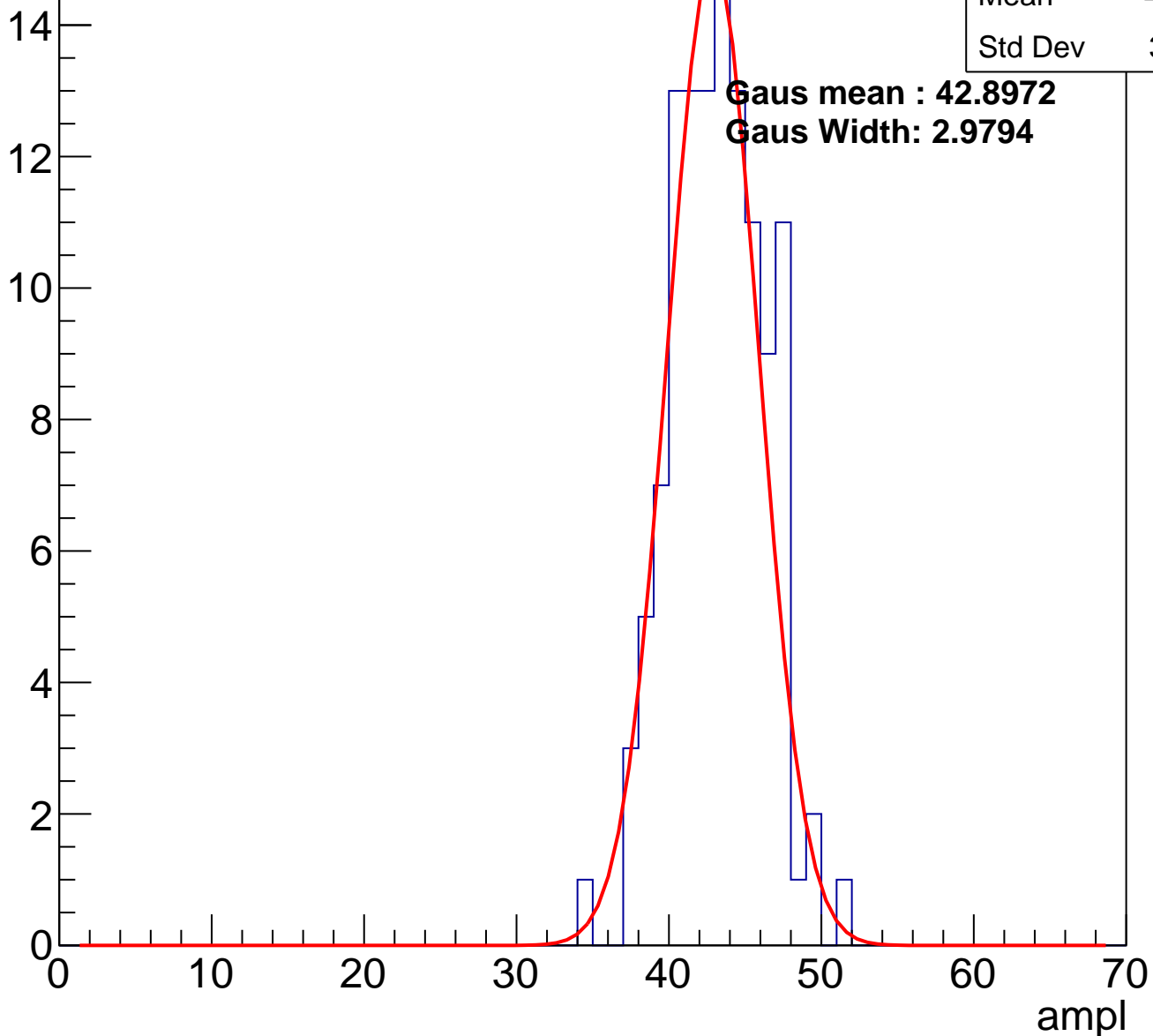
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	42.77
Std Dev	3.021

Gaus mean : 42.8972

Gaus Width: 2.9794

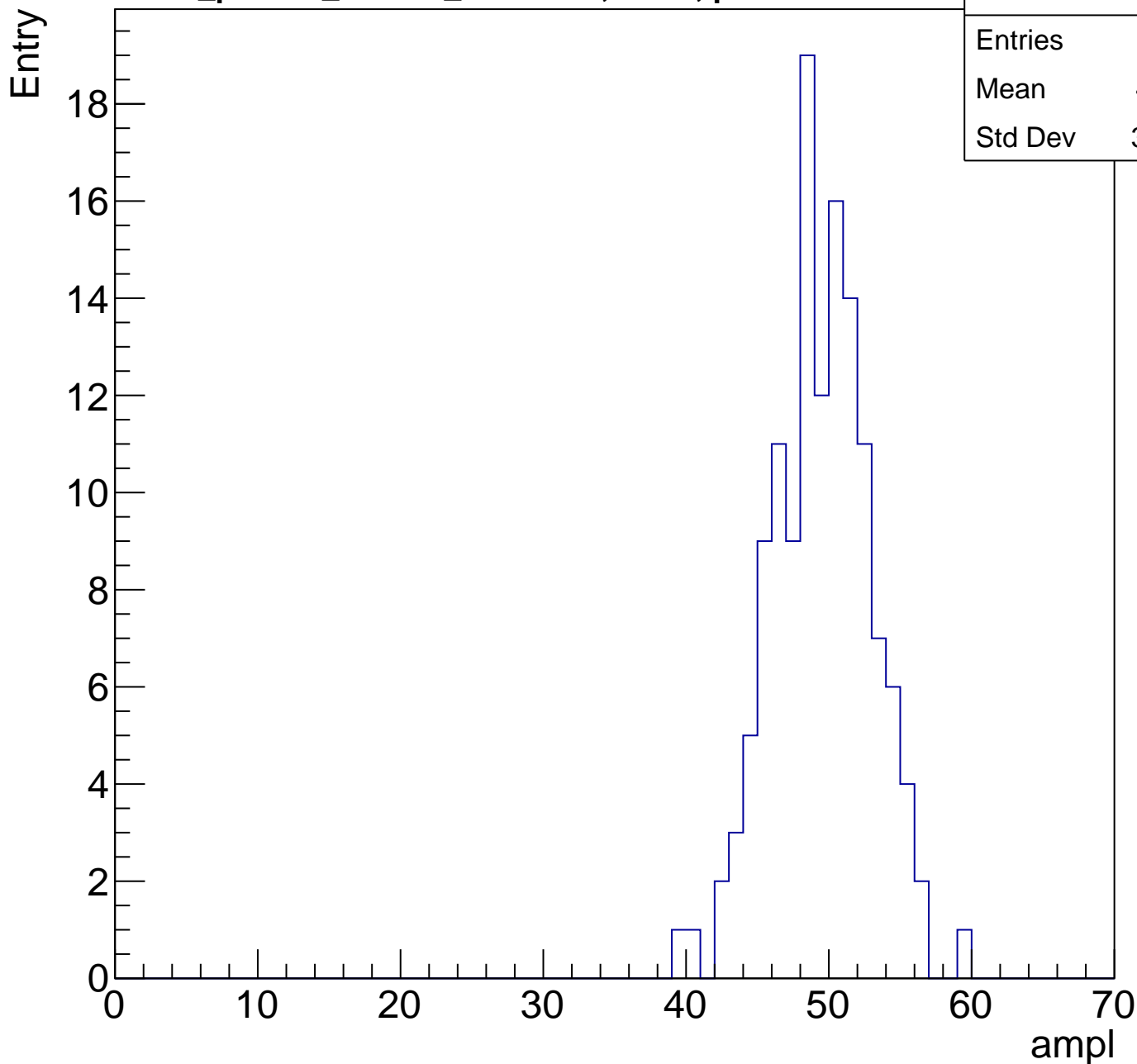
Entry



B1L001S, U20-ch13, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	49.01
Std Dev	3.452



B1L001S, U20-ch13, adc4

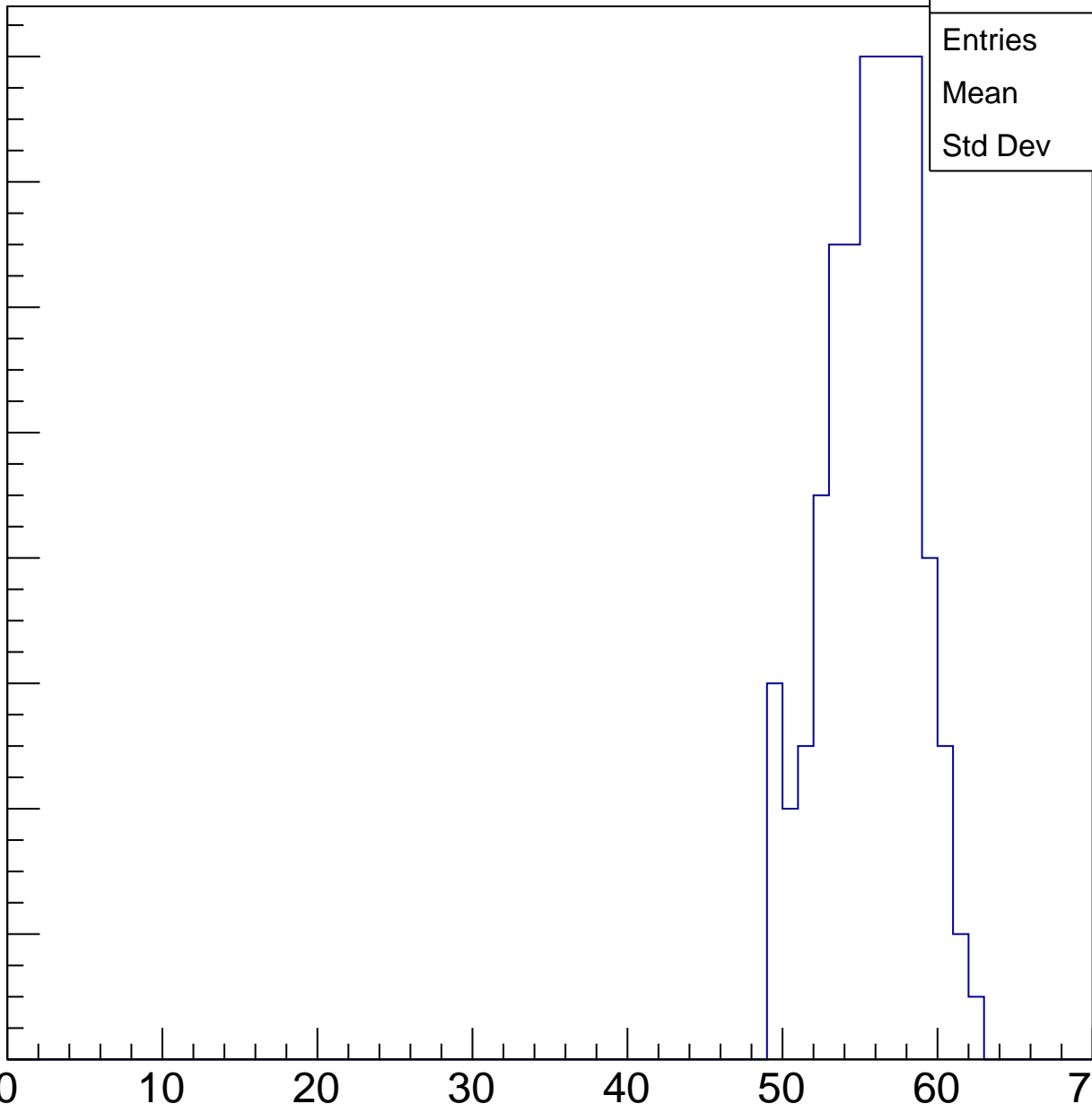
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	55.23
Std Dev	2.952

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch13, adc5

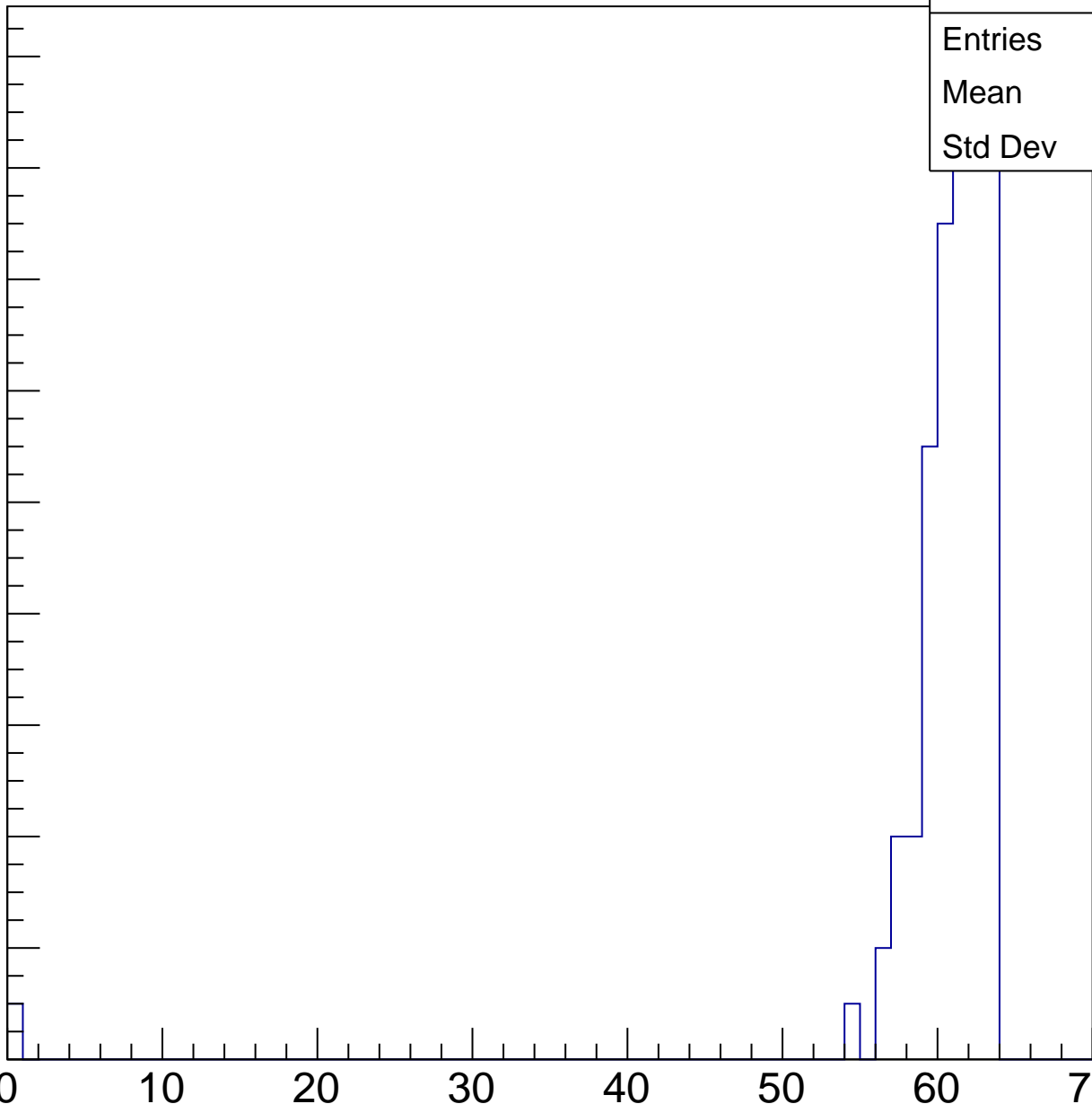
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.94
Std Dev	6.671

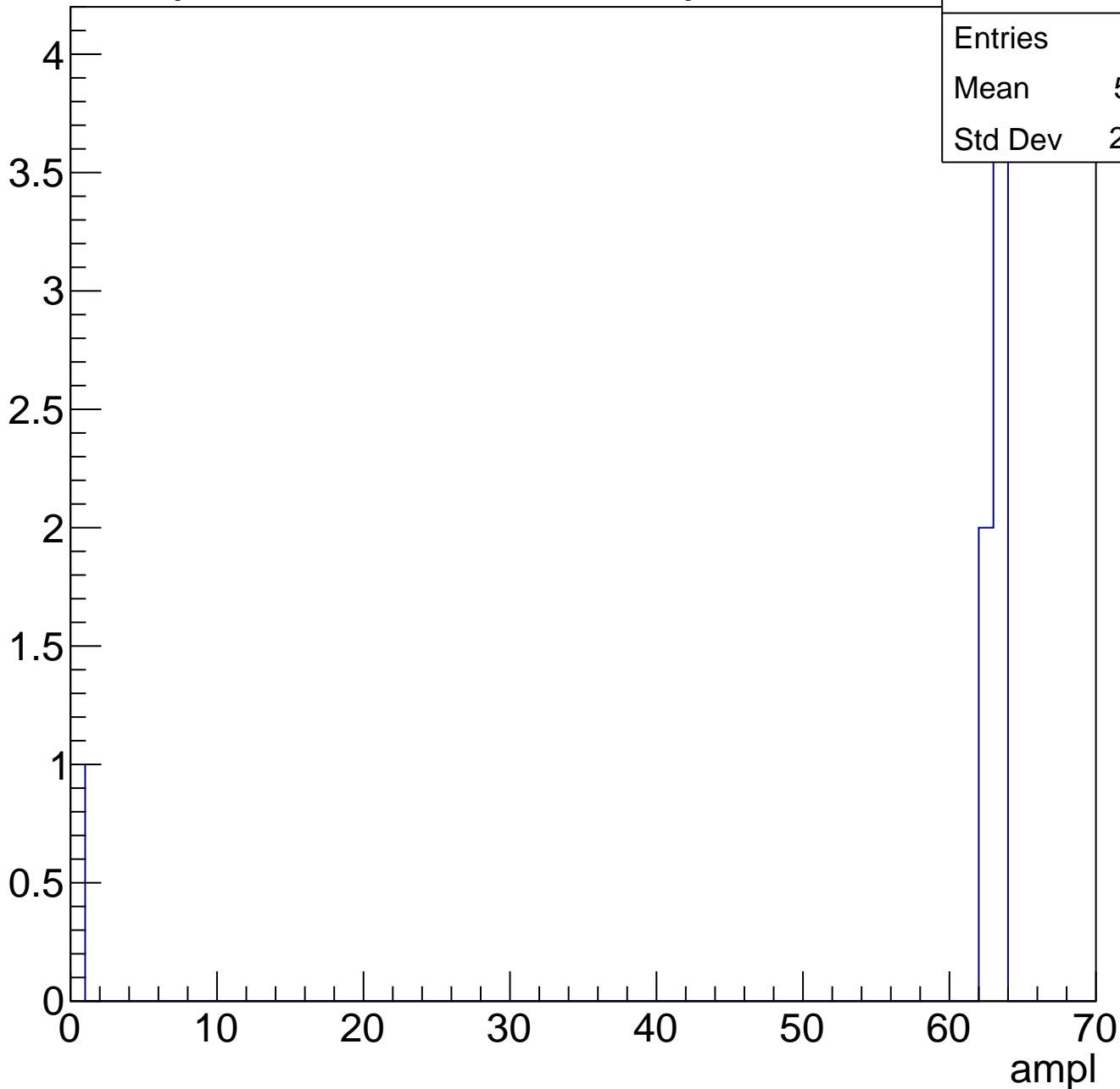
ampl



B1L001S, U20-ch13, adc6

calib_packv5_042523_0143.root, FC#2, port C2

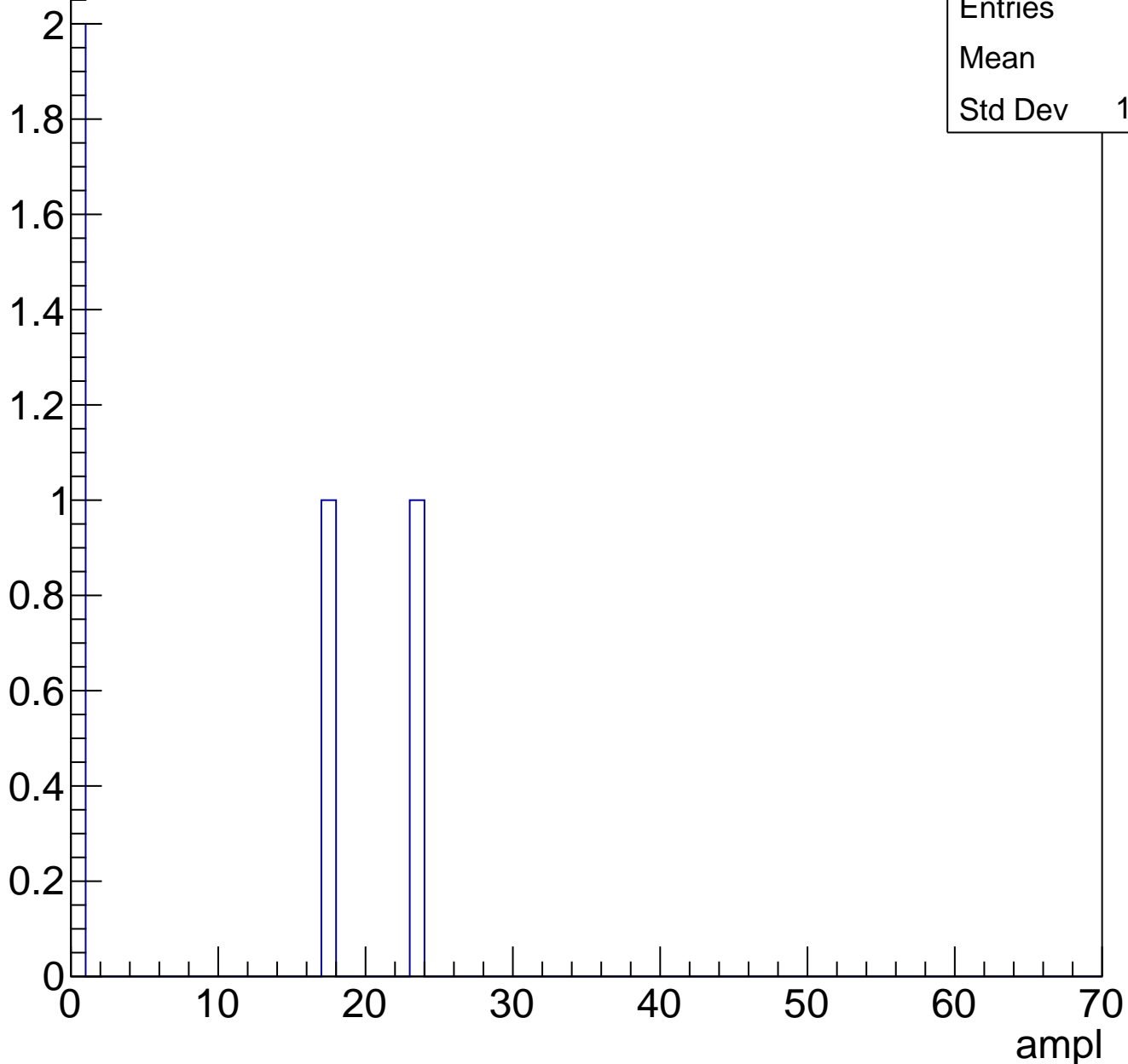
Entry



B1L001S, U20-ch13, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	10
Std Dev	10.22

B1L001S, U20-ch14, adc0

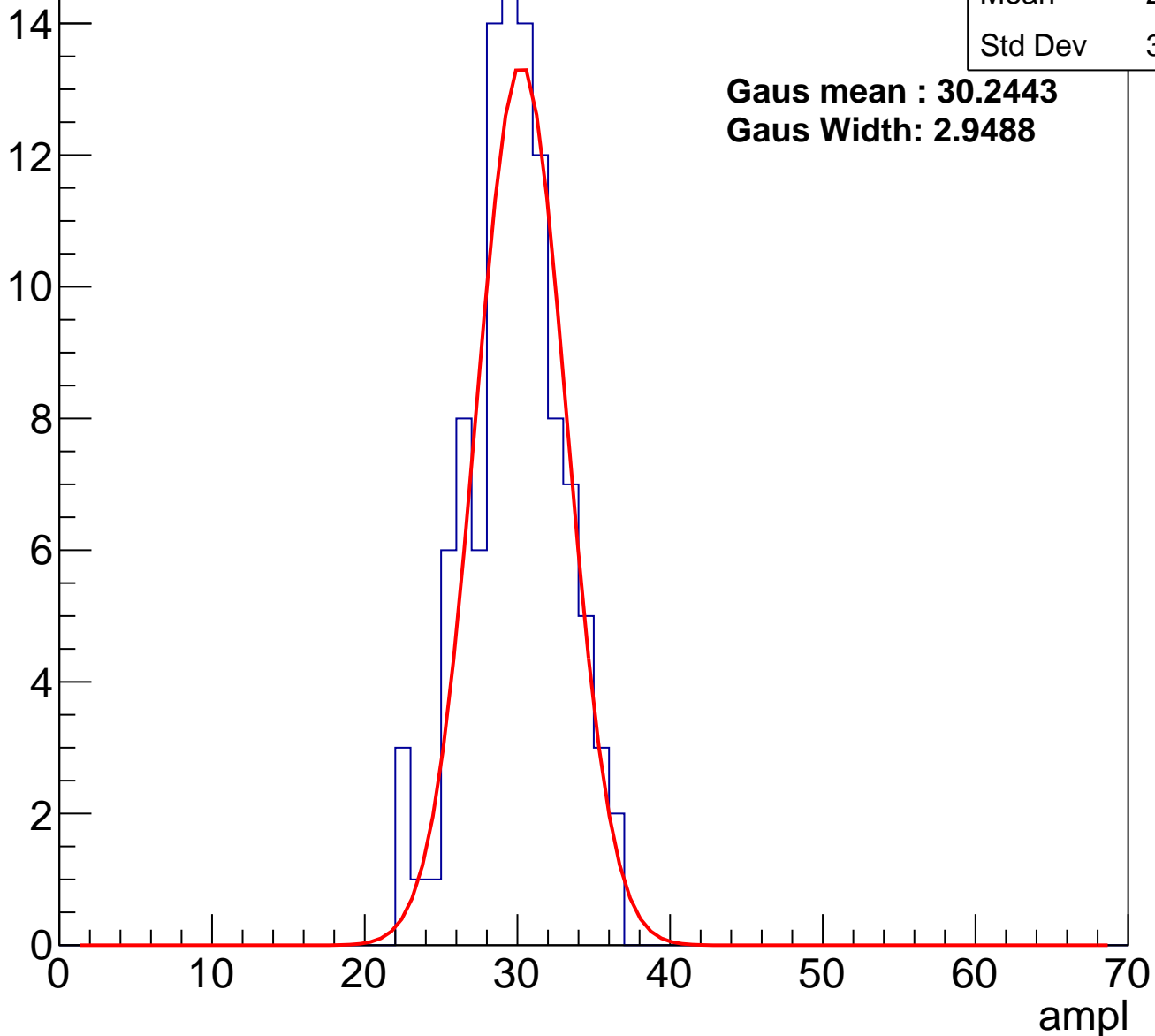
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	29.39
Std Dev	3.047

Gaus mean : 30.2443

Gaus Width: 2.9488

Entry



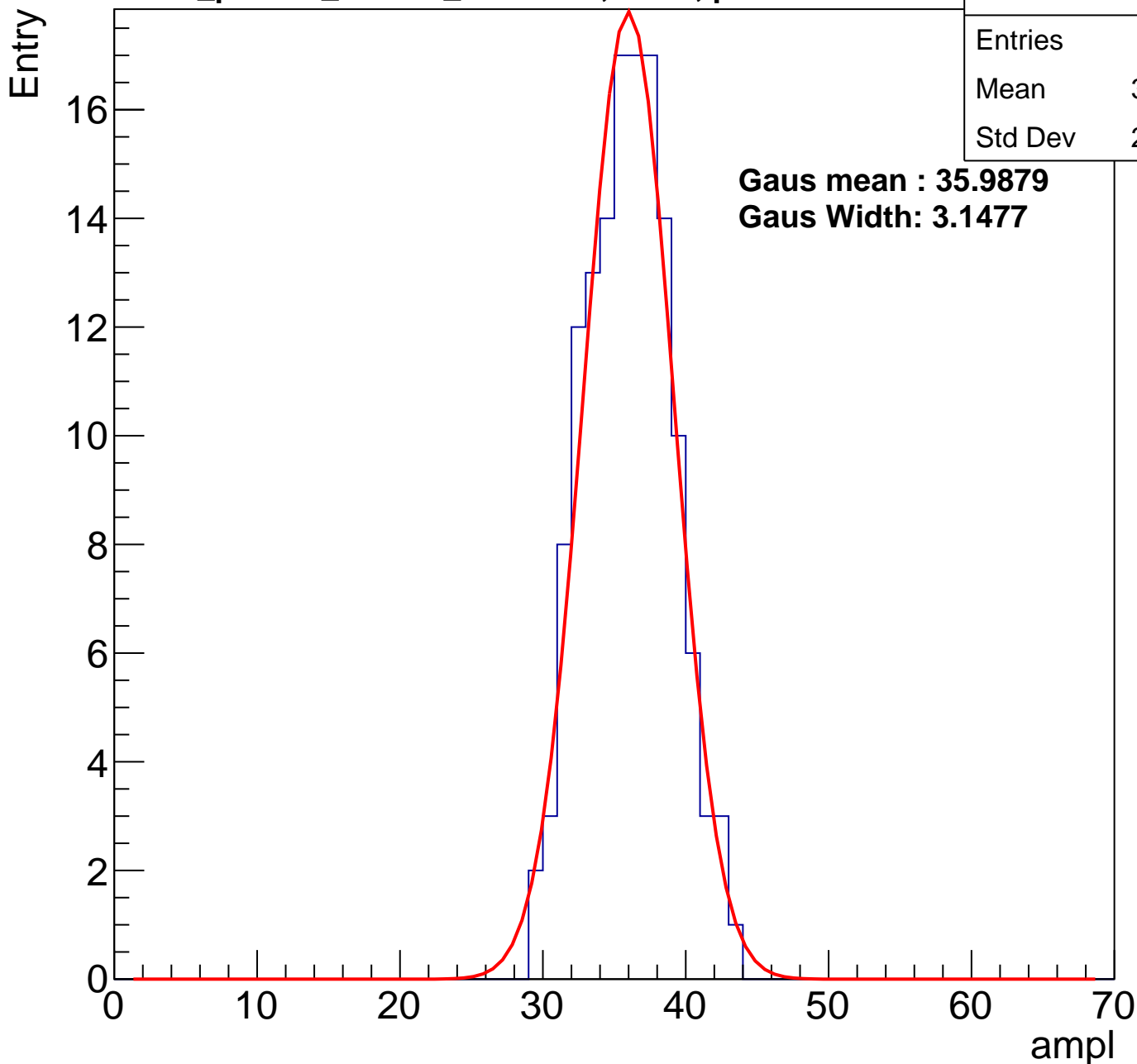
B1L001S, U20-ch14, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	35.54
Std Dev	2.987

Gaus mean : 35.9879

Gaus Width: 3.1477



B1L001S, U20-ch14, adc2

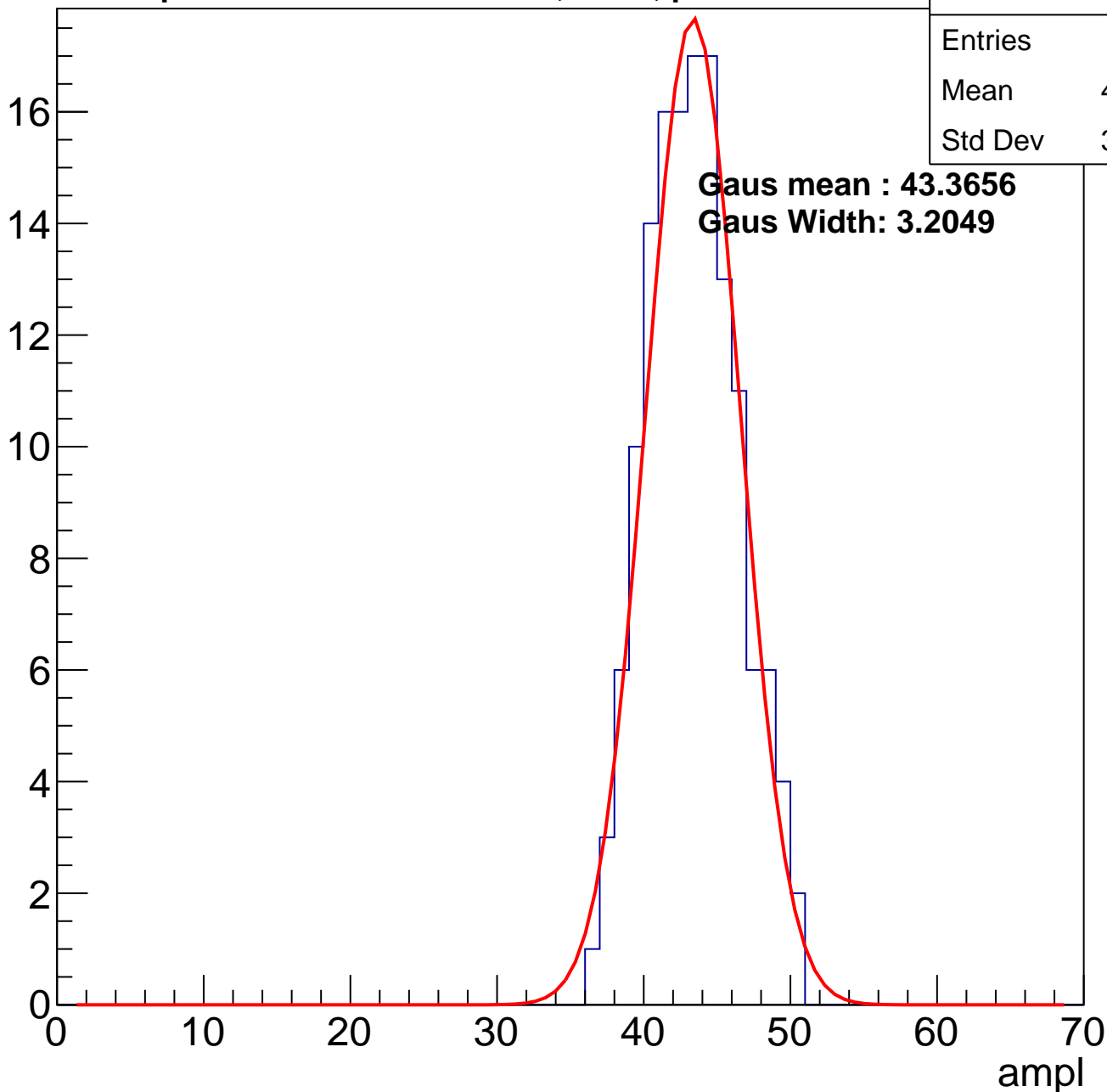
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	42.88
Std Dev	3.057

Gaus mean : 43.3656

Gaus Width: 3.2049

Entry



B1L001S, U20-ch14, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	49.01
Std Dev	3.009

Entry

12

10

8

6

4

2

0

0

10

20

30

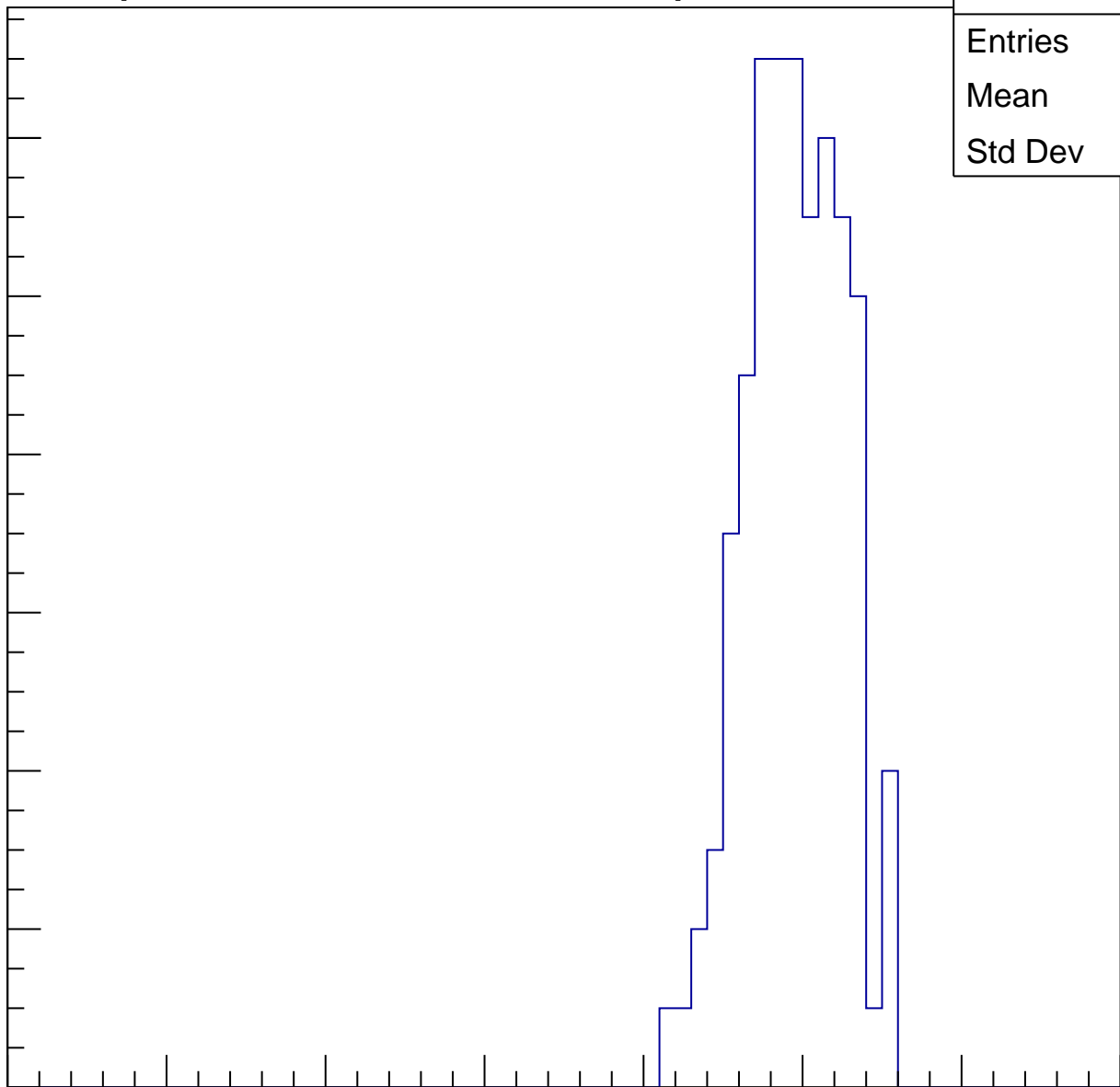
40

50

60

70

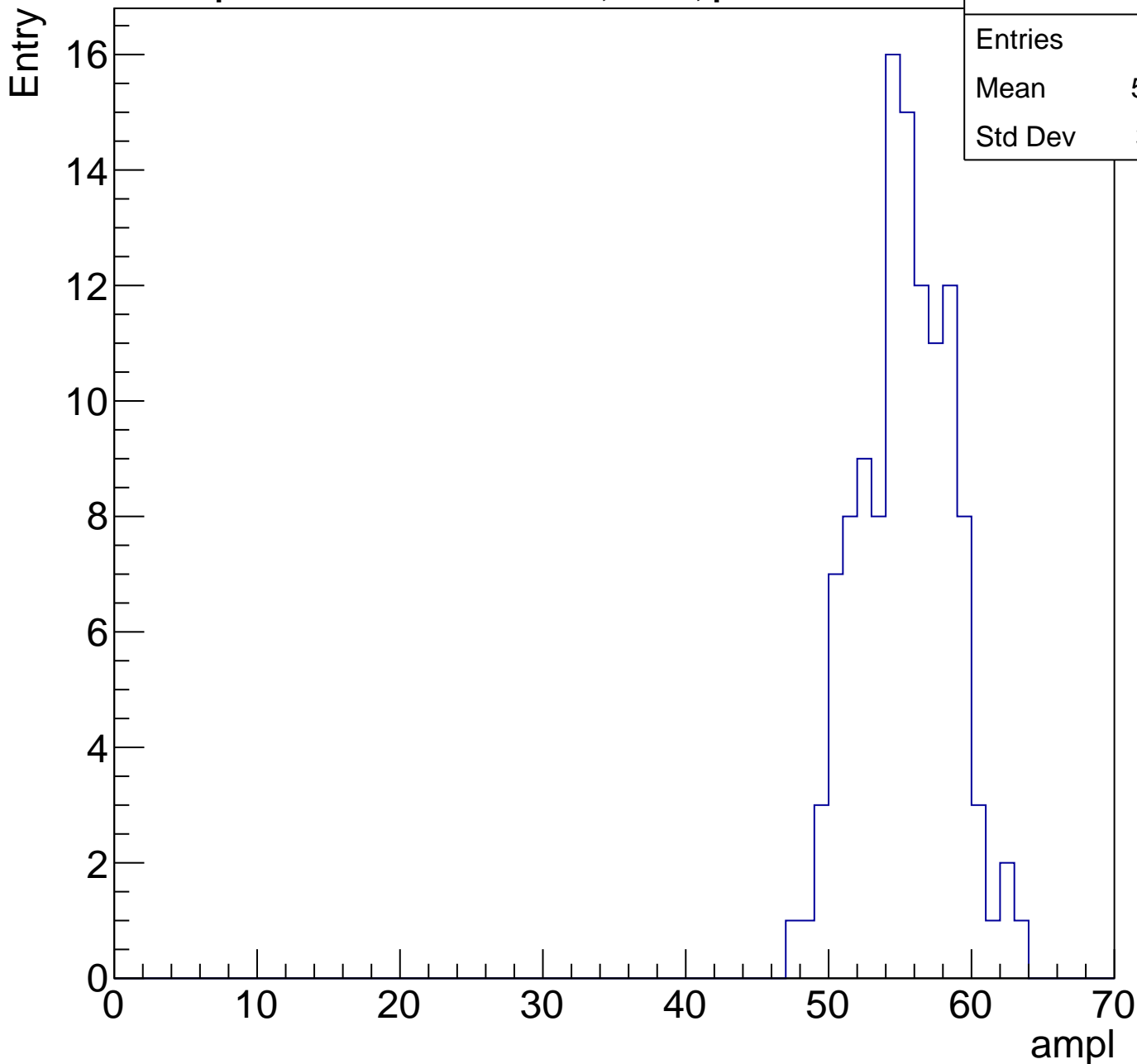
ampl



B1L001S, U20-ch14, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	54.88
Std Dev	3.211



B1L001S, U20-ch14, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	59.05
Std Dev	6.34

Entry

18
16
14
12
10
8
6
4
2
0

0

10

20

30

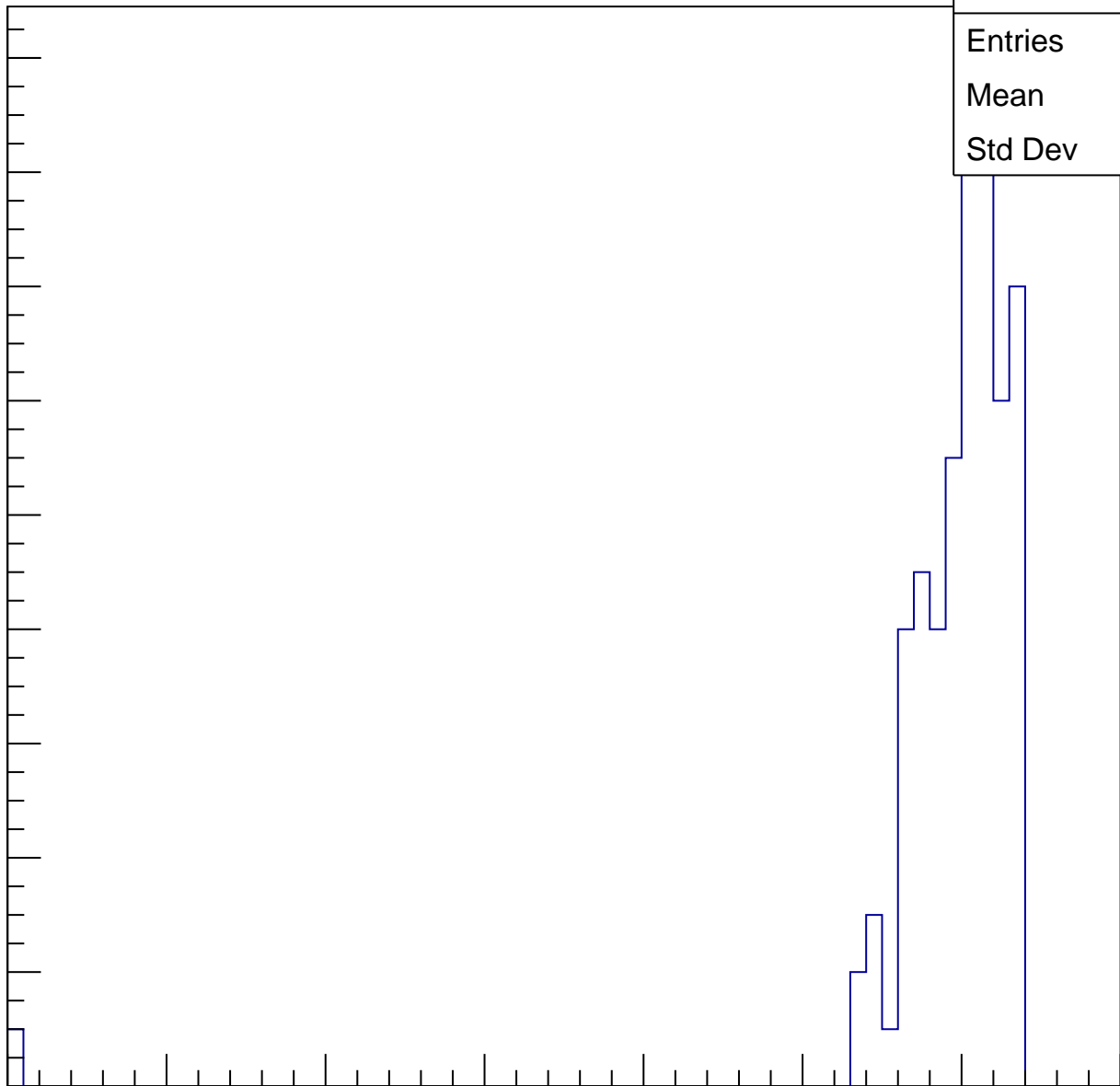
40

50

60

70

ampl

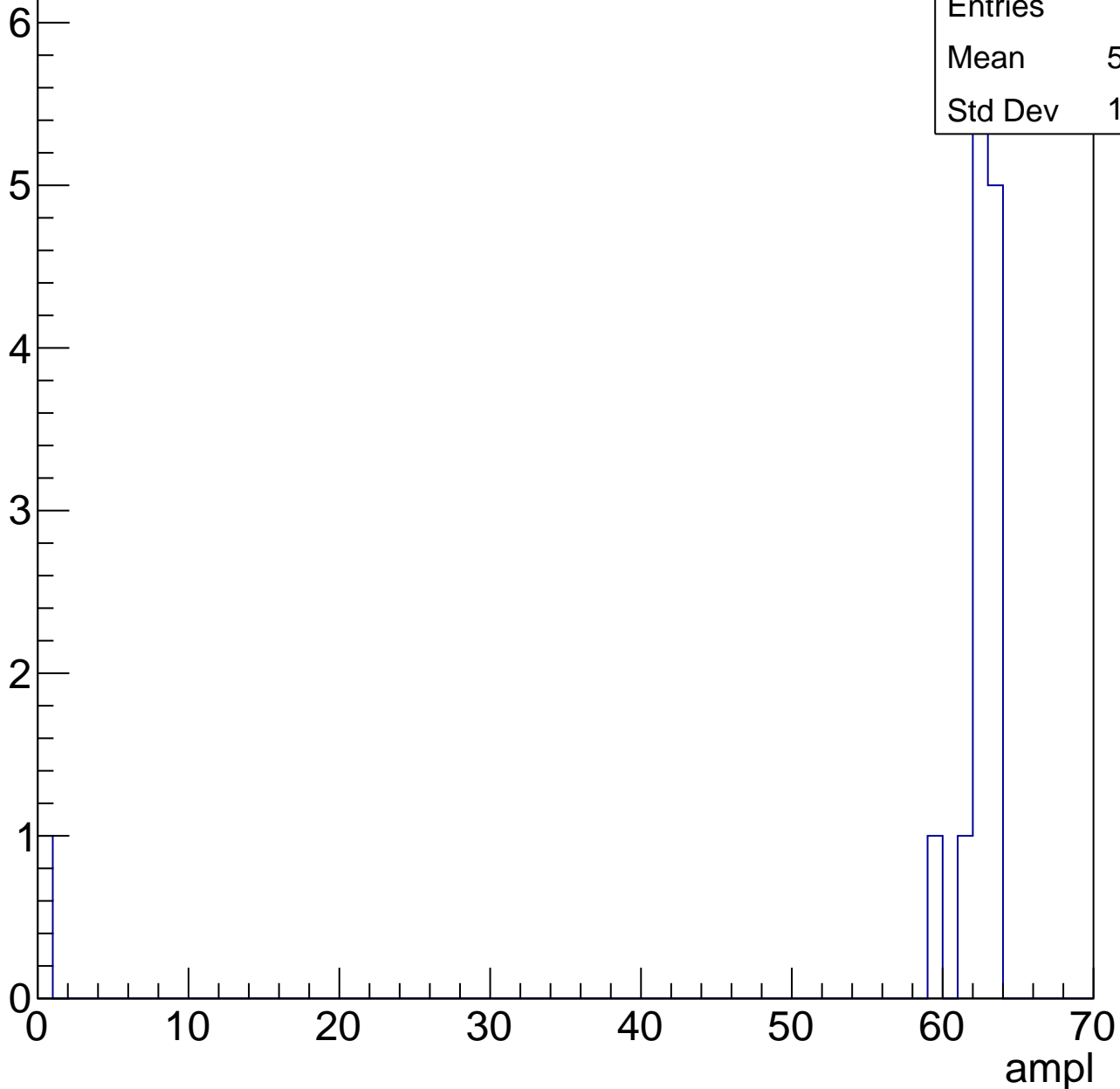


B1L001S, U20-ch14, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

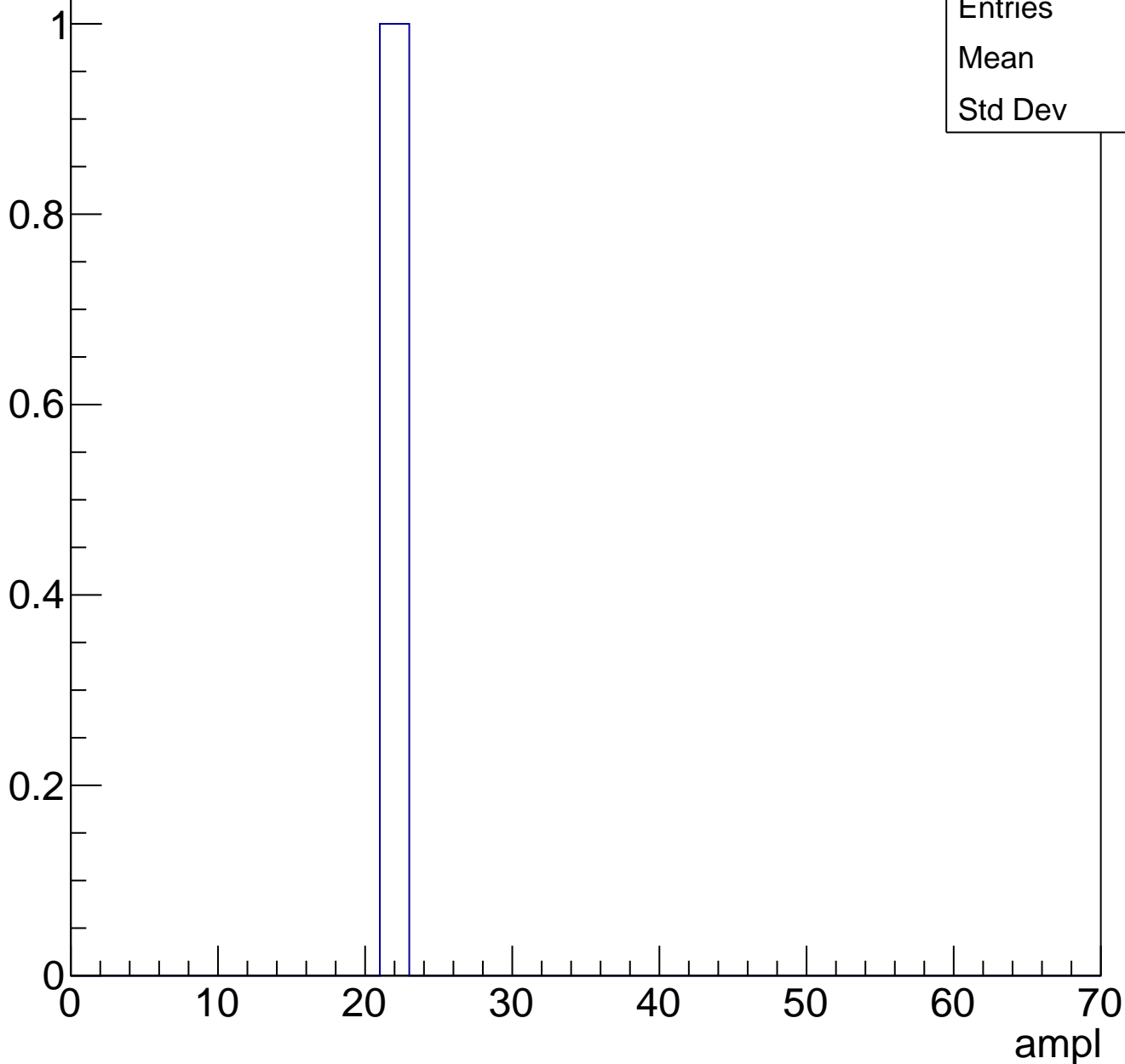
Entries	14
Mean	57.64
Std Dev	16.02



B1L001S, U20-ch14, adc7

calib_packv5_042523_0143.root, FC#2, port C2

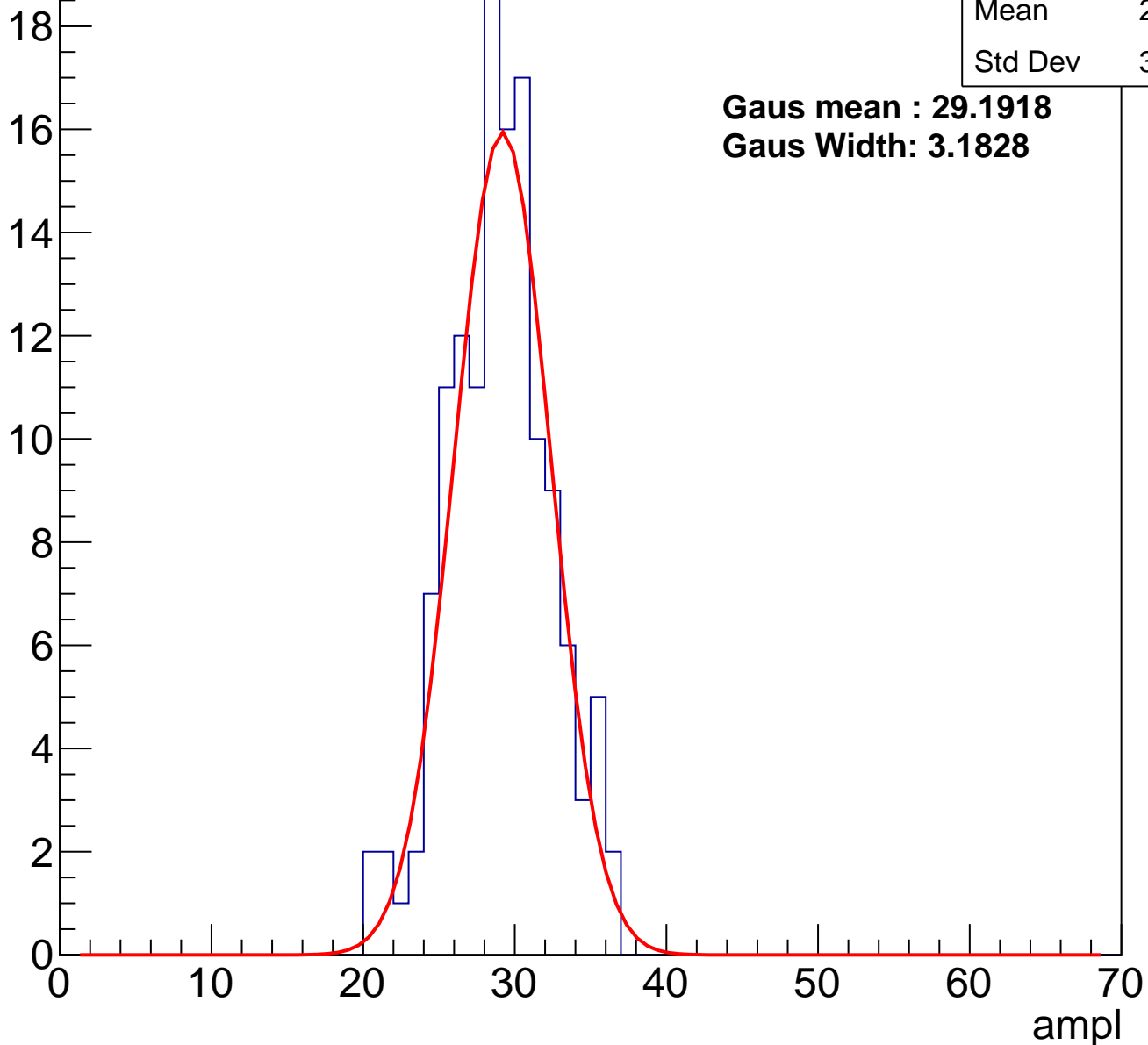
Entry



B1L001S, U20-ch15, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



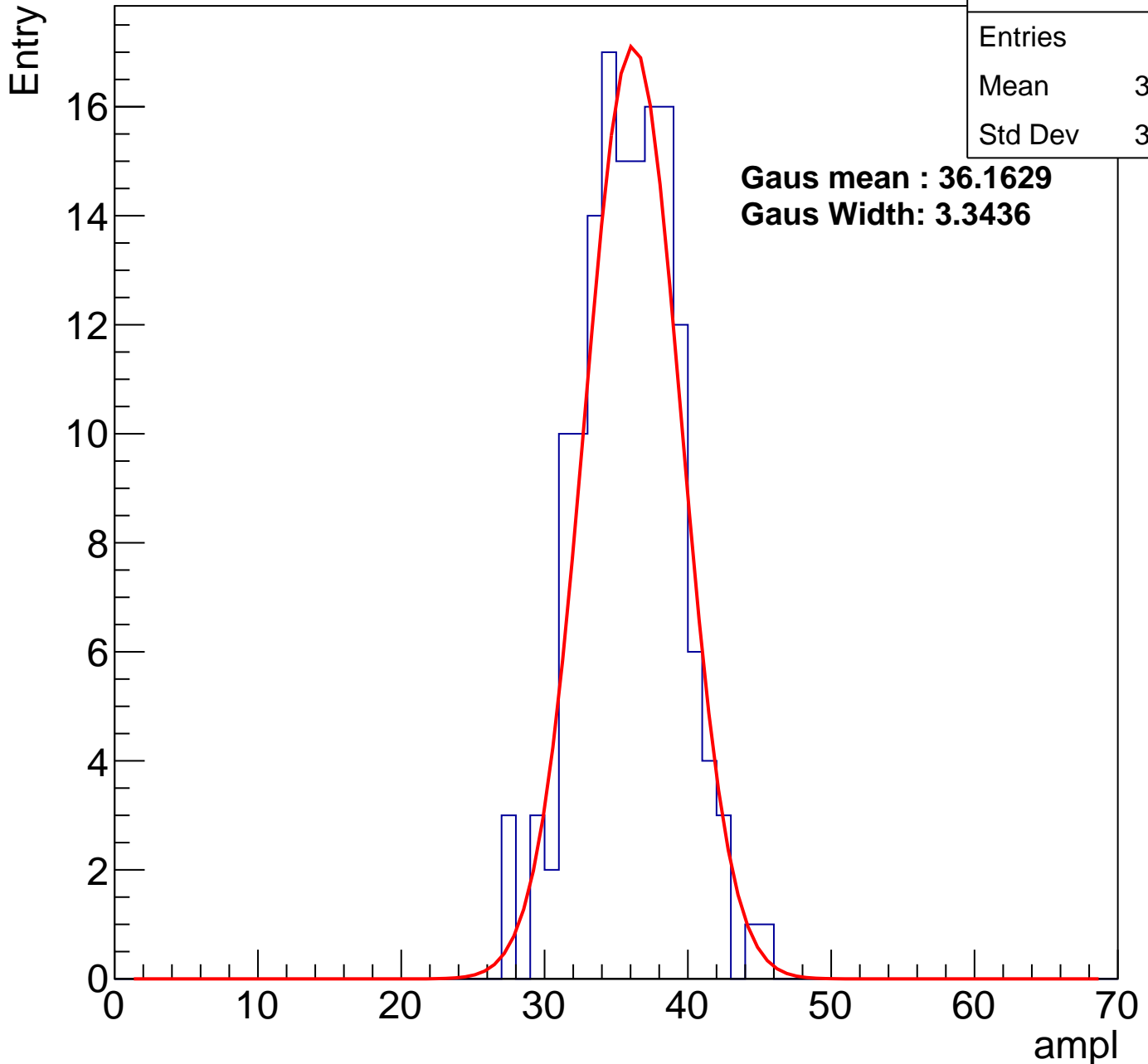
B1L001S, U20-ch15, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	35.47
Std Dev	3.366

Gaus mean : 36.1629

Gaus Width: 3.3436



B1L001S, U20-ch15, adc2

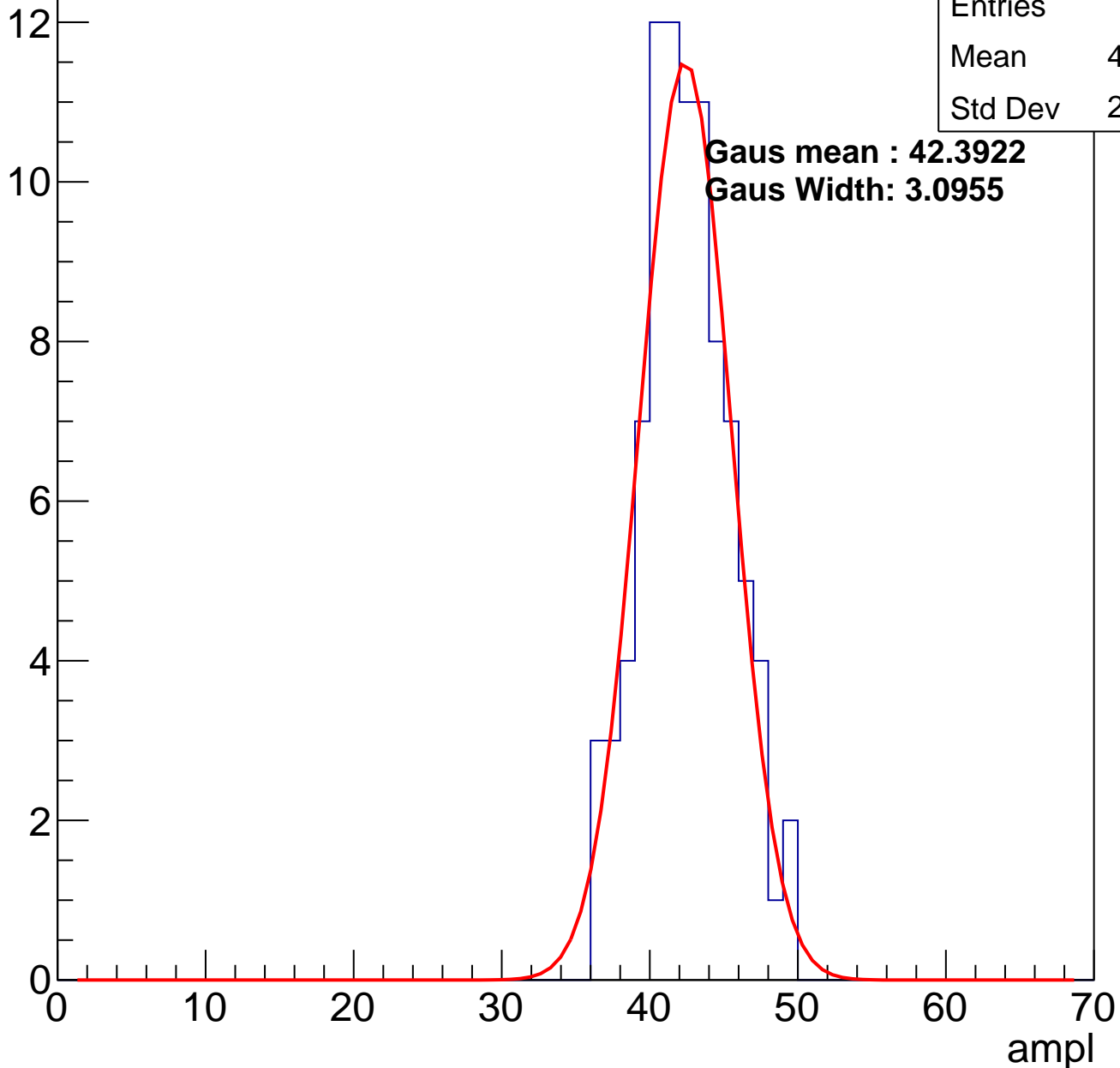
calib_packv5_042523_0143.root, FC#2, port C2

Entries	90
Mean	42.02
Std Dev	2.963

Gaus mean : 42.3922

Gaus Width: 3.0955

Entry



B1L001S, U20-ch15, adc3

calib_packv5_042523_0143.root, FC#2, port C2

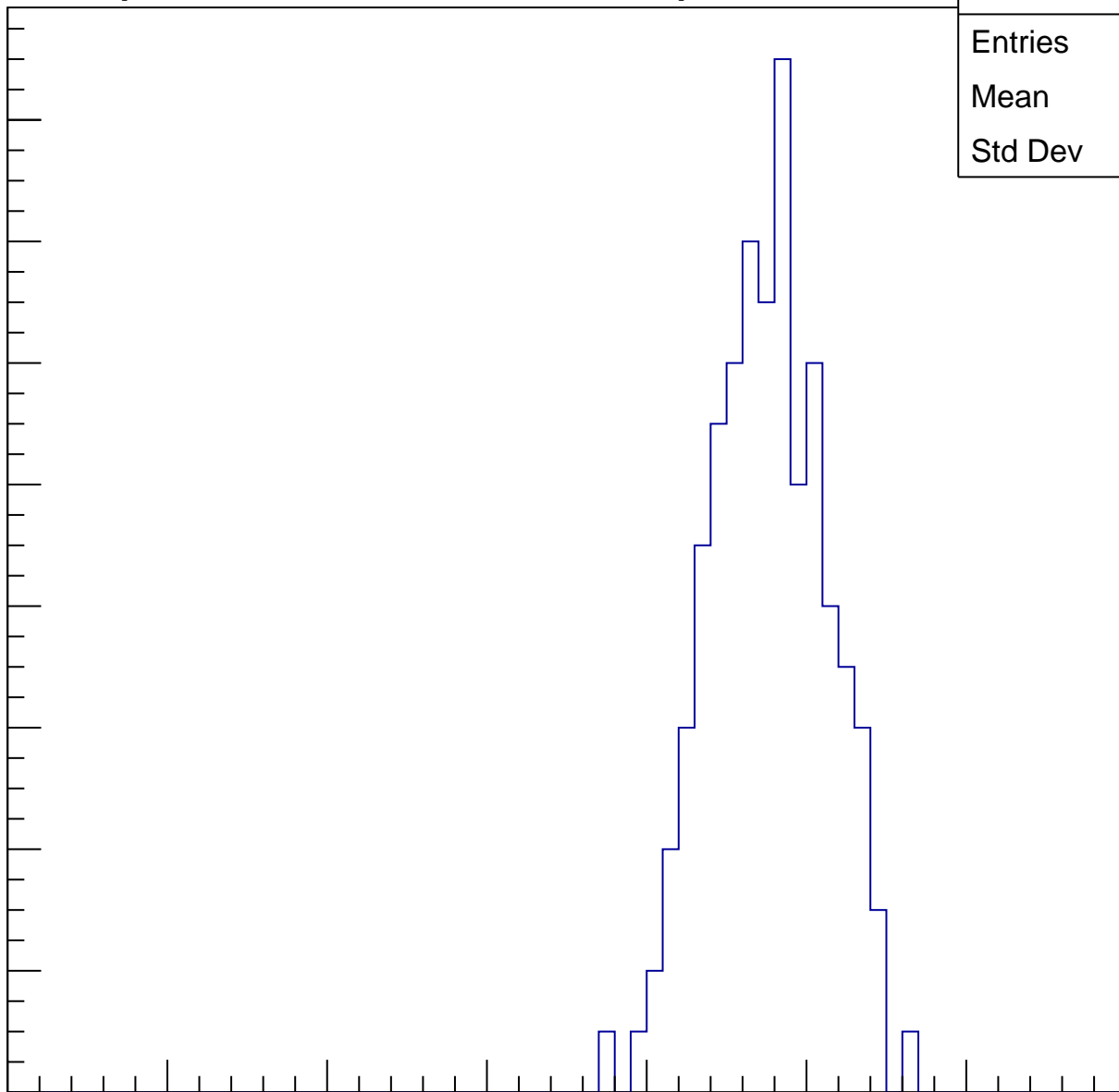
Entries	137
Mean	47.09
Std Dev	3.596

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

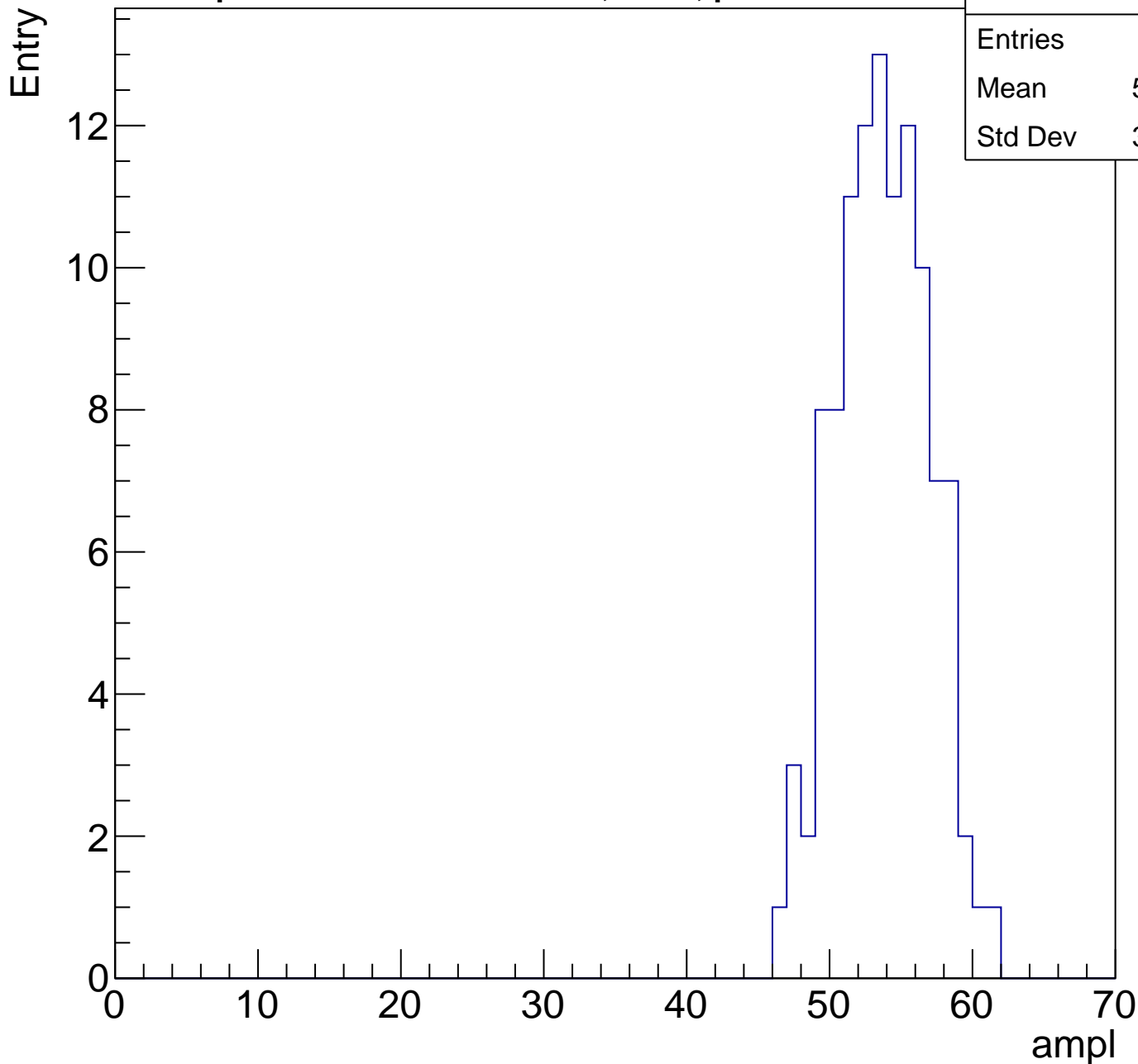
ampl



B1L001S, U20-ch15, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	53.28
Std Dev	3.142



B1L001S, U20-ch15, adc5

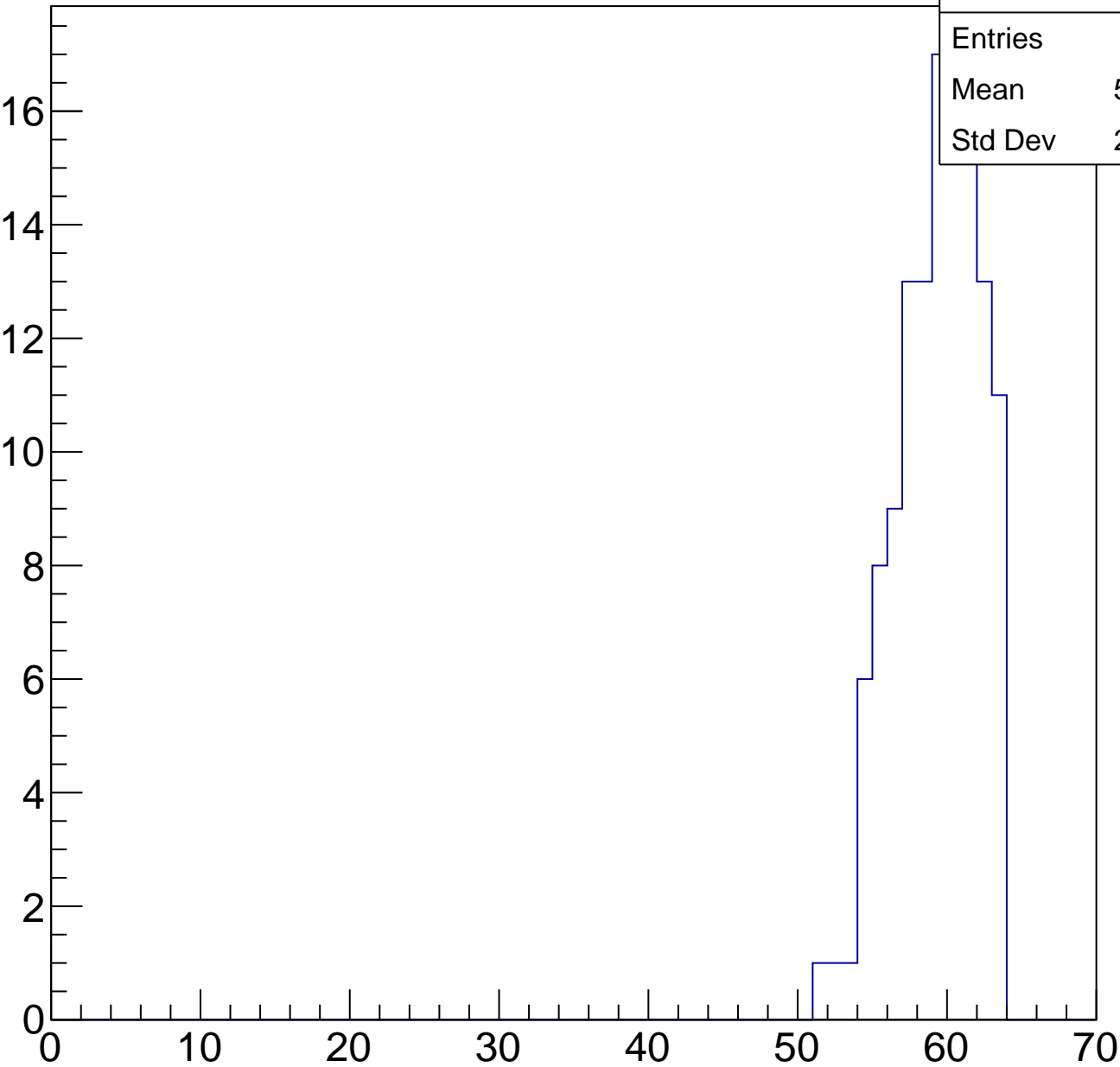
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	125
Mean	58.86
Std Dev	2.747

ampl

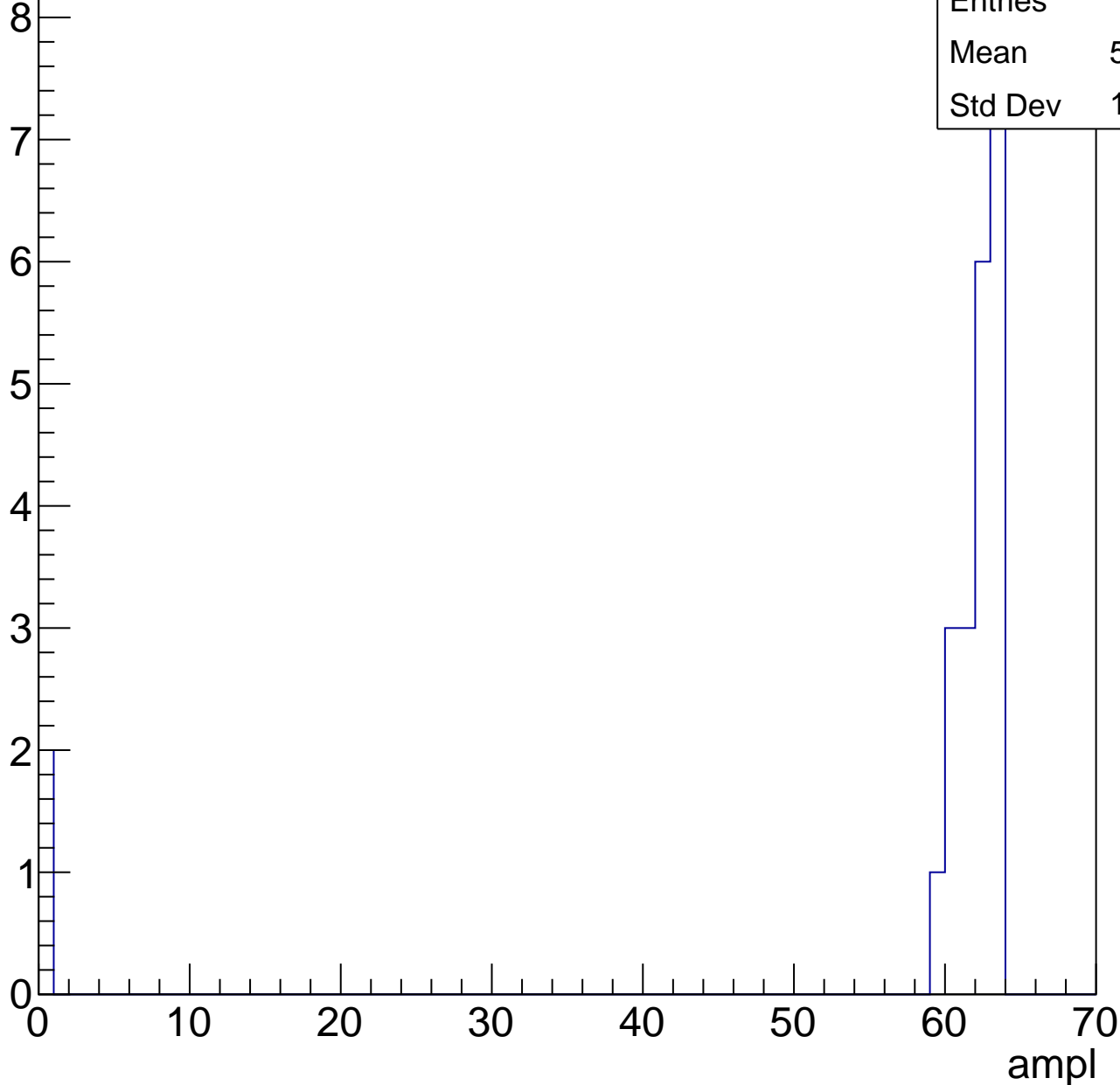


B1L001S, U20-ch15, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	23
Mean	56.43
Std Dev	17.46

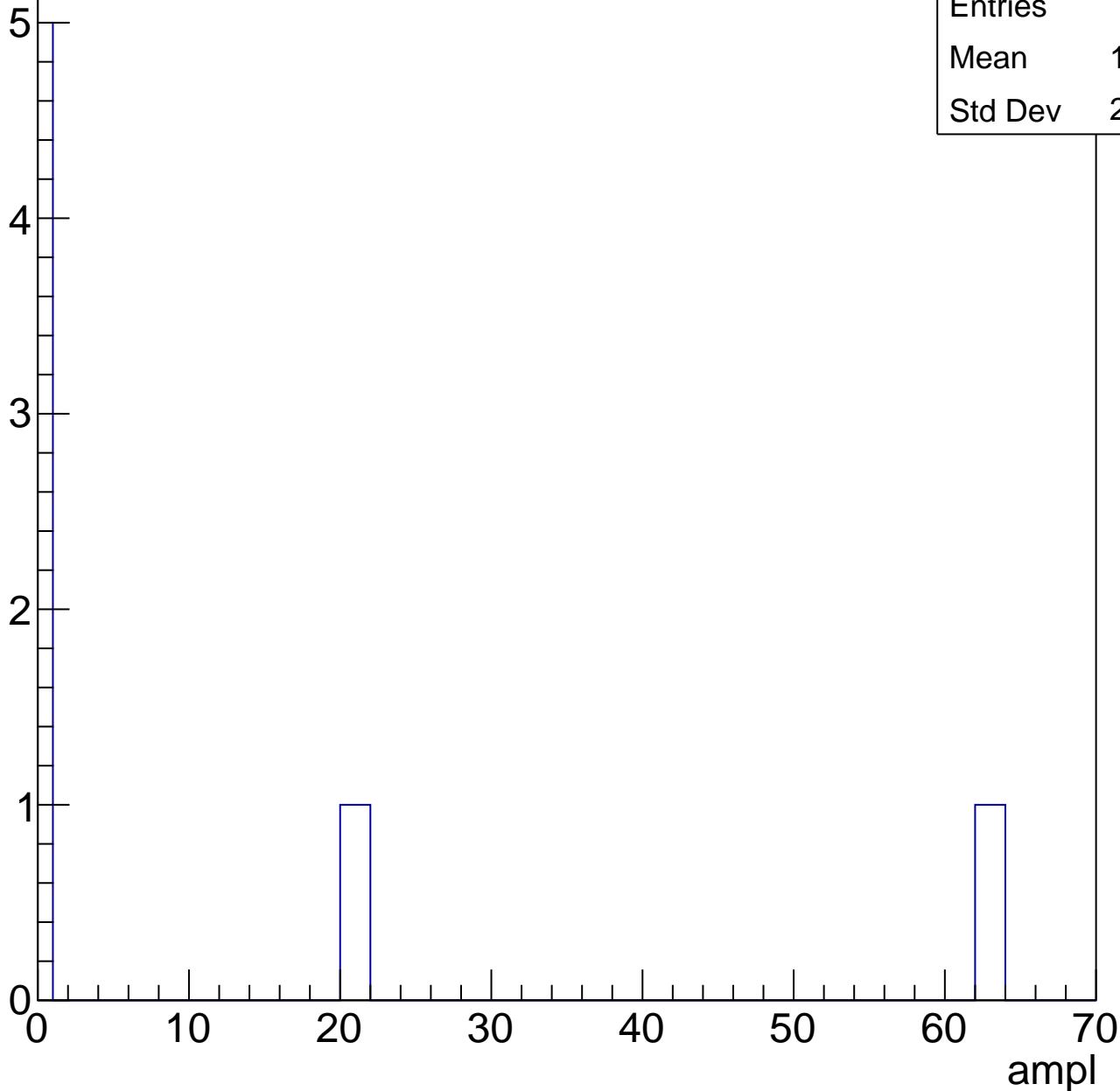


B1L001S, U20-ch15, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	9
Mean	18.44
Std Dev	24.93



B1L001S, U20-ch16, adc0

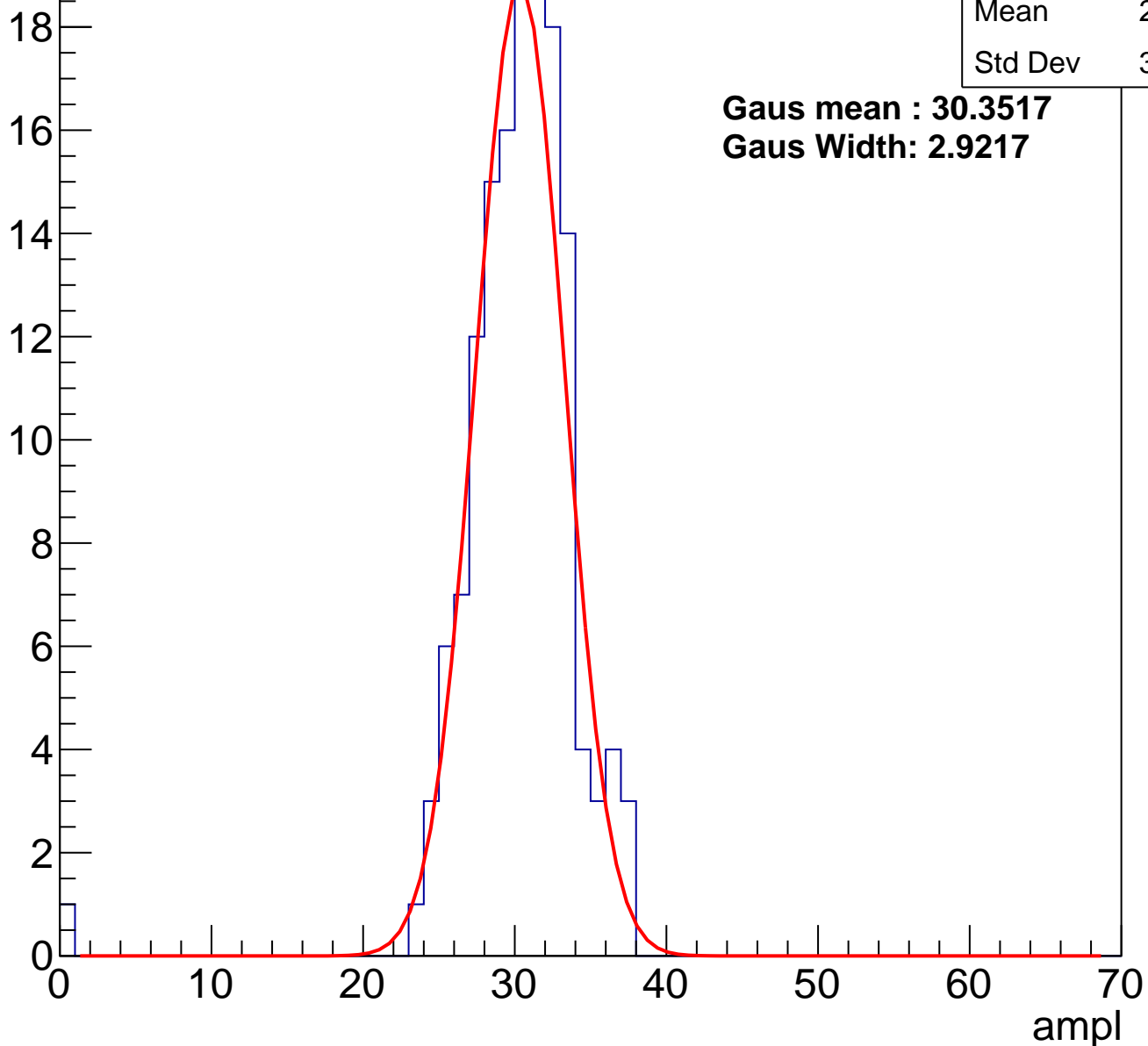
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	29.85
Std Dev	3.848

Gaus mean : 30.3517

Gaus Width: 2.9217

Entry

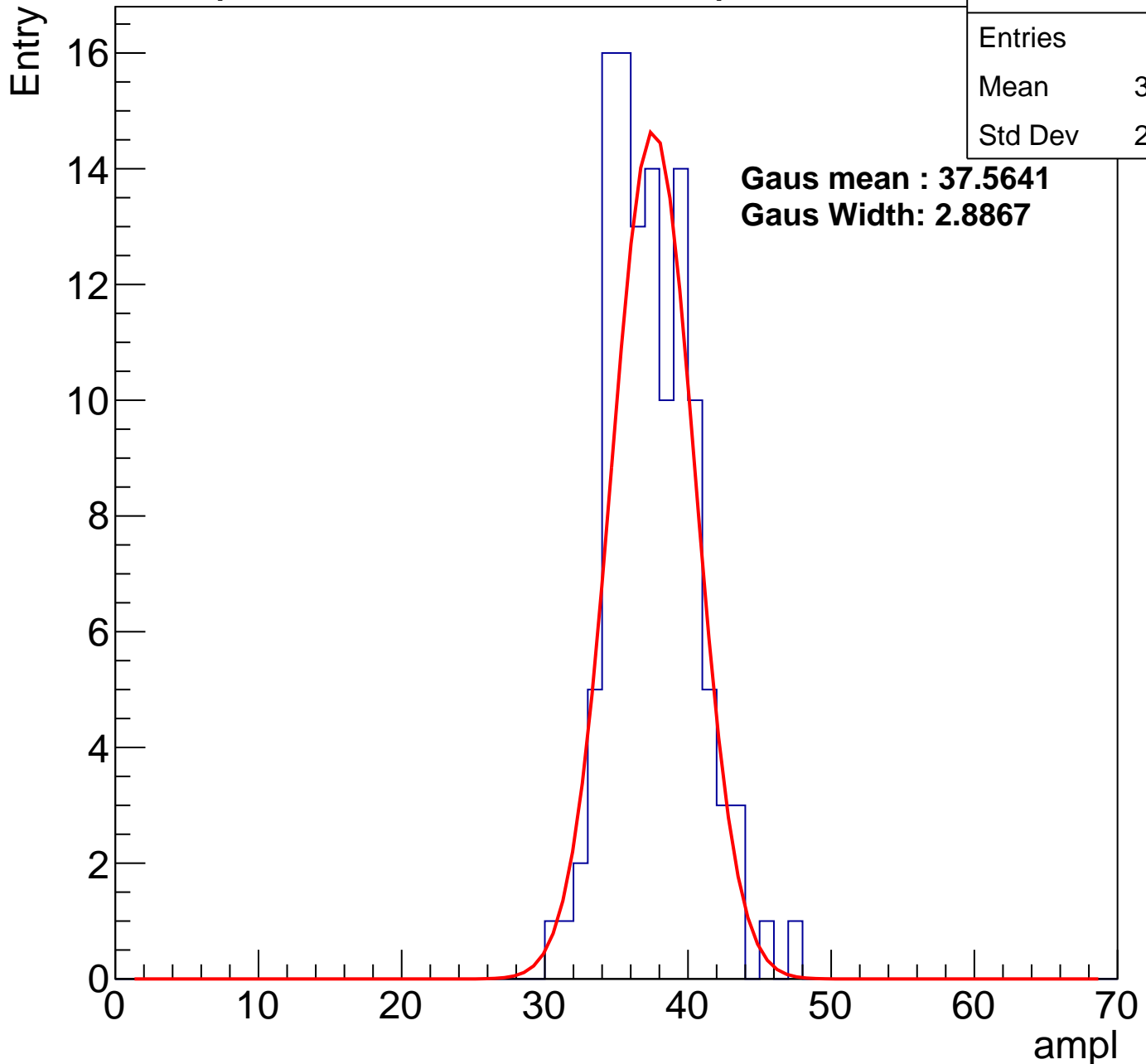


B1L001S, U20-ch16, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	37.03
Std Dev	2.977

Gaus mean : 37.5641
Gaus Width: 2.8867



B1L001S, U20-ch16, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

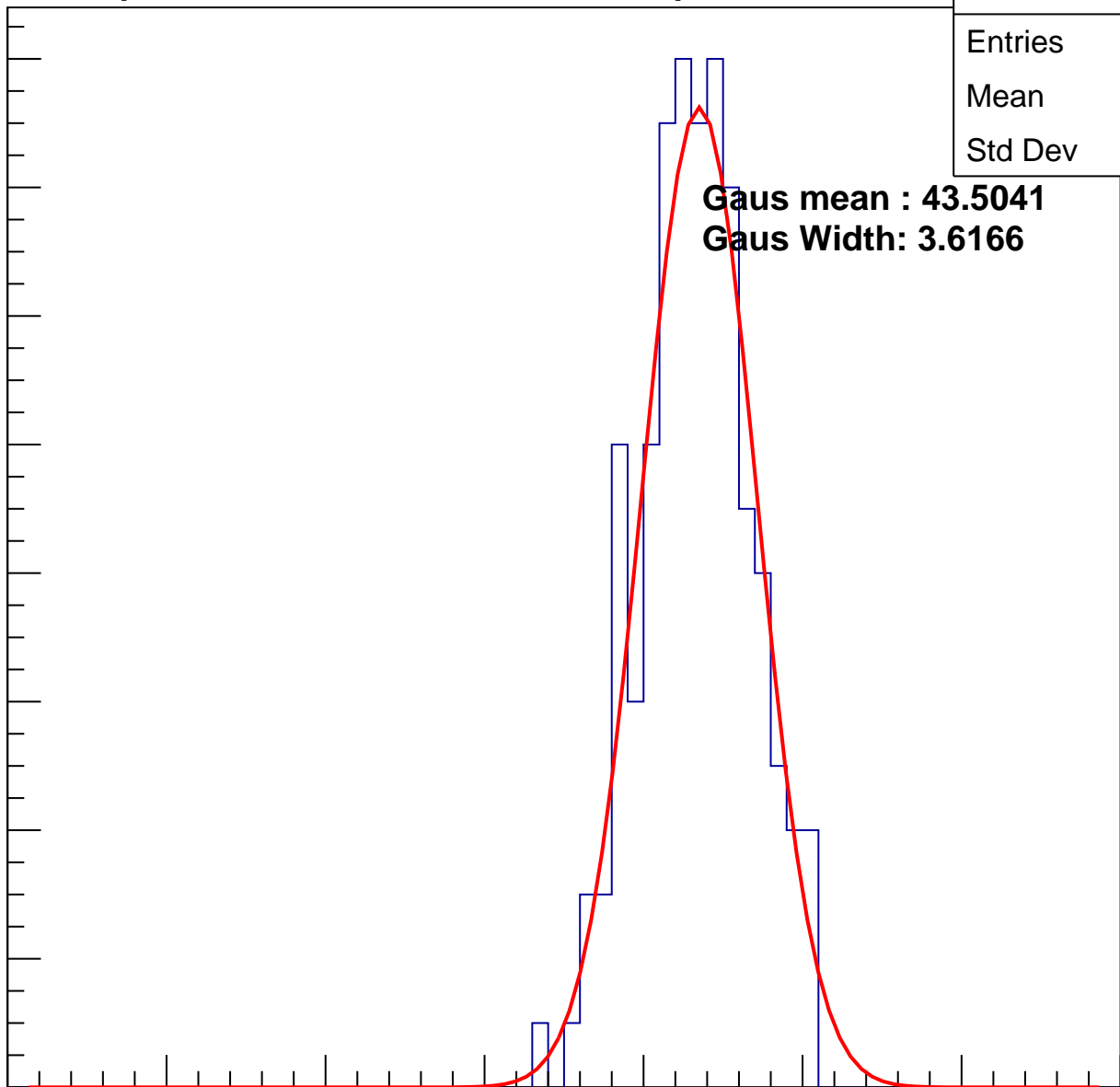
Entries	140
Mean	42.81
Std Dev	3.456

Gaus mean : 43.5041

Gaus Width: 3.6166

0 10 20 30 40 50 60 70

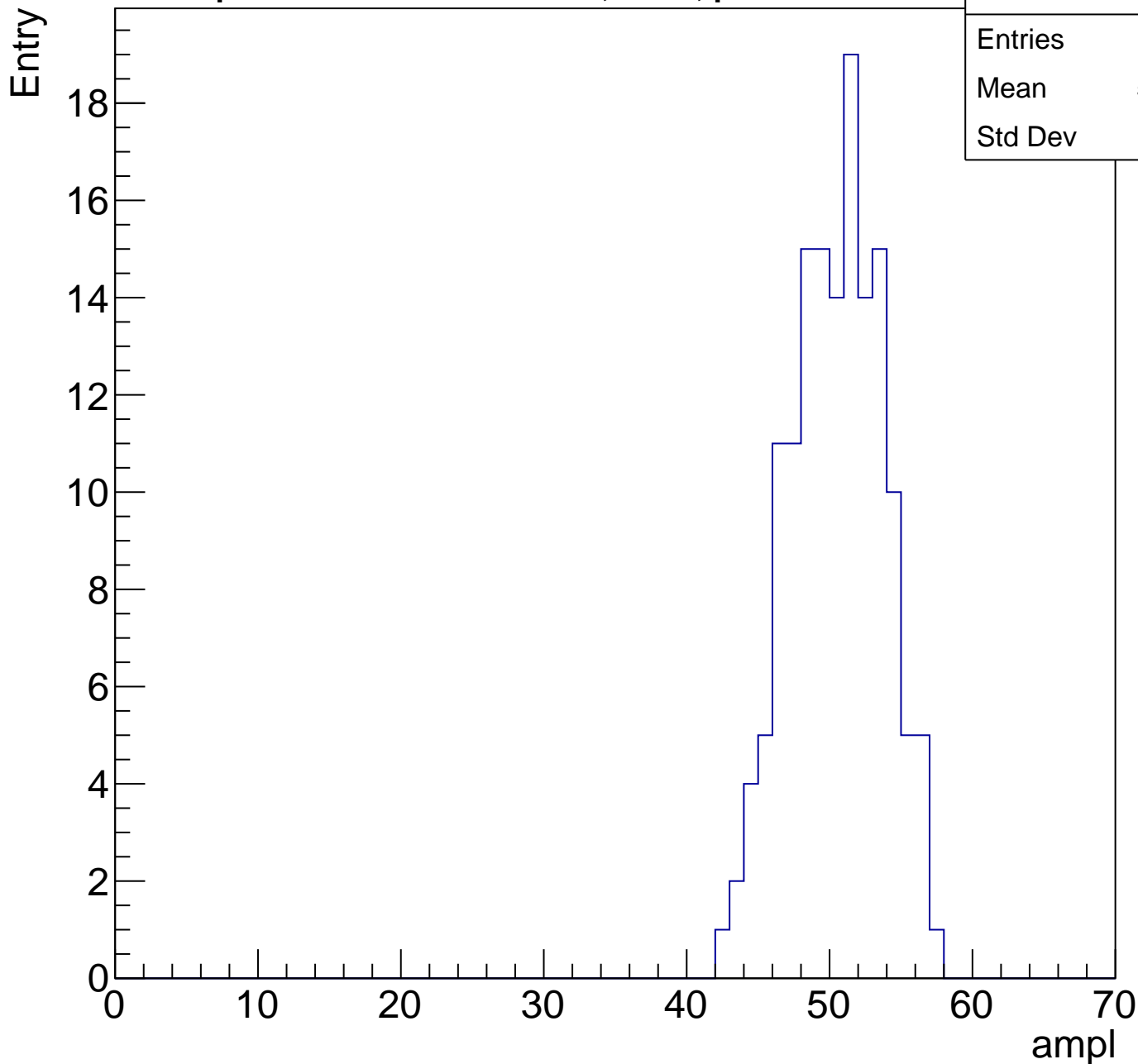
ampl



B1L001S, U20-ch16, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	50.01
Std Dev	3.2



B1L001S, U20-ch16, adc4

calib_packv5_042523_0143.root, FC#2, port C2

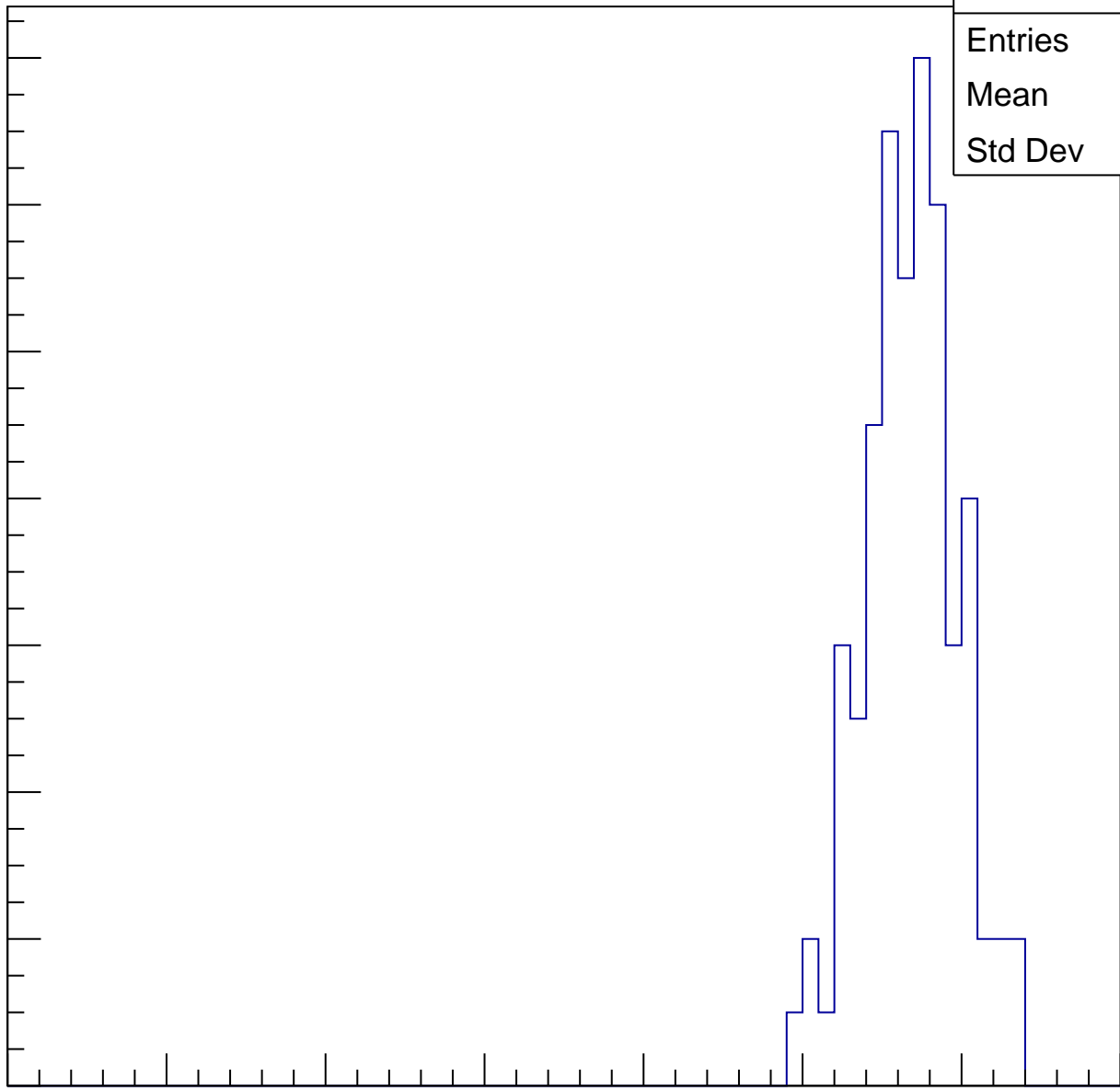
Entries	94
Mean	56.32
Std Dev	2.9

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch16, adc5

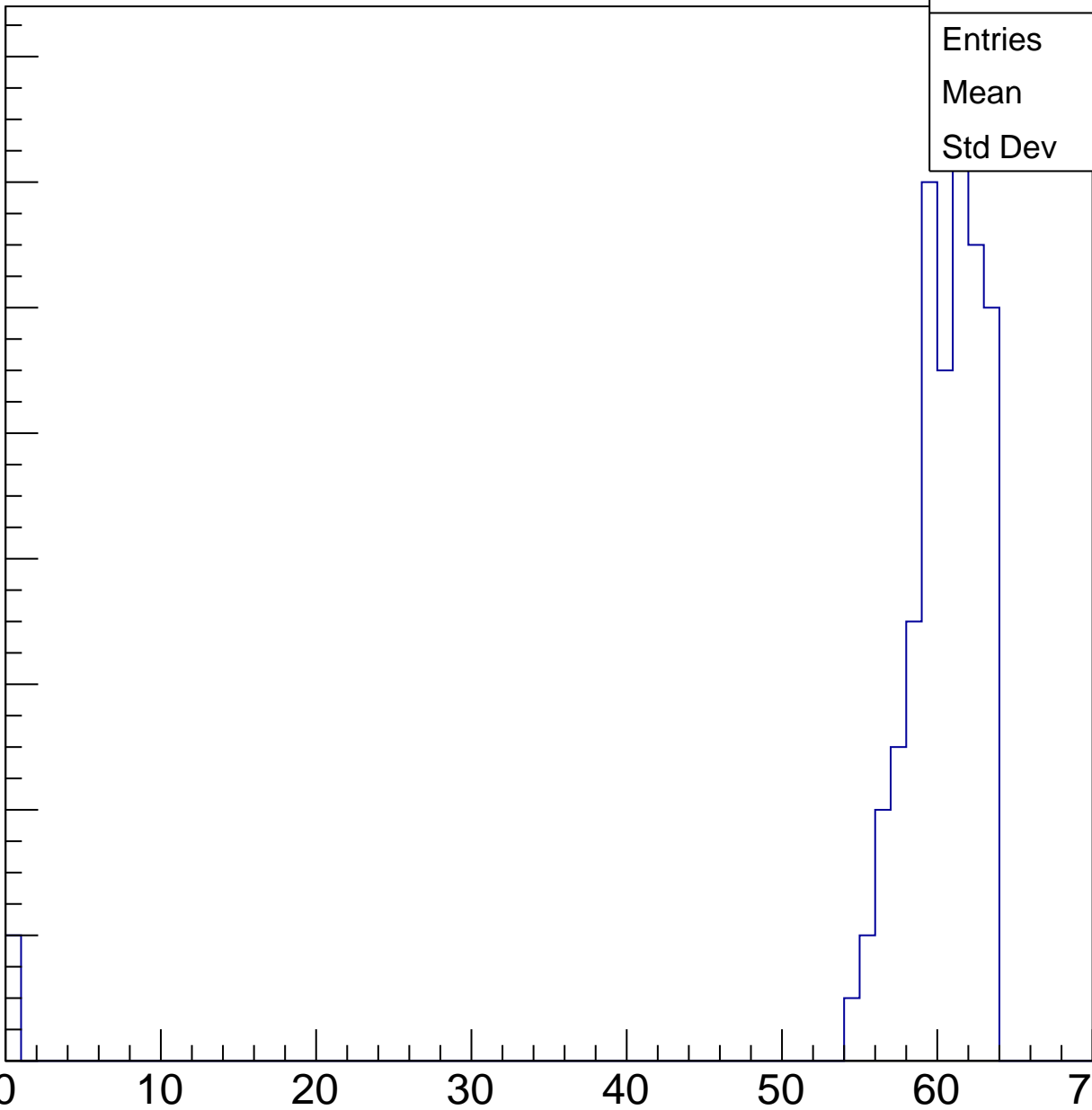
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	58.66
Std Dev	9.257

ampl

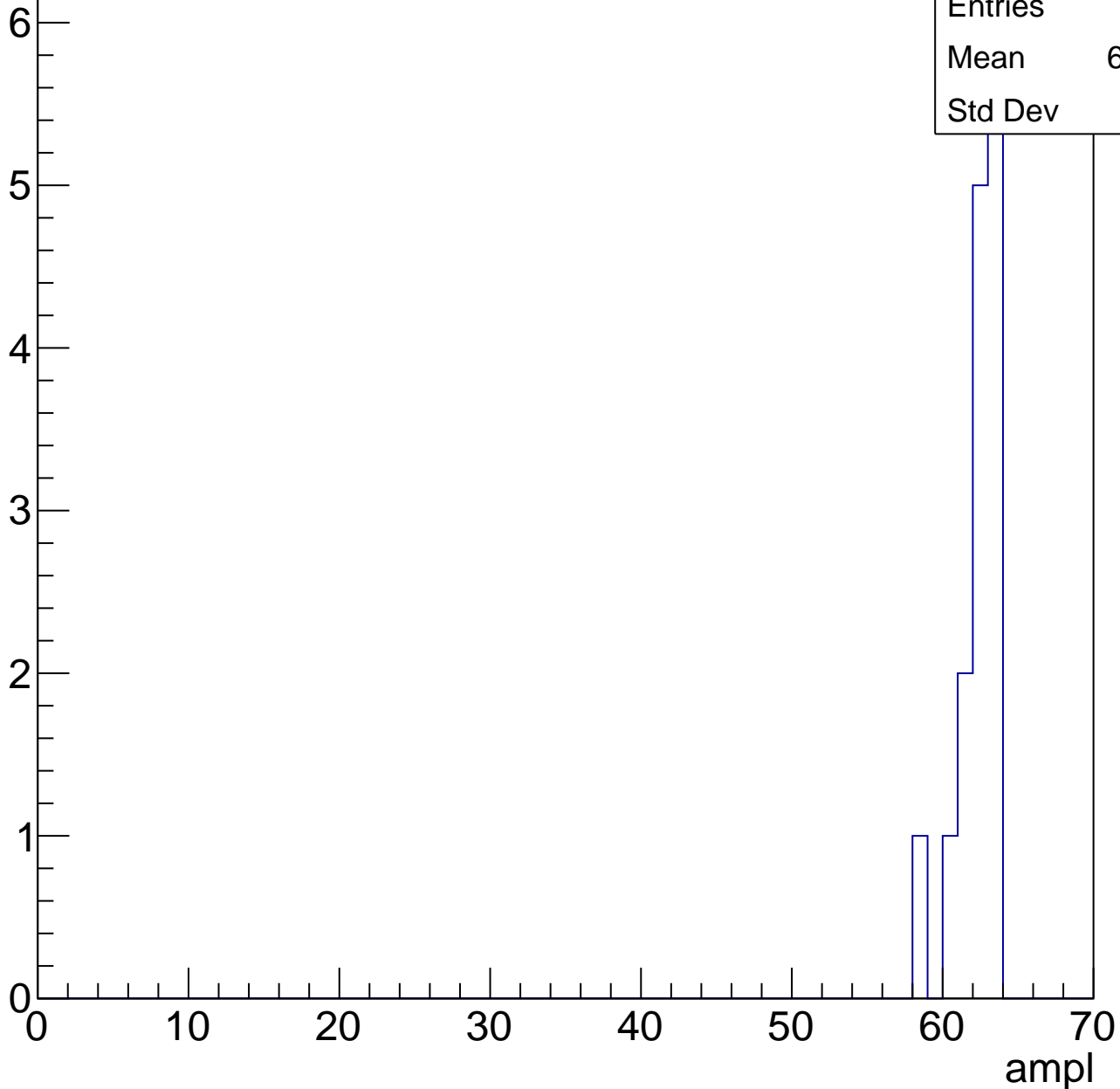


B1L001S, U20-ch16, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	61.87
Std Dev	1.36



B1L001S, U20-ch16, adc7

calib_packv5_042523_0143.root, FC#2, port C2

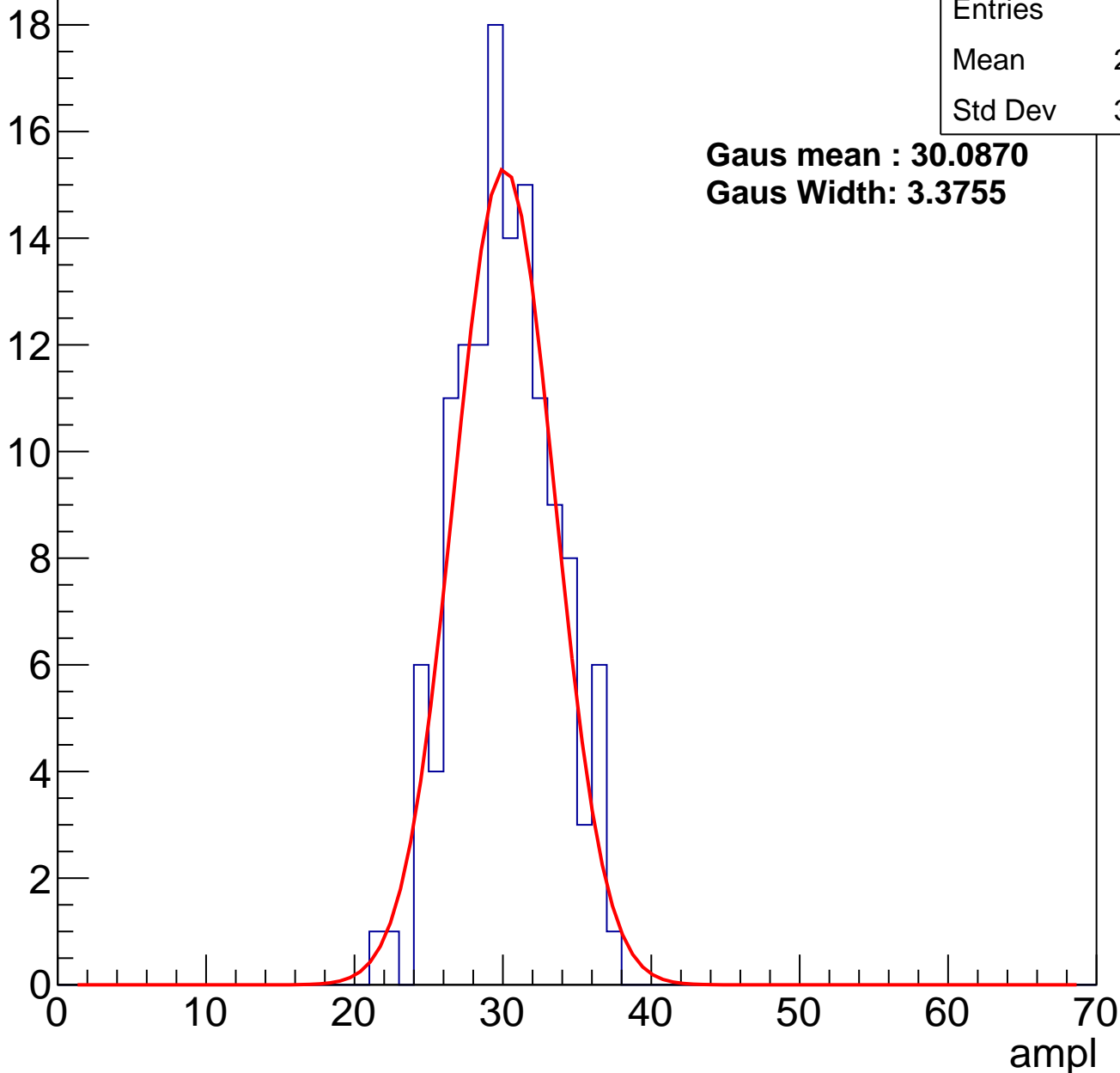
Entry



B1L001S, U20-ch17, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch17, adc1

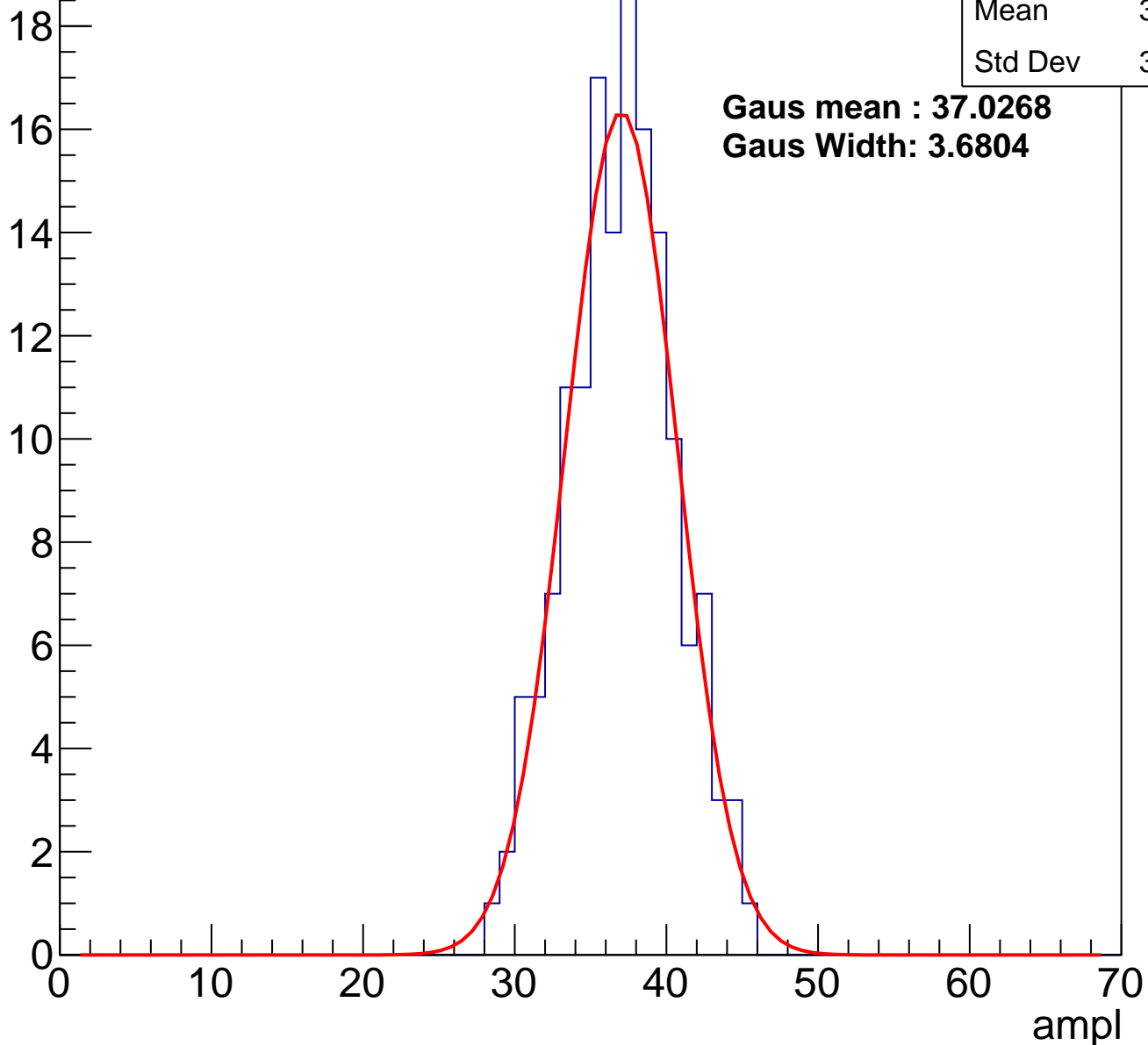
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	36.54
Std Dev	3.522

Gaus mean : 37.0268

Gaus Width: 3.6804

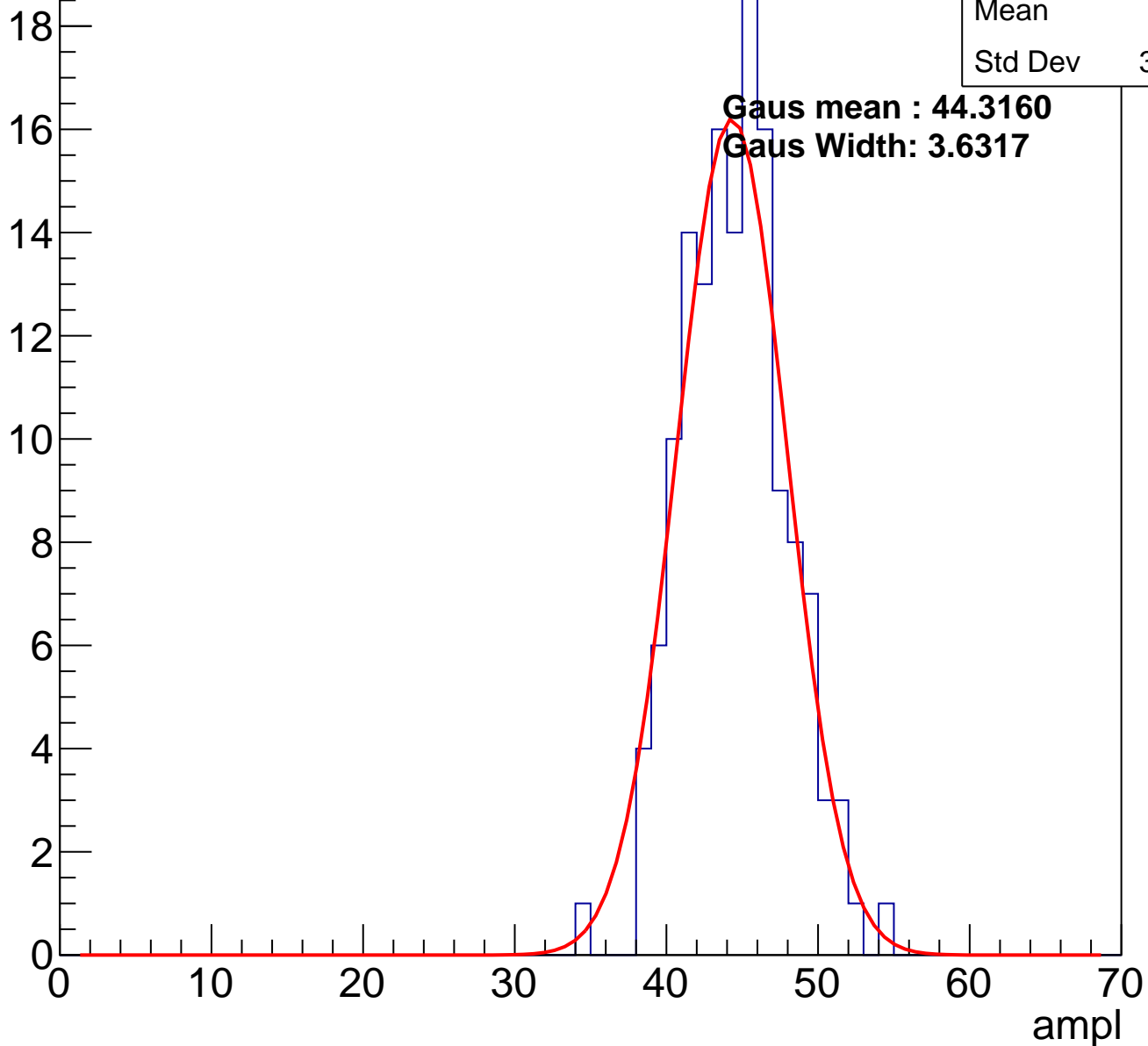
Entry



B1L001S, U20-ch17, adc2

calib_packv5_042523_0143.root, FC#2, port C2

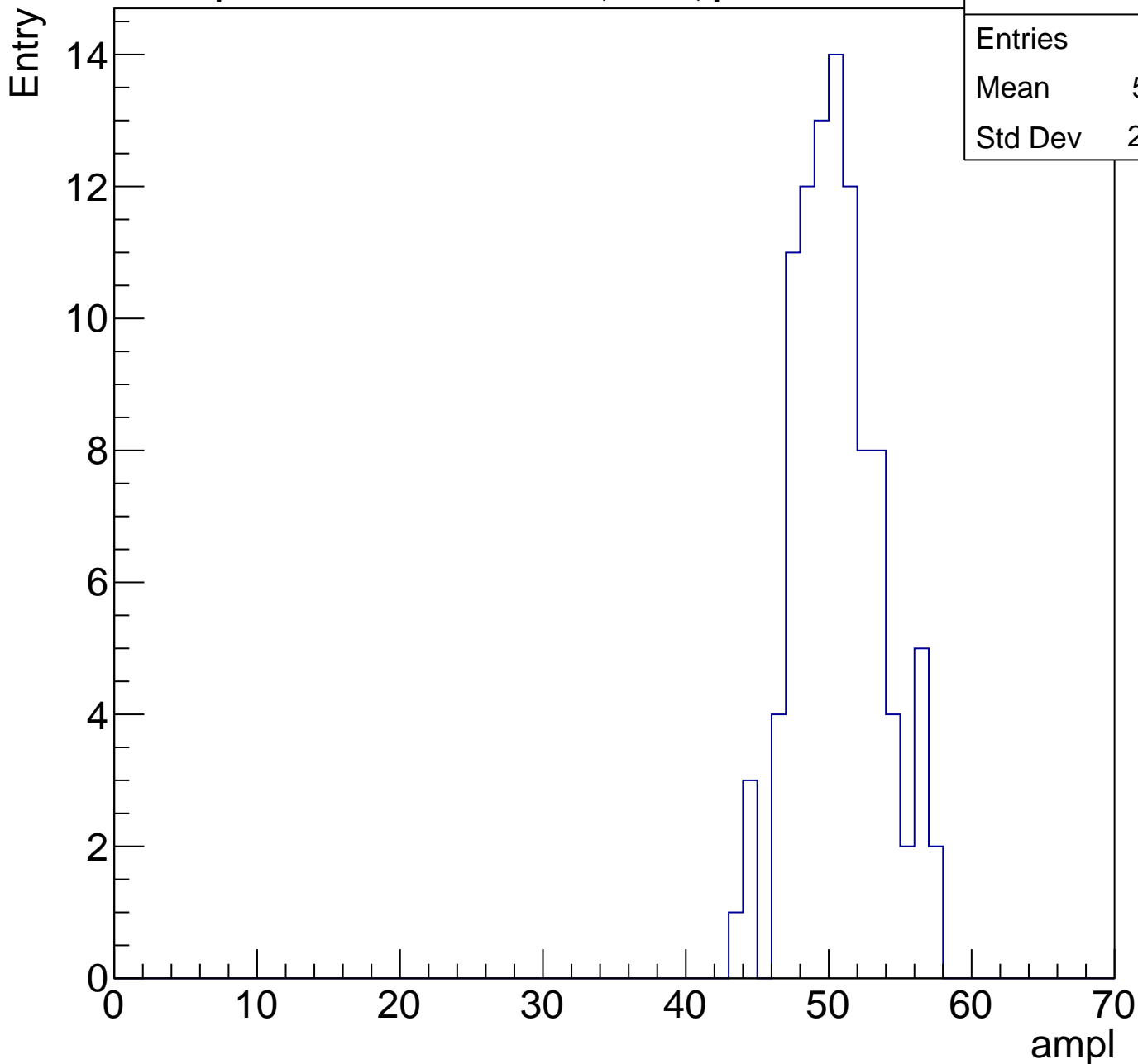
Entry



B1L001S, U20-ch17, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	99
Mean	50.11
Std Dev	2.995



B1L001S, U20-ch17, adc4

calib_packv5_042523_0143.root, FC#2, port C2

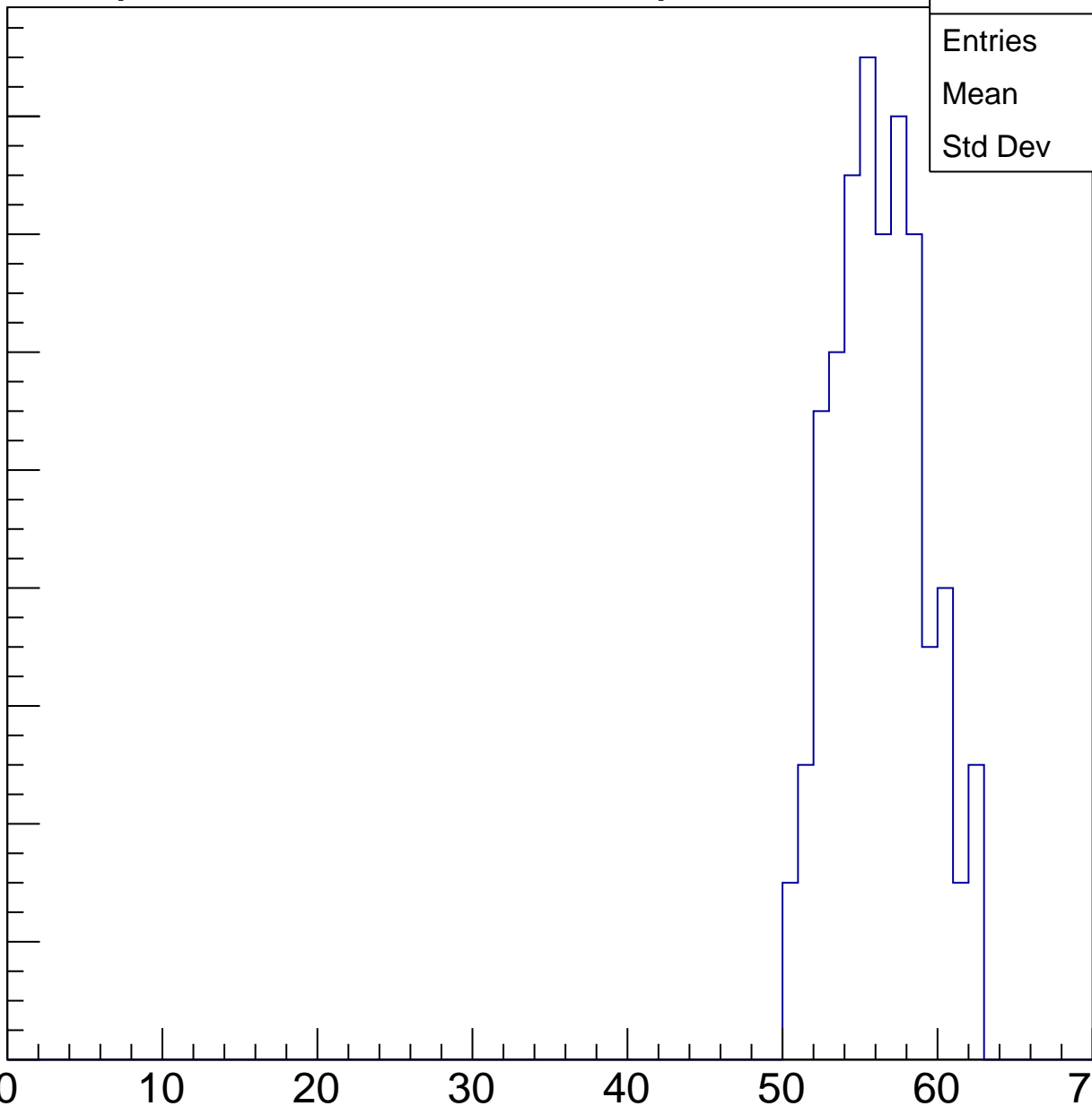
Entries	130
Mean	55.78
Std Dev	2.917

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch17, adc5

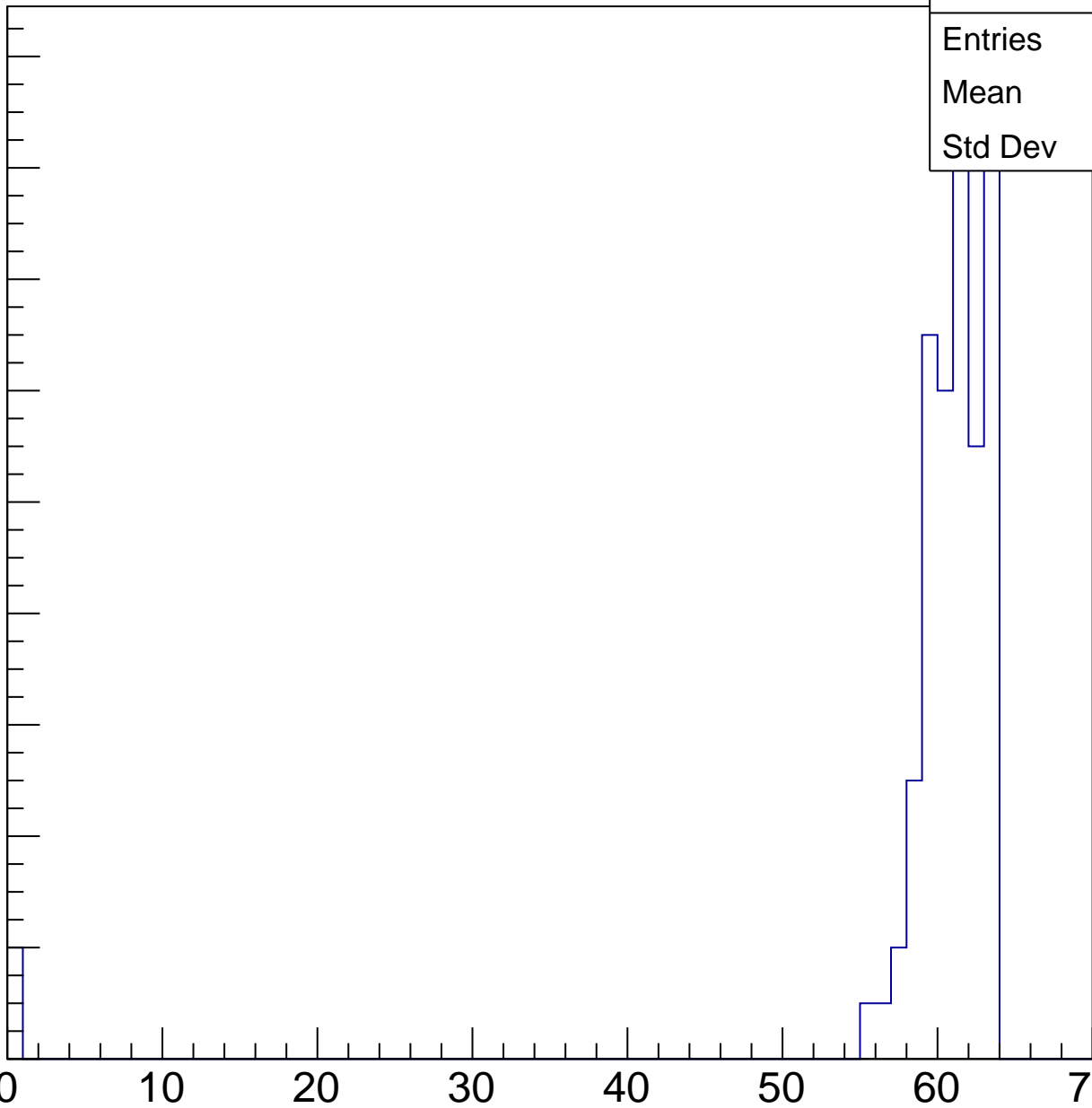
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	81
Mean	59.19
Std Dev	9.597

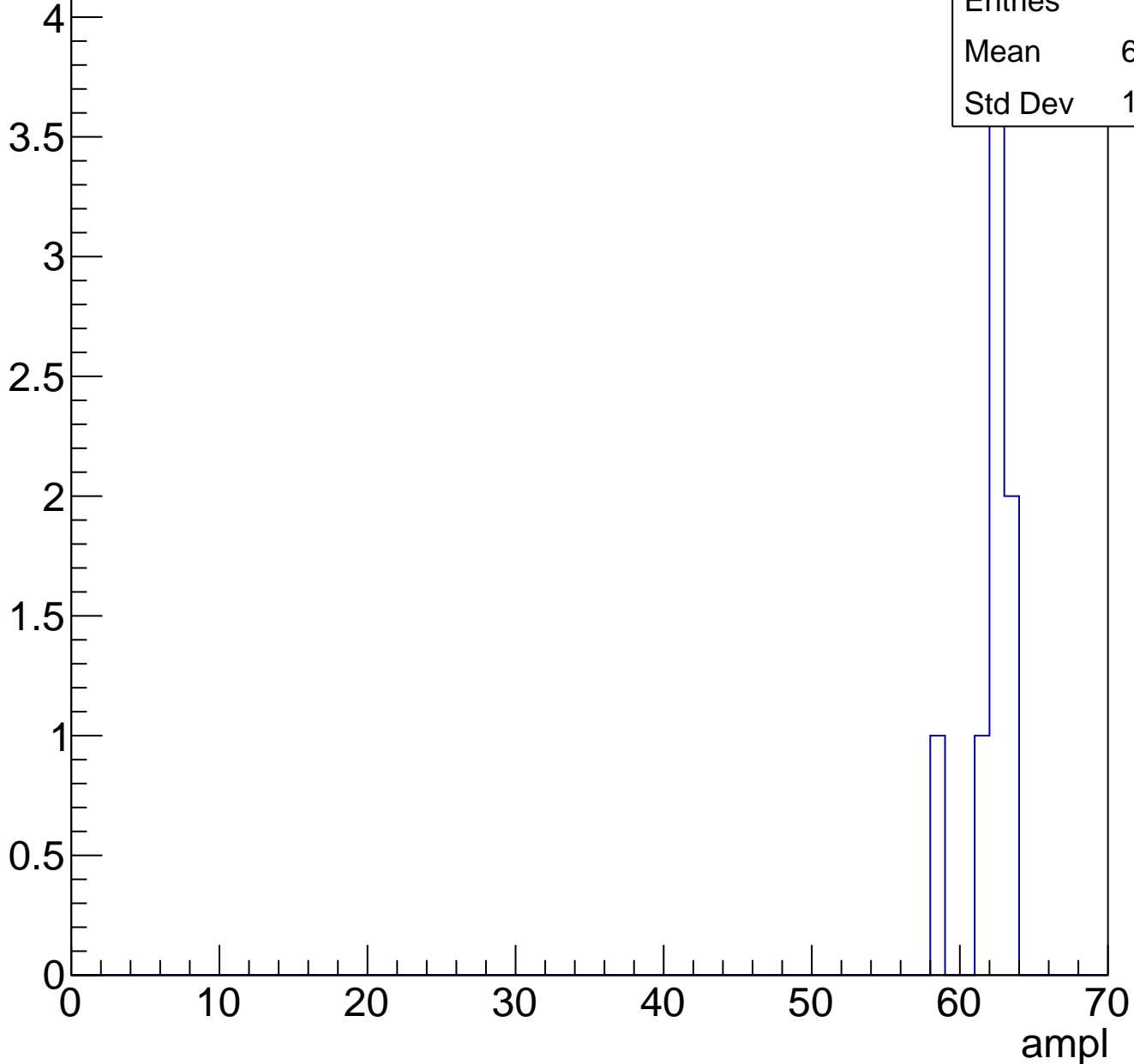
ampl



B1L001S, U20-ch17, adc6

calib_packv5_042523_0143.root, FC#2, port C2

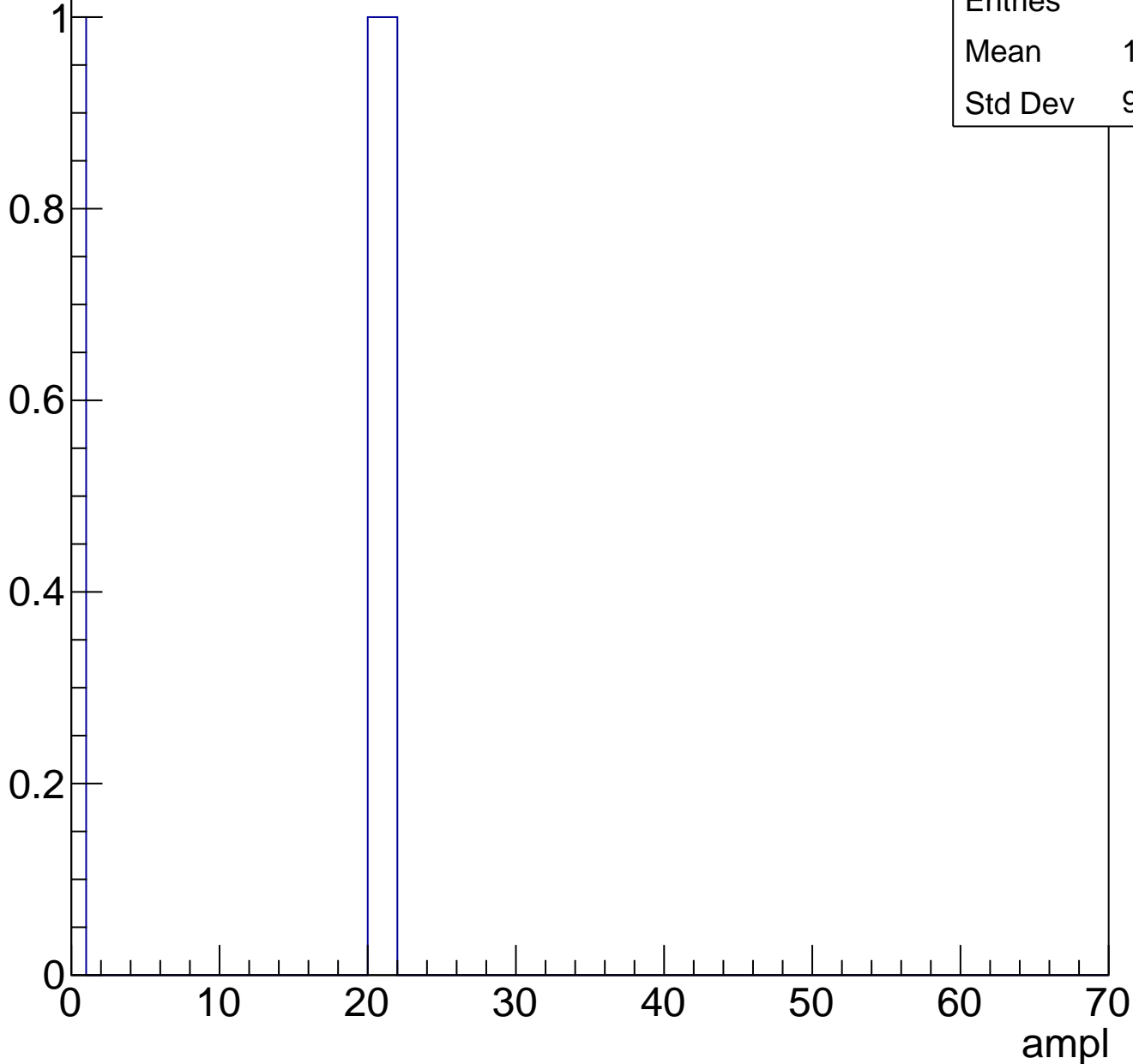
Entry



B1L001S, U20-ch17, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	13.67
Std Dev	9.672

B1L001S, U20-ch18, adc0

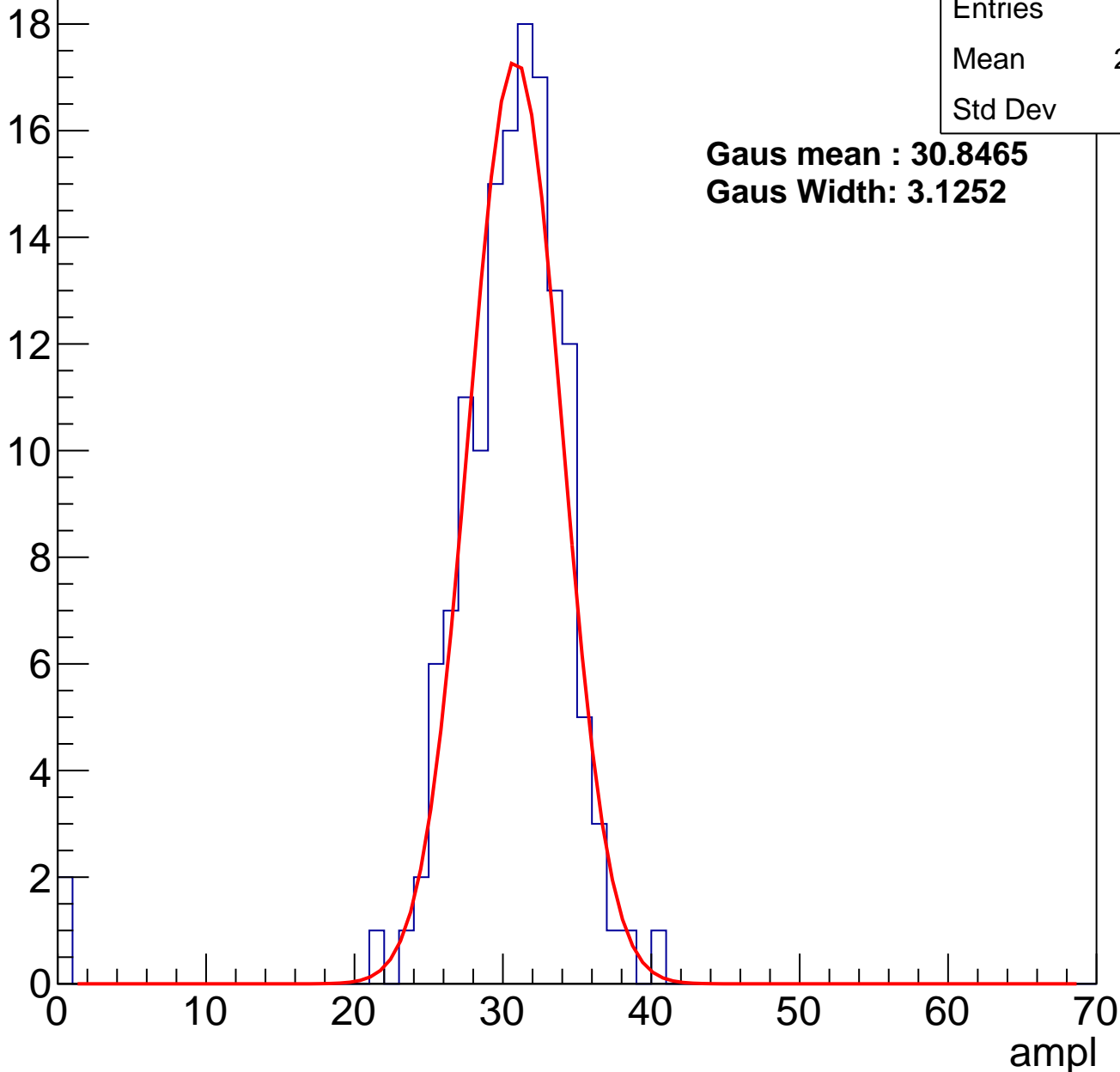
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	29.95
Std Dev	4.79

Gaus mean : 30.8465

Gaus Width: 3.1252

Entry



B1L001S, U20-ch18, adc1

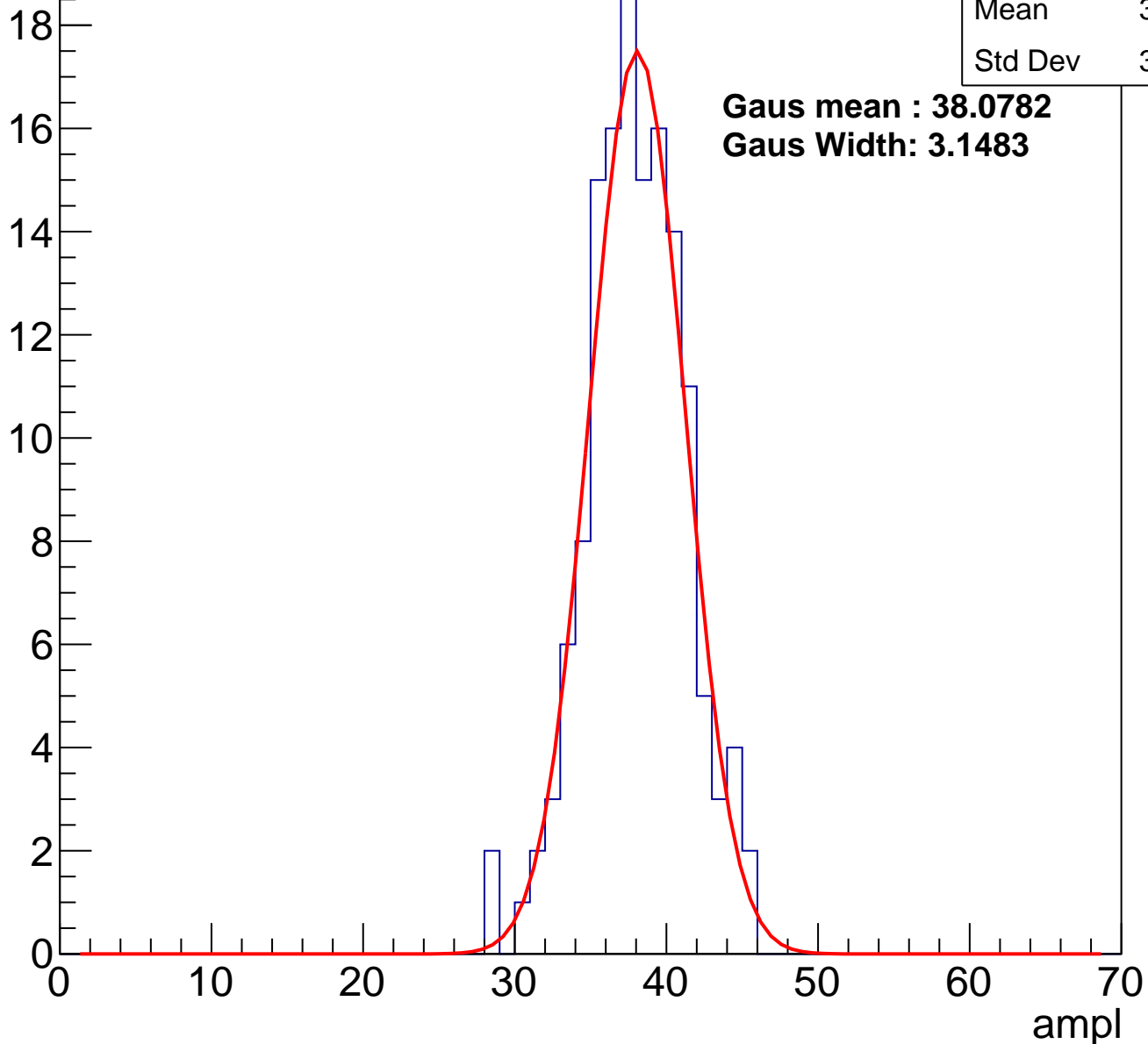
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	37.52
Std Dev	3.252

Gaus mean : 38.0782

Gaus Width: 3.1483

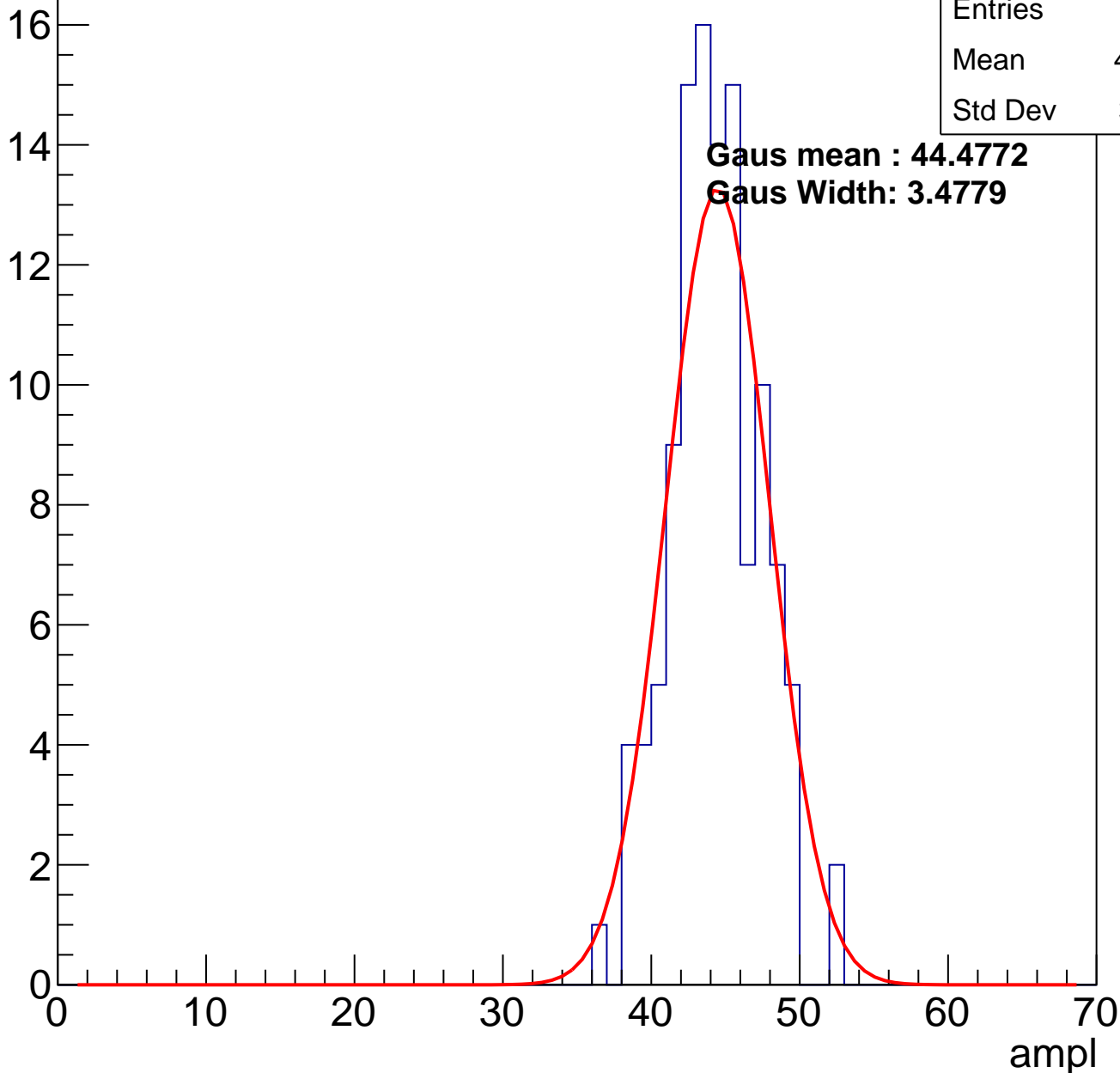
Entry



B1L001S, U20-ch18, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

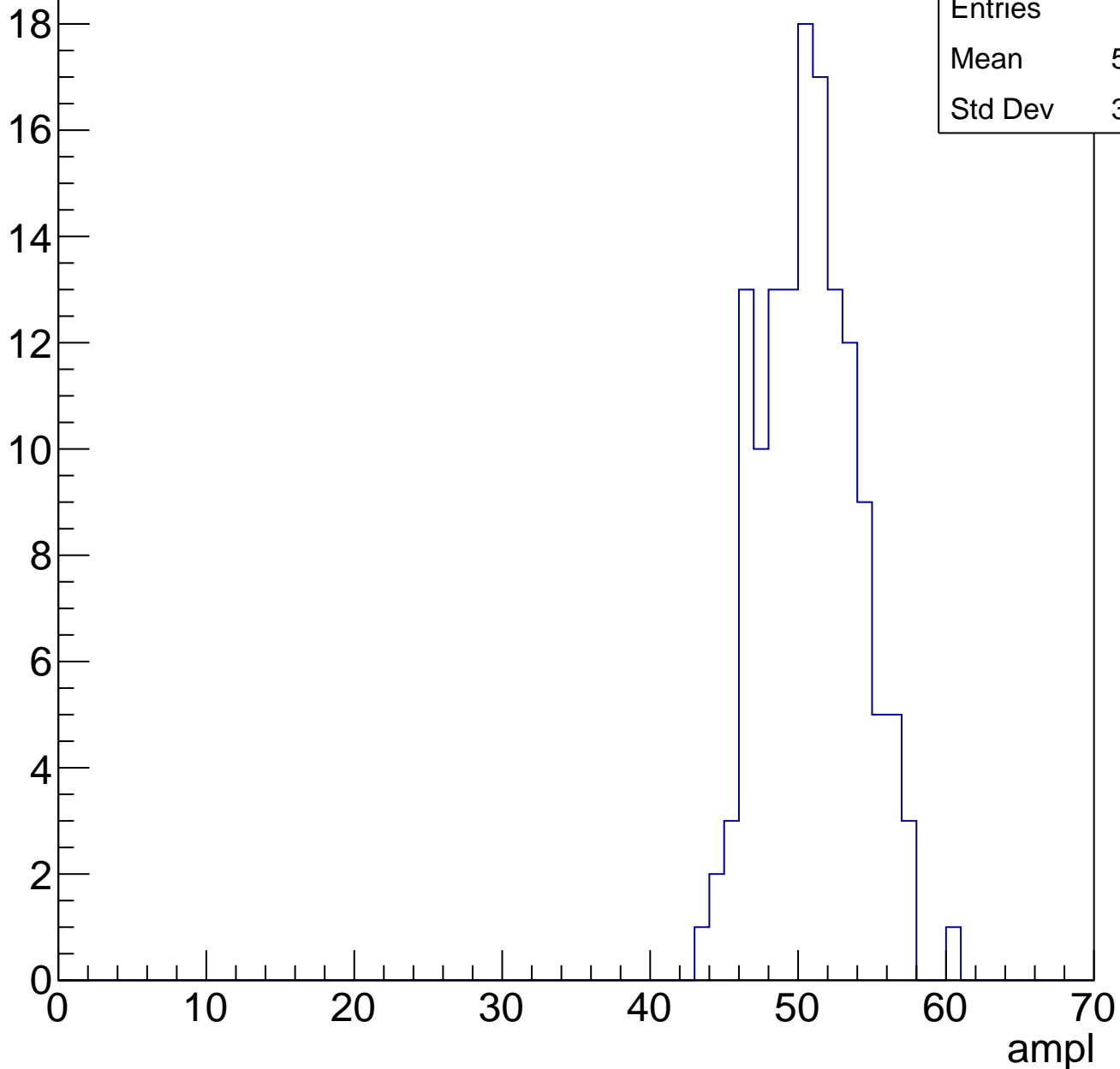


B1L001S, U20-ch18, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	50.33
Std Dev	3.204

Entry



B1L001S, U20-ch18, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	56.41
Std Dev	3.083

Entry

14

12

10

8

6

4

2

0

0

10

20

30

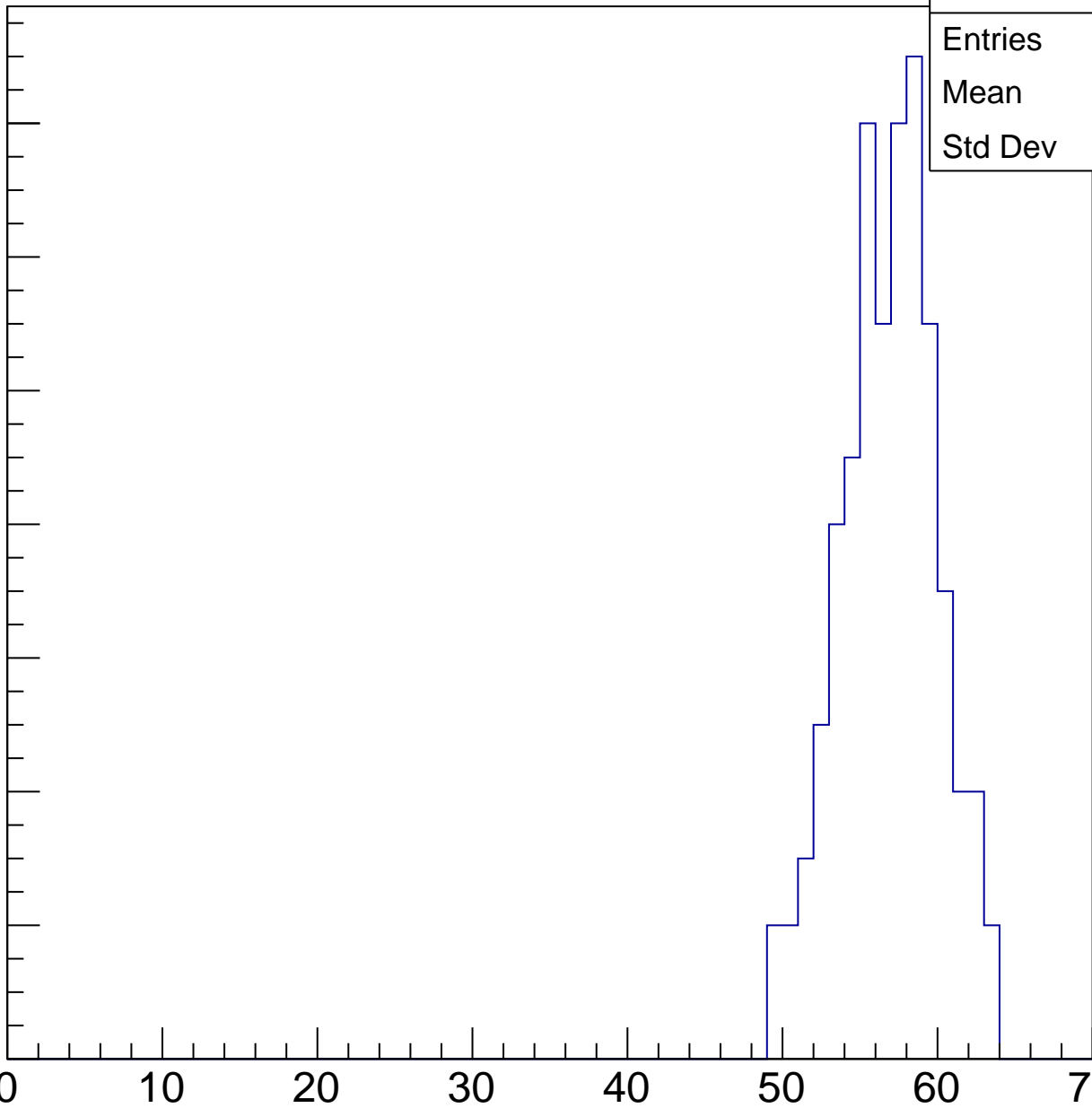
40

50

60

70

ampl



B1L001S, U20-ch18, adc5

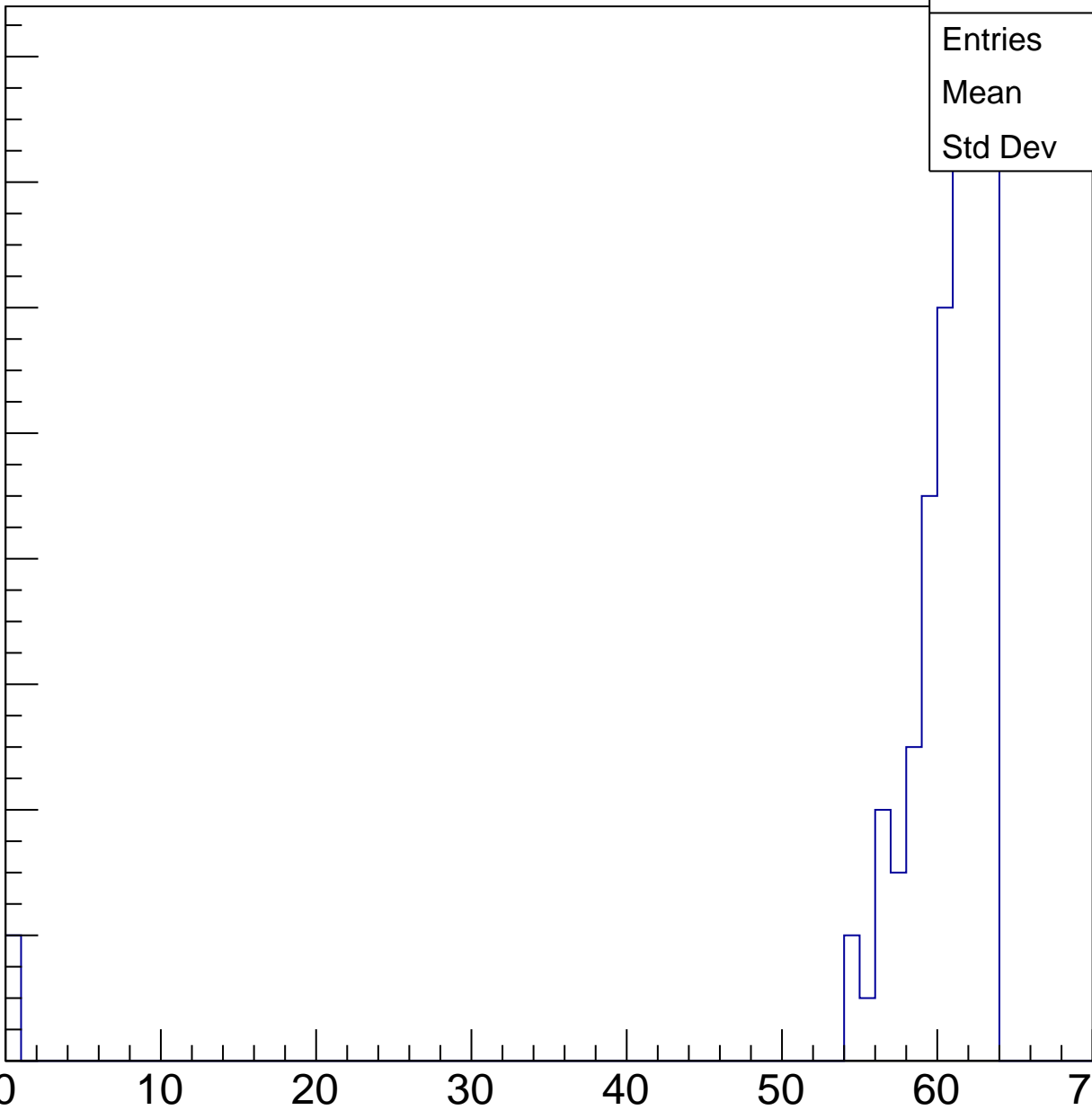
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.01
Std Dev	9.374

ampl



B1L001S, U20-ch18, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L001S, U20-ch18, adc7

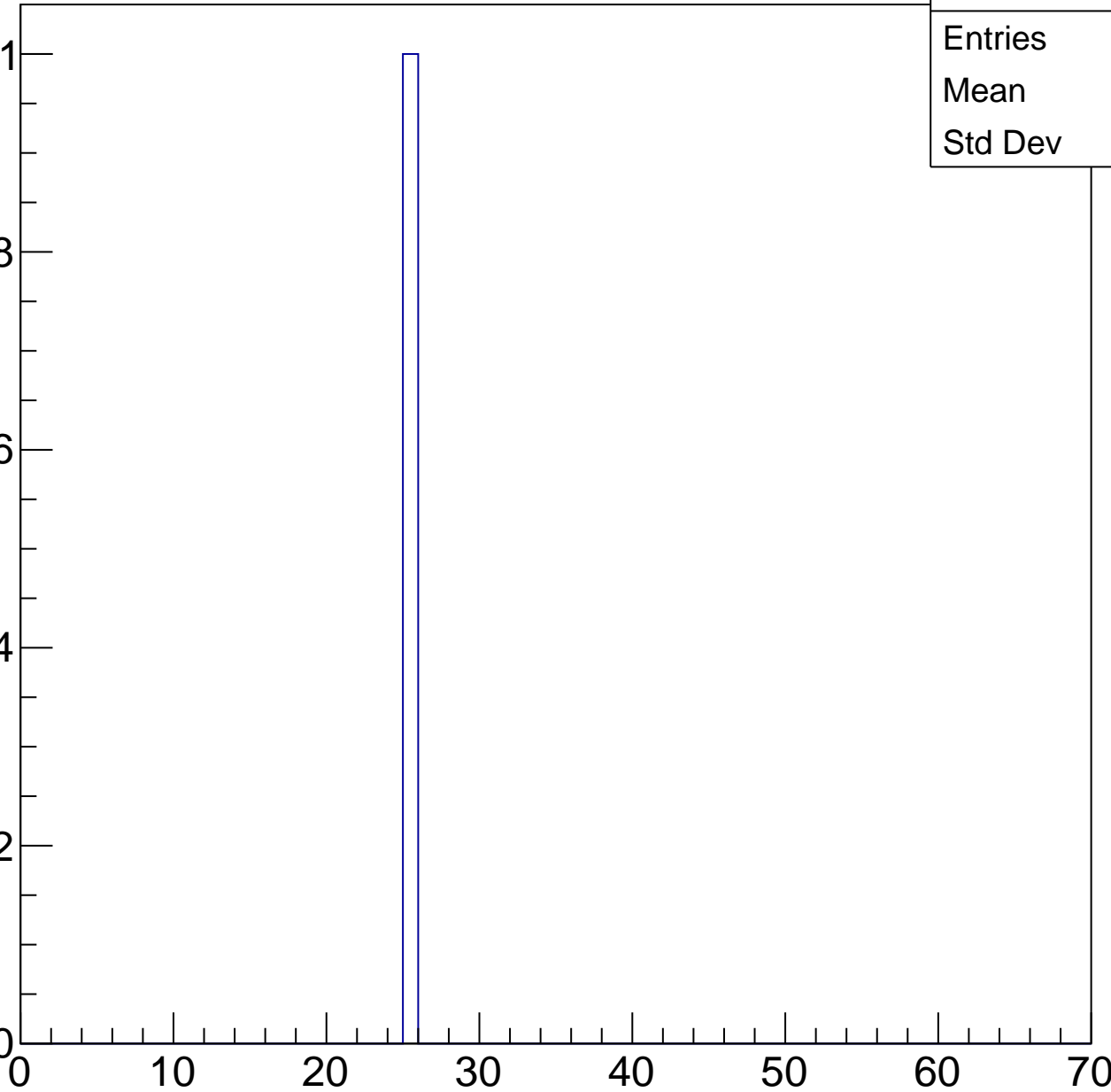
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B1L001S, U20-ch19, adc0

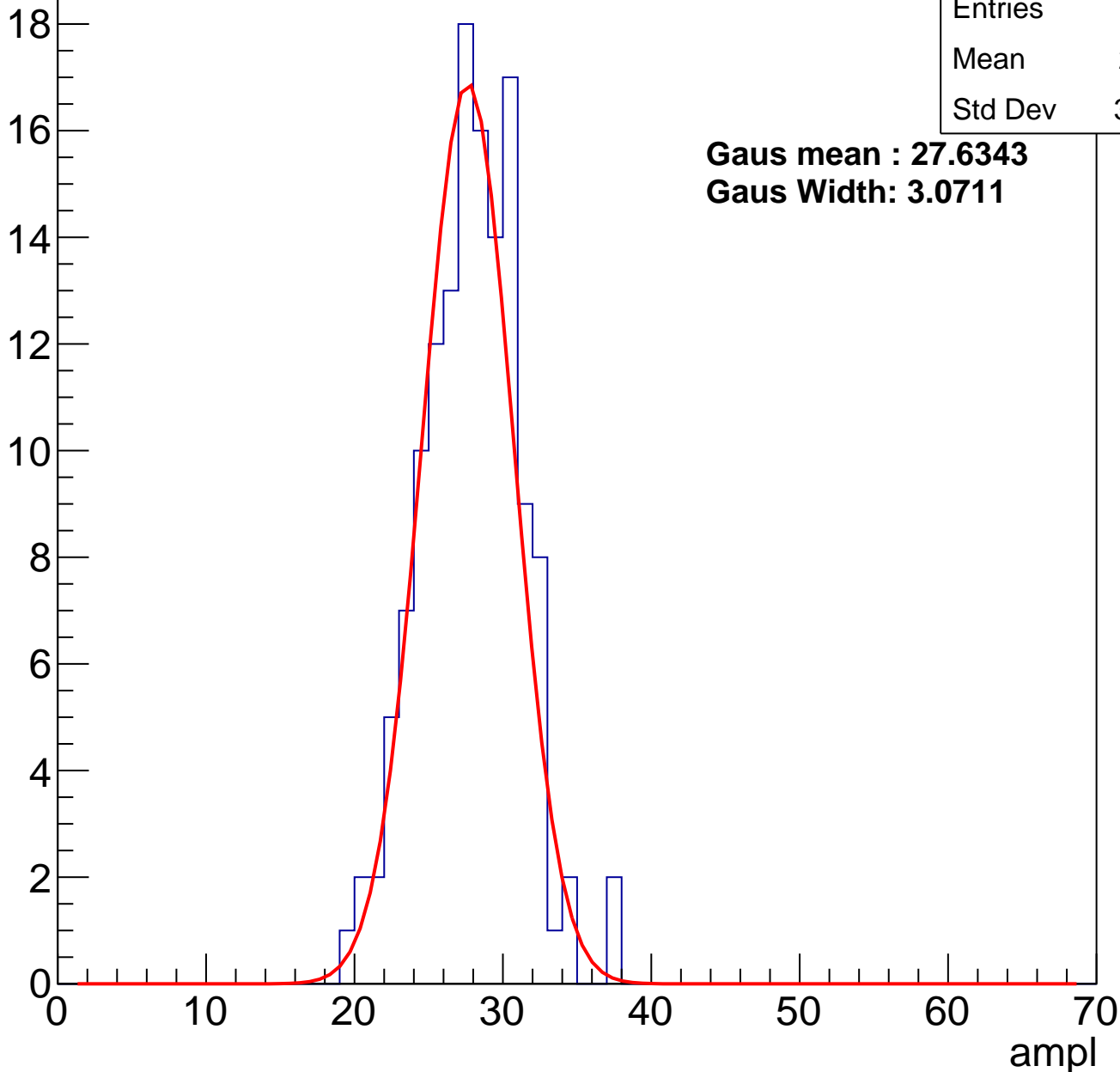
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	27.41
Std Dev	3.287

Gaus mean : 27.6343

Gaus Width: 3.0711

Entry



B1L001S, U20-ch19, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

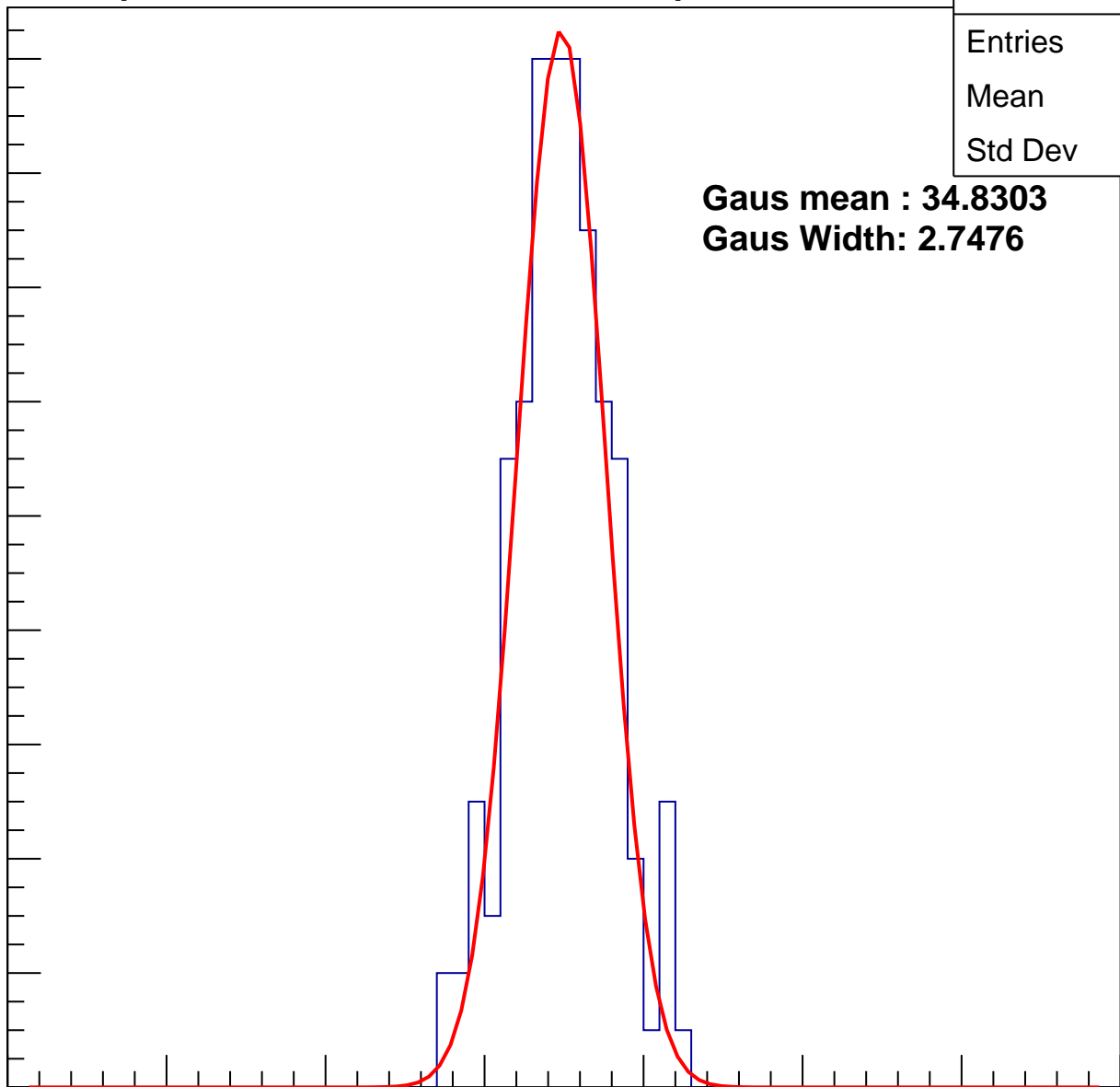
Entries	138
Mean	34.43
Std Dev	3.057

Gaus mean : 34.8303

Gaus Width: 2.7476

0 10 20 30 40 50 60 70

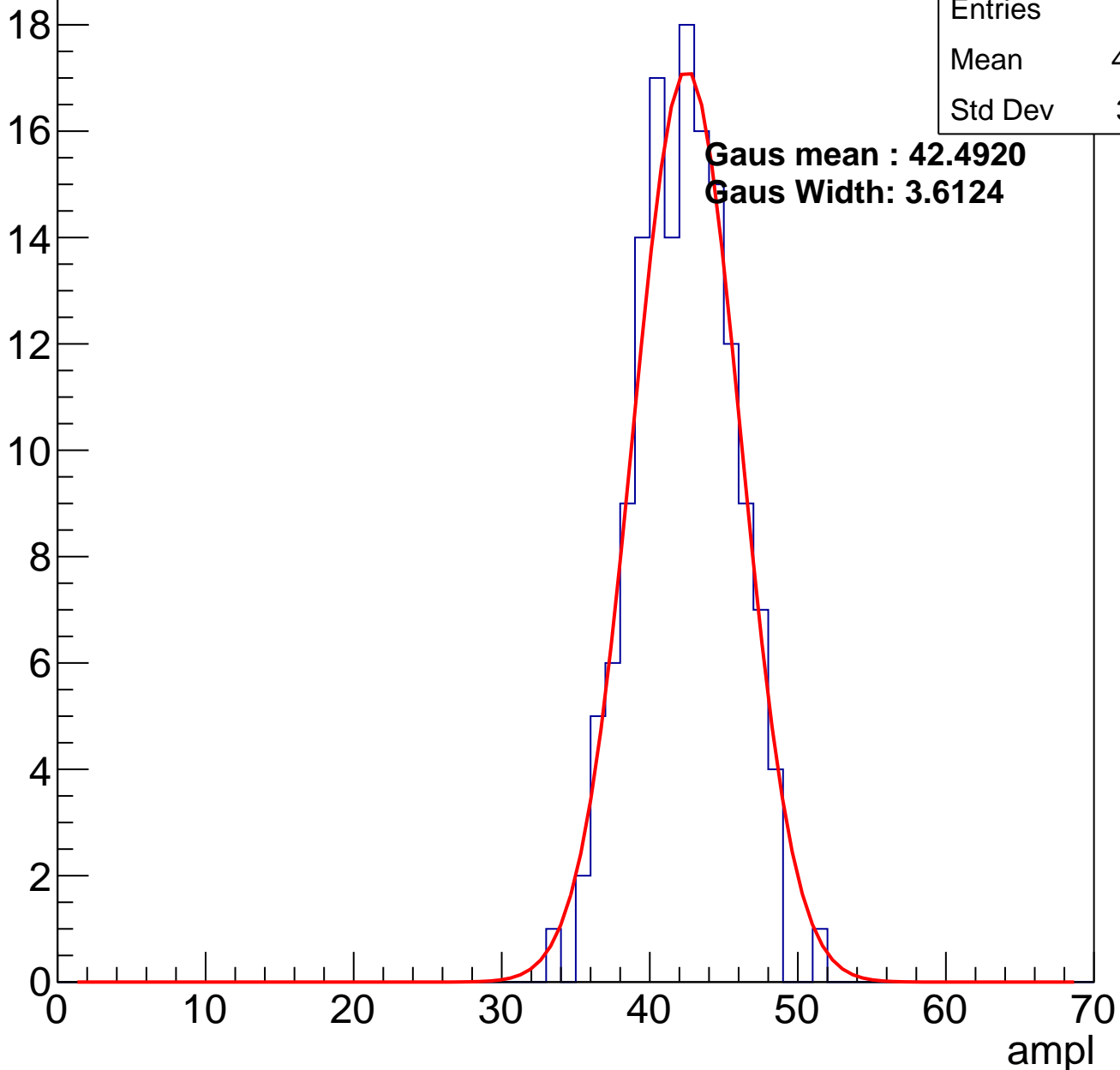
ampl



B1L001S, U20-ch19, adc2

calib_packv5_042523_0143.root, FC#2, port C2

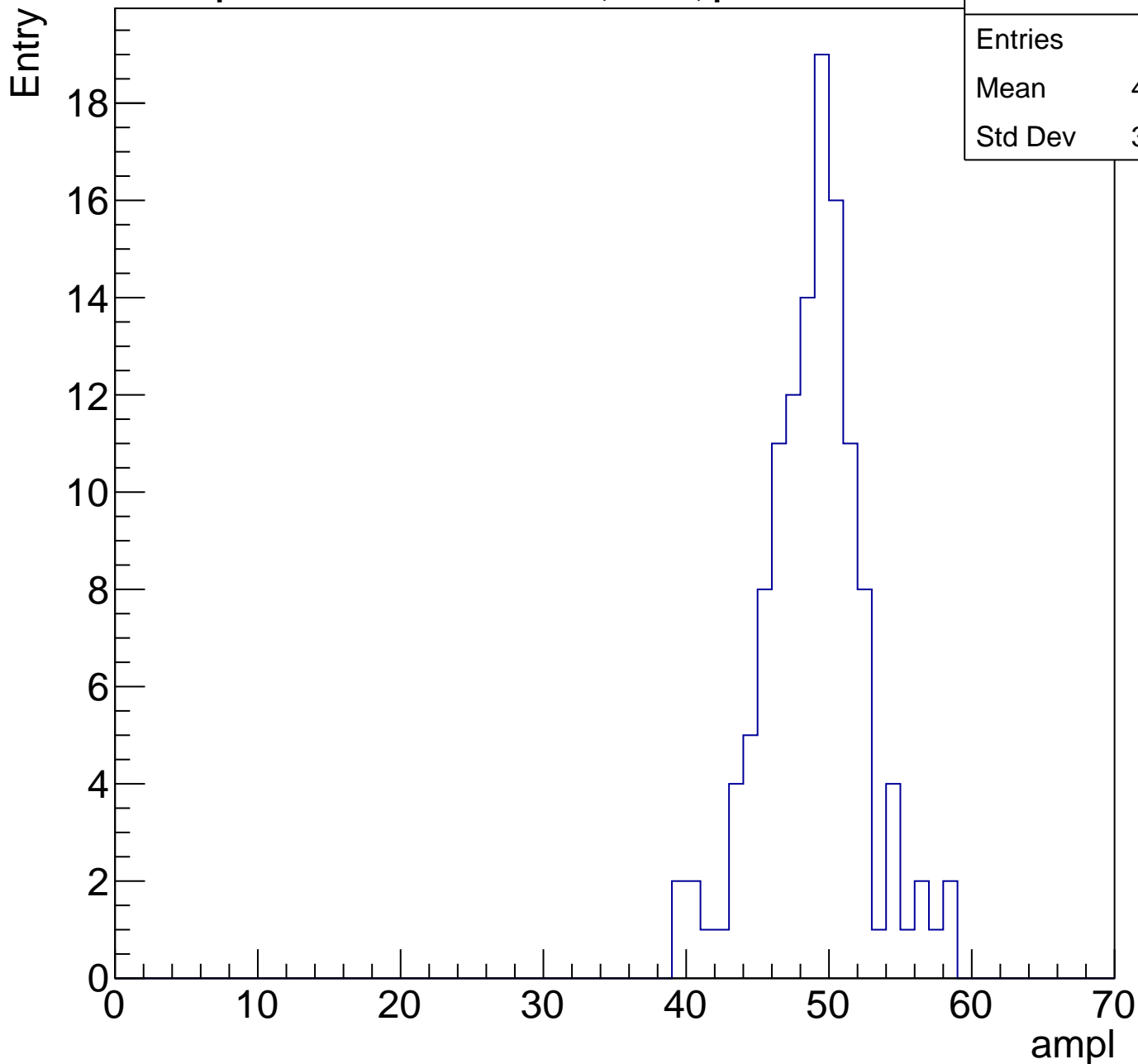
Entry



B1L001S, U20-ch19, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	48.42
Std Dev	3.576



B1L001S, U20-ch19, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

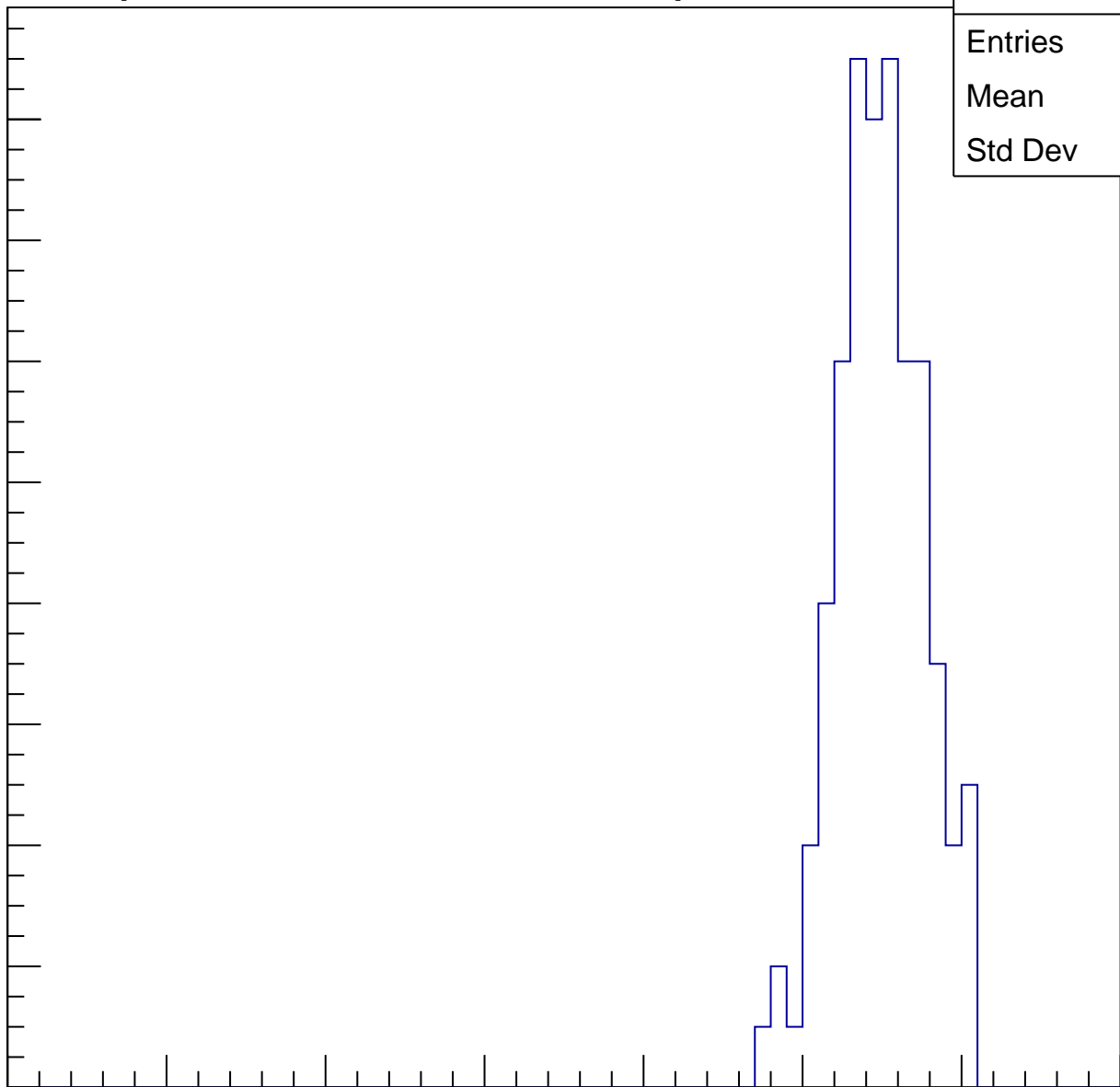
118

Mean

54.42

Std Dev

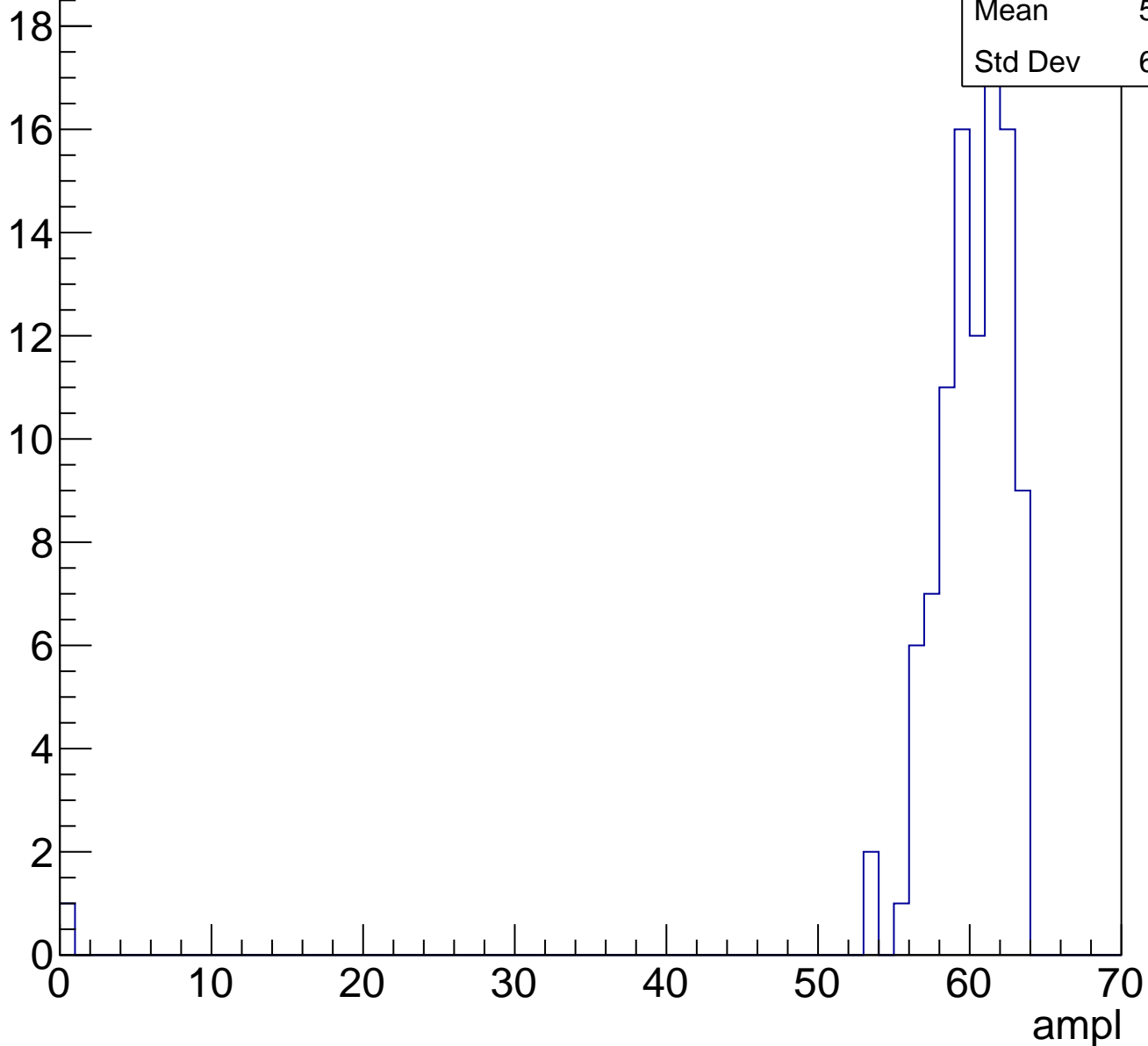
2.748



B1L001S, U20-ch19, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

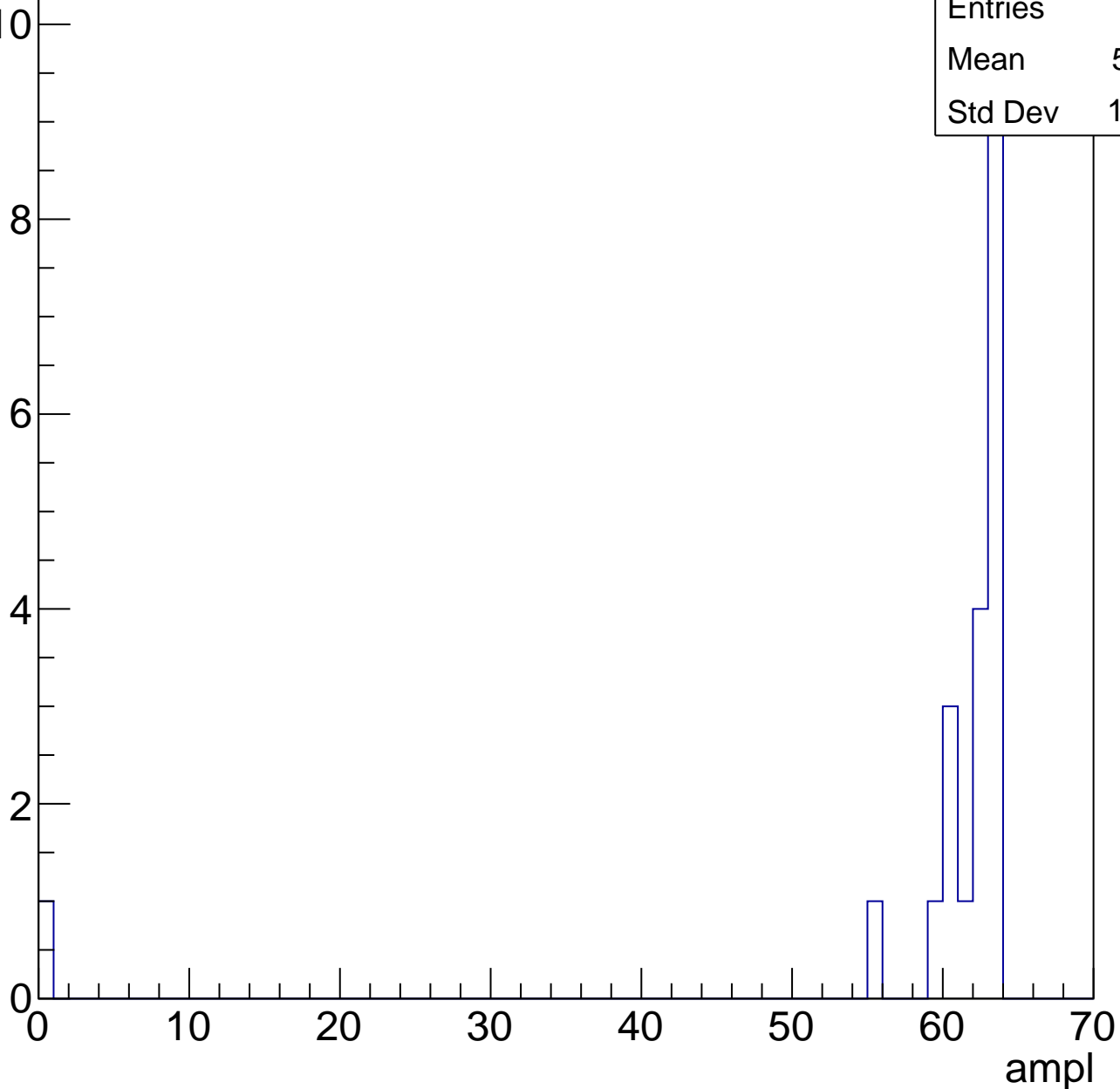


B1L001S, U20-ch19, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	21
Mean	58.71
Std Dev	13.27



B1L001S, U20-ch19, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch20, adc0

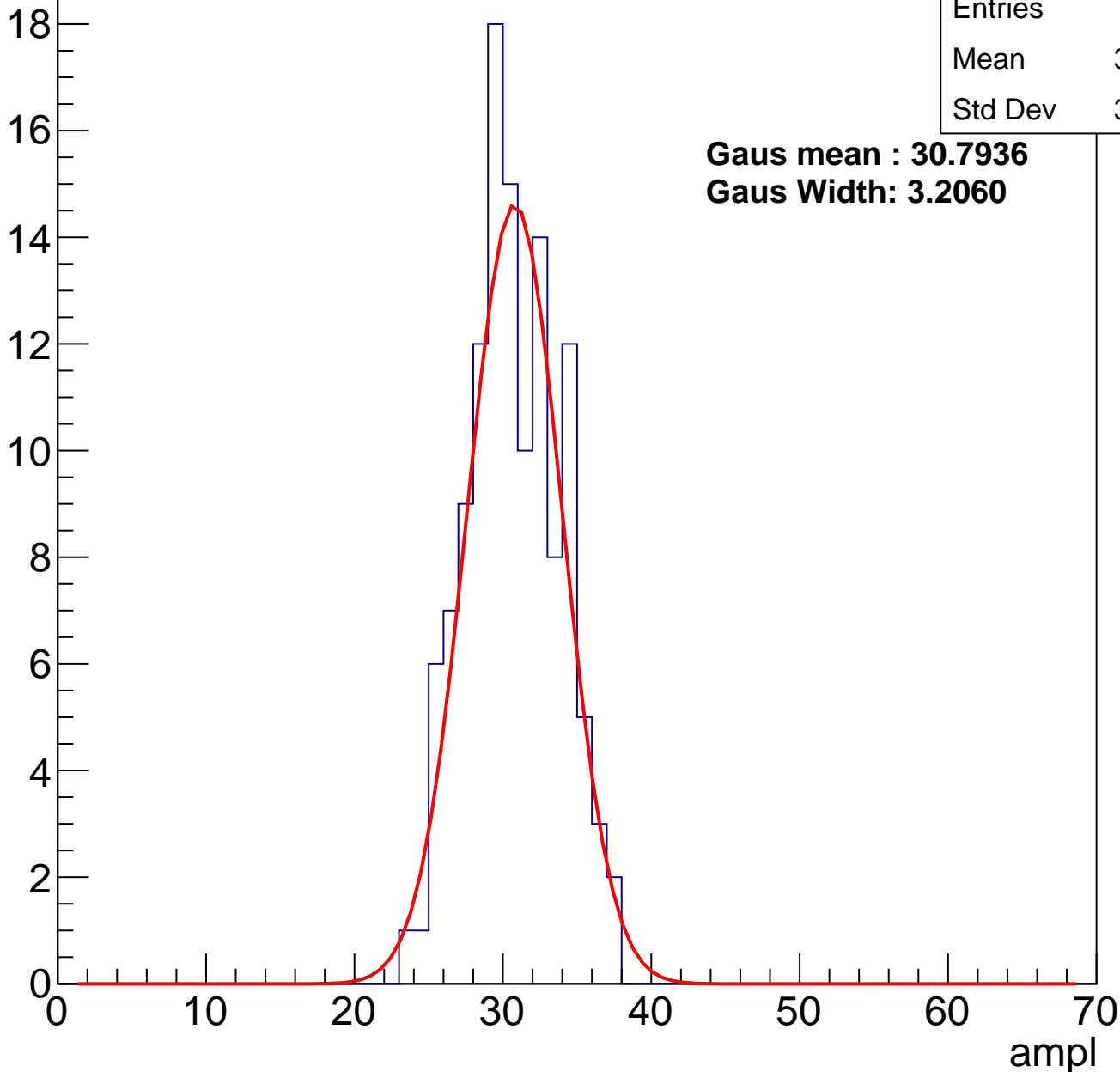
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	30.22
Std Dev	3.056

Gaus mean : 30.7936

Gaus Width: 3.2060

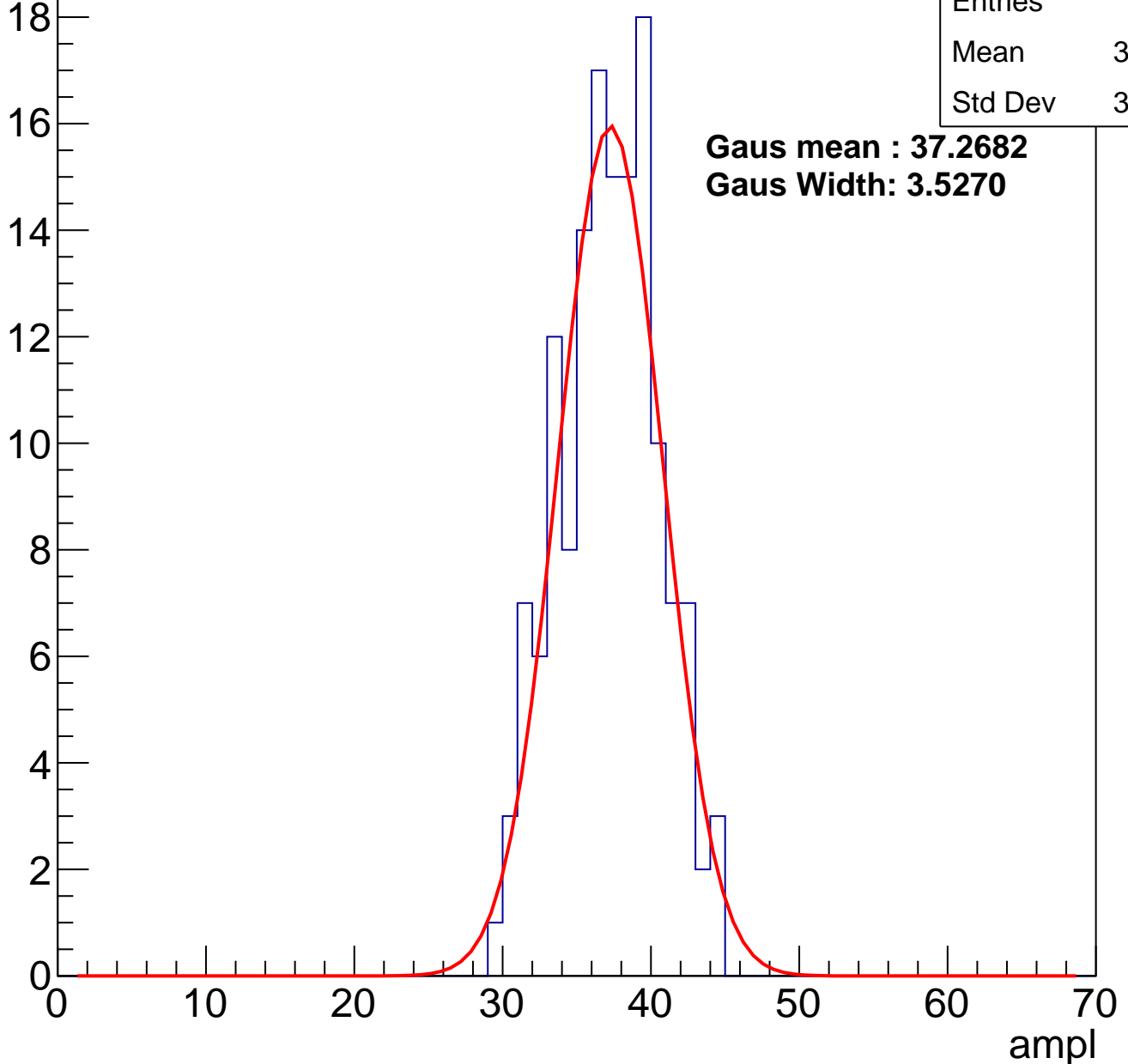
Entry



B1L001S, U20-ch20, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch20, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

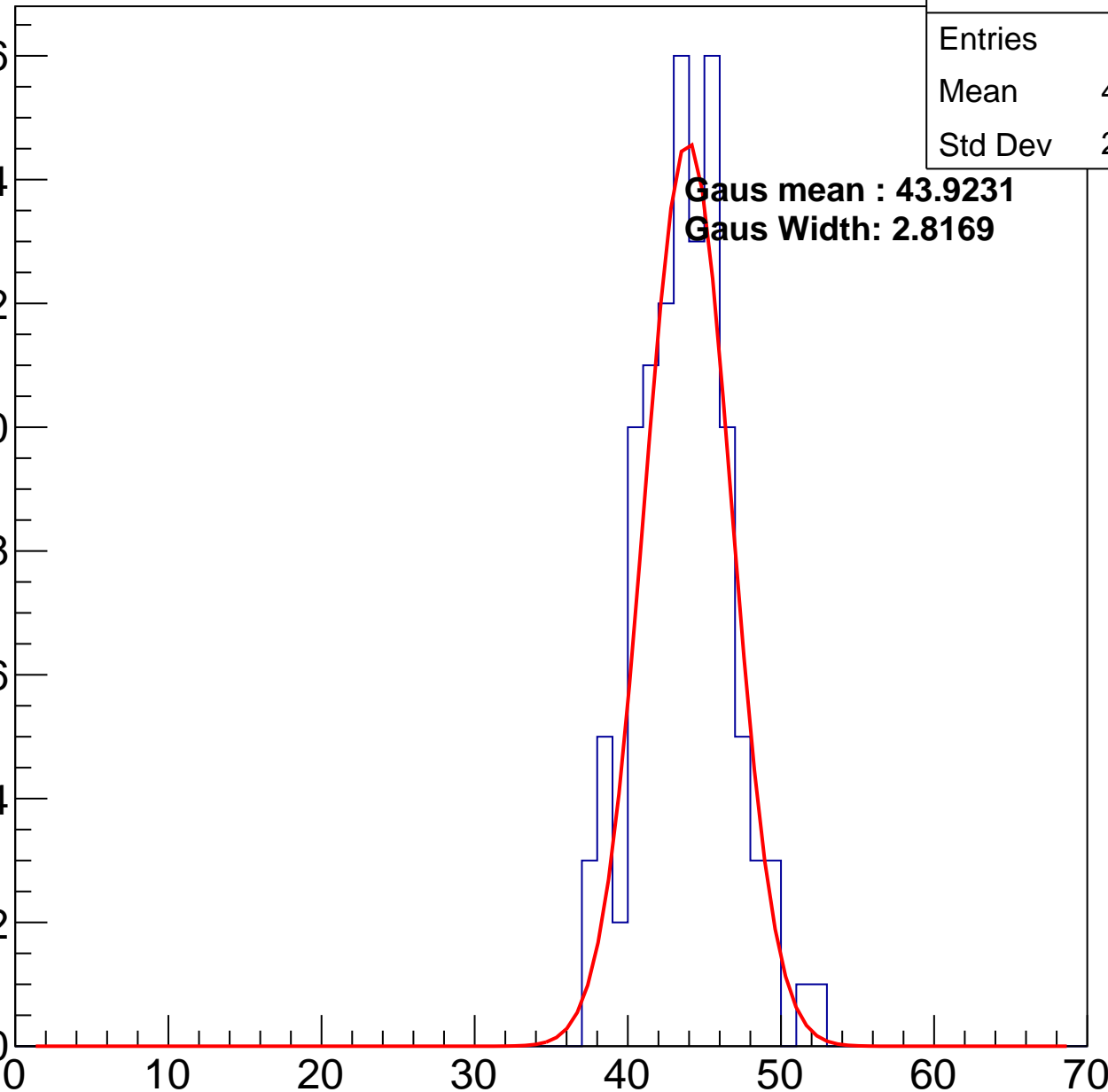
16
14
12
10
8
6
4
2
0

Entries	111
Mean	43.27
Std Dev	2.968

Gaus mean : 43.9231

Gaus Width: 2.8169

ampl



B1L001S, U20-ch20, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	49.27
Std Dev	3.166

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

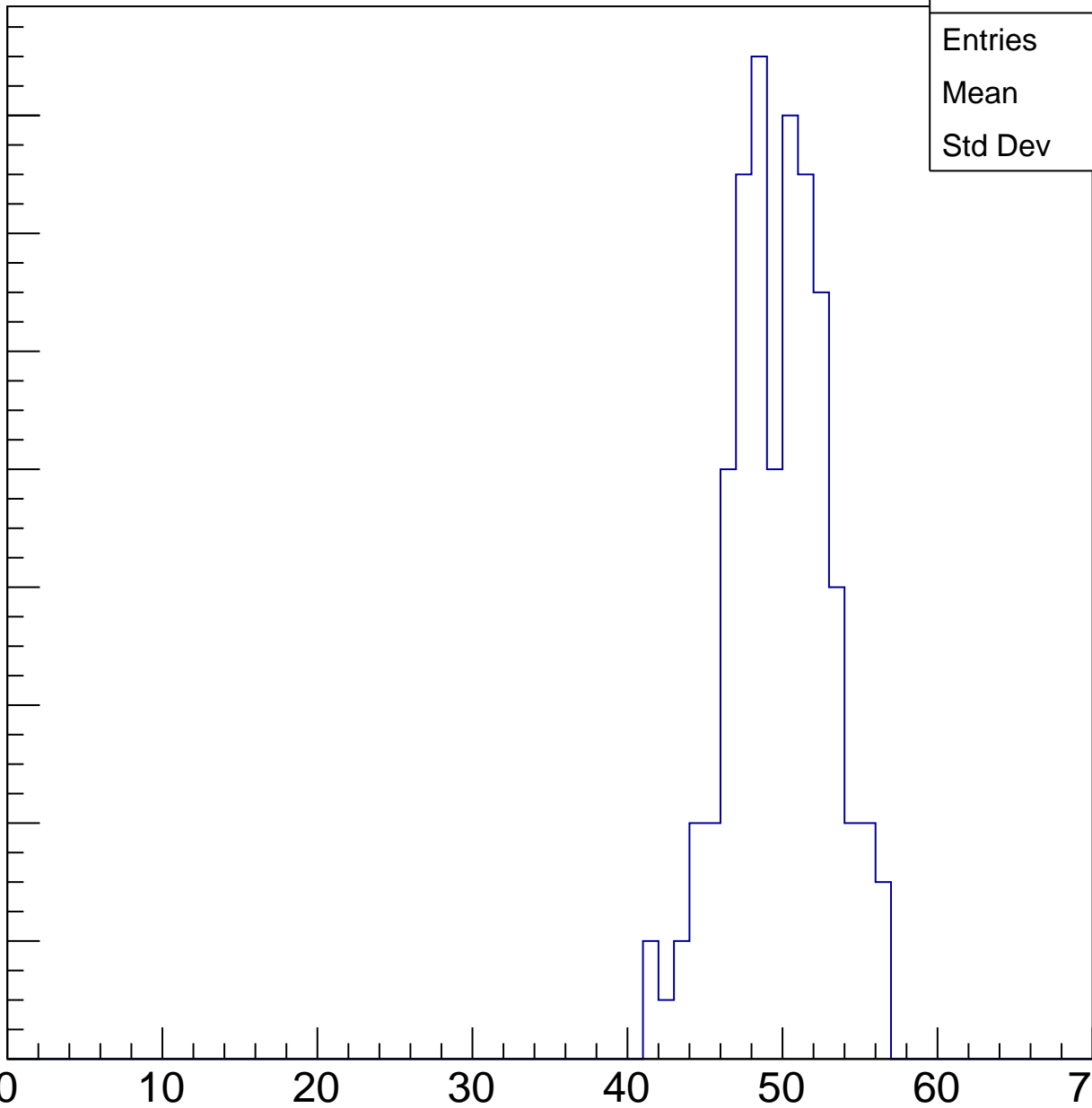
40

50

60

70

ampl



B1L001S, U20-ch20, adc4

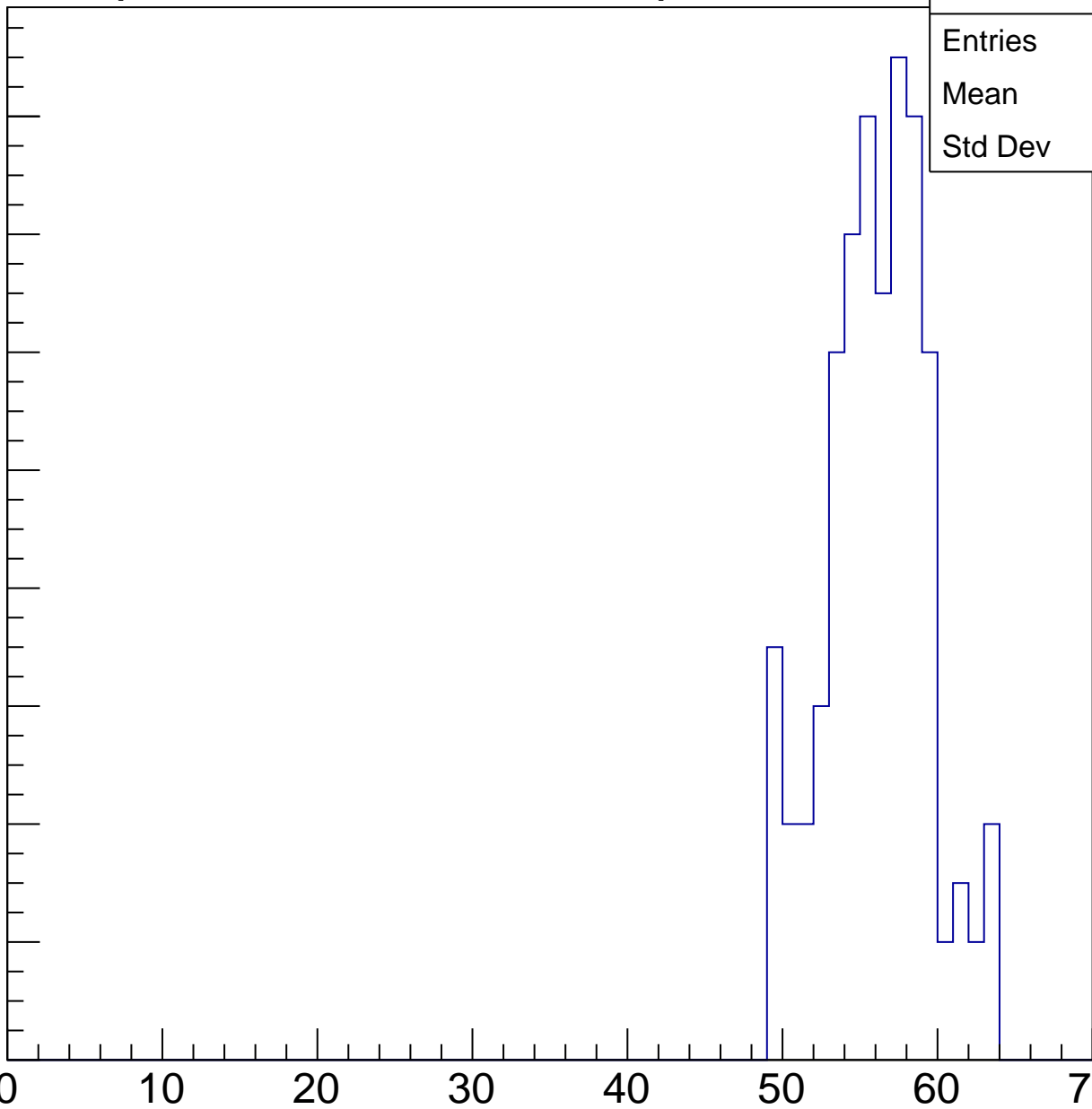
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	55.63
Std Dev	3.267

ampl



B1L001S, U20-ch20, adc5

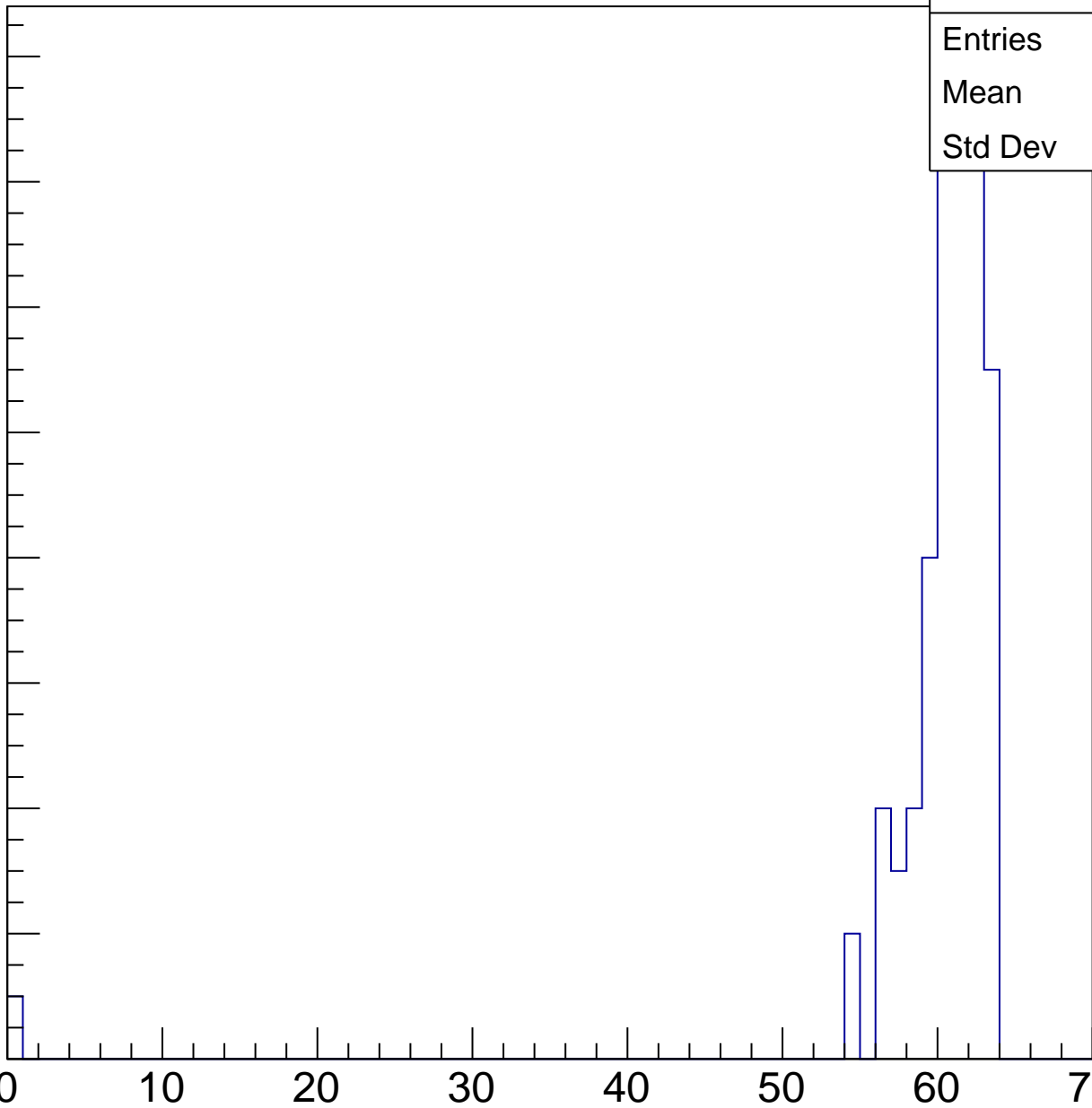
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	59.56
Std Dev	7.067

ampl

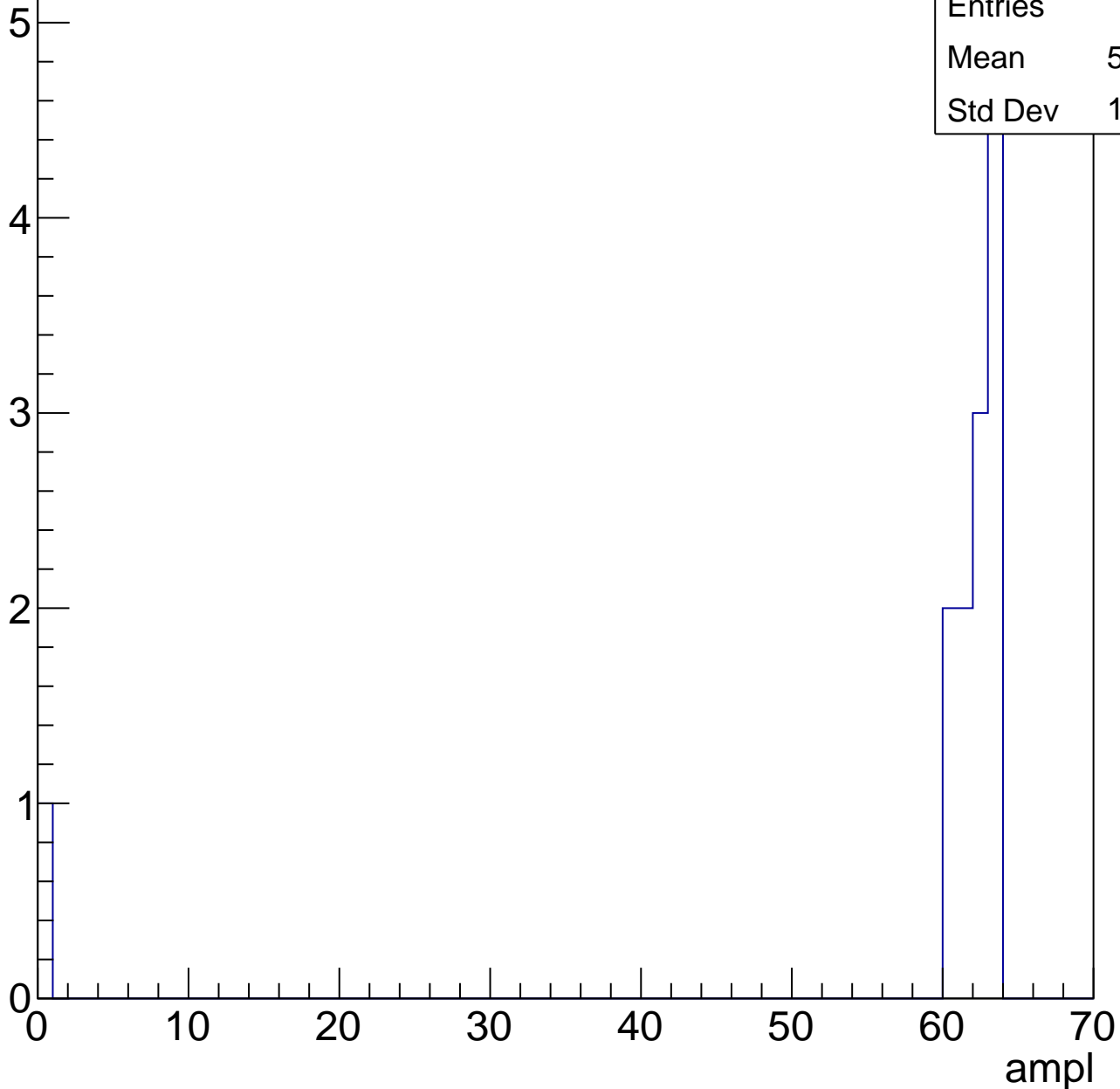


B1L001S, U20-ch20, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	57.15
Std Dev	16.53



B1L001S, U20-ch20, adc7

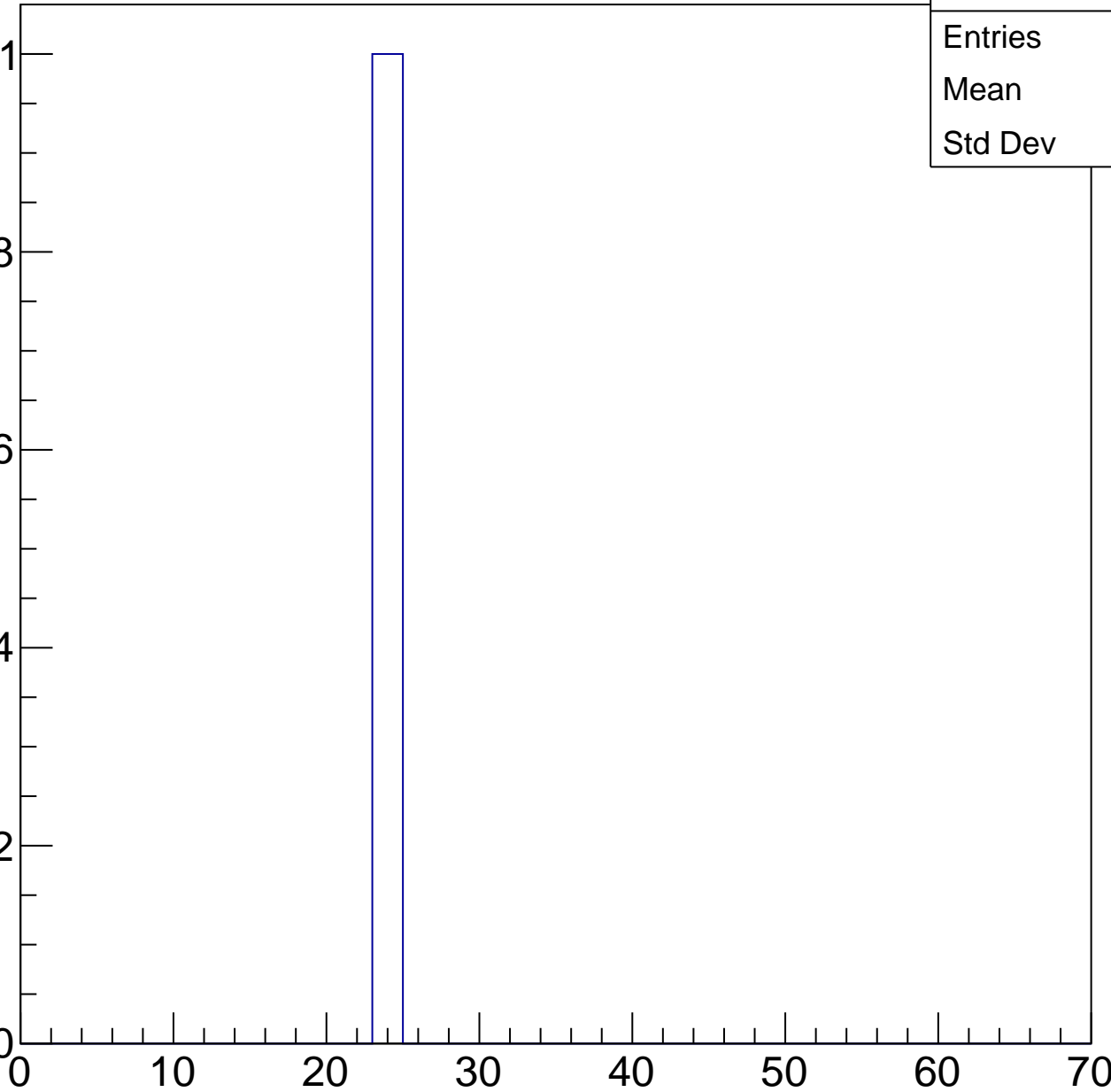
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23.5
Std Dev	0.5

ampl



B1L001S, U20-ch21, adc0

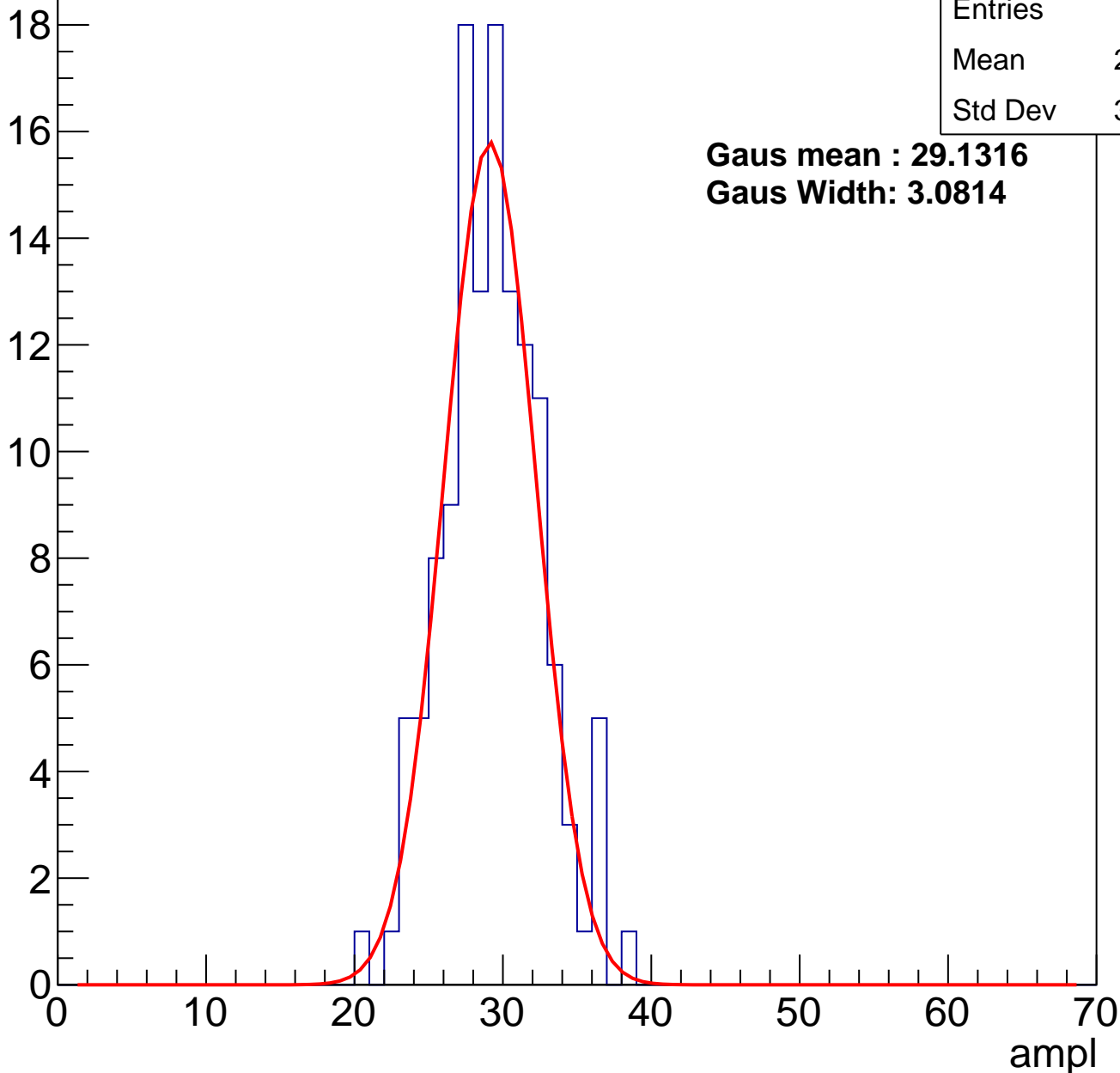
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	28.85
Std Dev	3.304

Gaus mean : 29.1316

Gaus Width: 3.0814

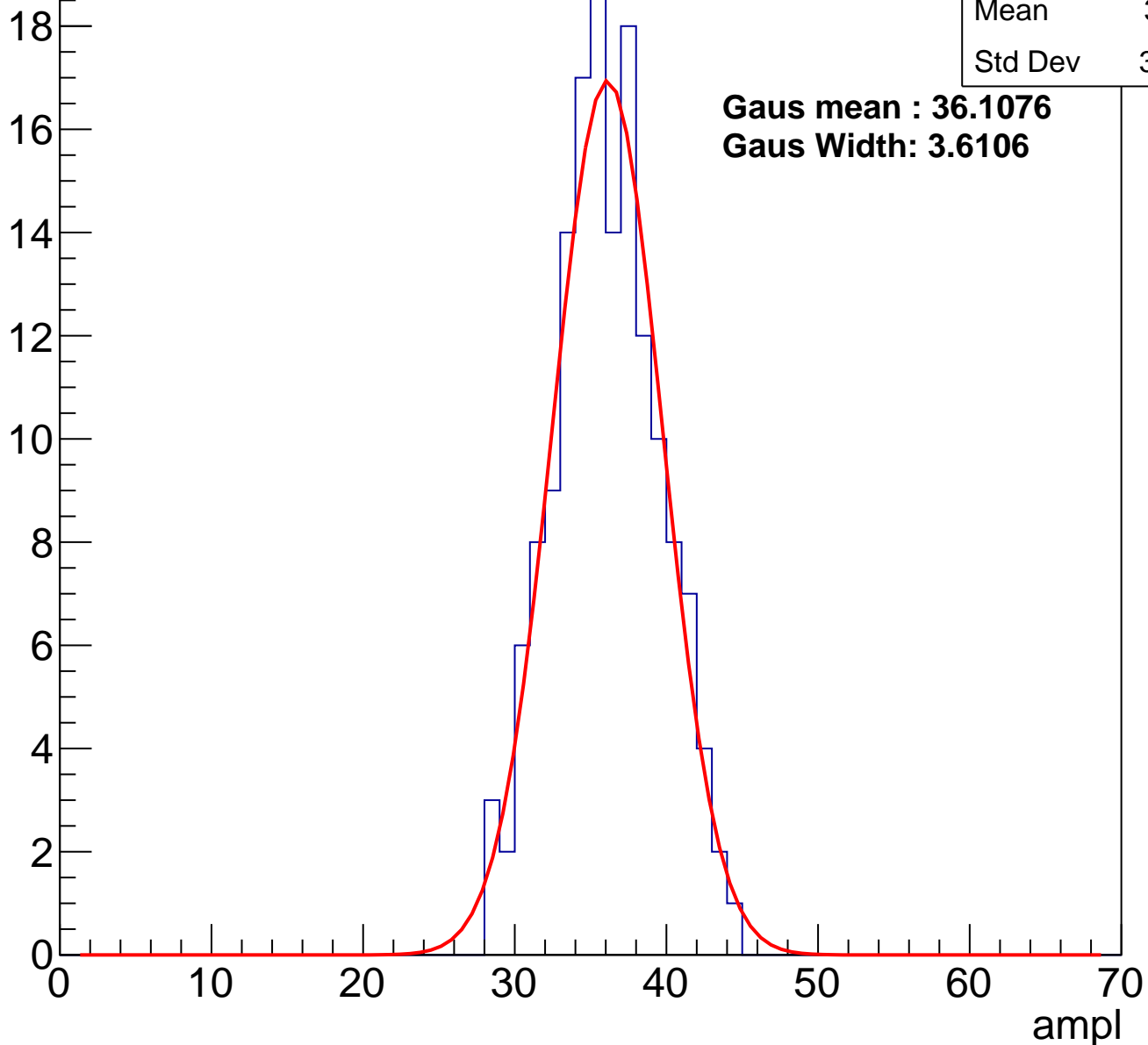
Entry



B1L001S, U20-ch21, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch21, adc2

calib_packv5_042523_0143.root, FC#2, port C2

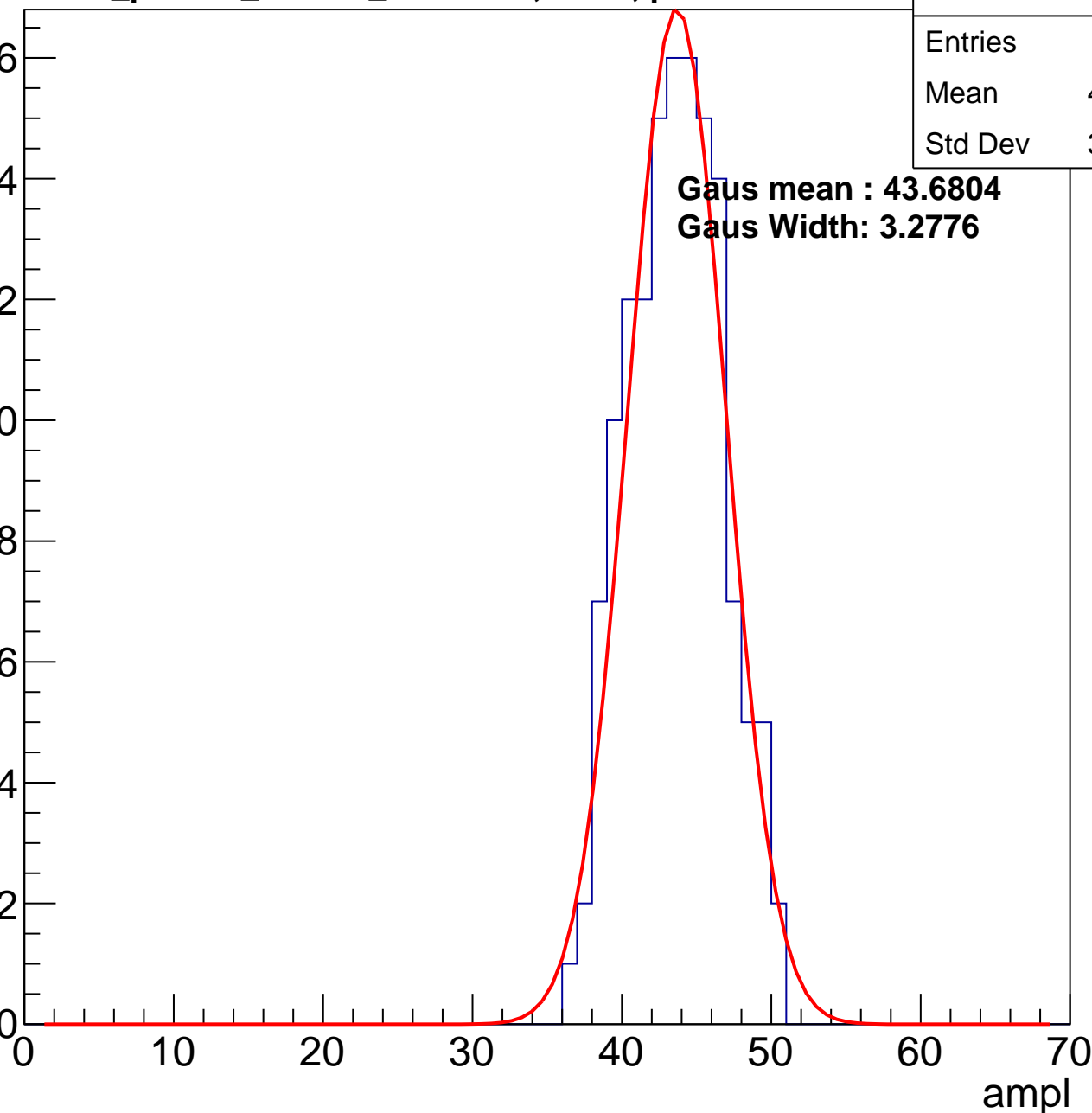
Entry

16
14
12
10
8
6
4
2
0

Entries	139
Mean	43.12
Std Dev	3.107

Gaus mean : 43.6804

Gaus Width: 3.2776

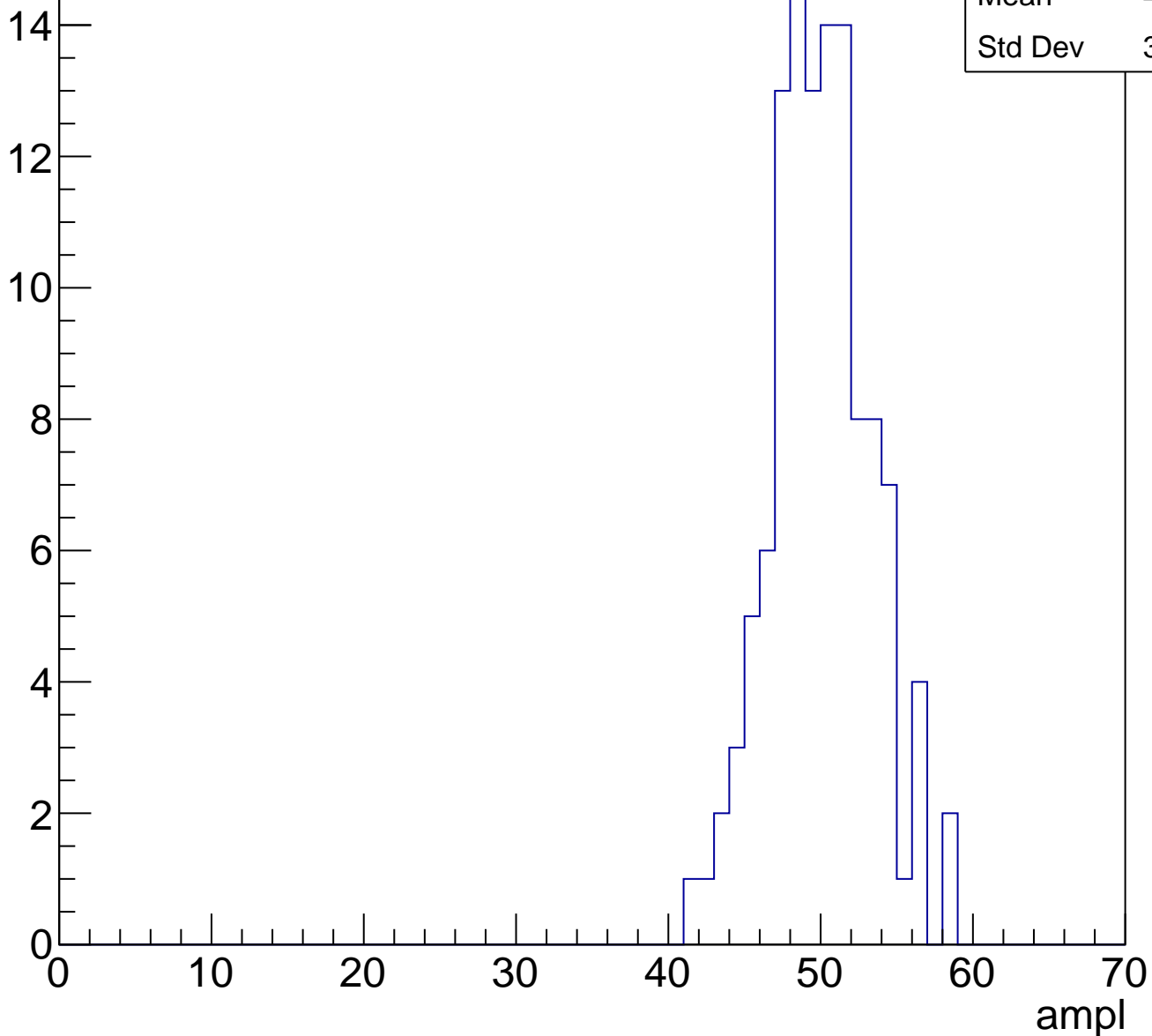


B1L001S, U20-ch21, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	49.55
Std Dev	3.288

Entry



B1L001S, U20-ch21, adc4

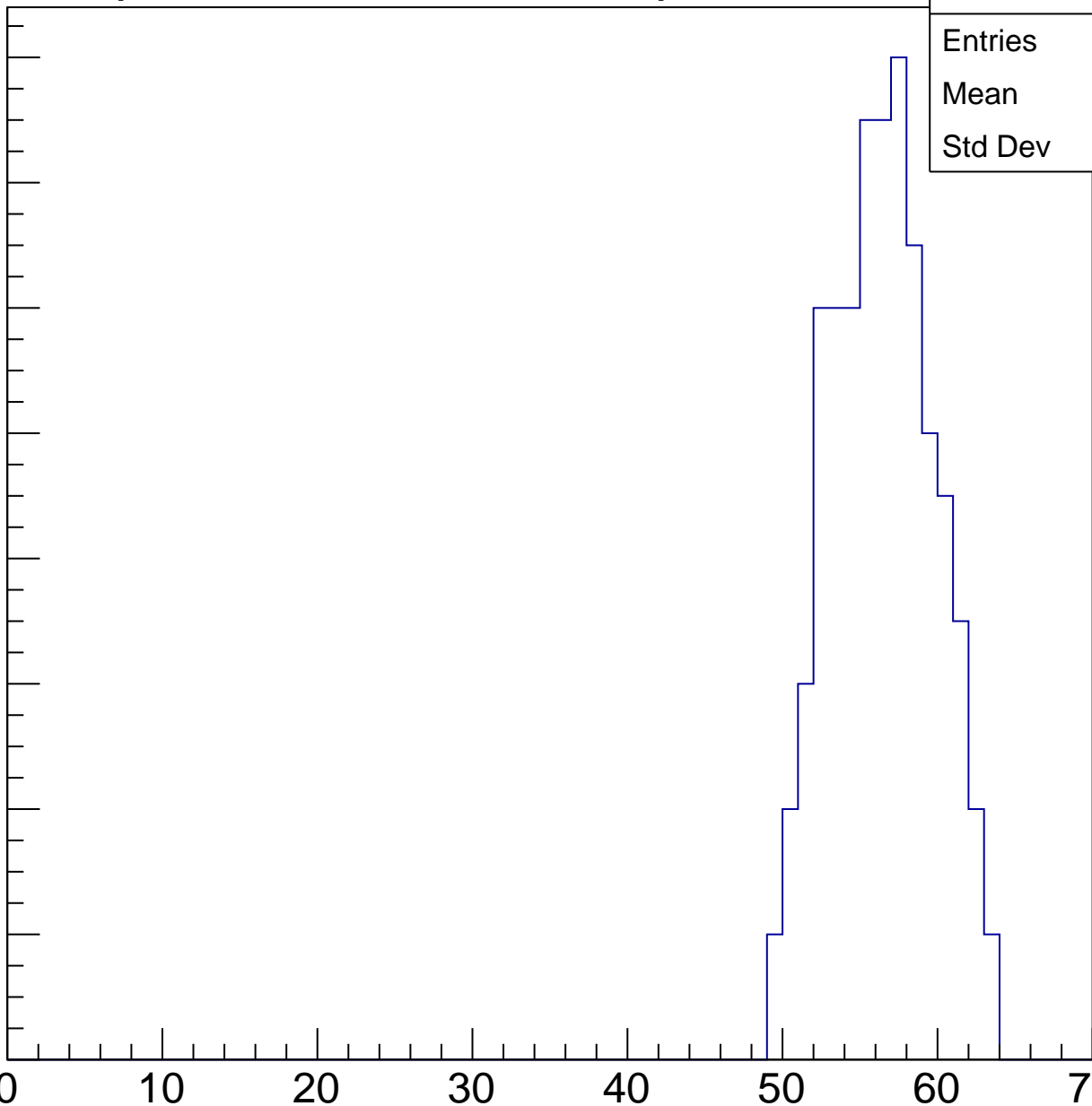
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	139
Mean	55.93
Std Dev	3.256

ampl



B1L001S, U20-ch21, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	67
Mean	59.09
Std Dev	7.627

0

2

4

6

8

10

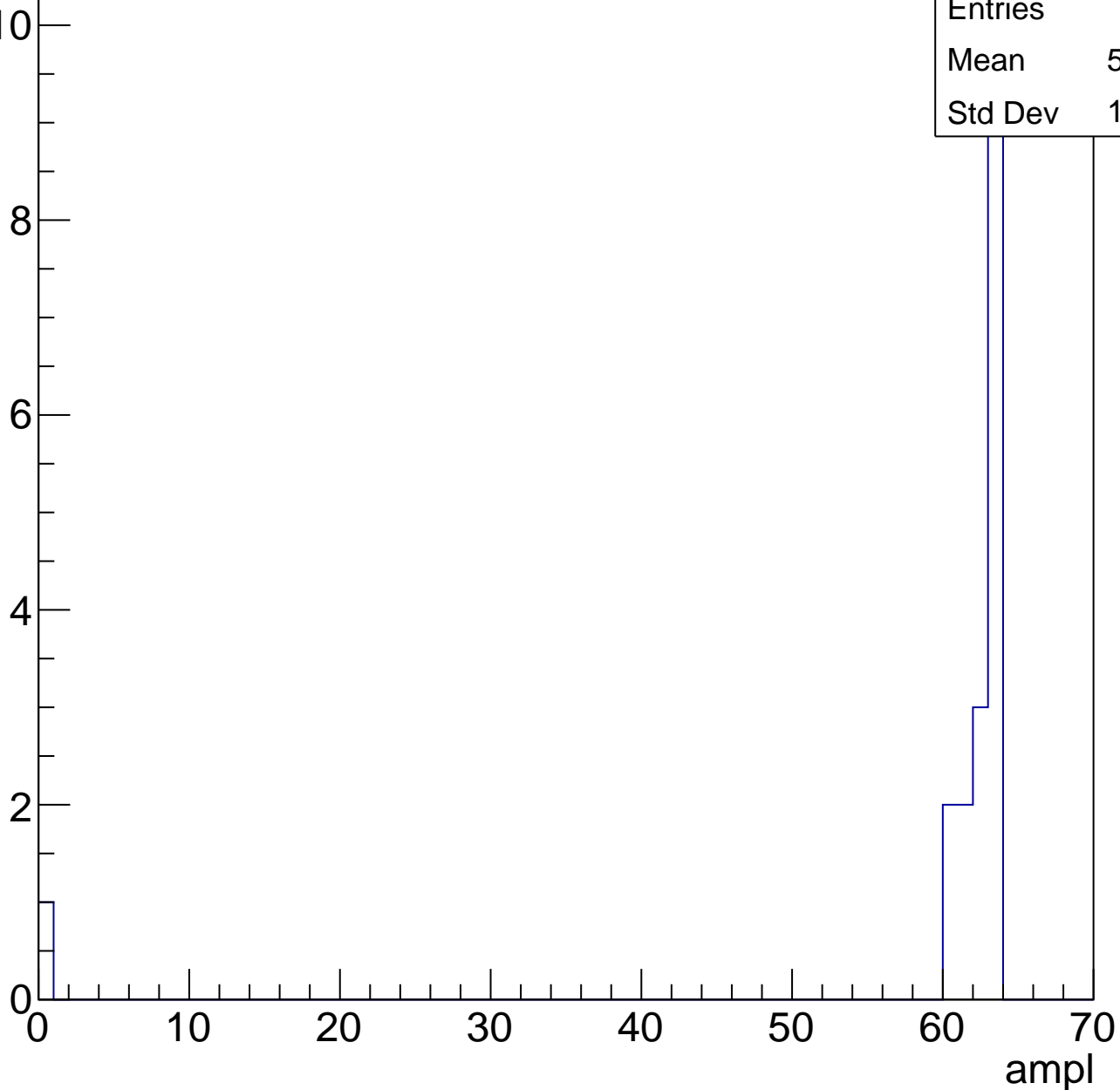
12

B1L001S, U20-ch21, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	58.78
Std Dev	14.29



B1L001S, U20-ch21, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	19
Std Dev	0

B1L001S, U20-ch22, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	31.01
Std Dev	3.02

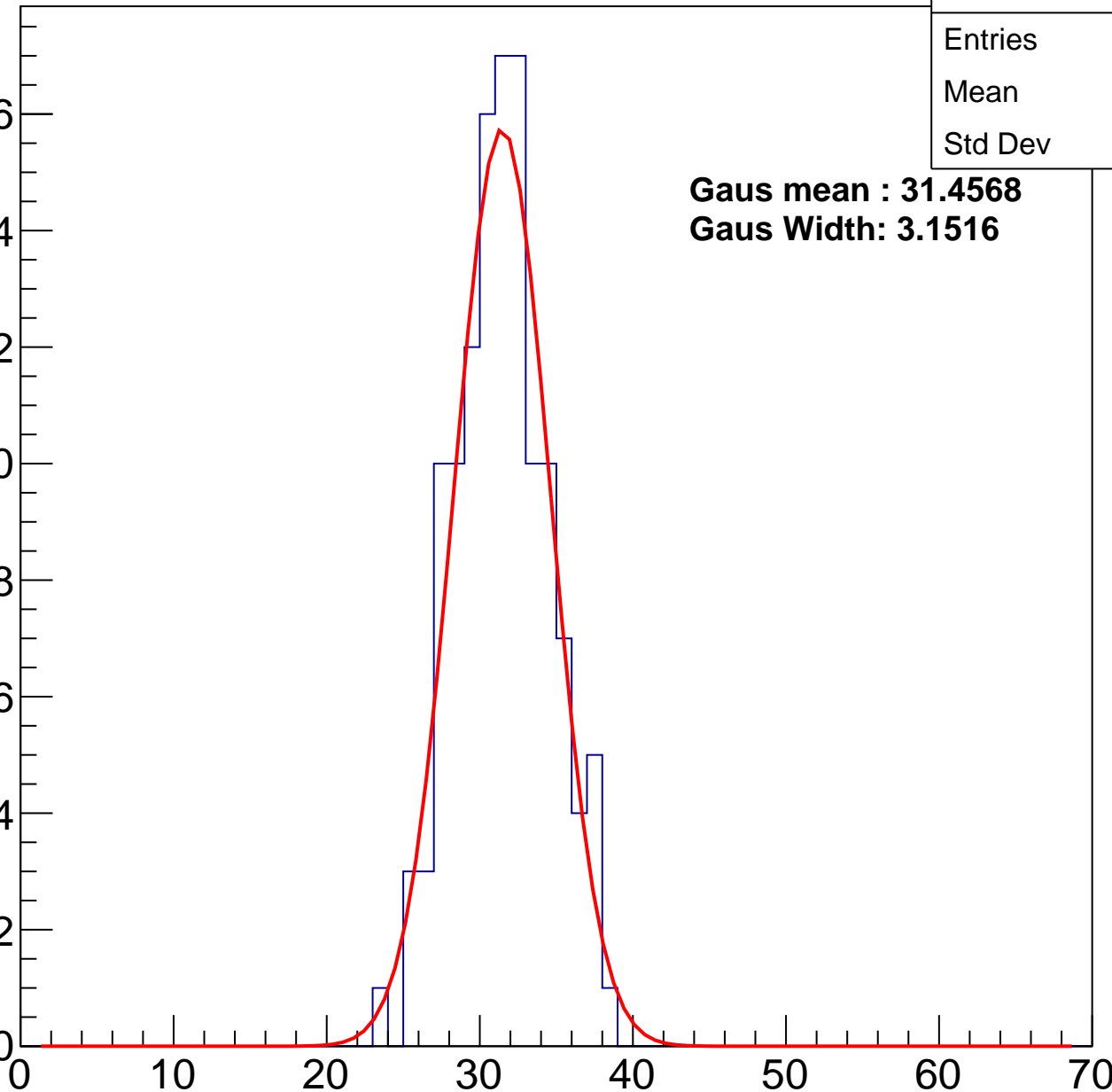
Gaus mean : 31.4568

Gaus Width: 3.1516

Entry

16
14
12
10
8
6
4
2
0

ampl

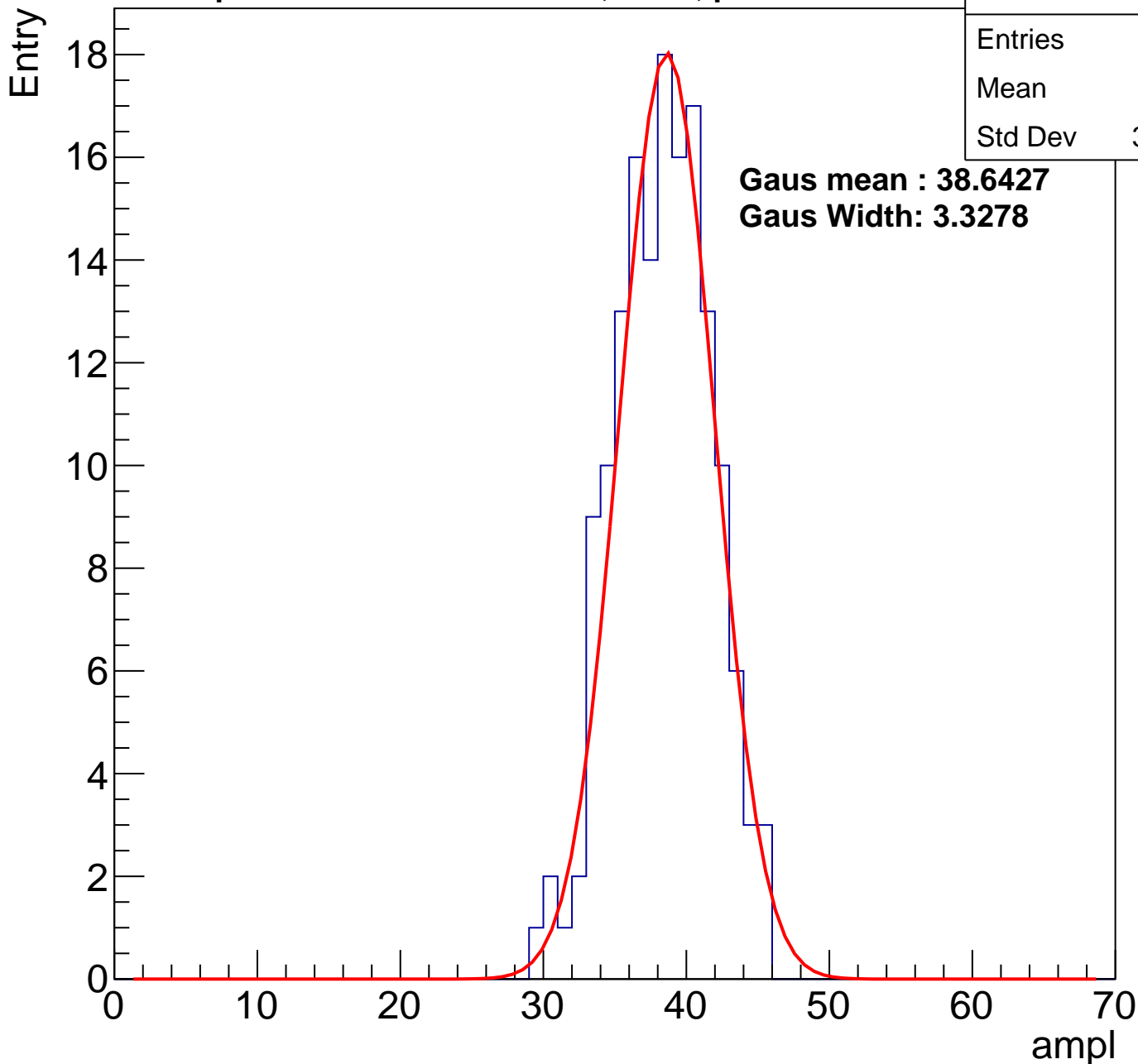


B1L001S, U20-ch22, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	37.9
Std Dev	3.297

Gaus mean : 38.6427
Gaus Width: 3.3278



B1L001S, U20-ch22, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	44.54
Std Dev	2.768

Gaus mean : 45.1599

Gaus Width: 2.8337

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

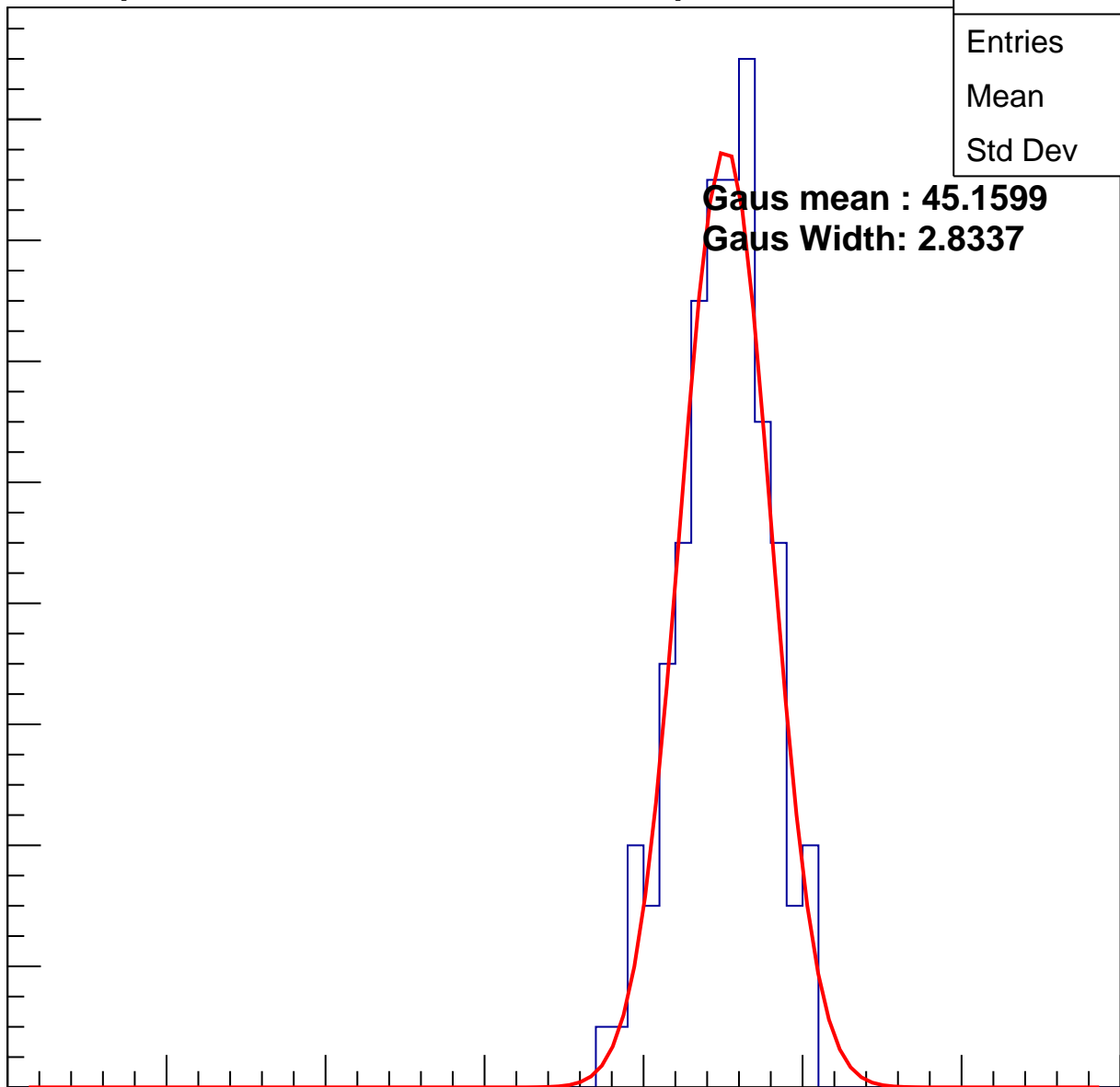
40

50

60

70

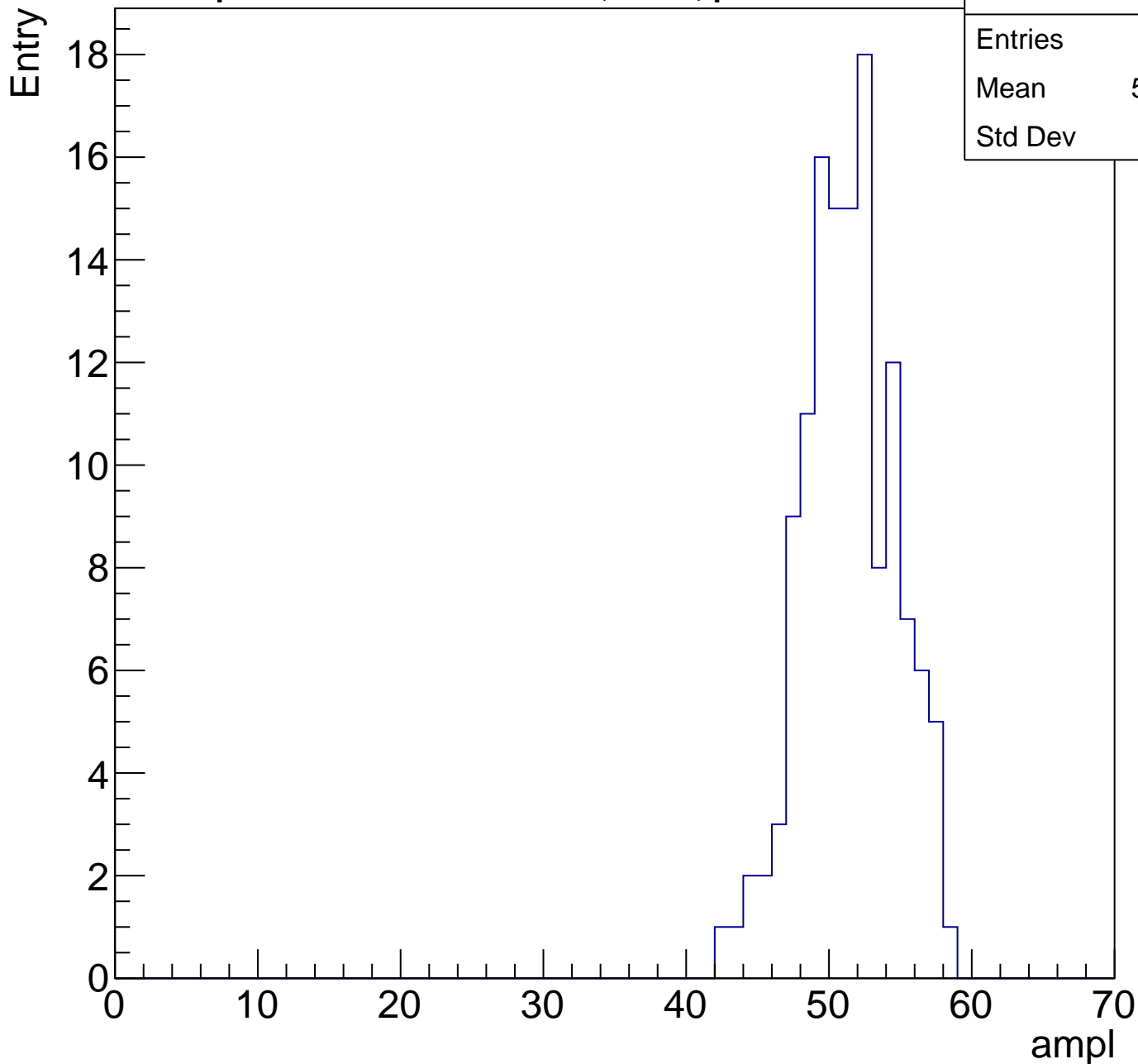
ampl



B1L001S, U20-ch22, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	50.93
Std Dev	3.21



B1L001S, U20-ch22, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	95
Mean	55.94
Std Dev	2.783

Entry

12

10

8

6

4

2

0

0

10

20

30

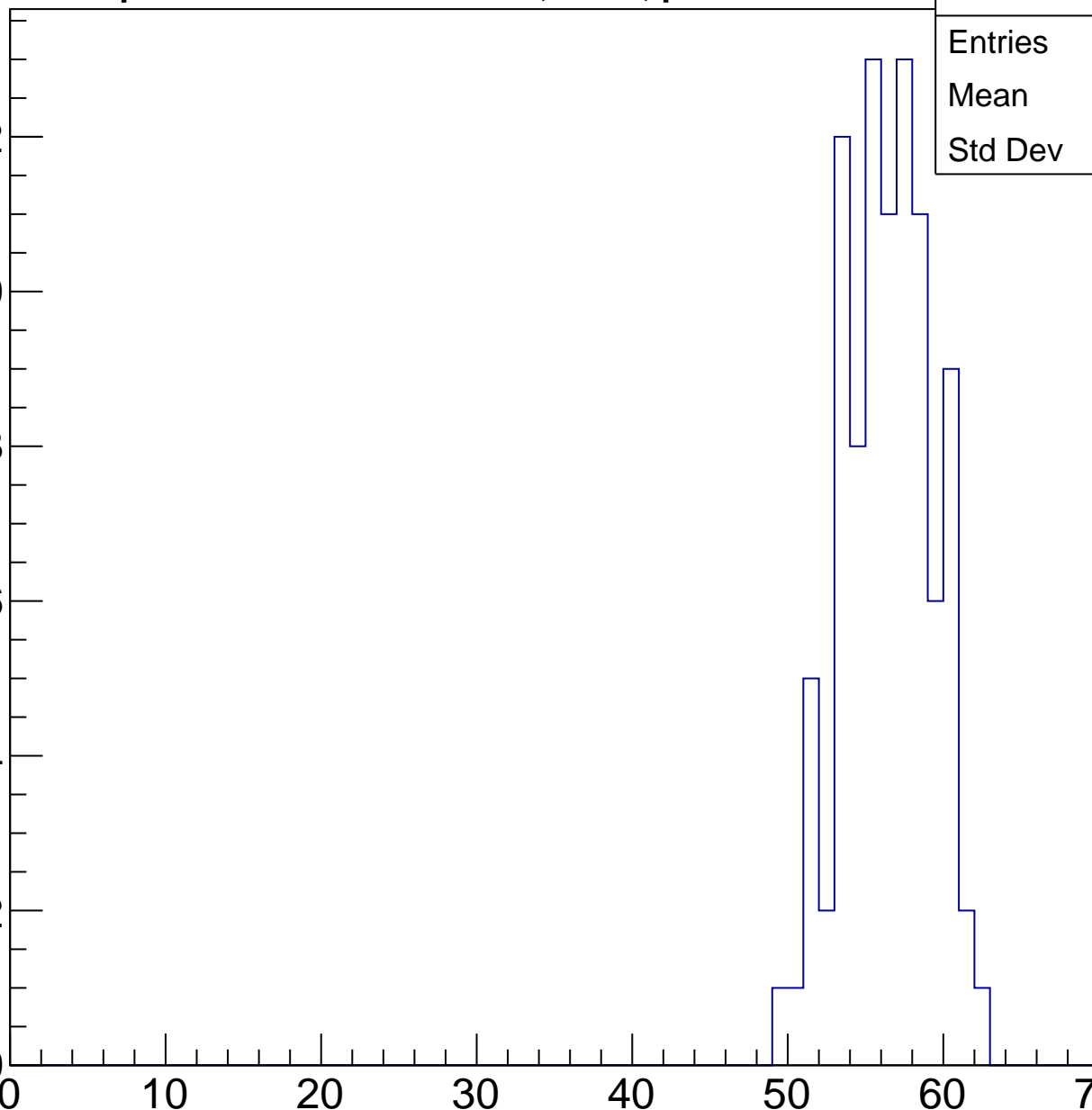
40

50

60

70

ampl



B1L001S, U20-ch22, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

88

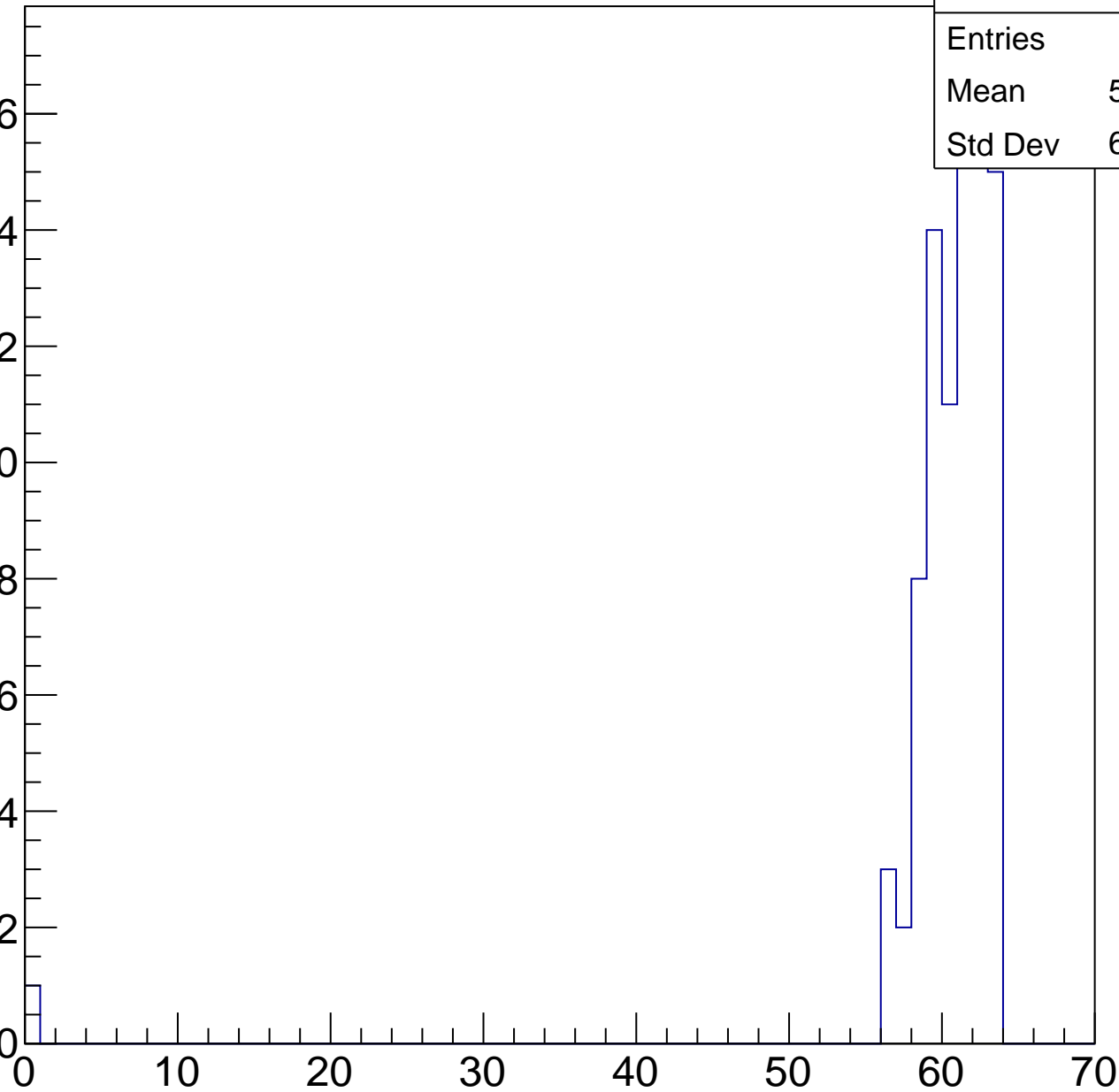
Mean

59.86

Std Dev

6.683

ampl

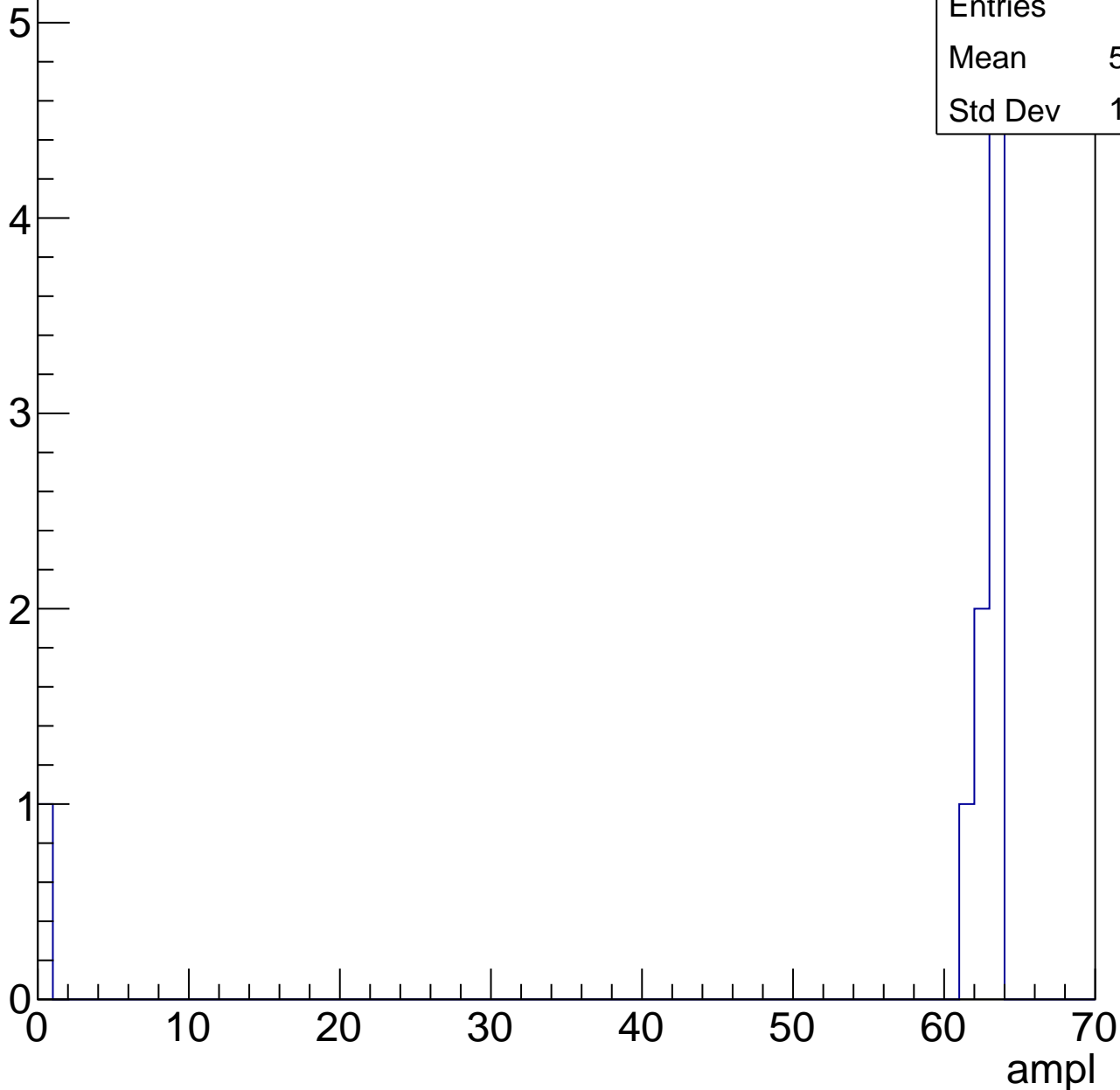


B1L001S, U20-ch22, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	9
Mean	55.56
Std Dev	19.65



B1L001S, U20-ch22, adc7

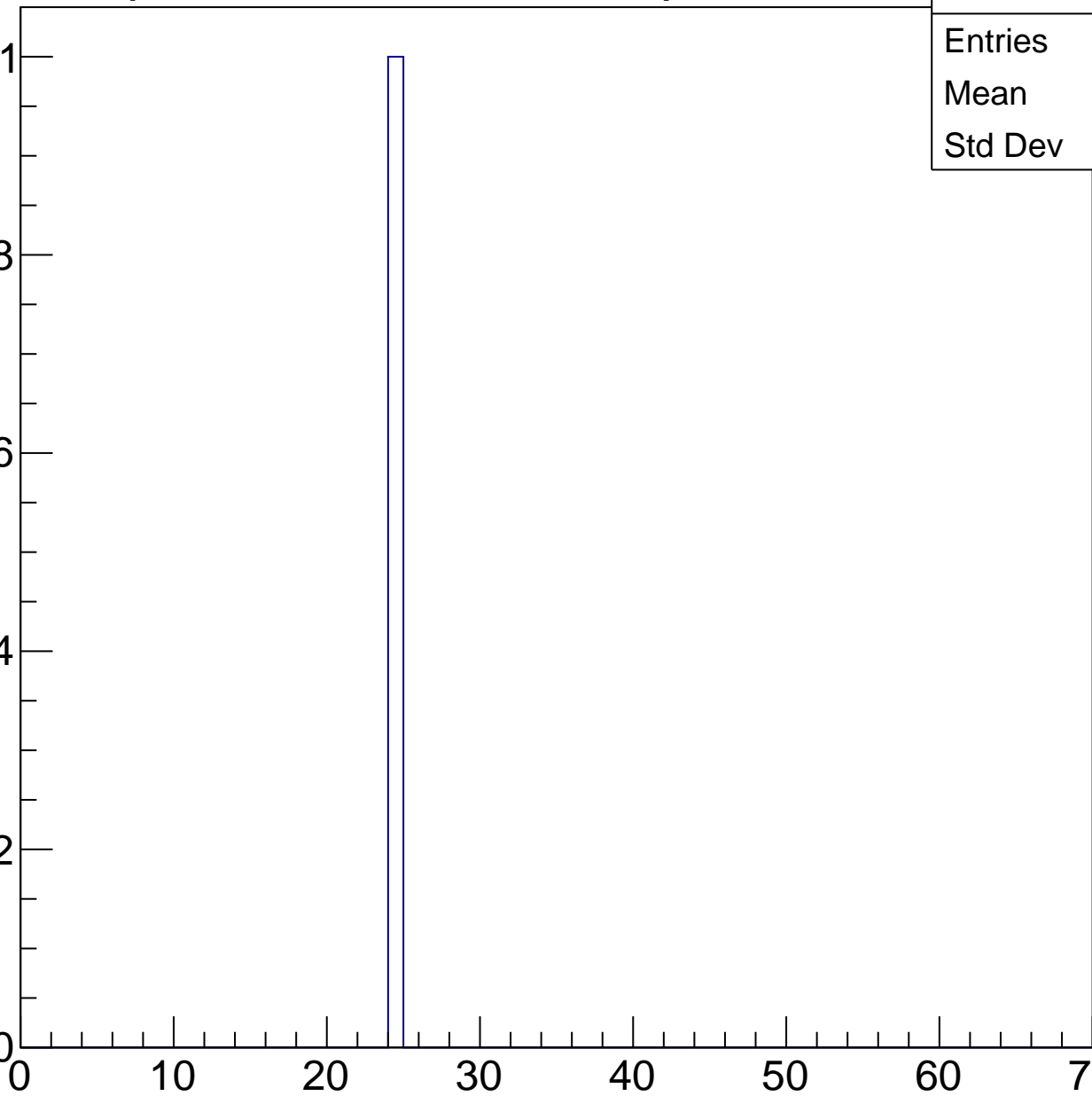
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L001S, U20-ch23, adc0

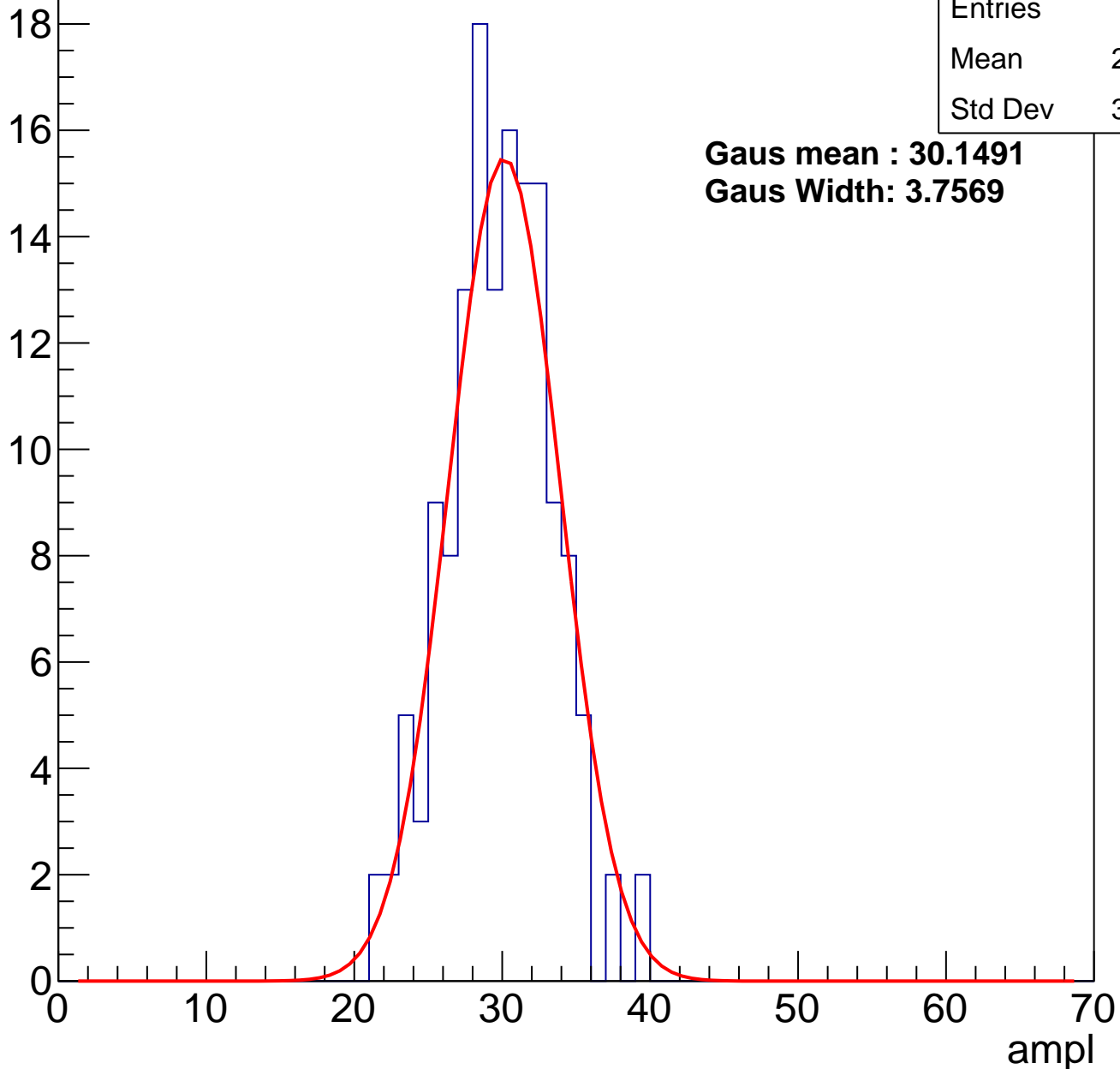
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	29.37
Std Dev	3.533

Gaus mean : 30.1491

Gaus Width: 3.7569

Entry



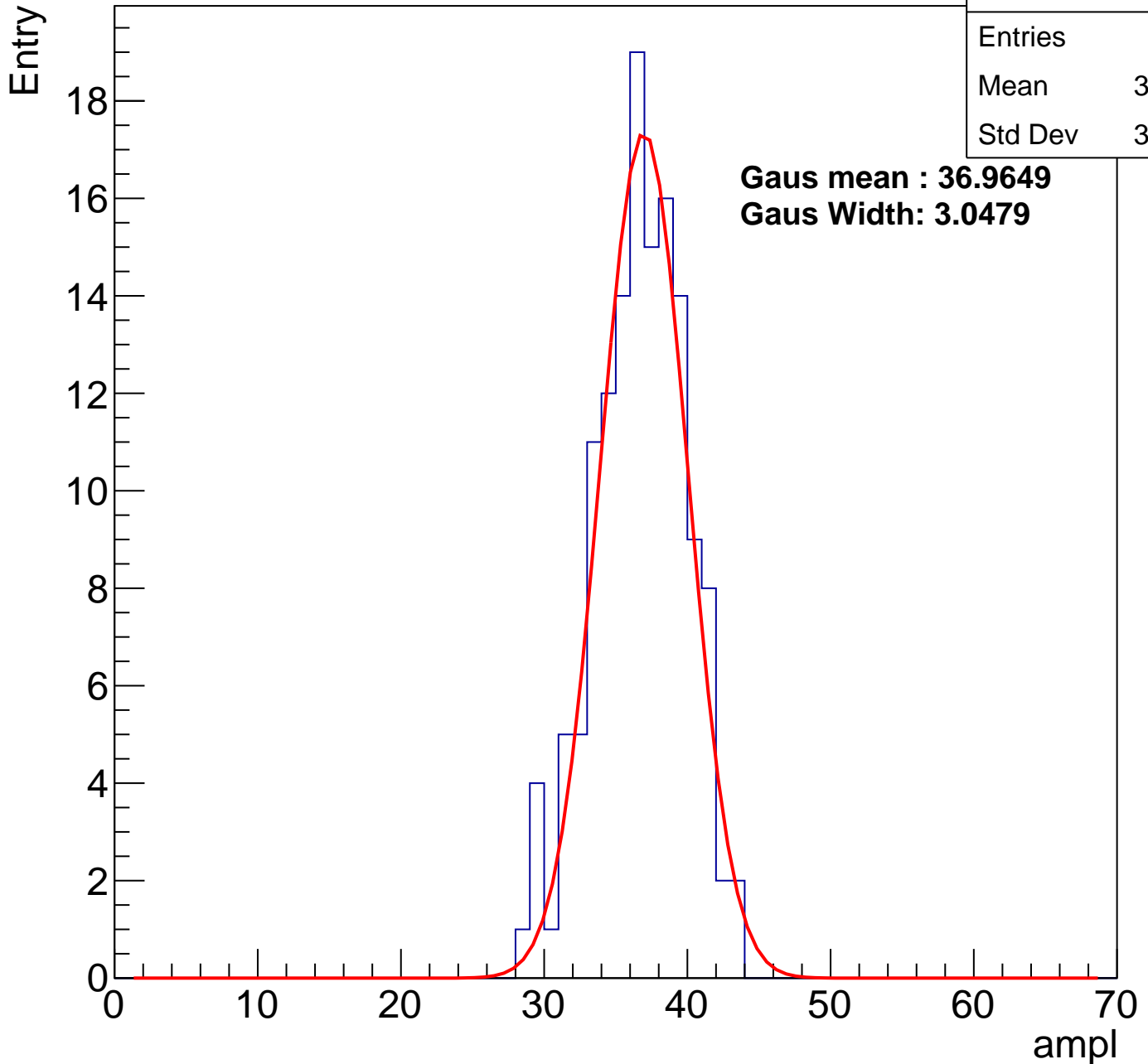
B1L001S, U20-ch23, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	36.24
Std Dev	3.154

Gaus mean : 36.9649

Gaus Width: 3.0479



B1L001S, U20-ch23, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	43.06
Std Dev	2.932

Gaus mean : 43.5857

Gaus Width: 2.8736

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

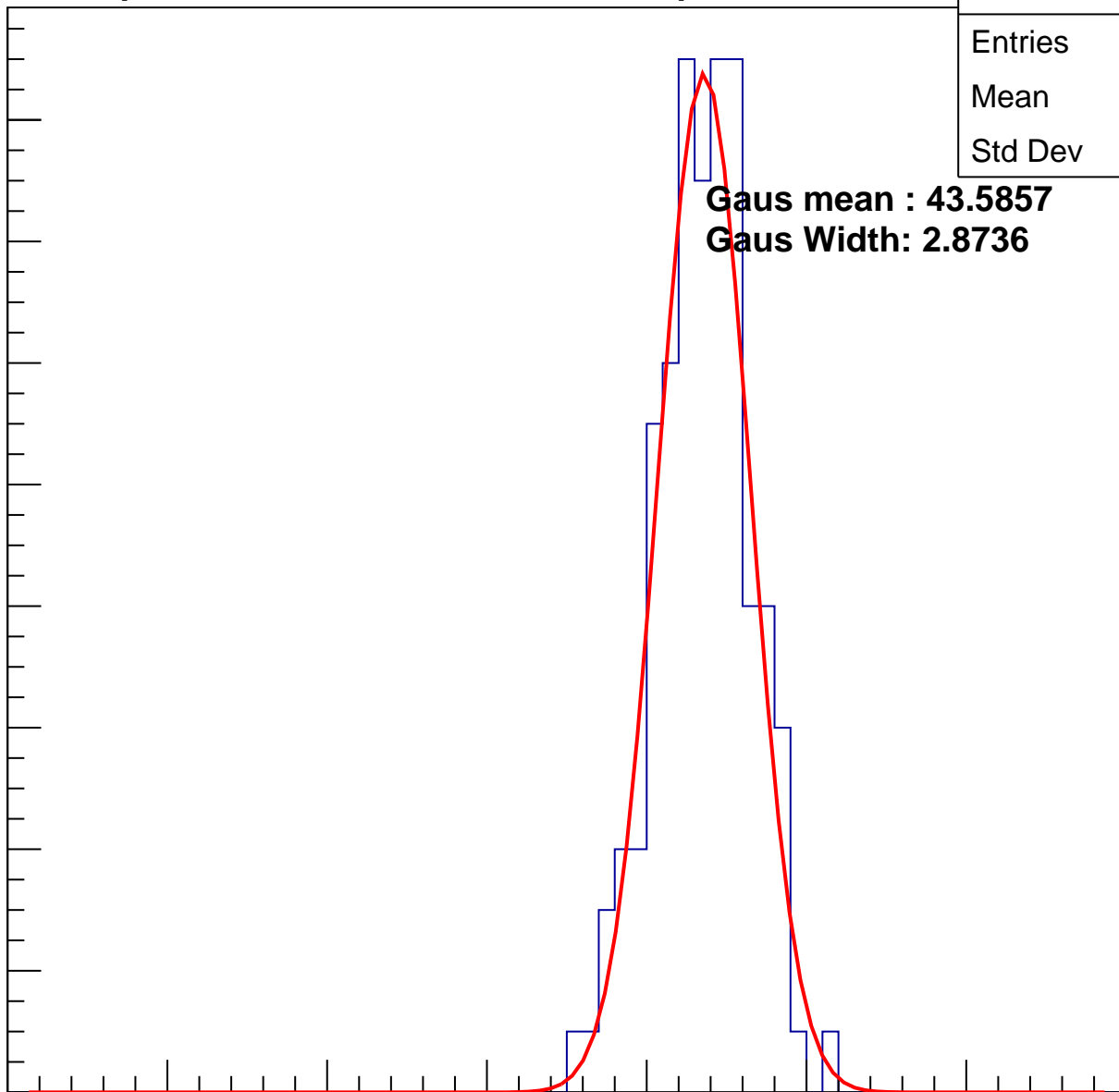
40

50

60

70

ampl

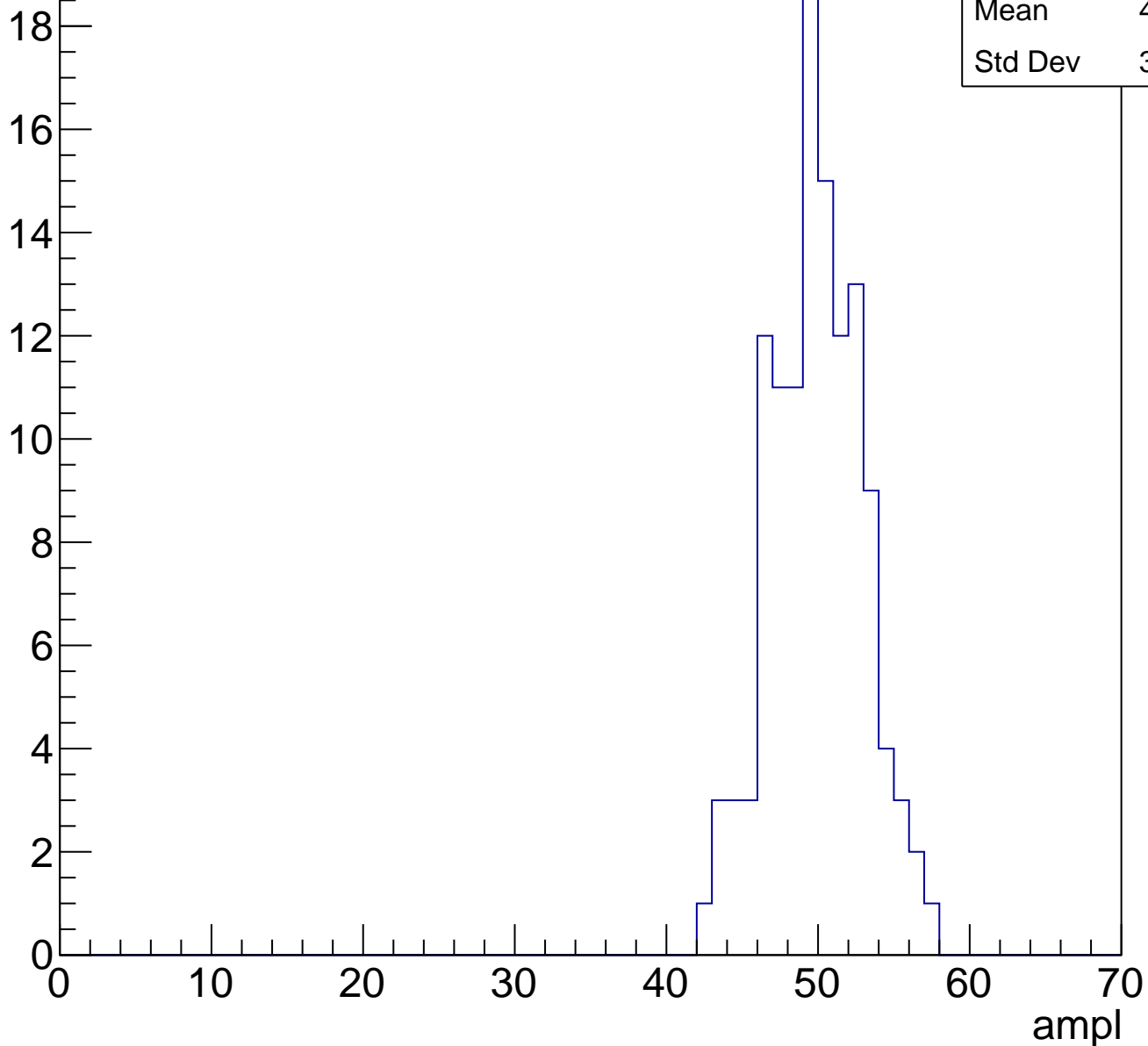


B1L001S, U20-ch23, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	49.43
Std Dev	3.022

Entry

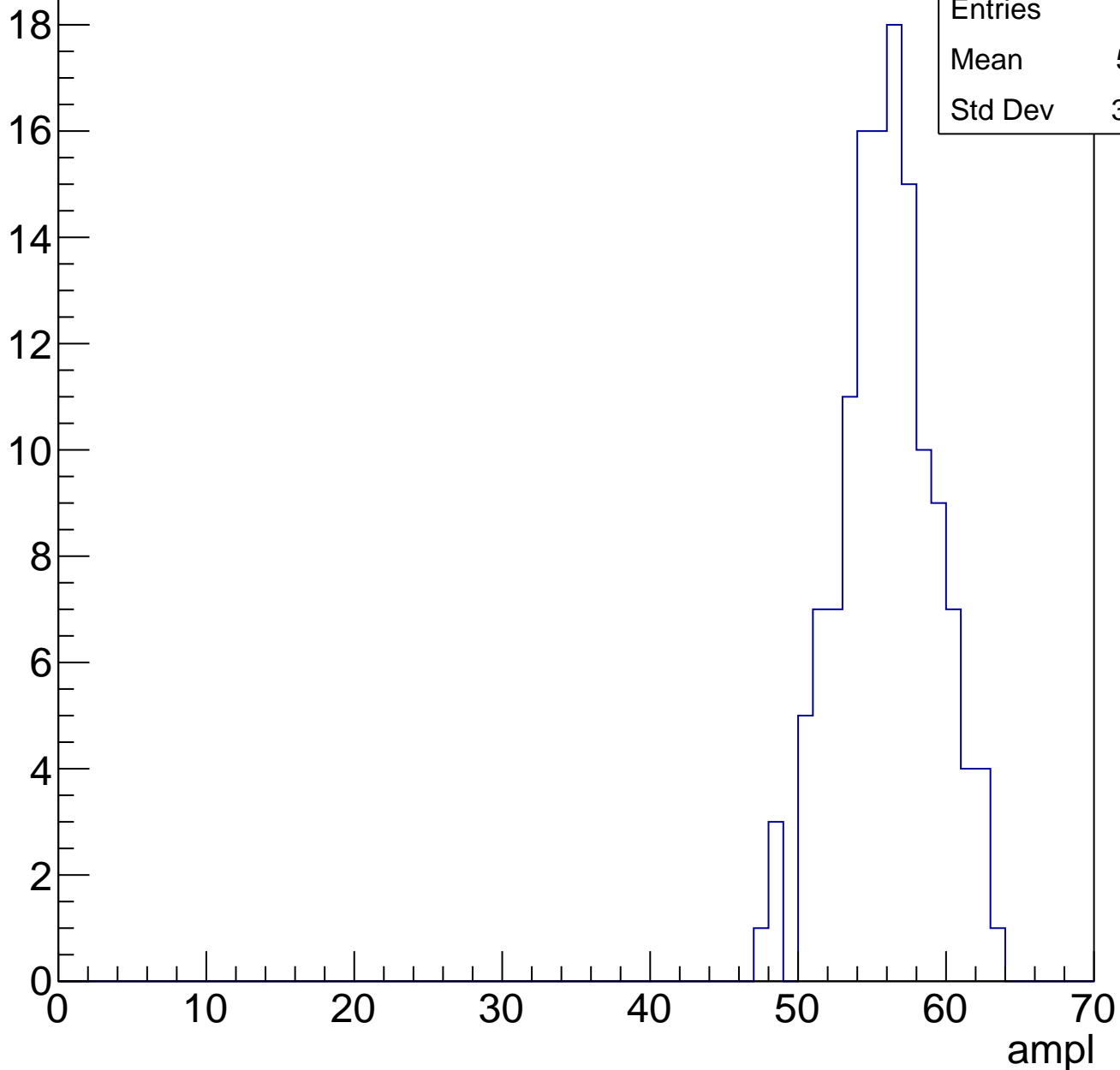


B1L001S, U20-ch23, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	55.51
Std Dev	3.264

Entry



B1L001S, U20-ch23, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	84
Mean	59.82
Std Dev	6.833

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

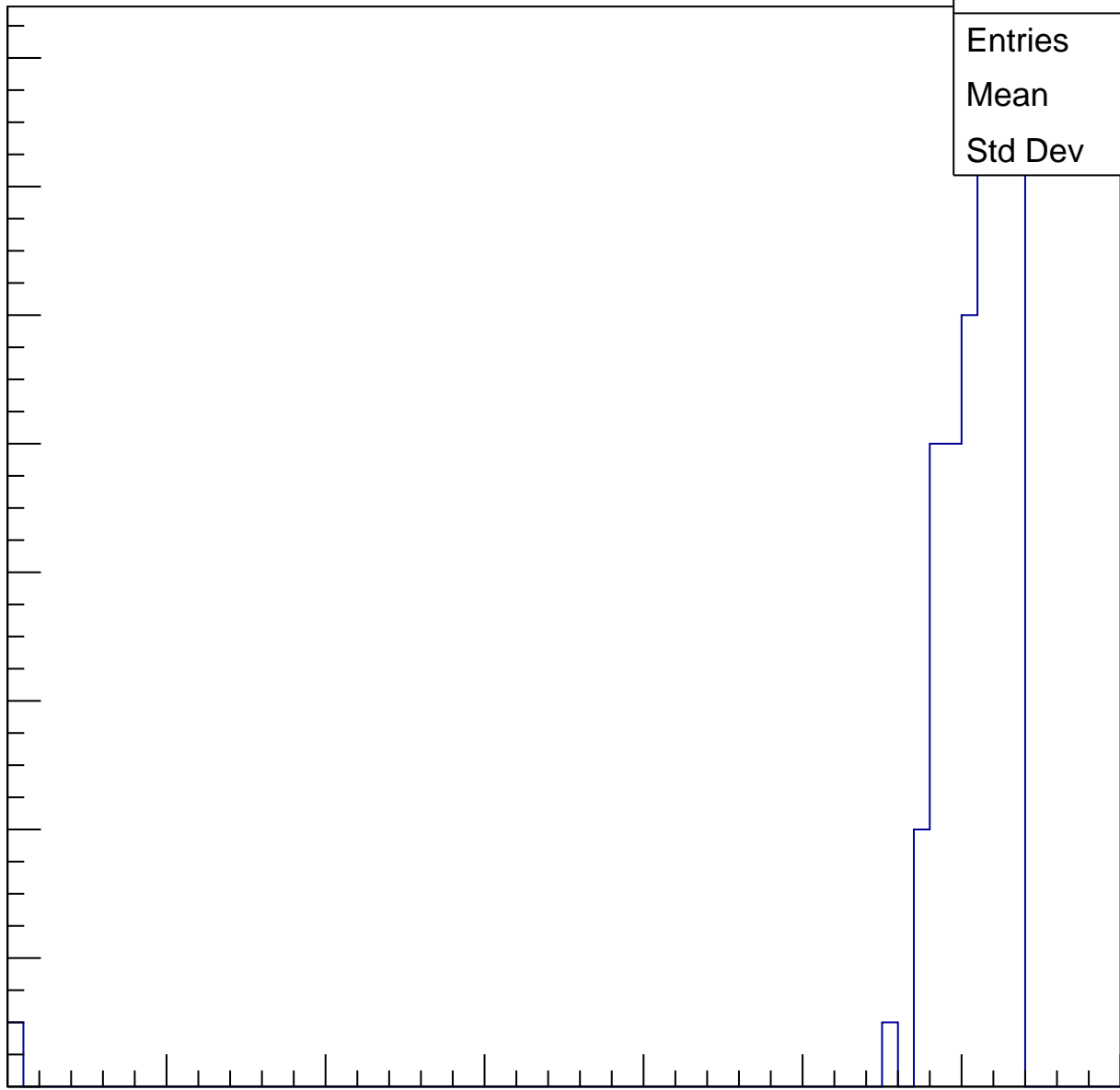
40

50

60

70

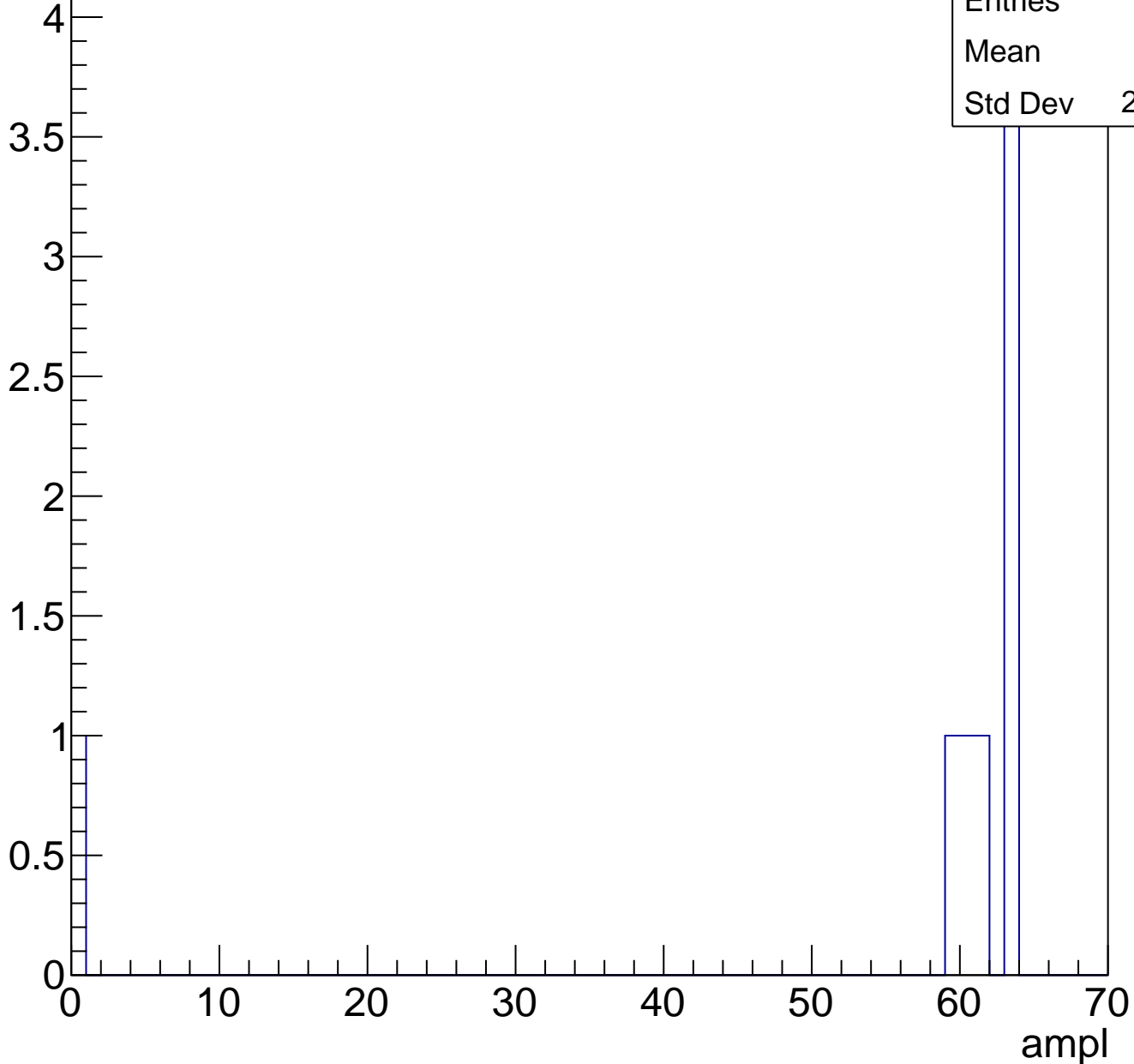
ampl



B1L001S, U20-ch23, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch23, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch24, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	30.01
Std Dev	4.236

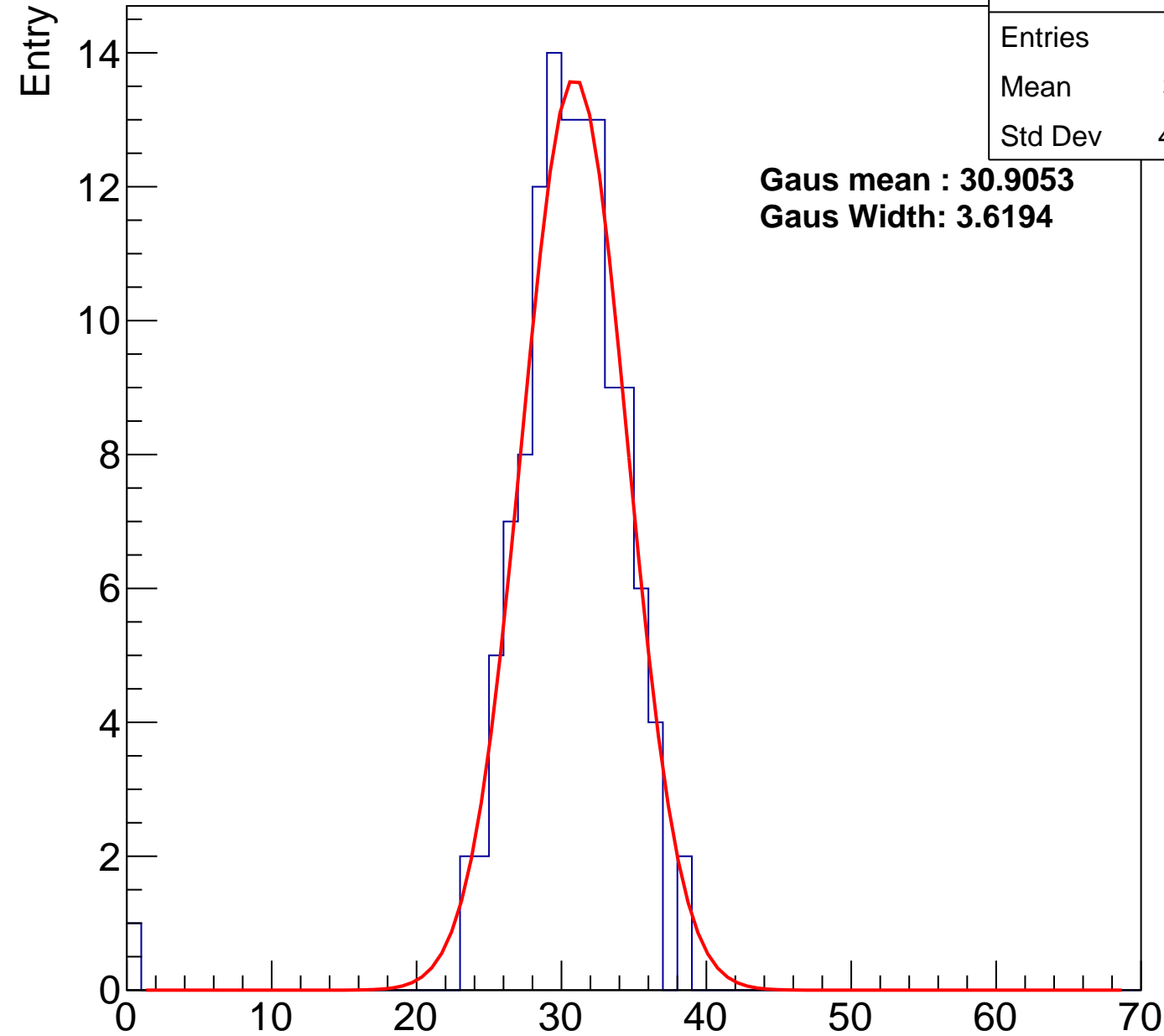
Gaus mean : 30.9053

Gaus Width: 3.6194

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch24, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	36.28
Std Dev	3.222

Gaus mean : 36.7538

Gaus Width: 3.2971

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

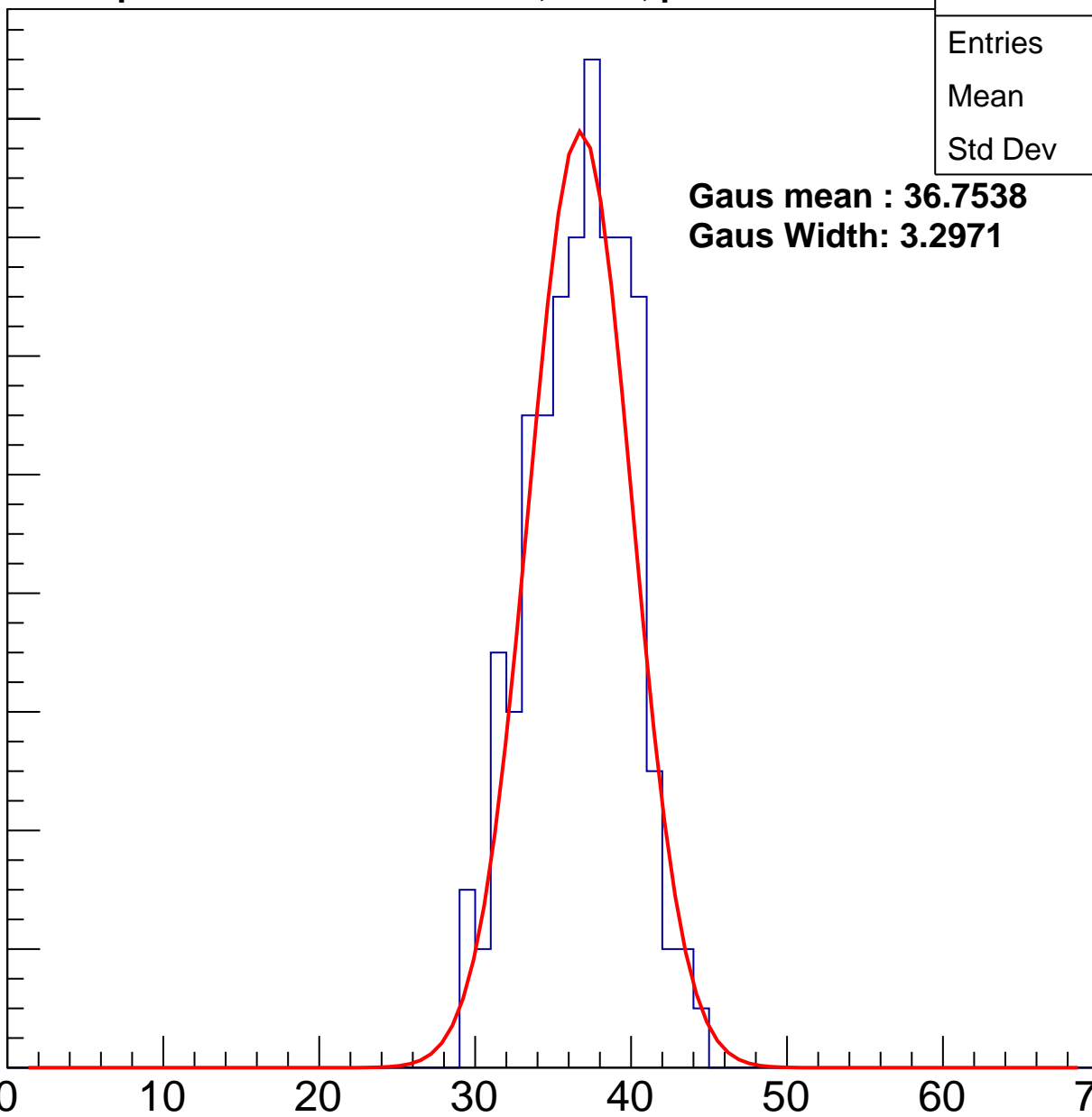
40

50

60

70

ampl



B1L001S, U20-ch24, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

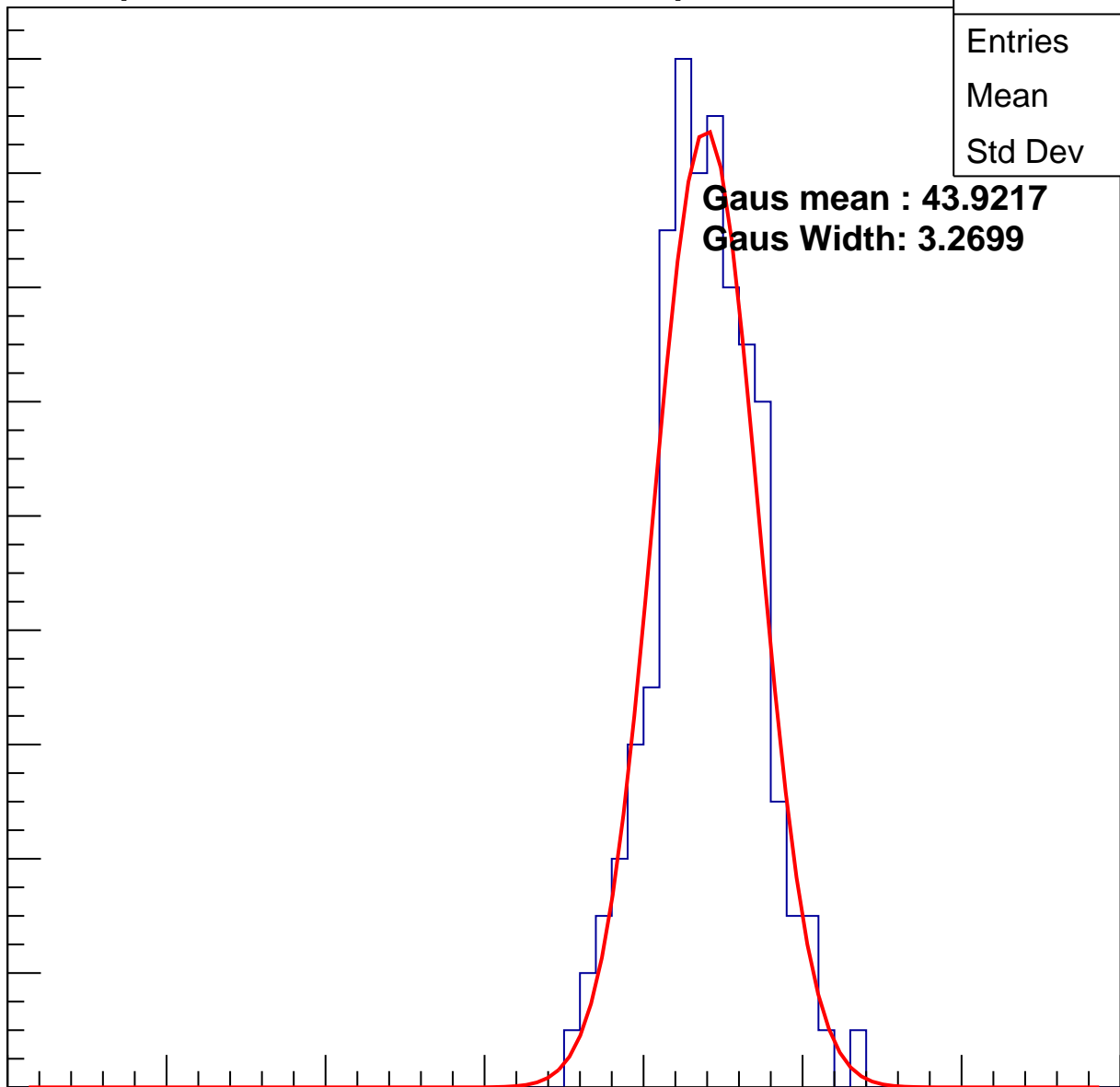
Entries	141
Mean	43.43
Std Dev	3.269

Gaus mean : 43.9217

Gaus Width: 3.2699

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch24, adc3

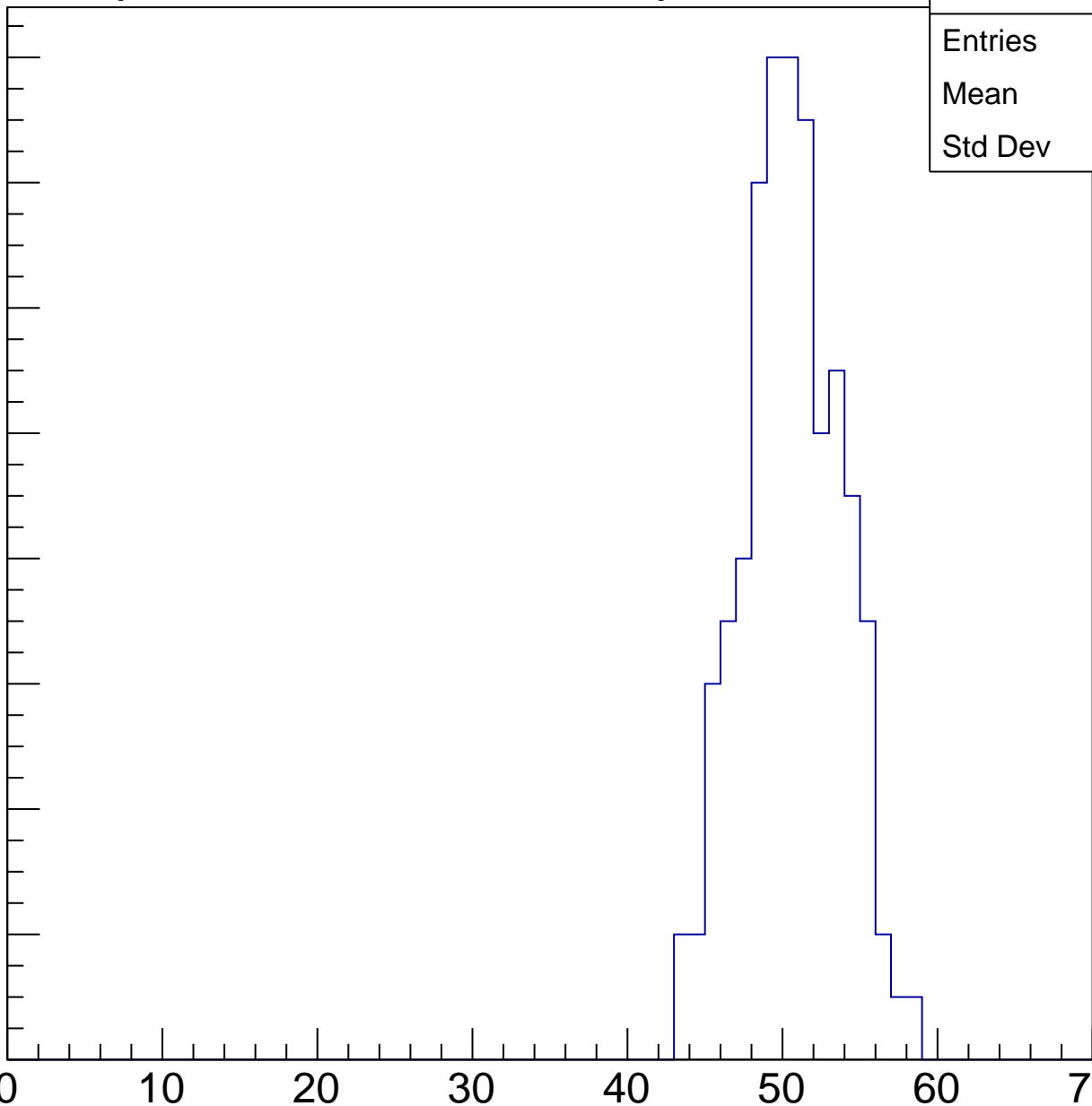
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	50.11
Std Dev	3.115

ampl



B1L001S, U20-ch24, adc4

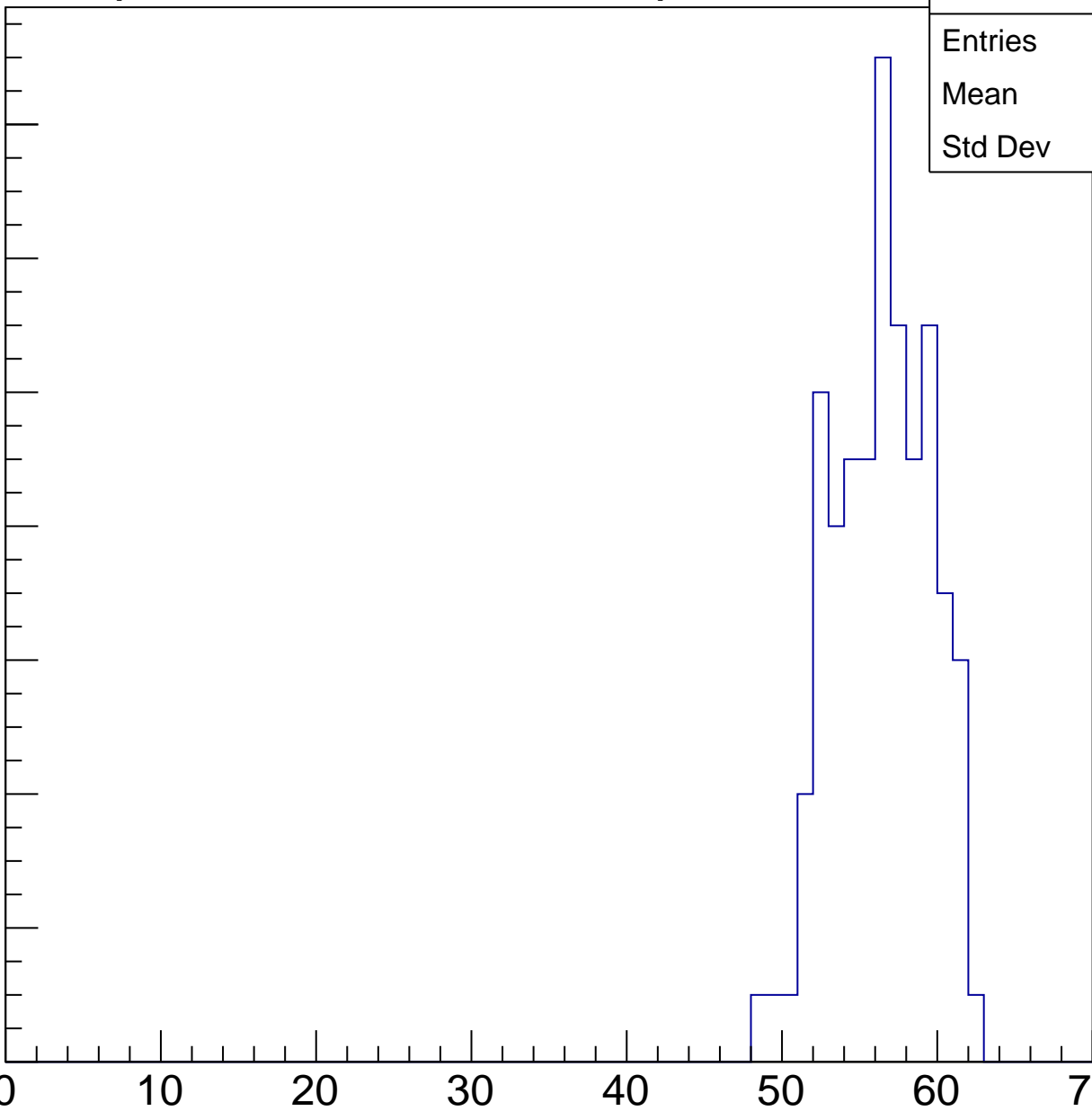
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	103
Mean	55.94
Std Dev	3.068

ampl



B1L001S, U20-ch24, adc5

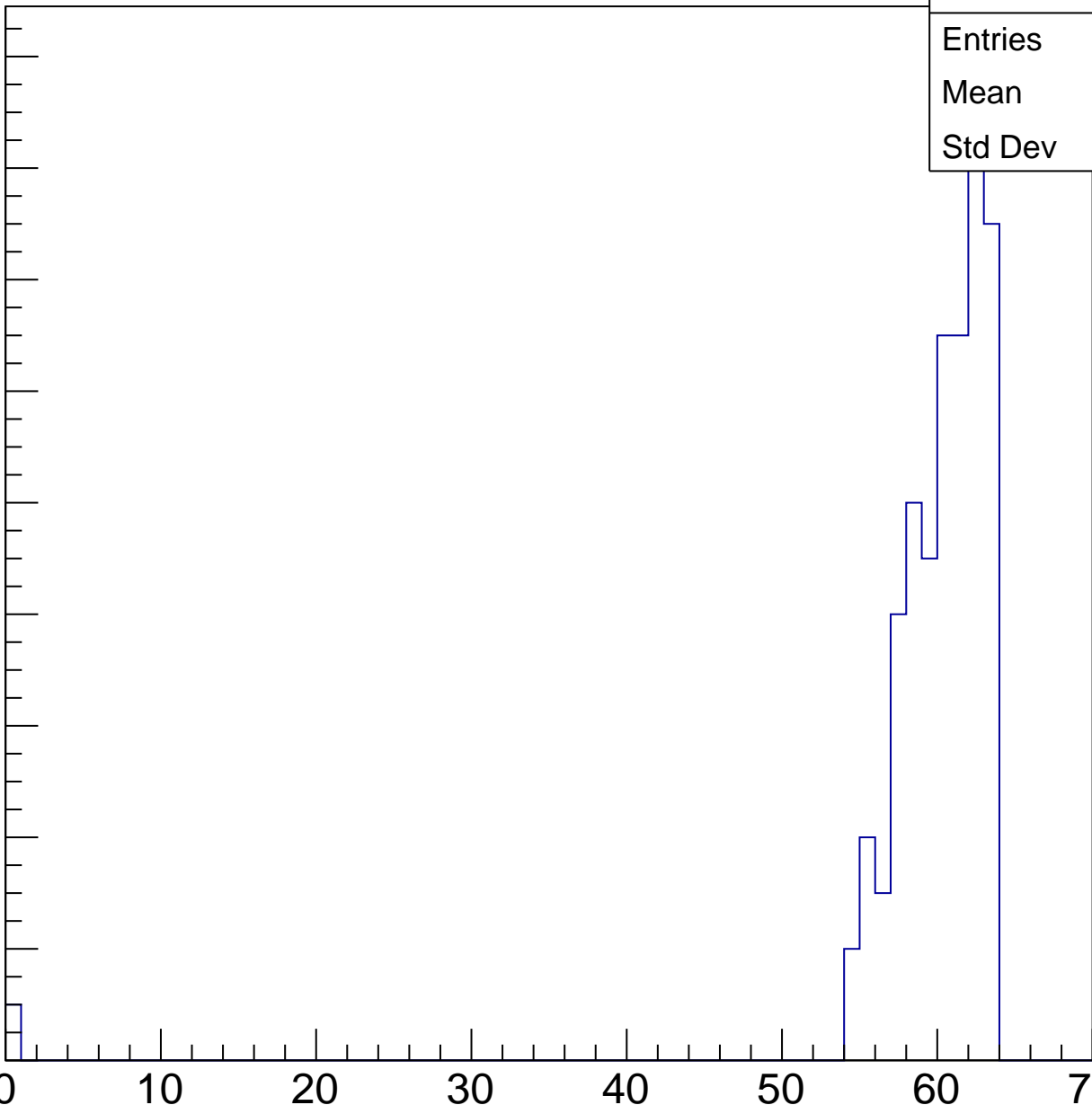
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.34
Std Dev	6.551

ampl

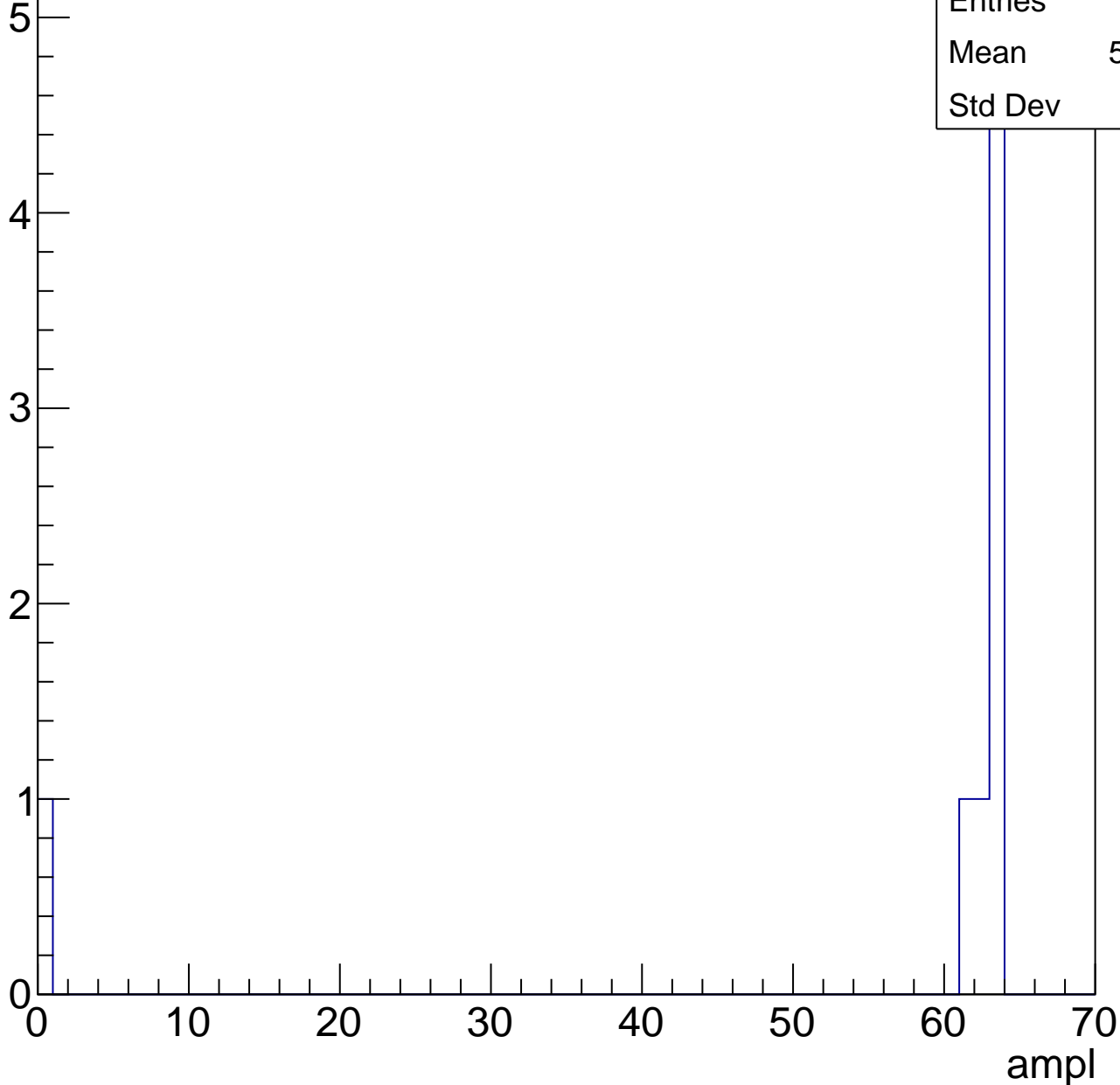


B1L001S, U20-ch24, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	54.75
Std Dev	20.7



B1L001S, U20-ch24, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U20-ch25, adc0

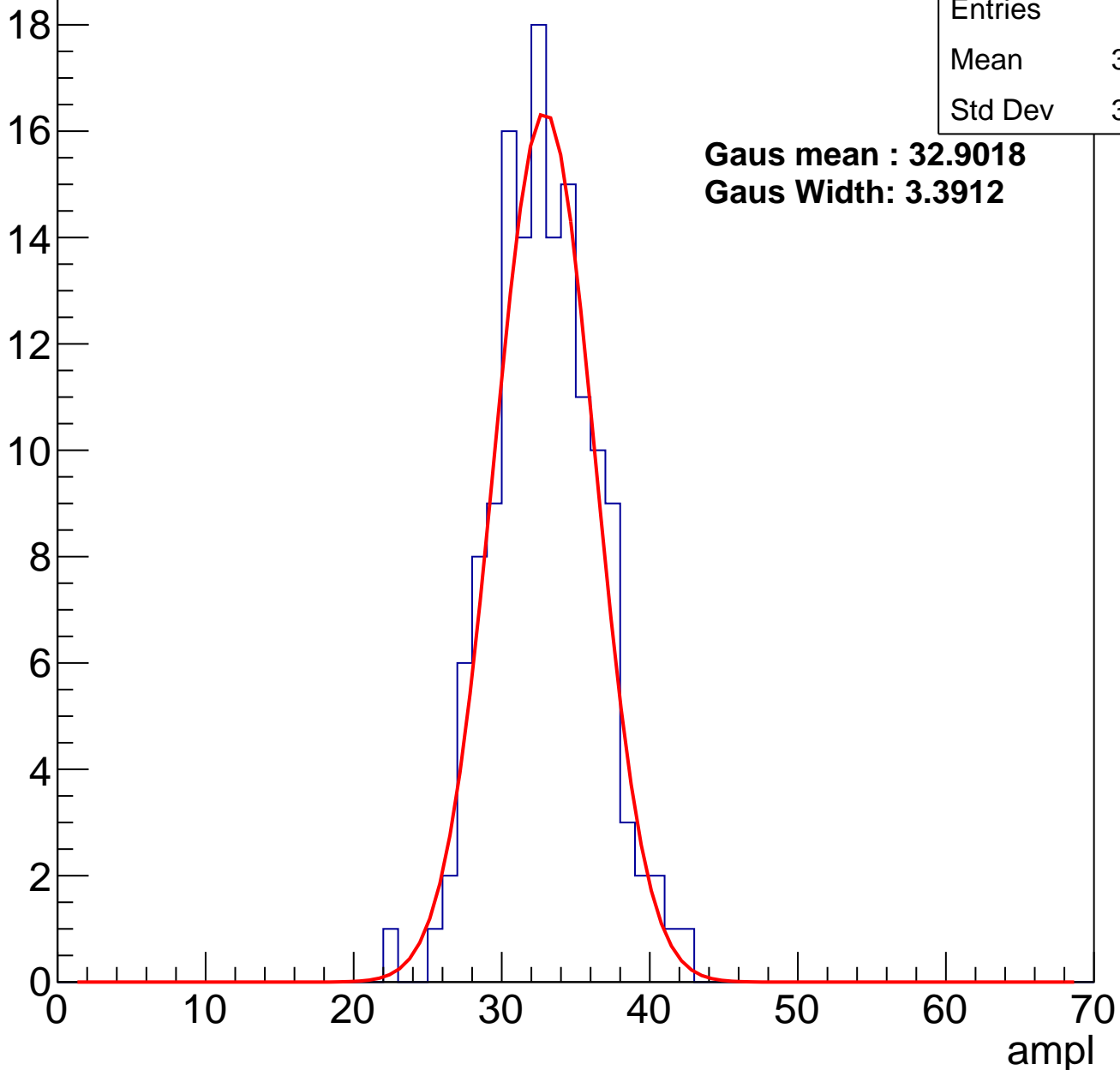
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	32.45
Std Dev	3.449

Gaus mean : 32.9018

Gaus Width: 3.3912

Entry



B1L001S, U20-ch25, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	38.5
Std Dev	3.074

Gaus mean : 38.7813

Gaus Width: 3.2395

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

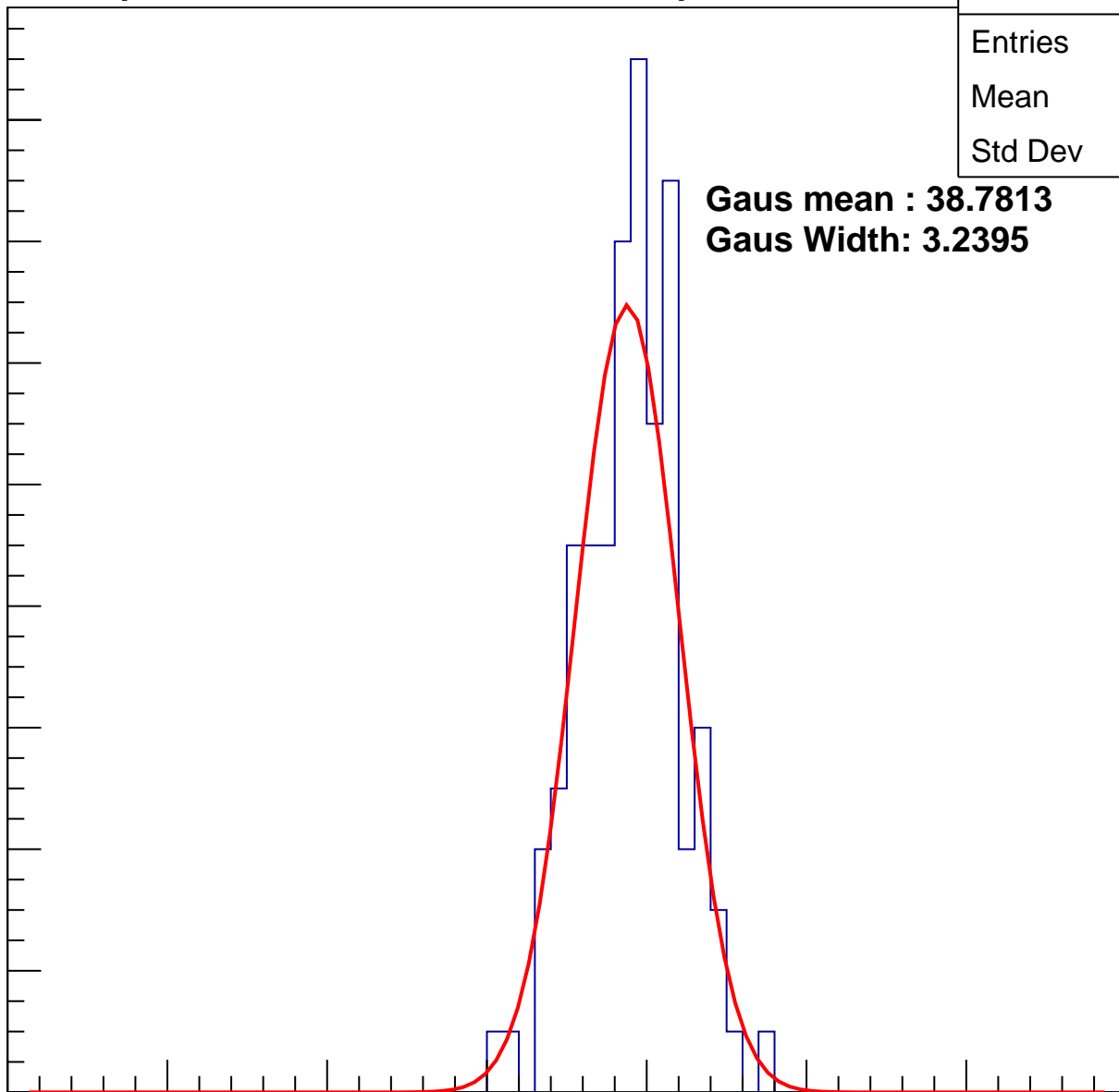
40

50

60

70

ampl



B1L001S, U20-ch25, adc2

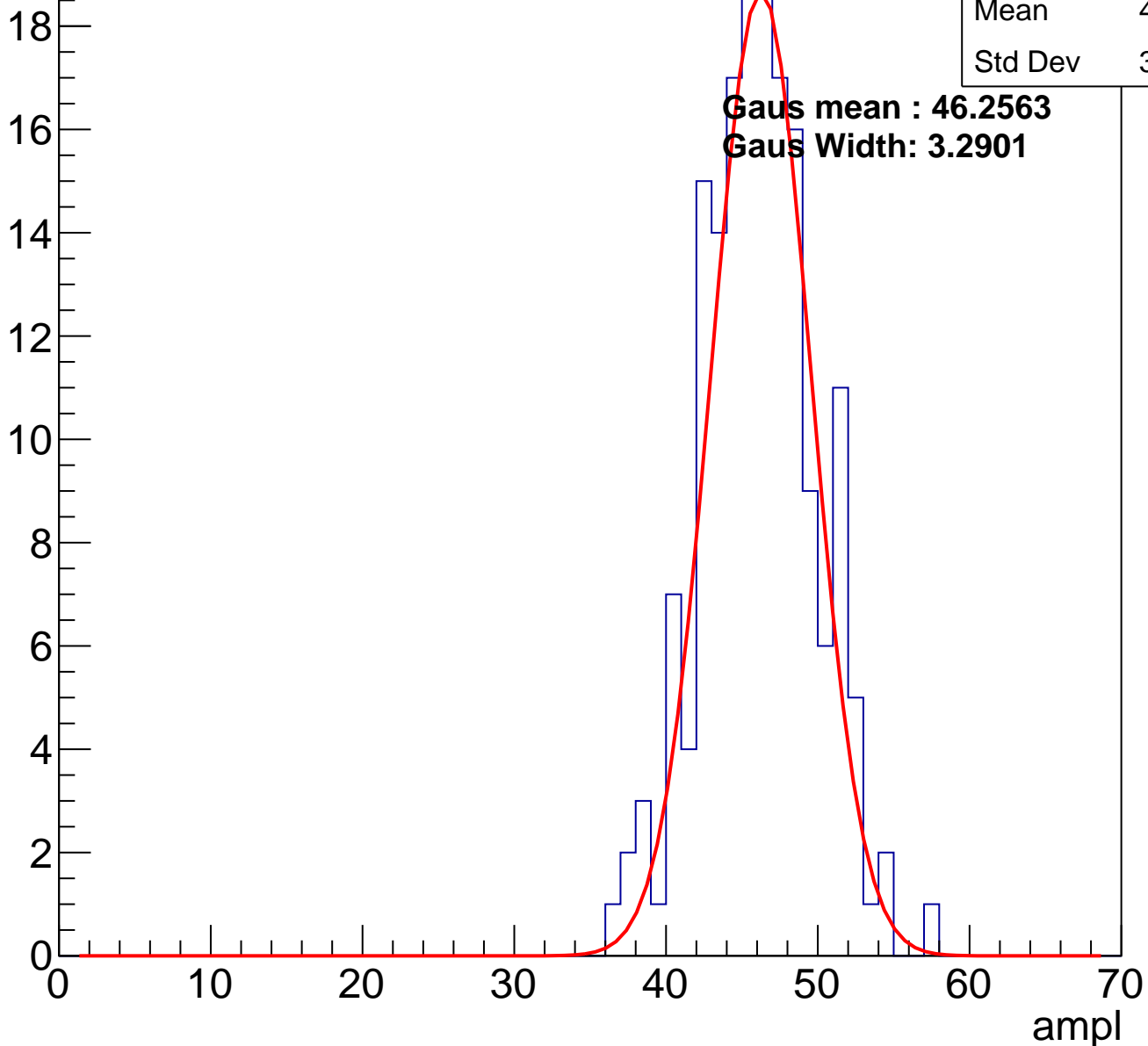
calib_packv5_042523_0143.root, FC#2, port C2

Entries	170
Mean	45.66
Std Dev	3.678

Gaus mean : 46.2563

Gaus Width: 3.2901

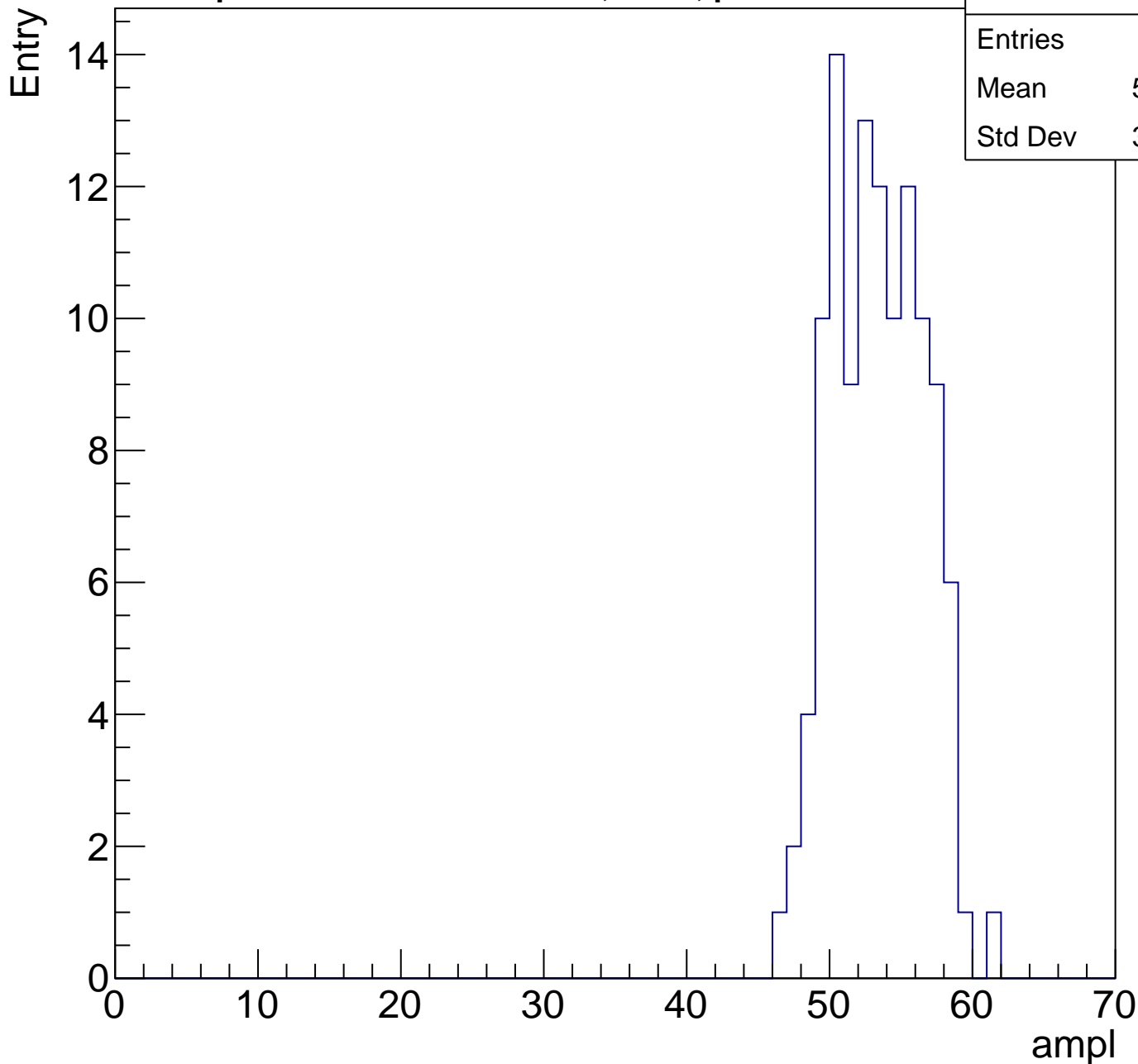
Entry



B1L001S, U20-ch25, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	52.93
Std Dev	3.103



B1L001S, U20-ch25, adc4

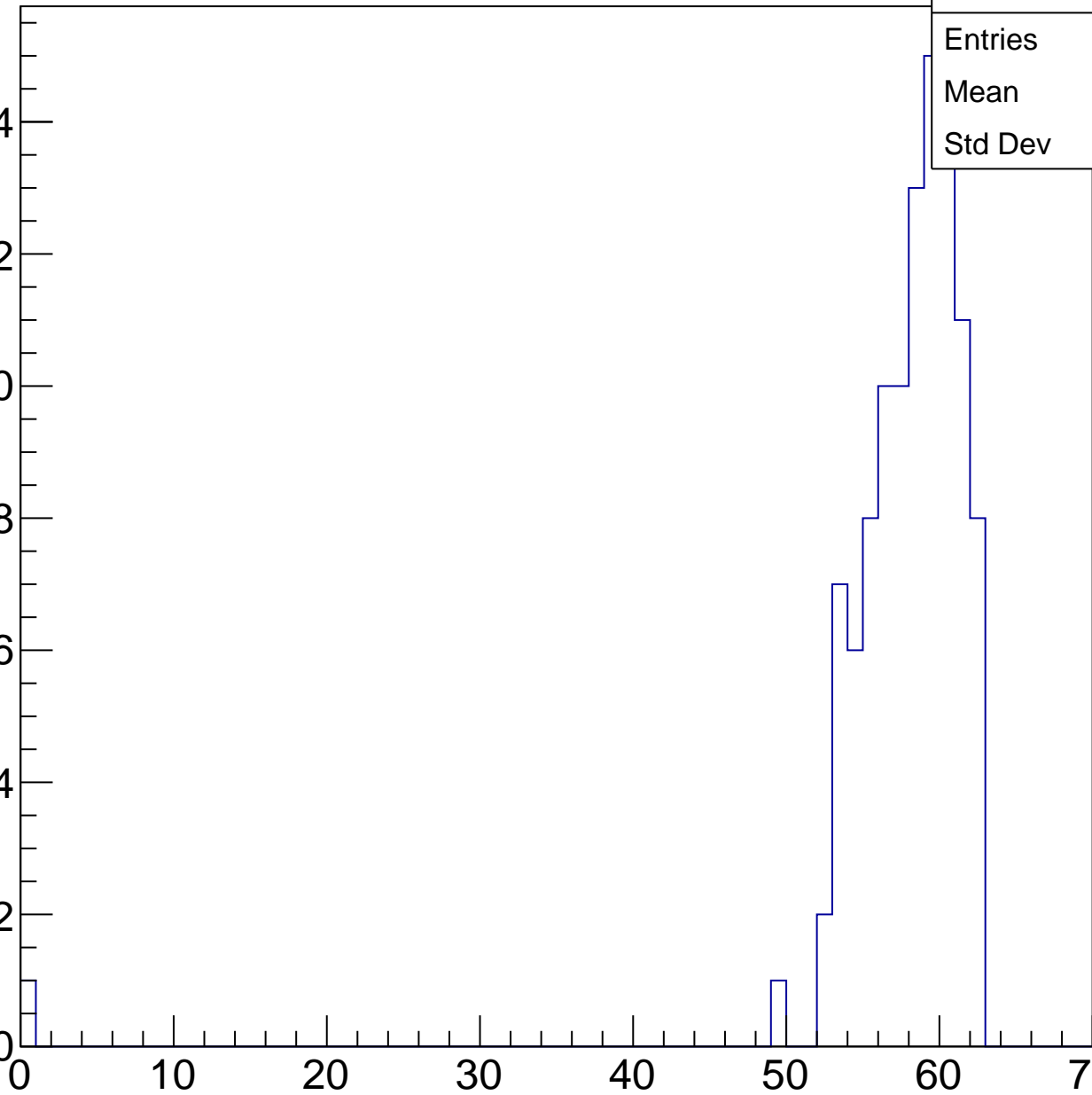
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	57.21
Std Dev	6.251

ampl



B1L001S, U20-ch25, adc5

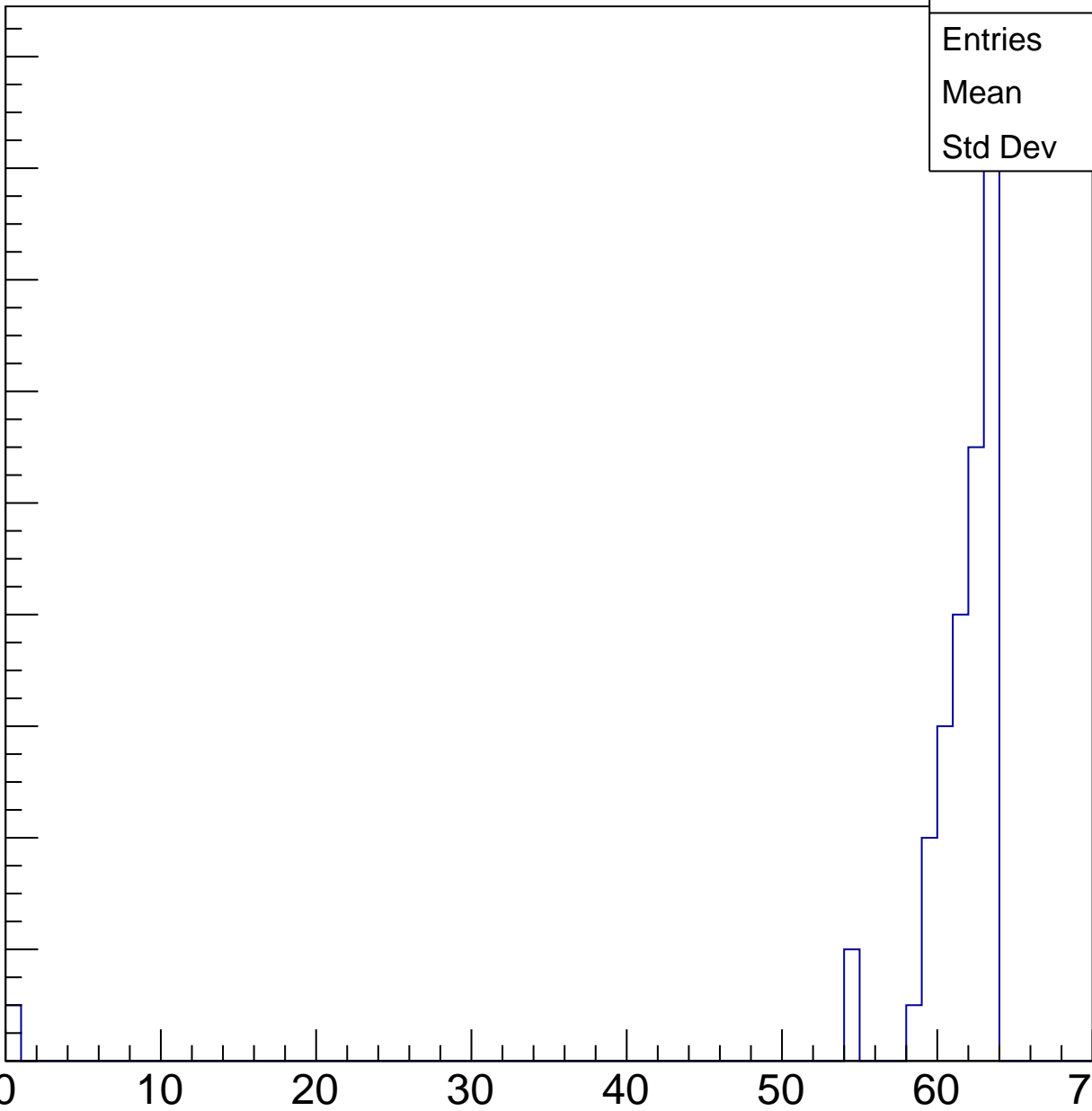
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	51
Mean	60.12
Std Dev	8.737

ampl



B1L001S, U20-ch25, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

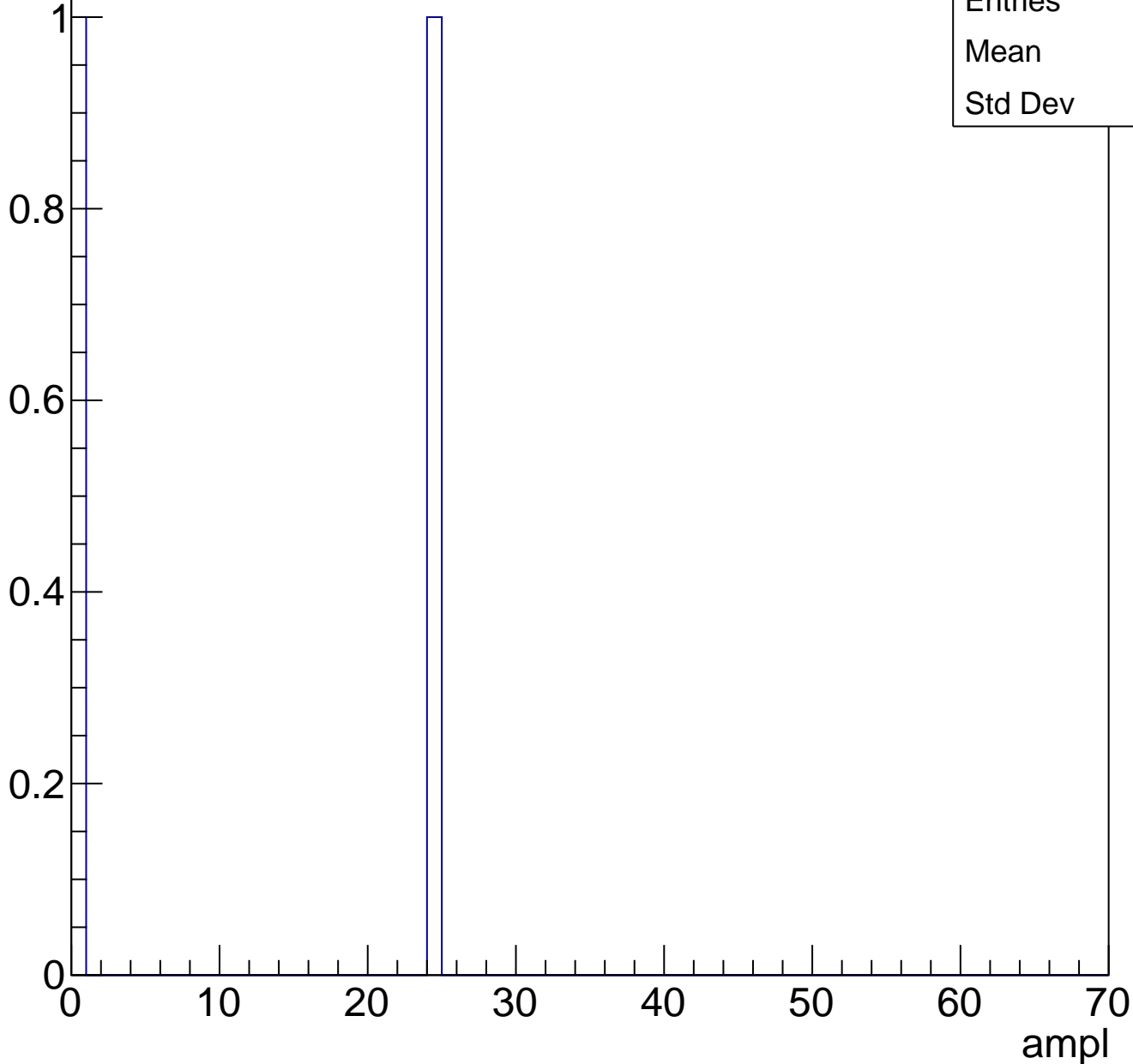
ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch25, adc7

calib_packv5_042523_0143.root, FC#2, port C2

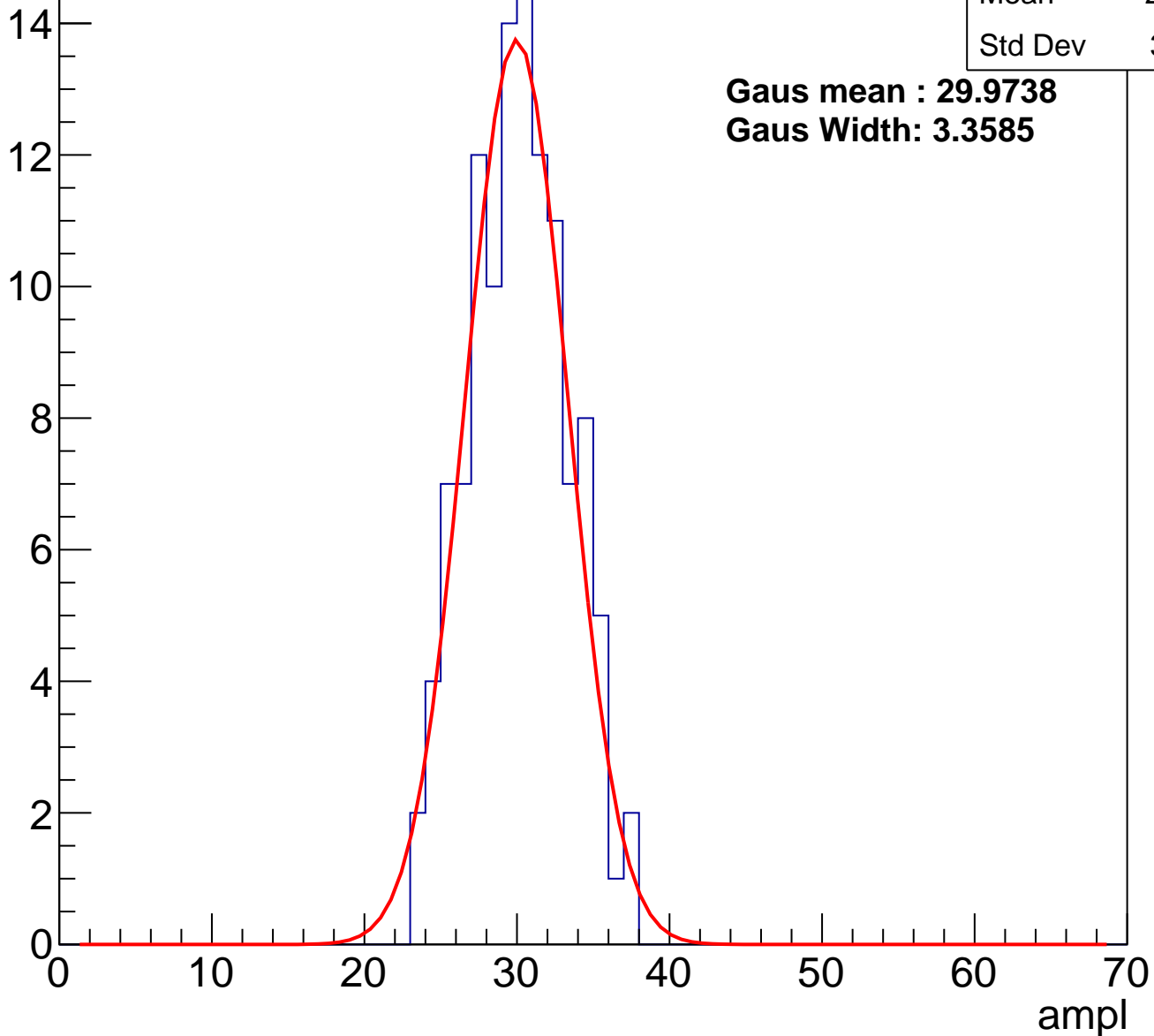
Entry



B1L001S, U20-ch26, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



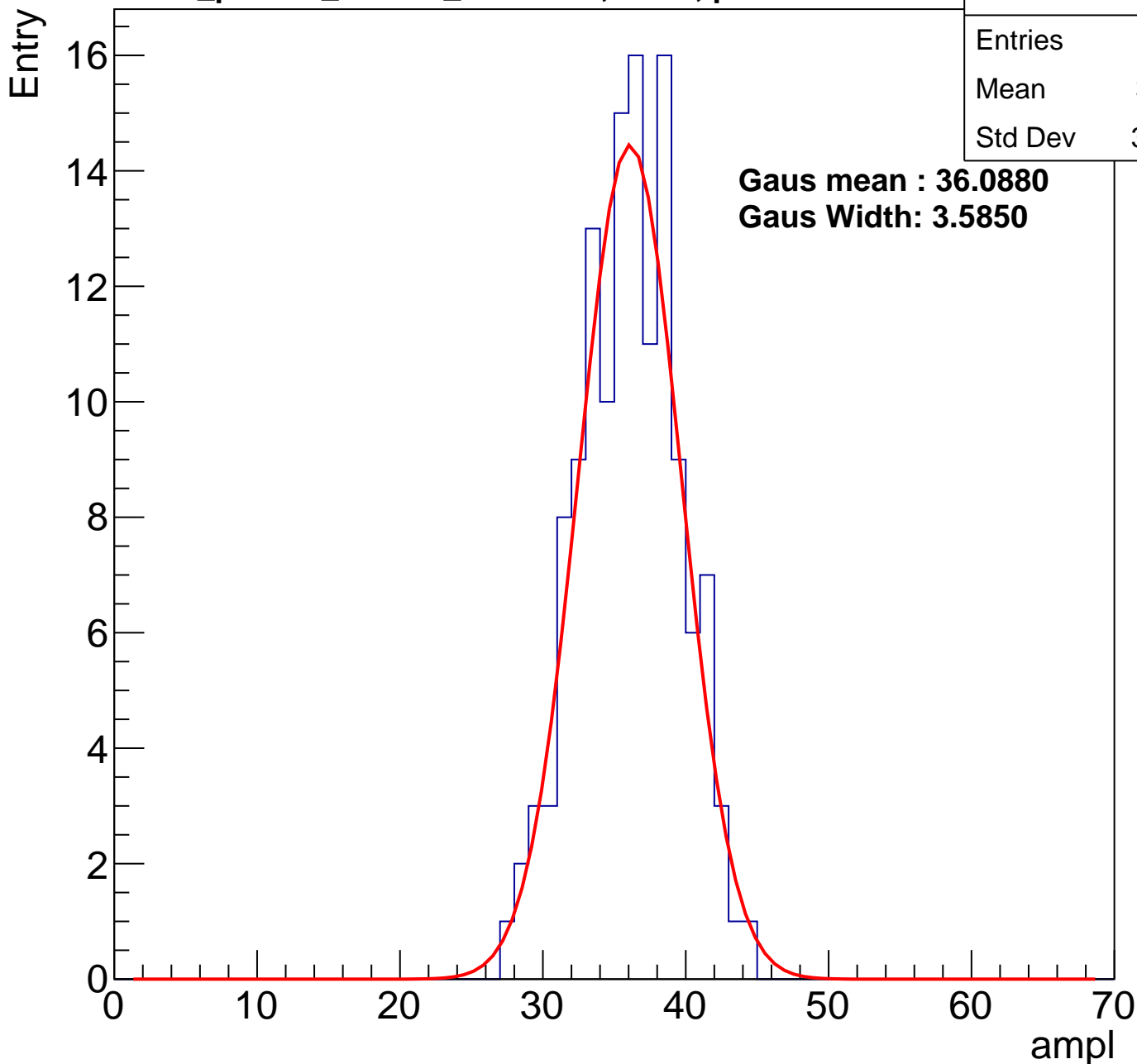
B1L001S, U20-ch26, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	35.61
Std Dev	3.464

Gaus mean : 36.0880

Gaus Width: 3.5850



B1L001S, U20-ch26, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	42.28
Std Dev	3.206

Gaus mean : 42.8708

Gaus Width: 3.3006

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U20-ch26, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	49.06
Std Dev	3.164

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

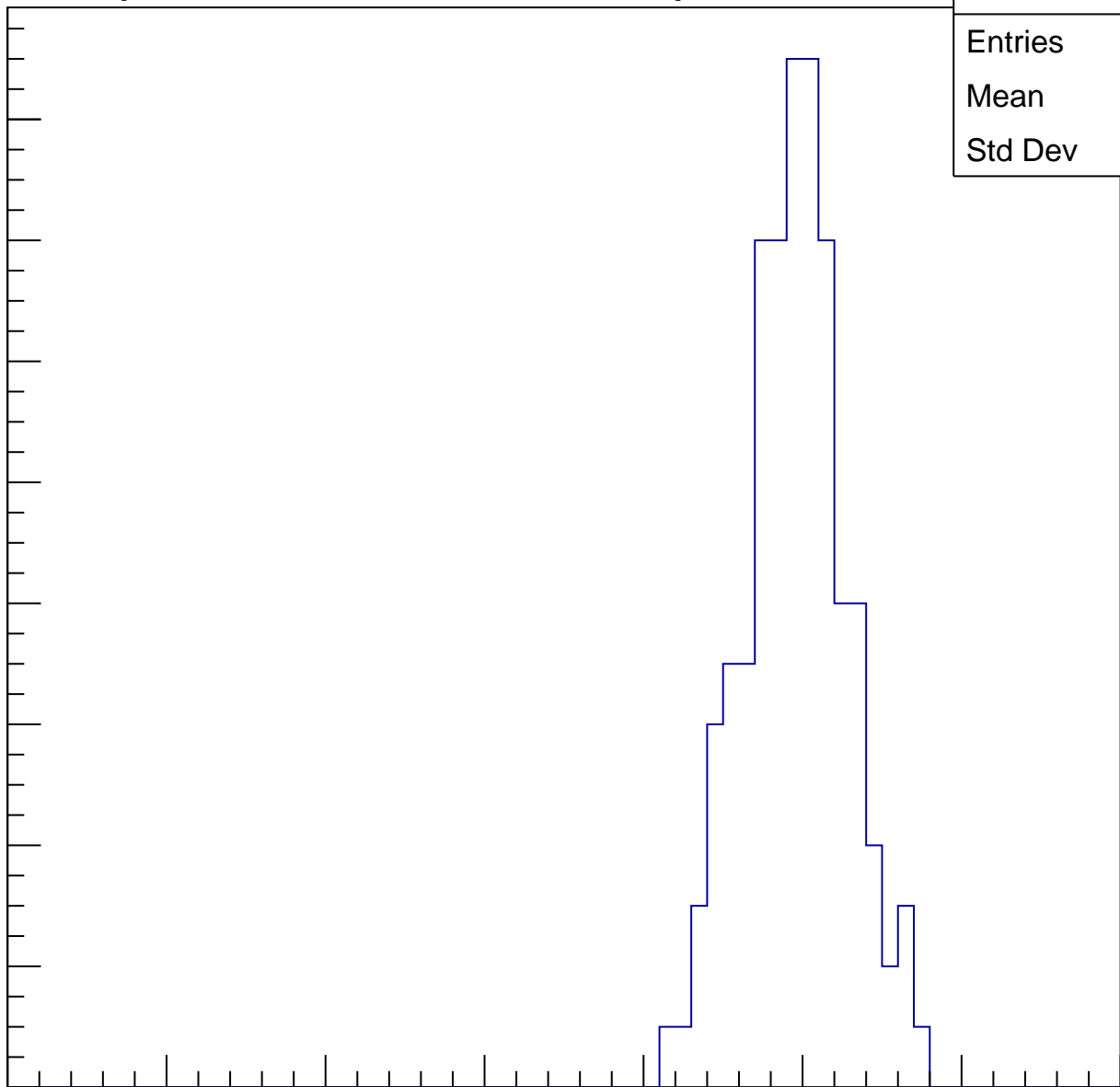
40

50

60

70

ampl



B1L001S, U20-ch26, adc4

calib_packv5_042523_0143.root, FC#2, port C2

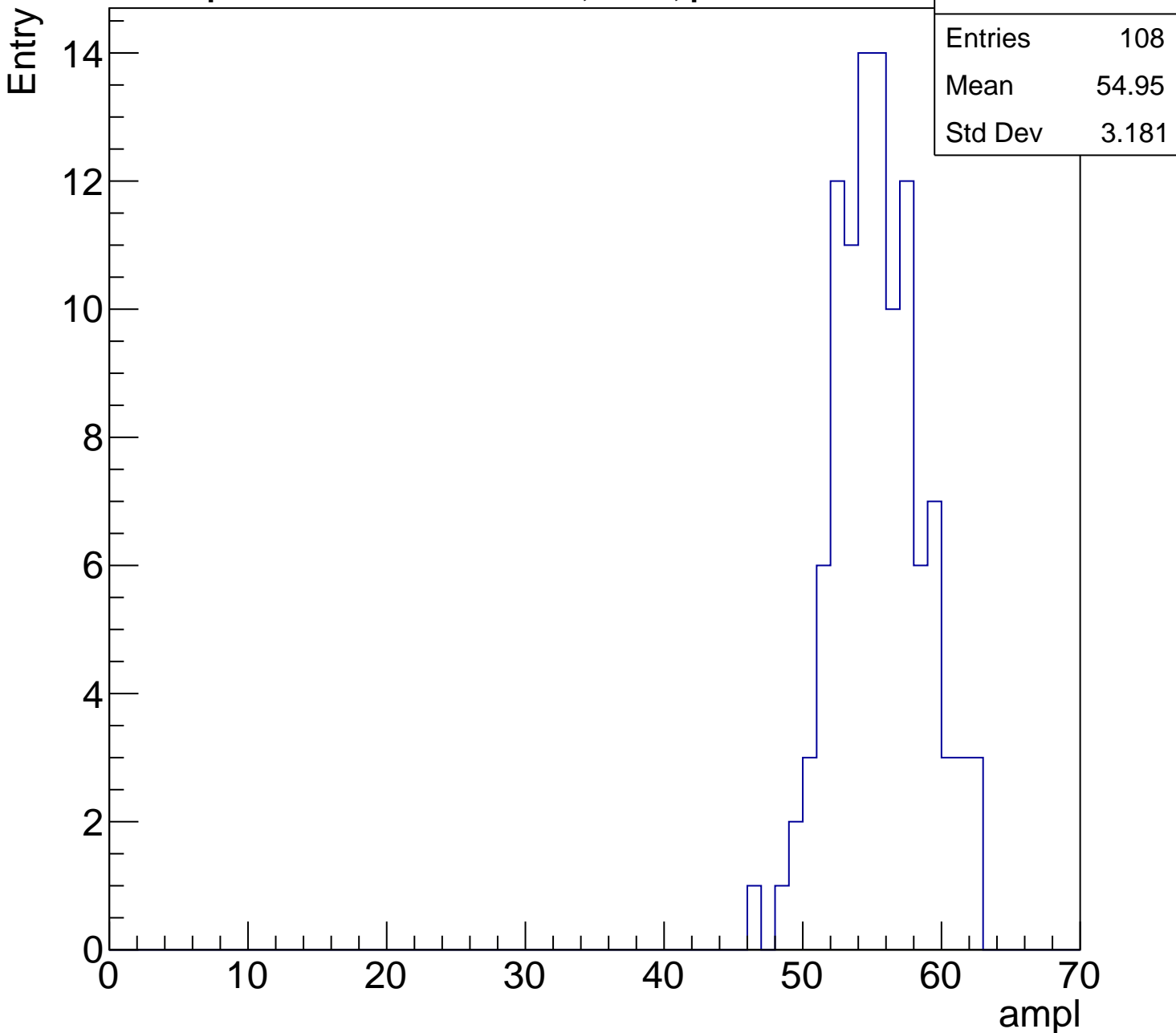
Entries	108
Mean	54.95
Std Dev	3.181

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch26, adc5

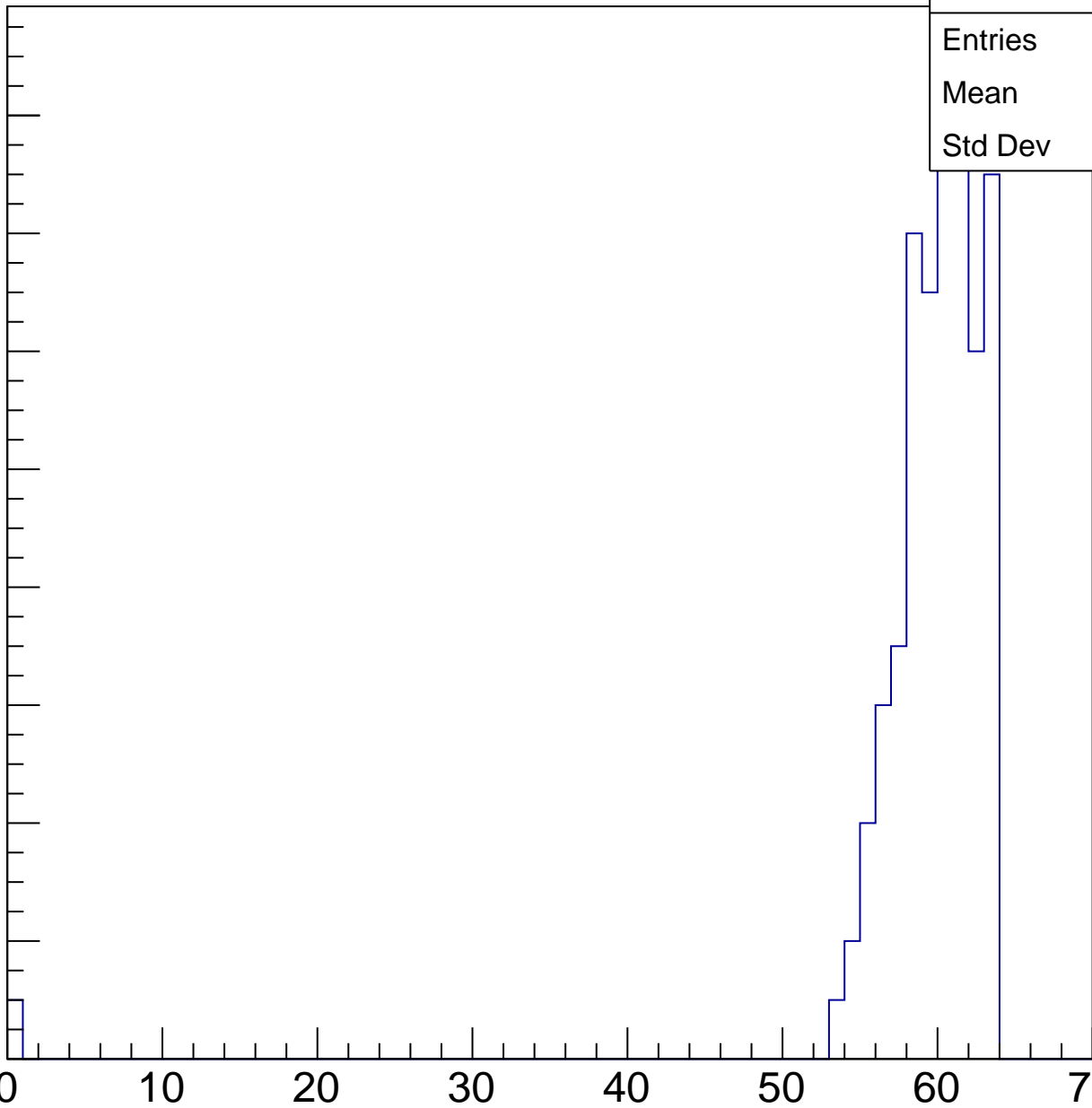
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	59.09
Std Dev	6.177

ampl

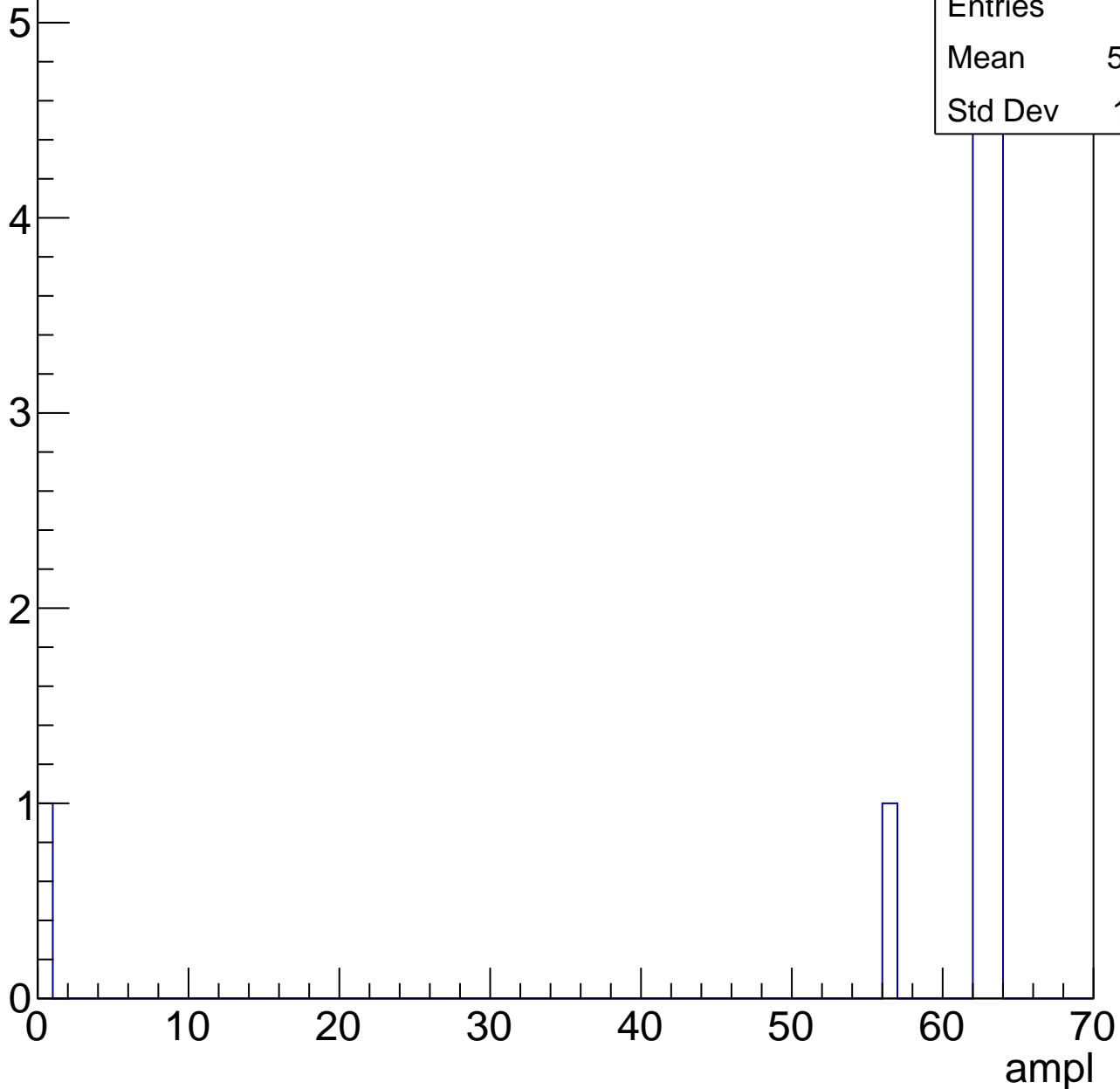


B1L001S, U20-ch26, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	56.75
Std Dev	17.21



B1L001S, U20-ch26, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch27, adc0

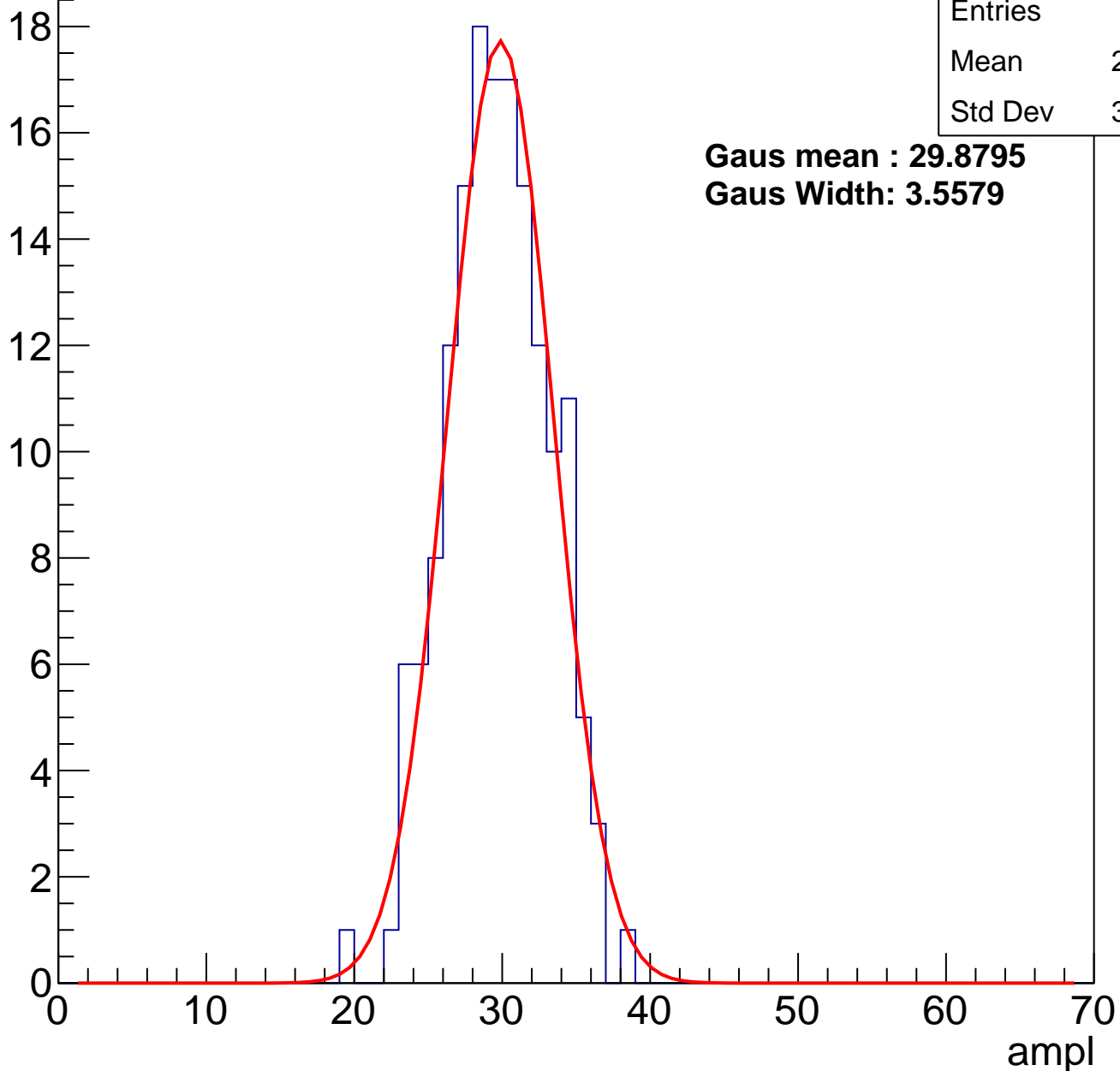
calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	29.25
Std Dev	3.419

Gaus mean : 29.8795

Gaus Width: 3.5579

Entry



B1L001S, U20-ch27, adc1

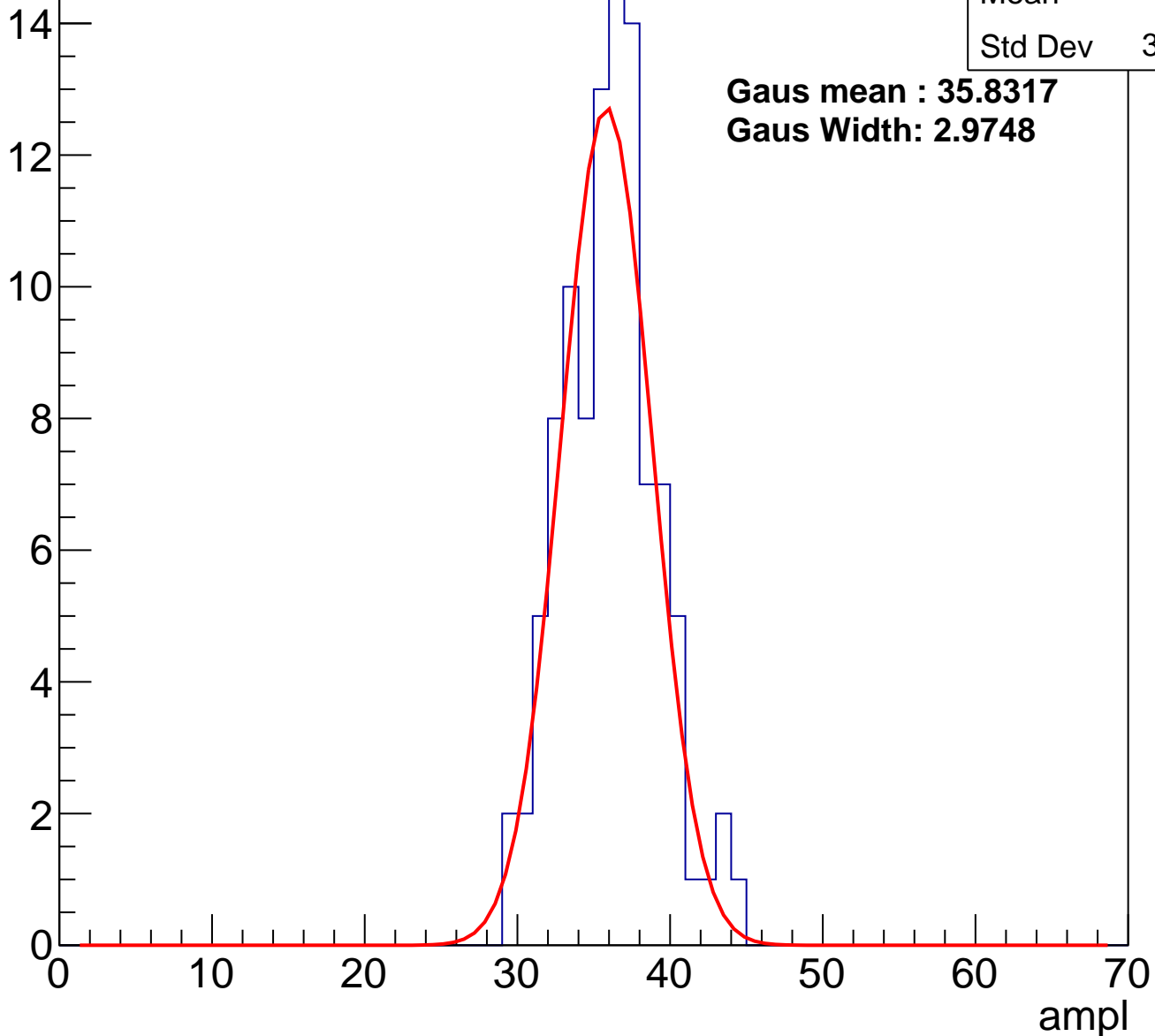
calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	35.6
Std Dev	3.067

Gaus mean : 35.8317

Gaus Width: 2.9748

Entry



B1L001S, U20-ch27, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	41.99
Std Dev	3.297

Gaus mean : 42.5327

Gaus Width: 3.3864

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

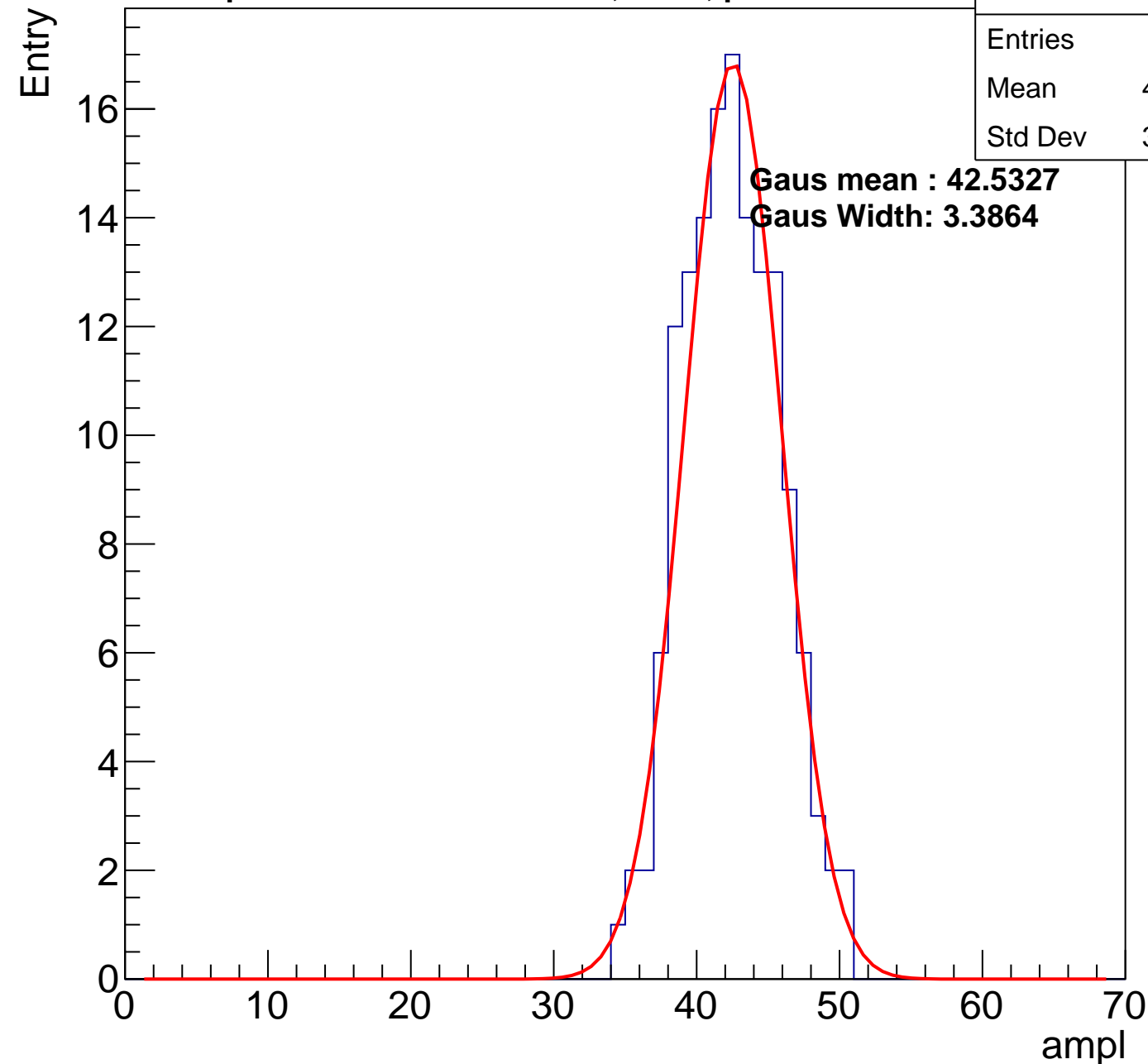
40

50

60

70

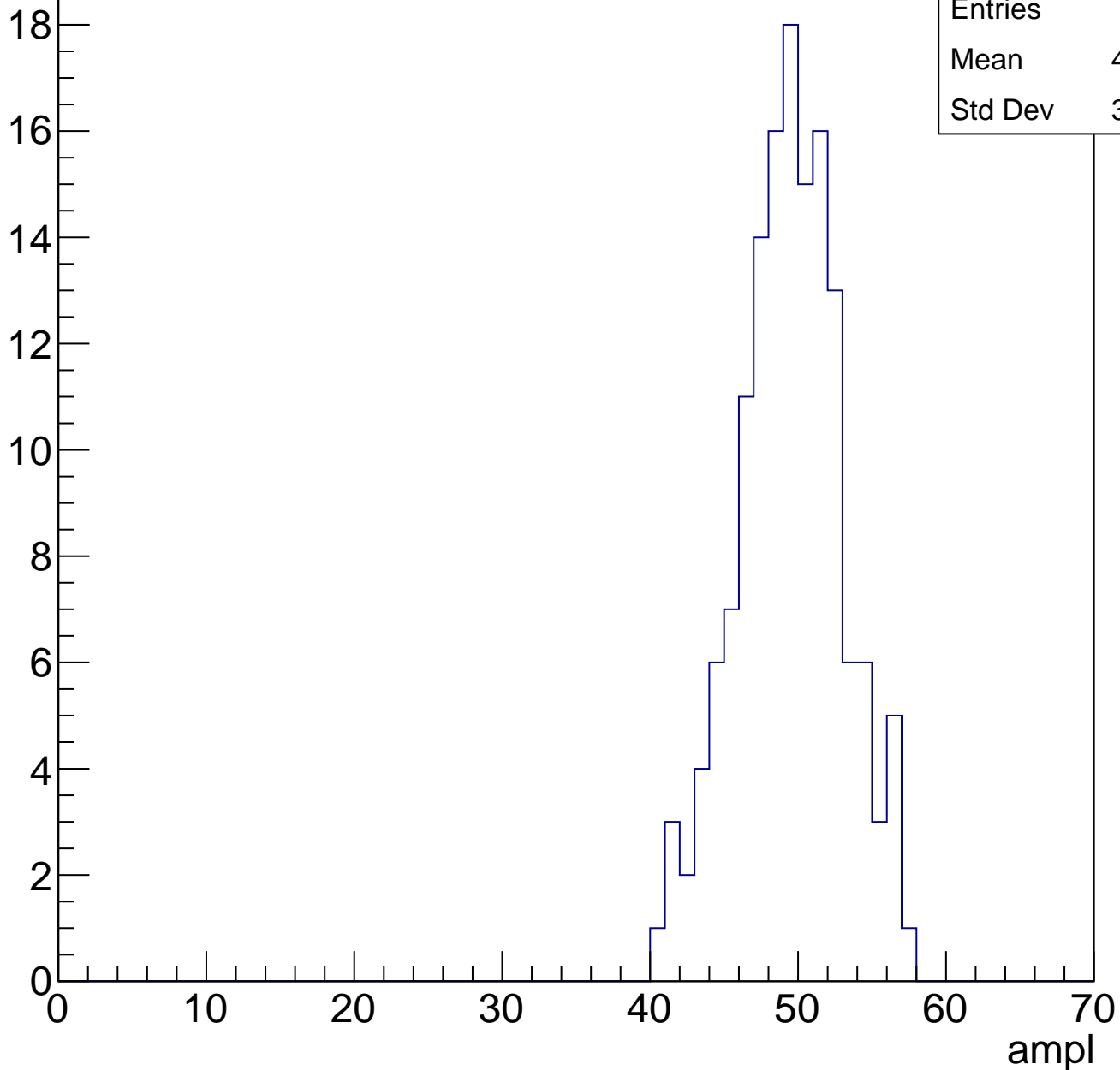
ampl



B1L001S, U20-ch27, adc3

calib_packv5_042523_0143.root, FC#2, port C2

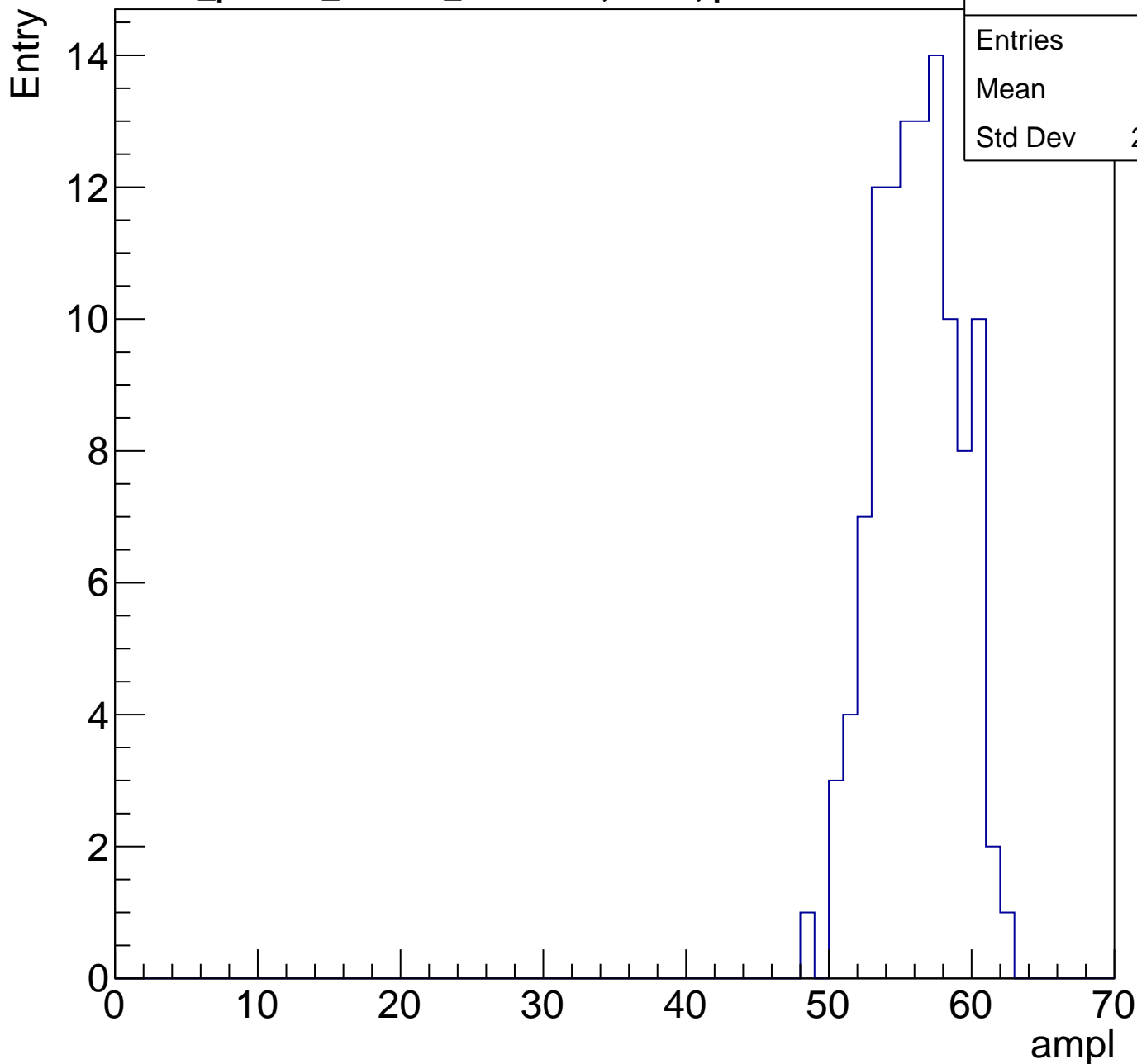
Entry



B1L001S, U20-ch27, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	55.7
Std Dev	2.884

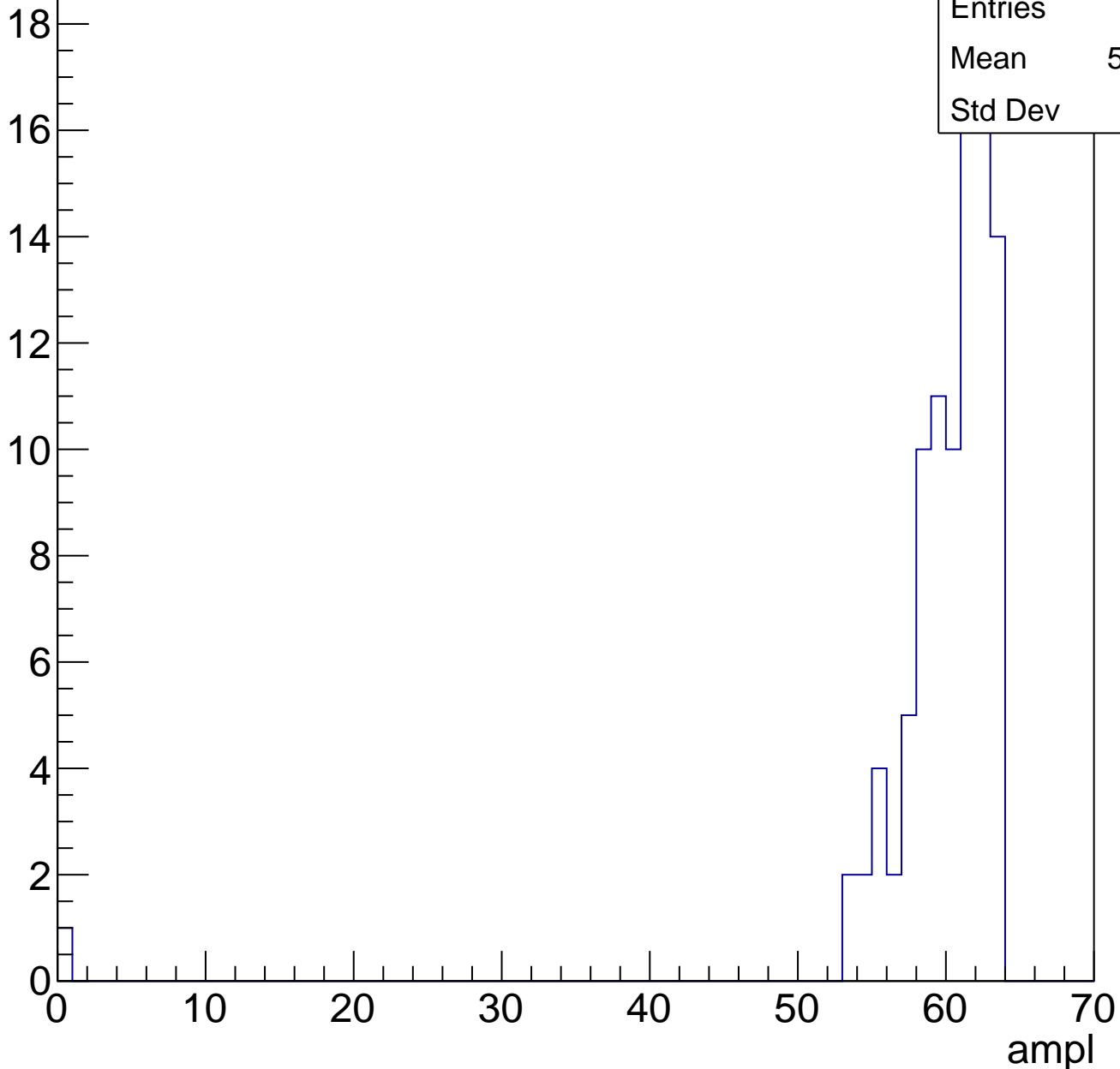


B1L001S, U20-ch27, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	59.32
Std Dev	6.59

Entry

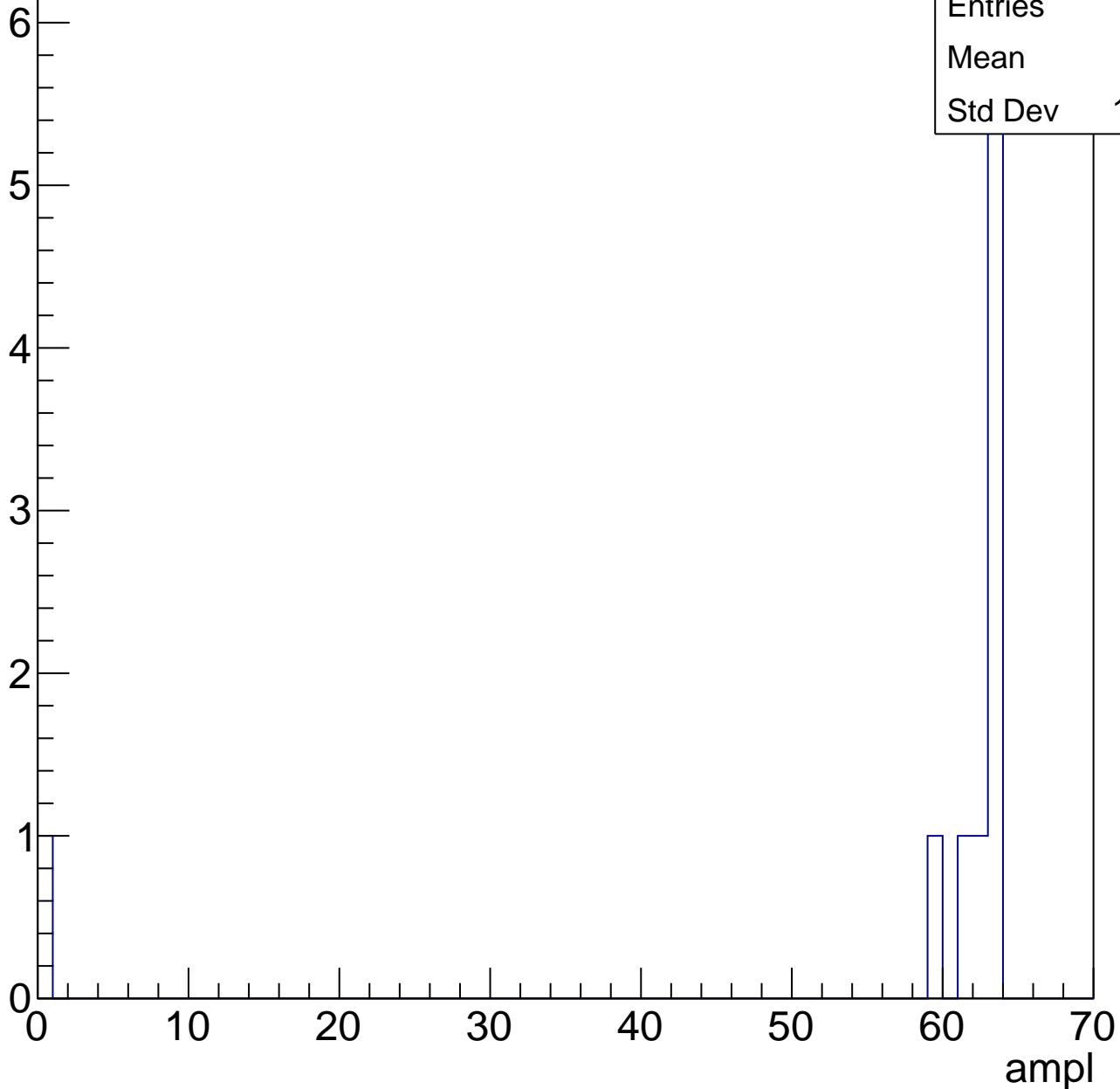


B1L001S, U20-ch27, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	56
Std Dev	18.71



B1L001S, U20-ch27, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch28, adc0

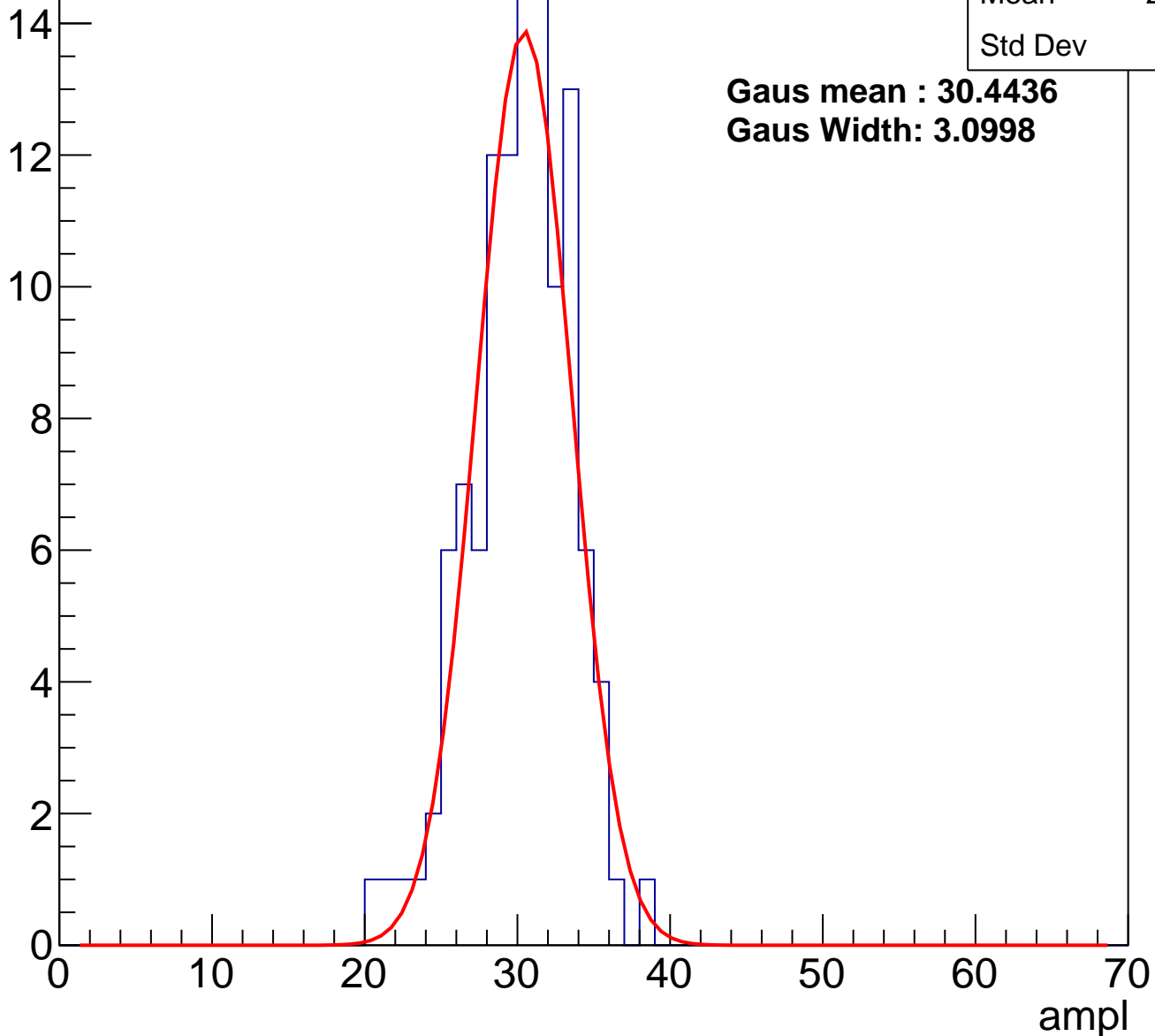
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	29.77
Std Dev	3.25

Gaus mean : 30.4436

Gaus Width: 3.0998

Entry



B1L001S, U20-ch28, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	36.3
Std Dev	3.305

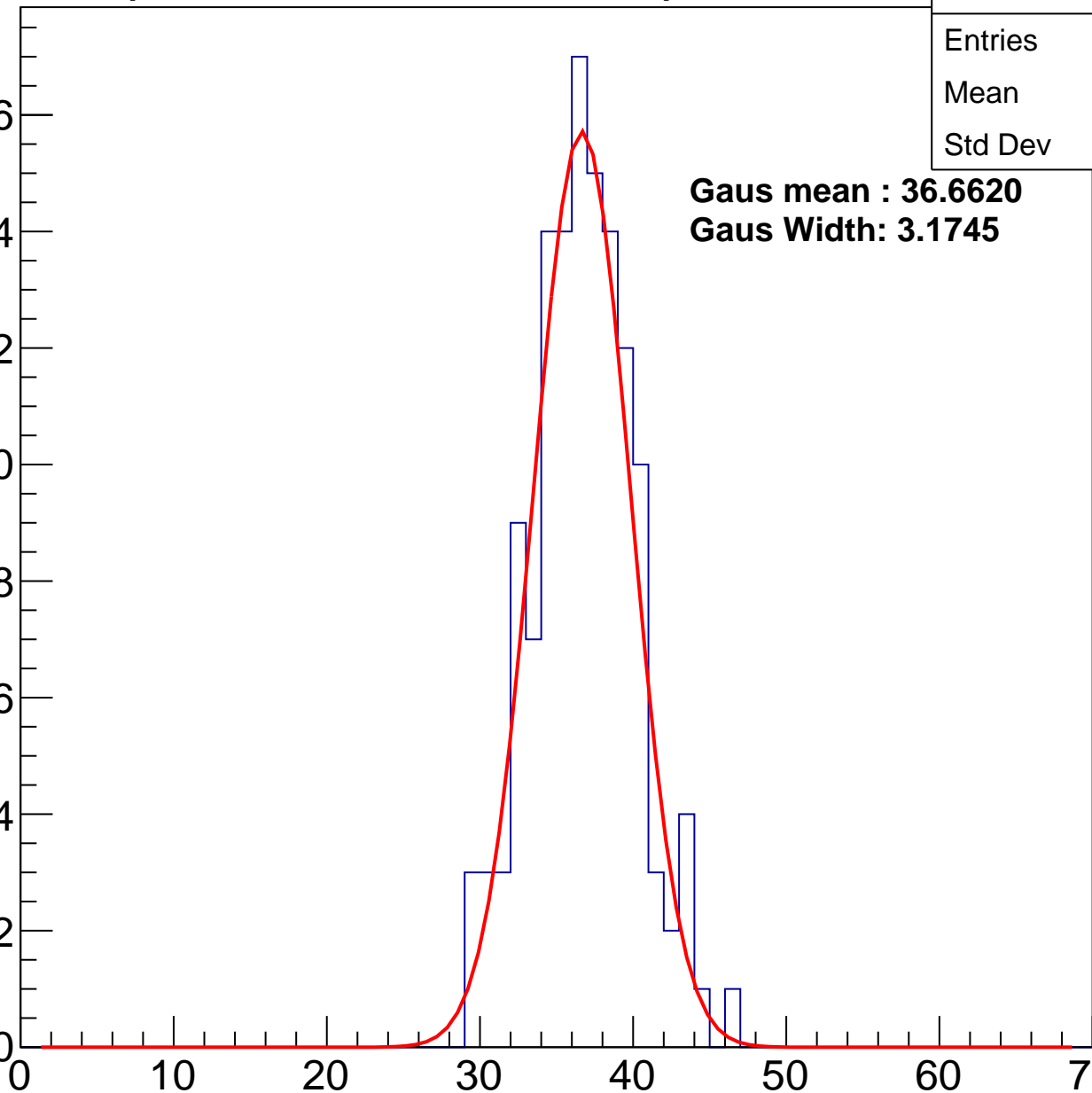
Gaus mean : 36.6620

Gaus Width: 3.1745

Entry

16
14
12
10
8
6
4
2
0

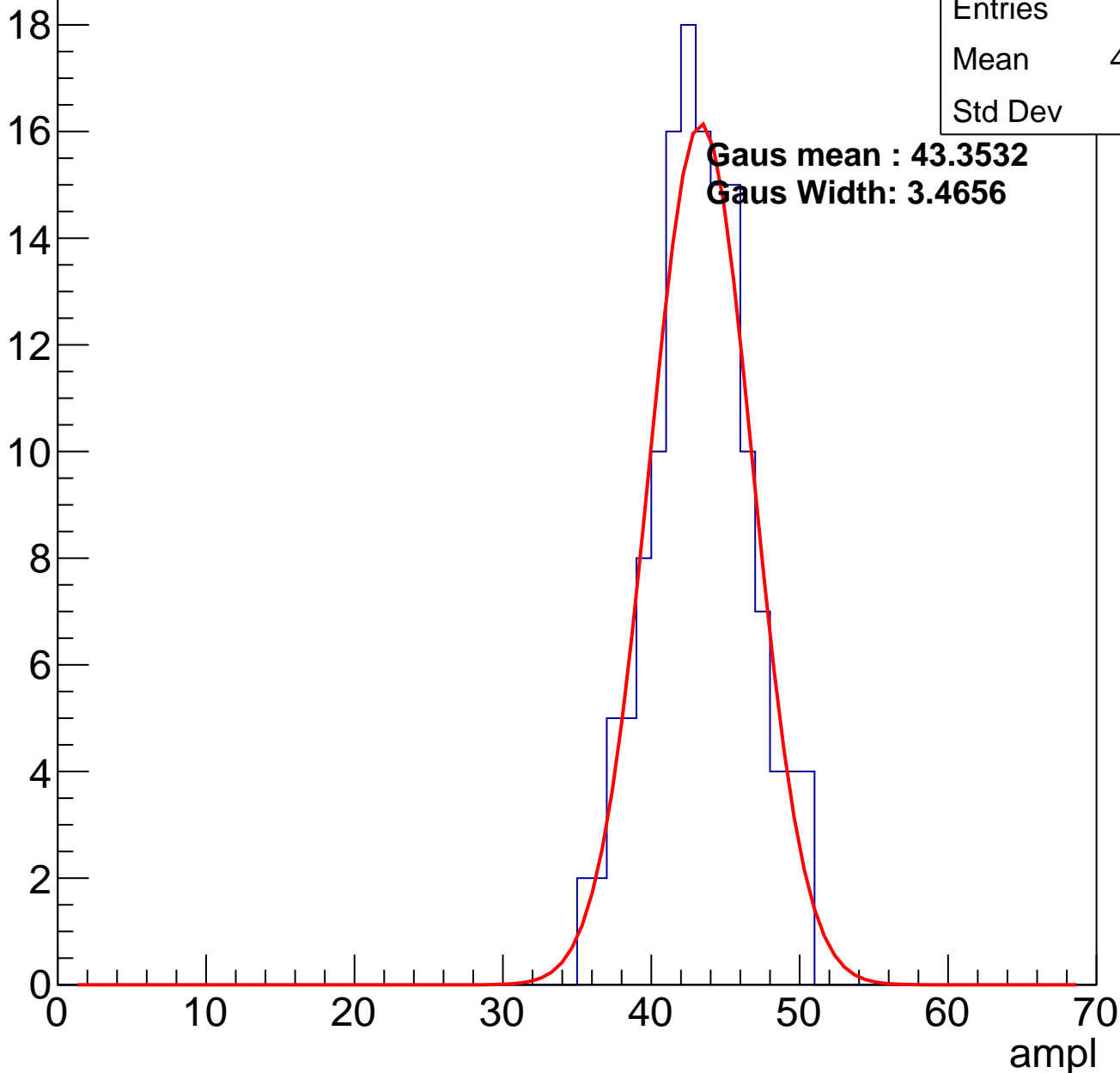
ampl



B1L001S, U20-ch28, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch28, adc3

calib_packv5_042523_0143.root, FC#2, port C2

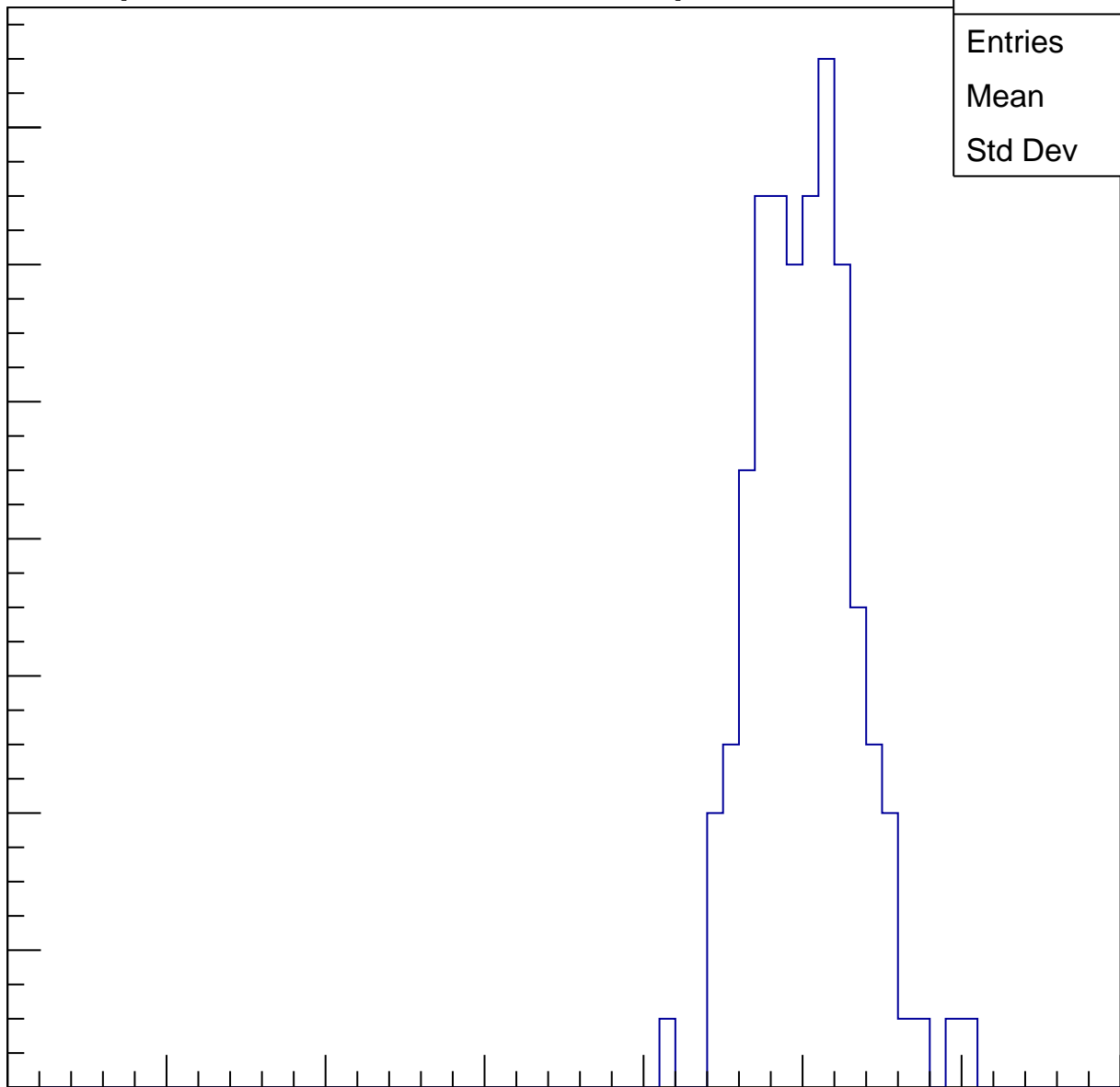
Entries	117
Mean	49.67
Std Dev	3.248

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch28, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	55.54
Std Dev	3.308

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

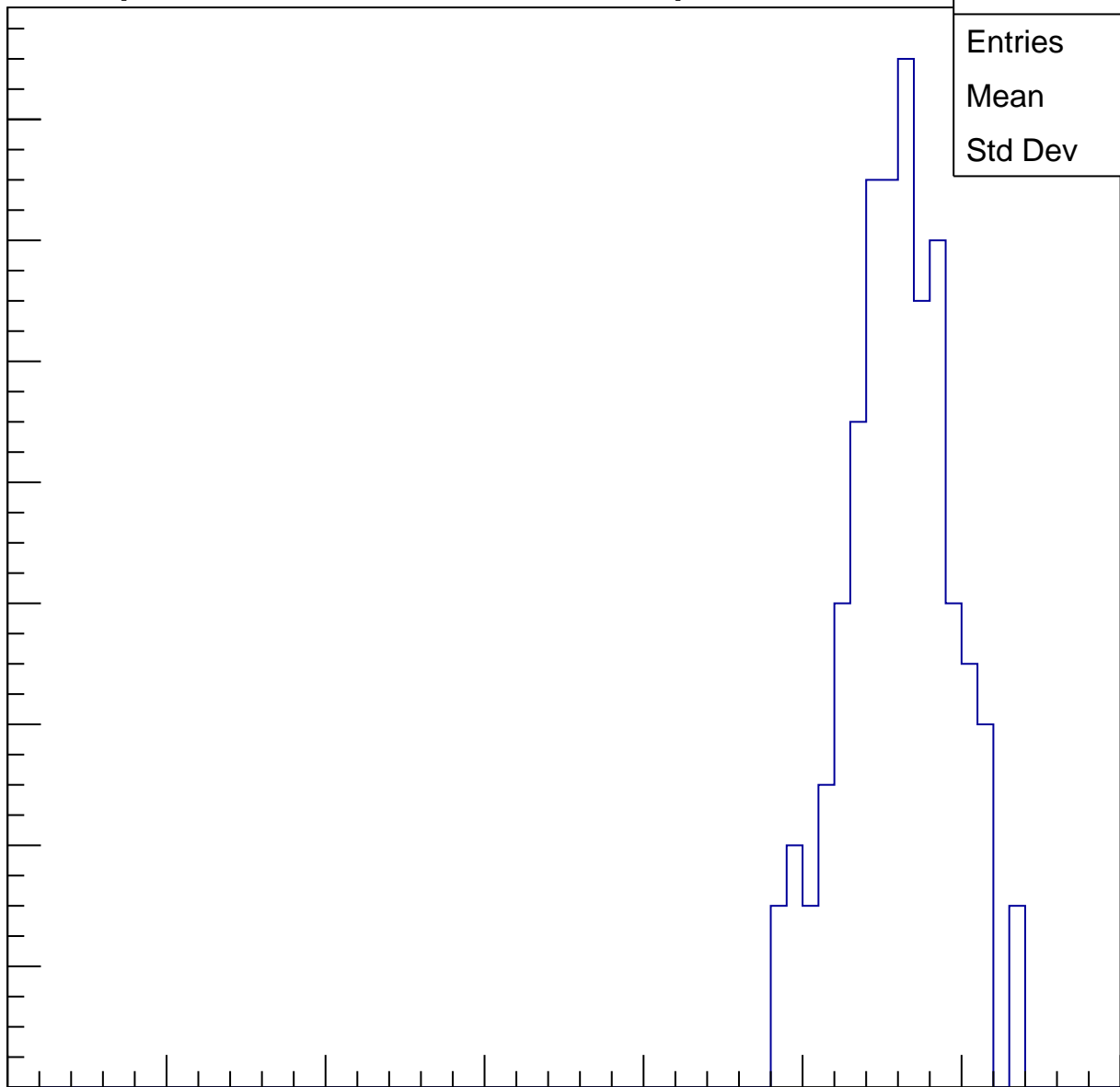
40

50

60

70

ampl



B1L001S, U20-ch28, adc5

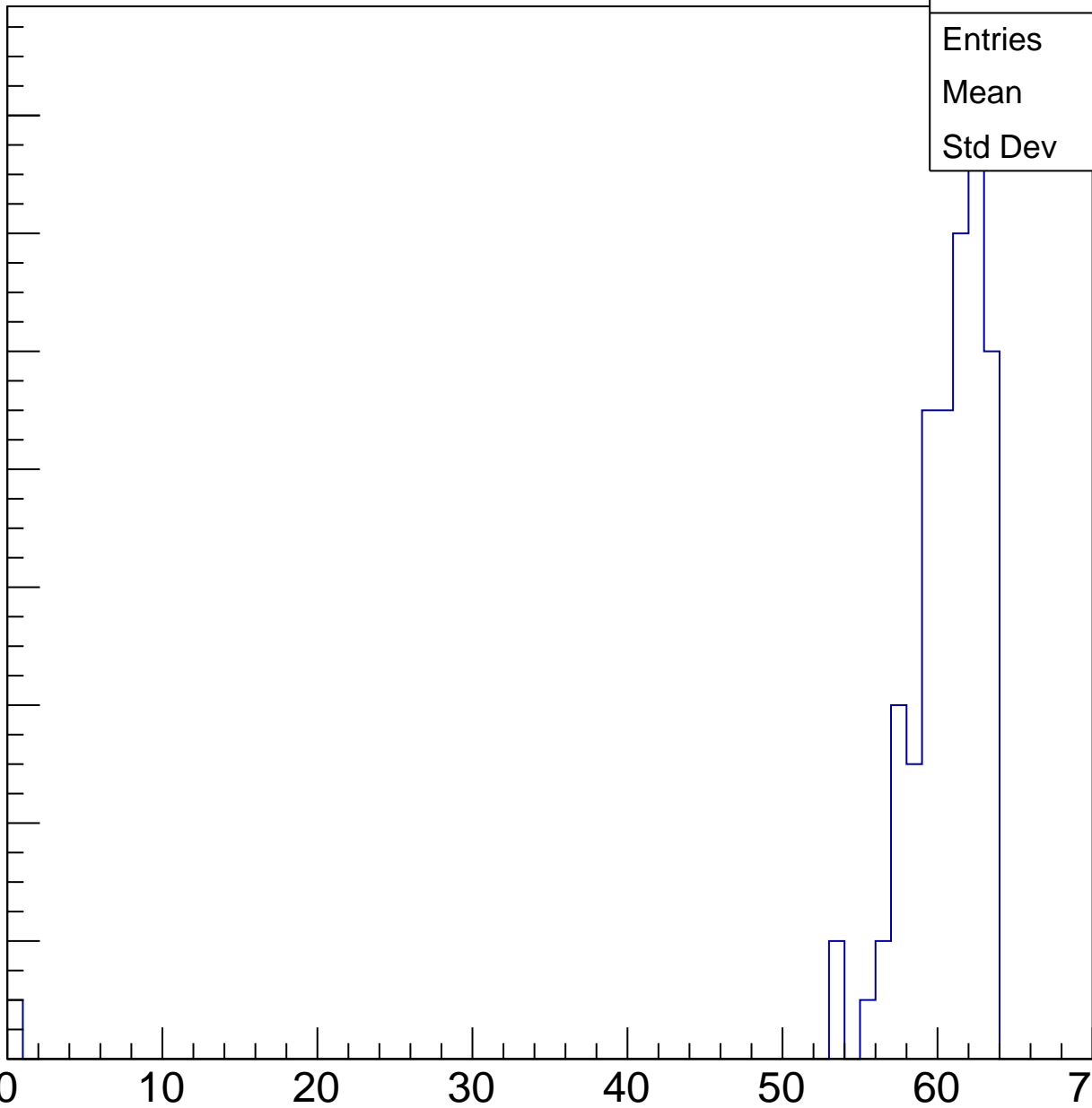
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	82
Mean	59.49
Std Dev	6.992

ampl

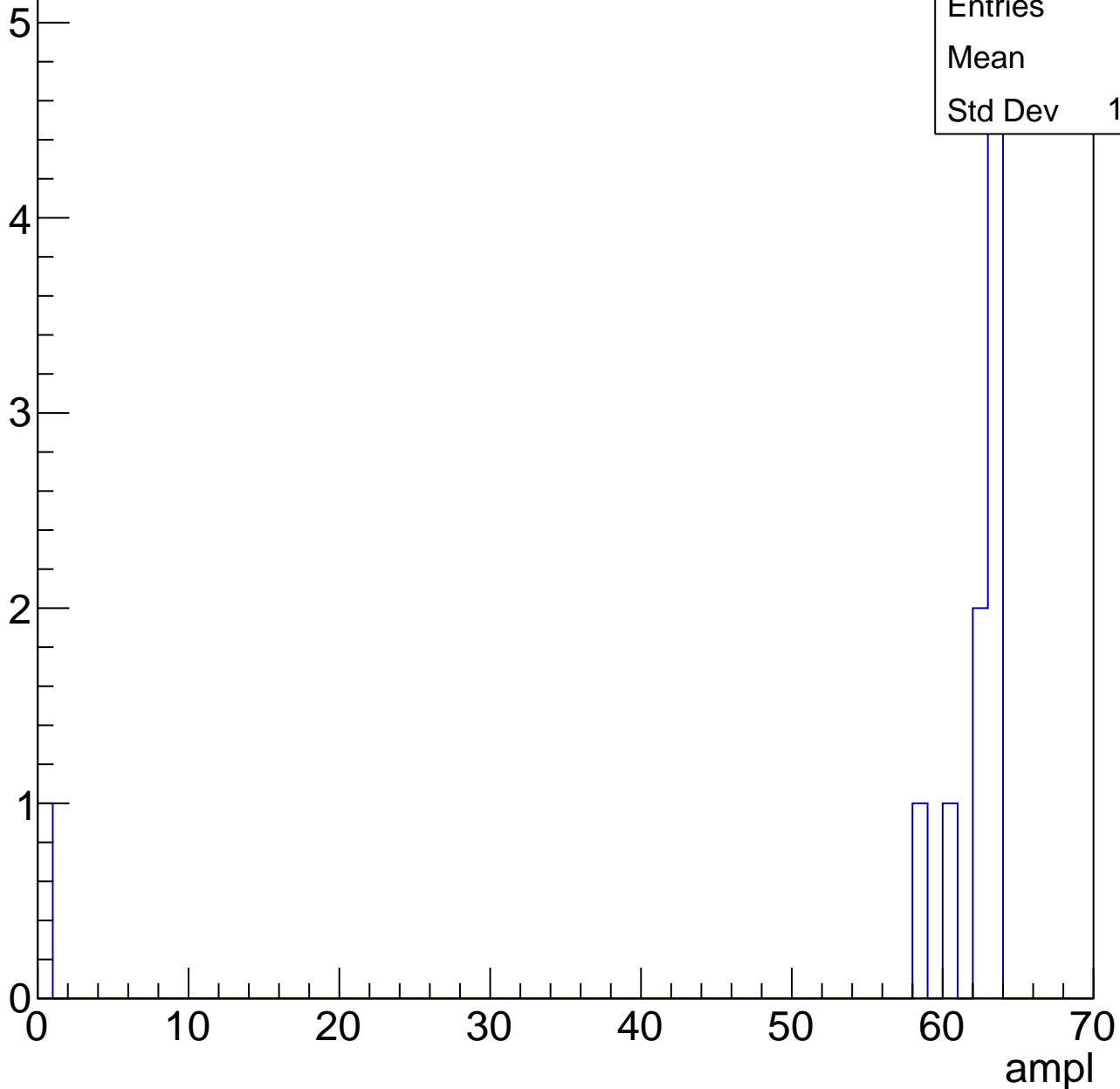


B1L001S, U20-ch28, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	55.7
Std Dev	18.63



B1L001S, U20-ch28, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch29, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	28.42
Std Dev	3.274

Gaus mean : 29.1214

Gaus Width: 3.6229

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

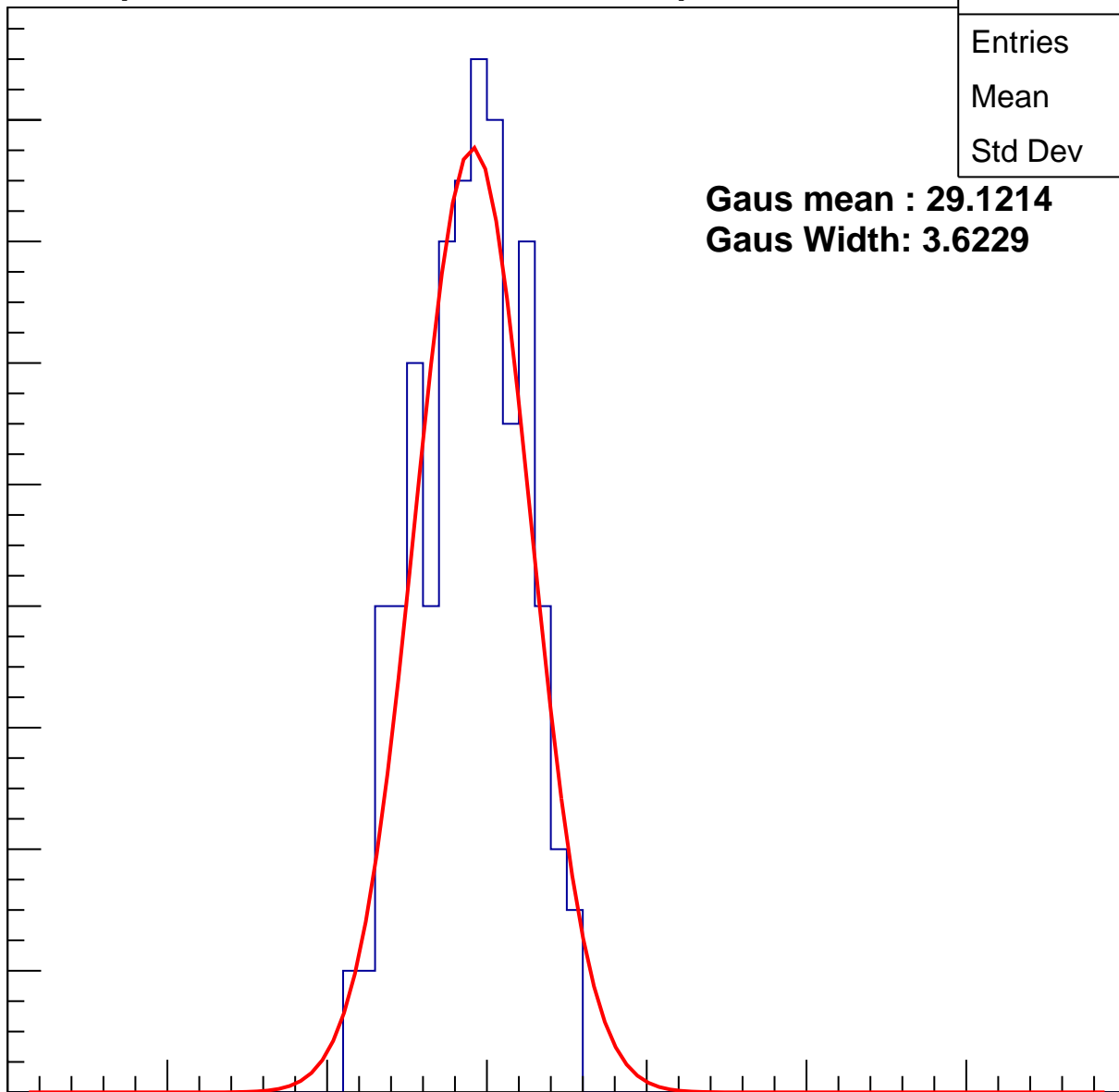
40

50

60

70

ampl



B1L001S, U20-ch29, adc1

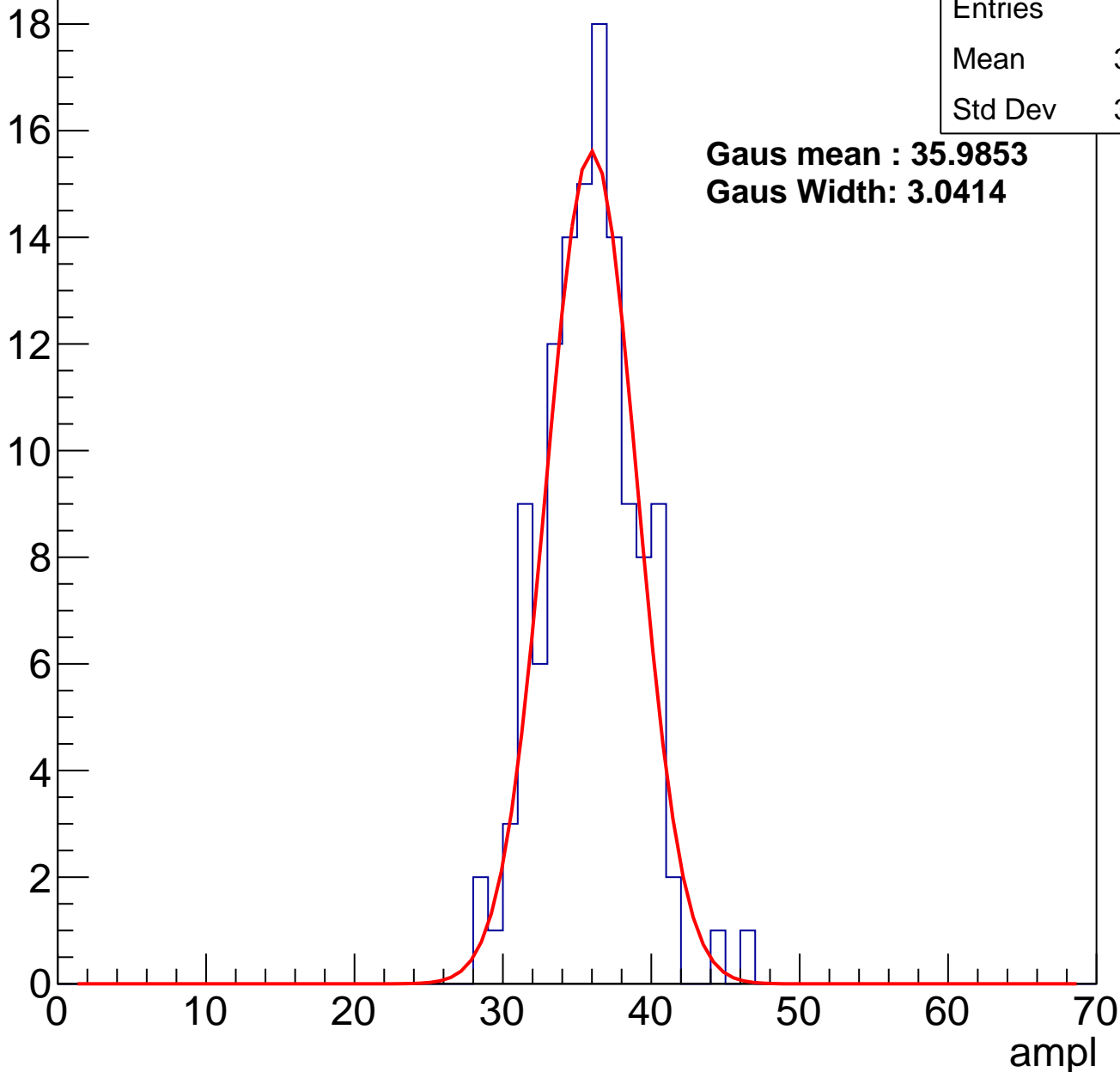
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	35.44
Std Dev	3.145

Gaus mean : 35.9853

Gaus Width: 3.0414

Entry



B1L001S, U20-ch29, adc2

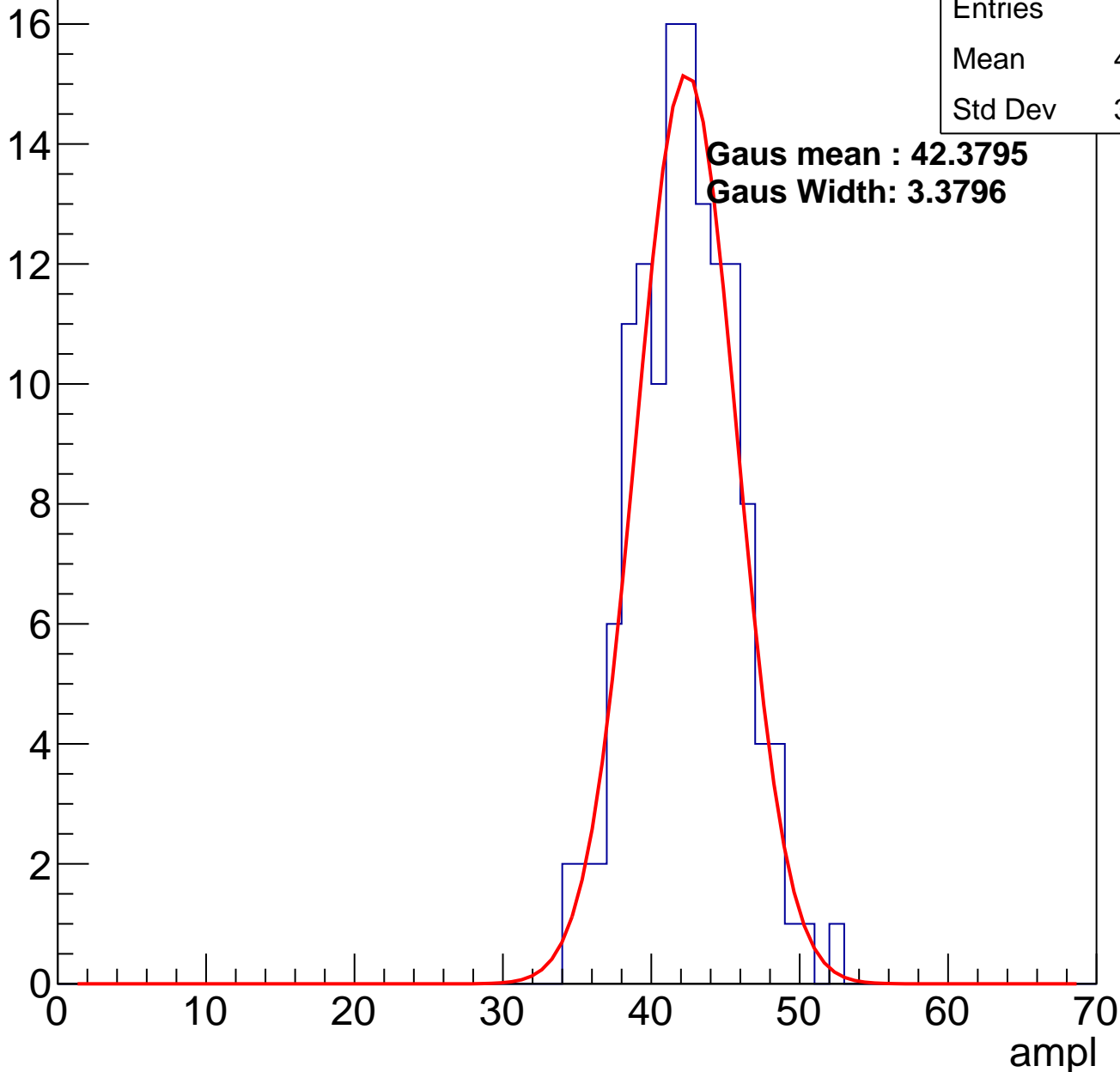
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	41.89
Std Dev	3.403

Gaus mean : 42.3795

Gaus Width: 3.3796

Entry



B1L001S, U20-ch29, adc3

calib_packv5_042523_0143.root, FC#2, port C2

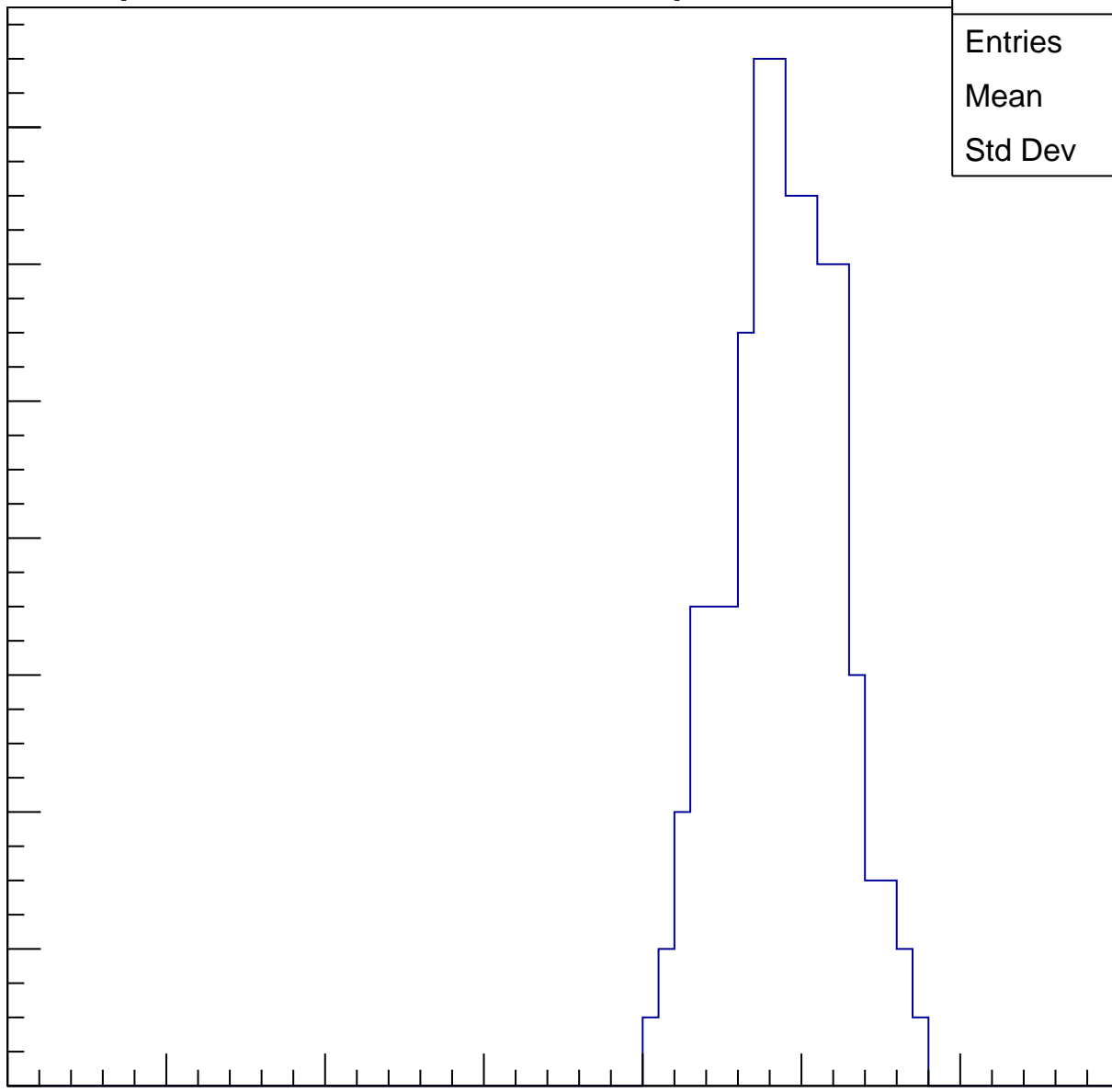
Entries	134
Mean	48.37
Std Dev	3.536

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

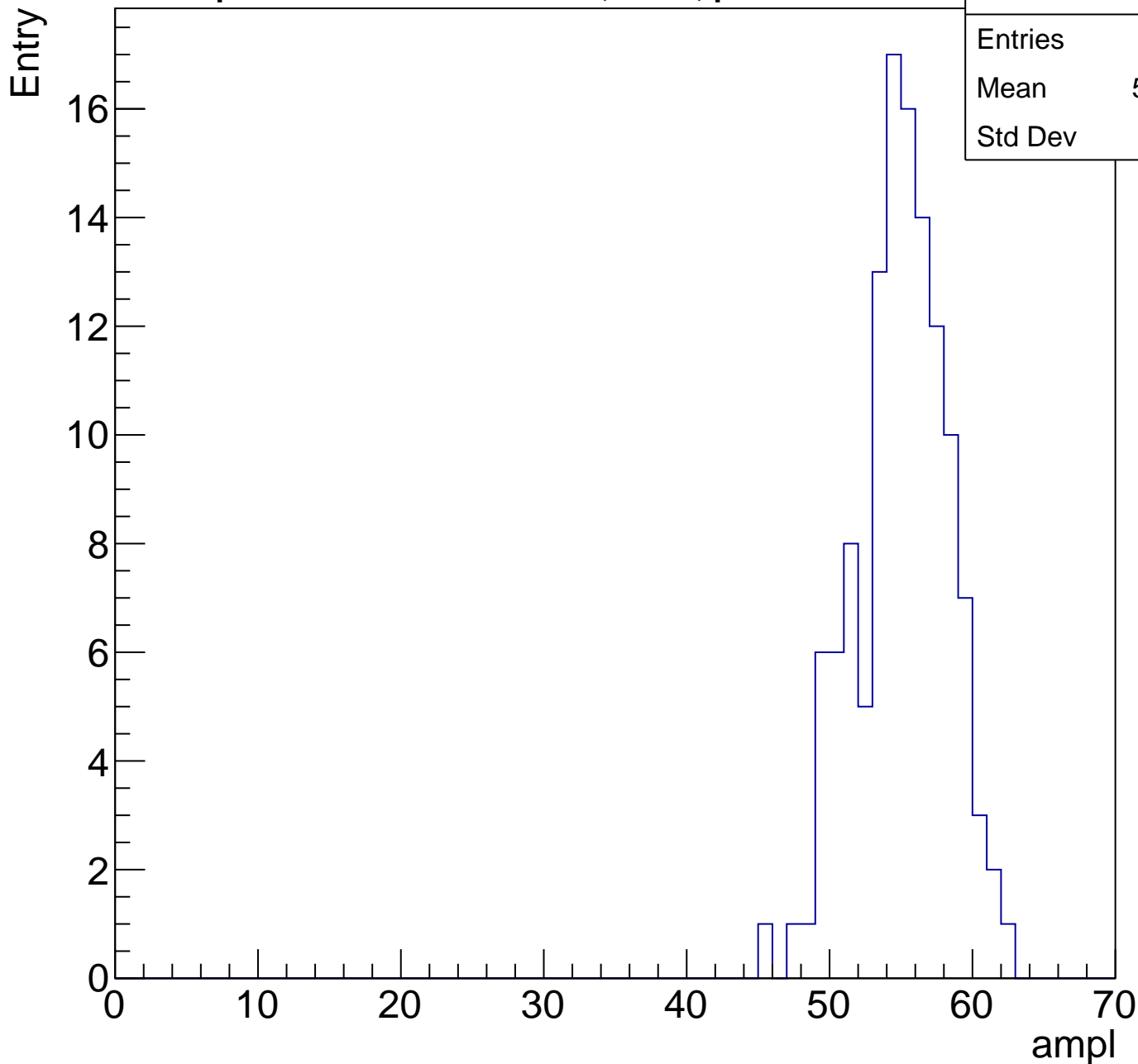
ampl



B1L001S, U20-ch29, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	54.59
Std Dev	3.2



B1L001S, U20-ch29, adc5

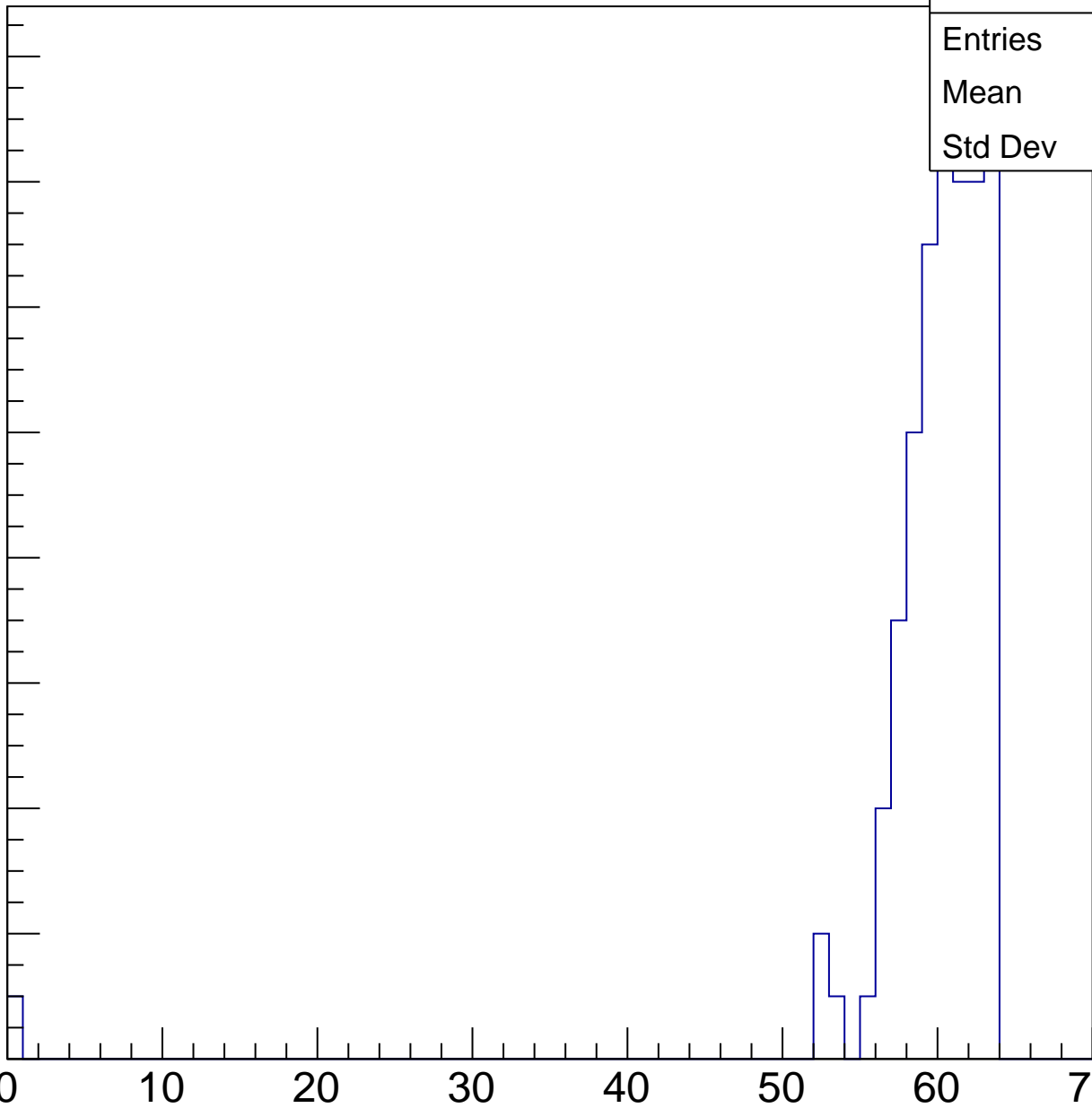
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.28
Std Dev	6.496

ampl

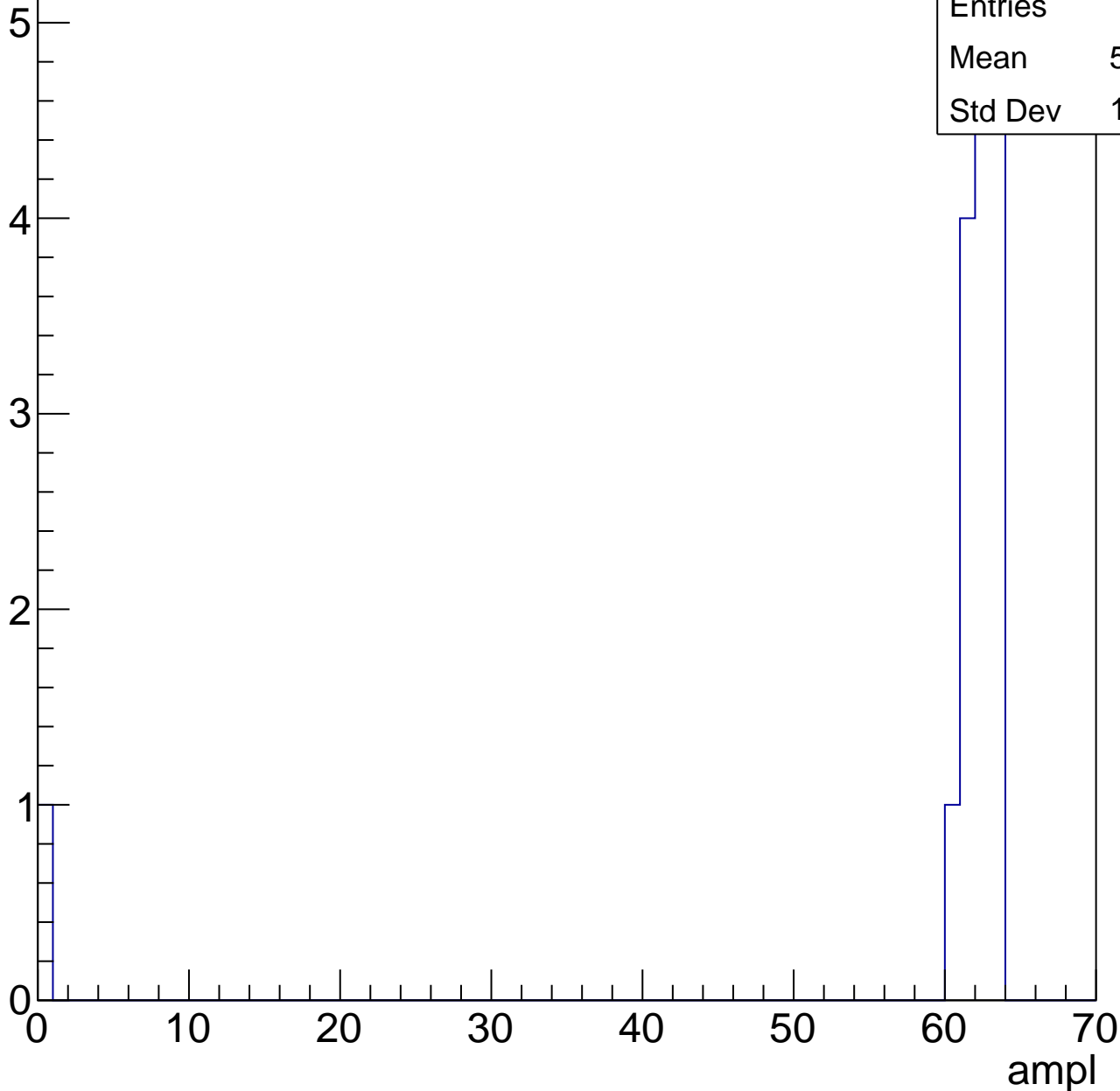


B1L001S, U20-ch29, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	58.06
Std Dev	15.02



B1L001S, U20-ch29, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch30, adc0

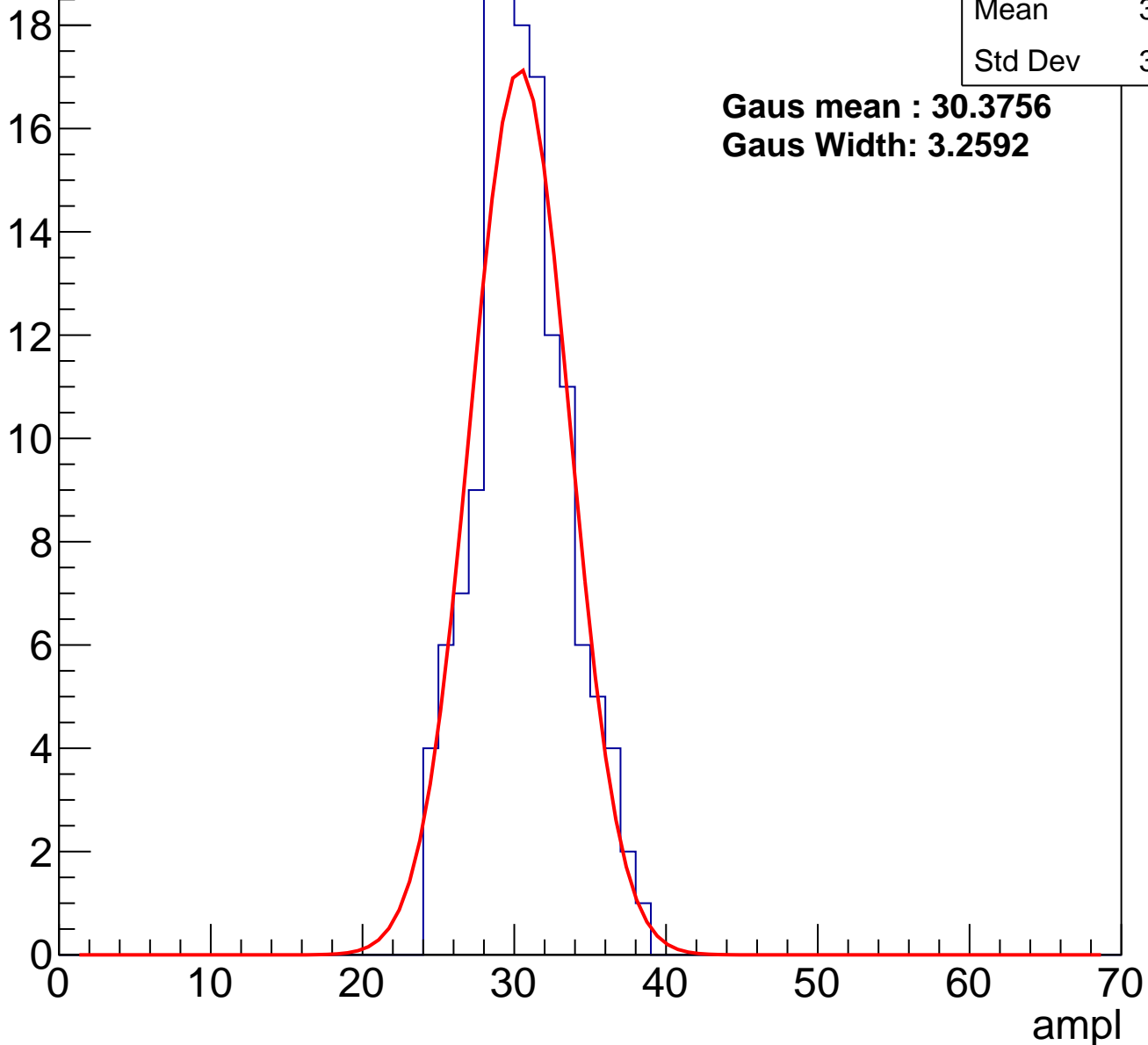
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	30.02
Std Dev	3.015

Gaus mean : 30.3756

Gaus Width: 3.2592

Entry



B1L001S, U20-ch30, adc1

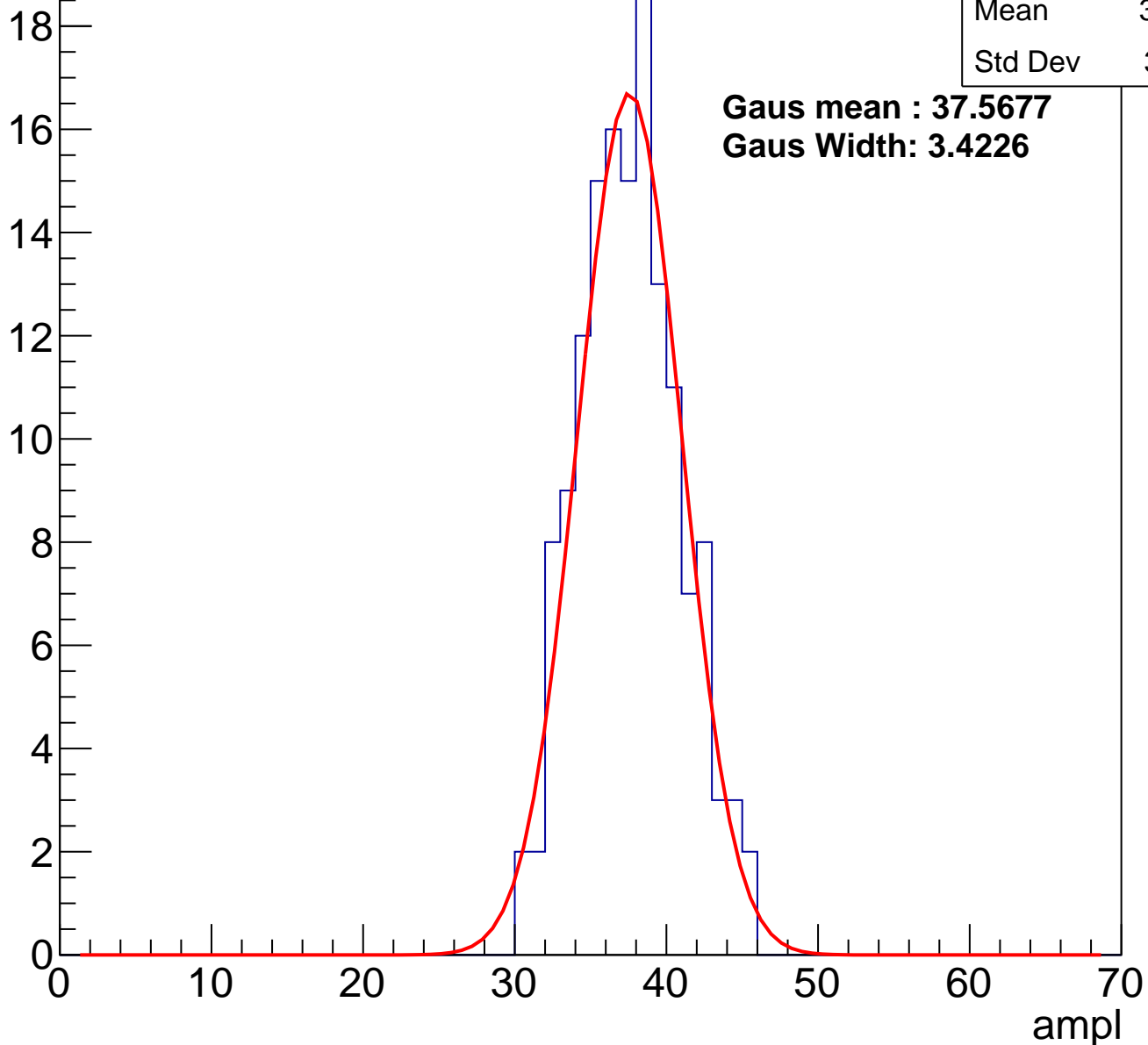
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	37.12
Std Dev	3.281

Gaus mean : 37.5677

Gaus Width: 3.4226

Entry



B1L001S, U20-ch30, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

108

Mean

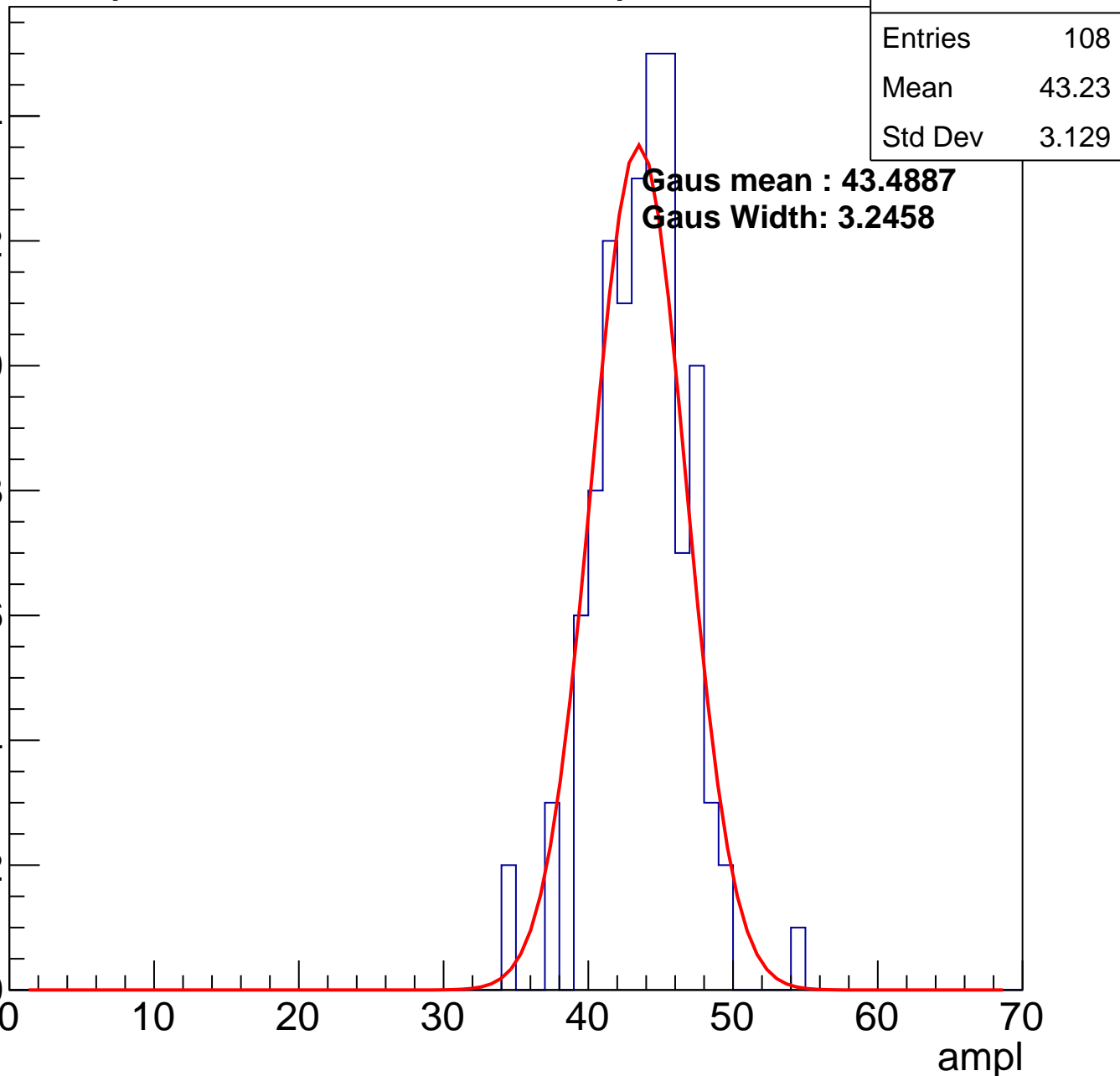
43.23

Std Dev

3.129

Gaus mean : 43.4887

Gaus Width: 3.2458



B1L001S, U20-ch30, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	49.5
Std Dev	3.517

Entry

16

14

12

10

8

6

4

2

0

0

10

20

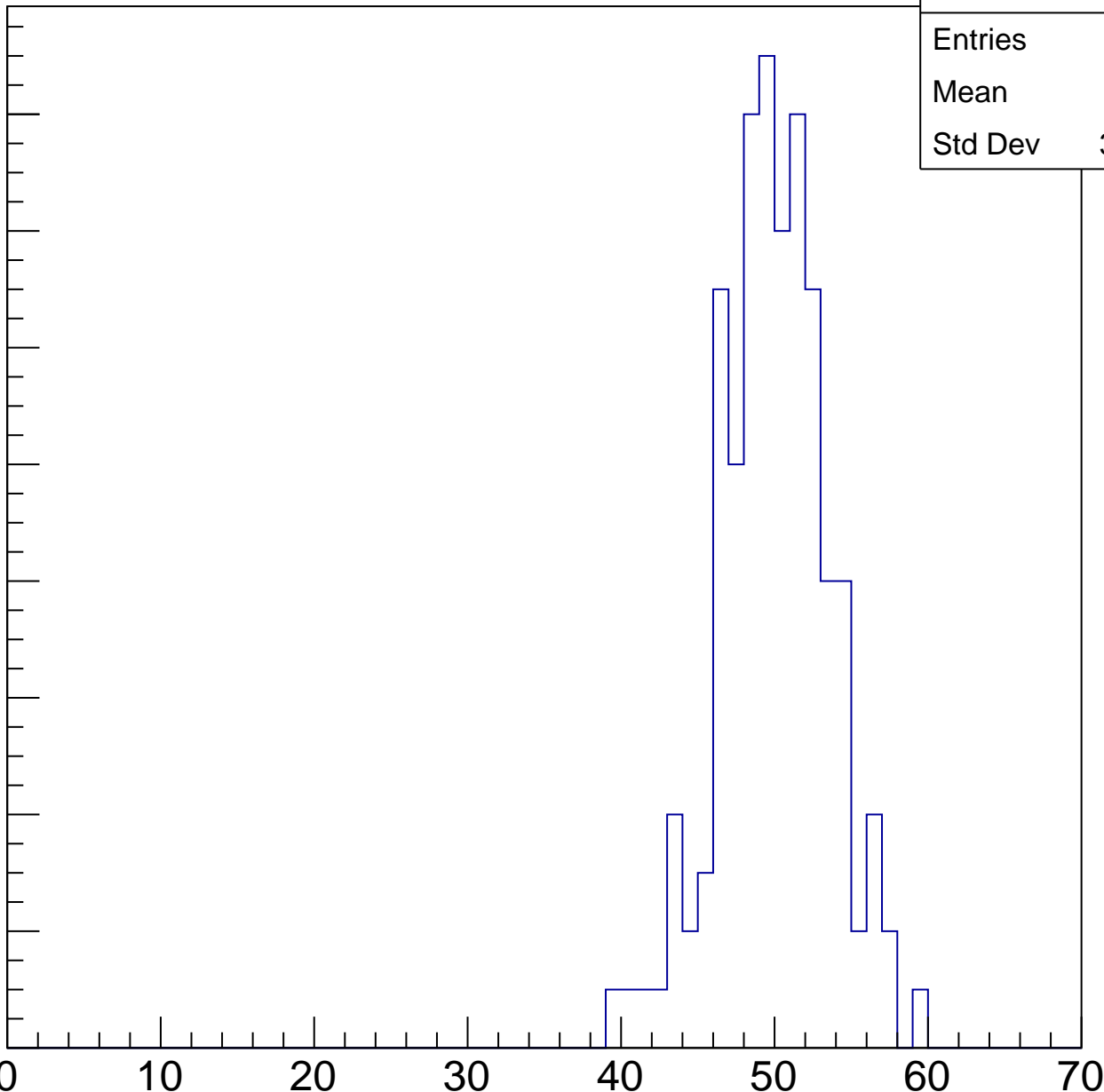
30

40

50

60

ampl

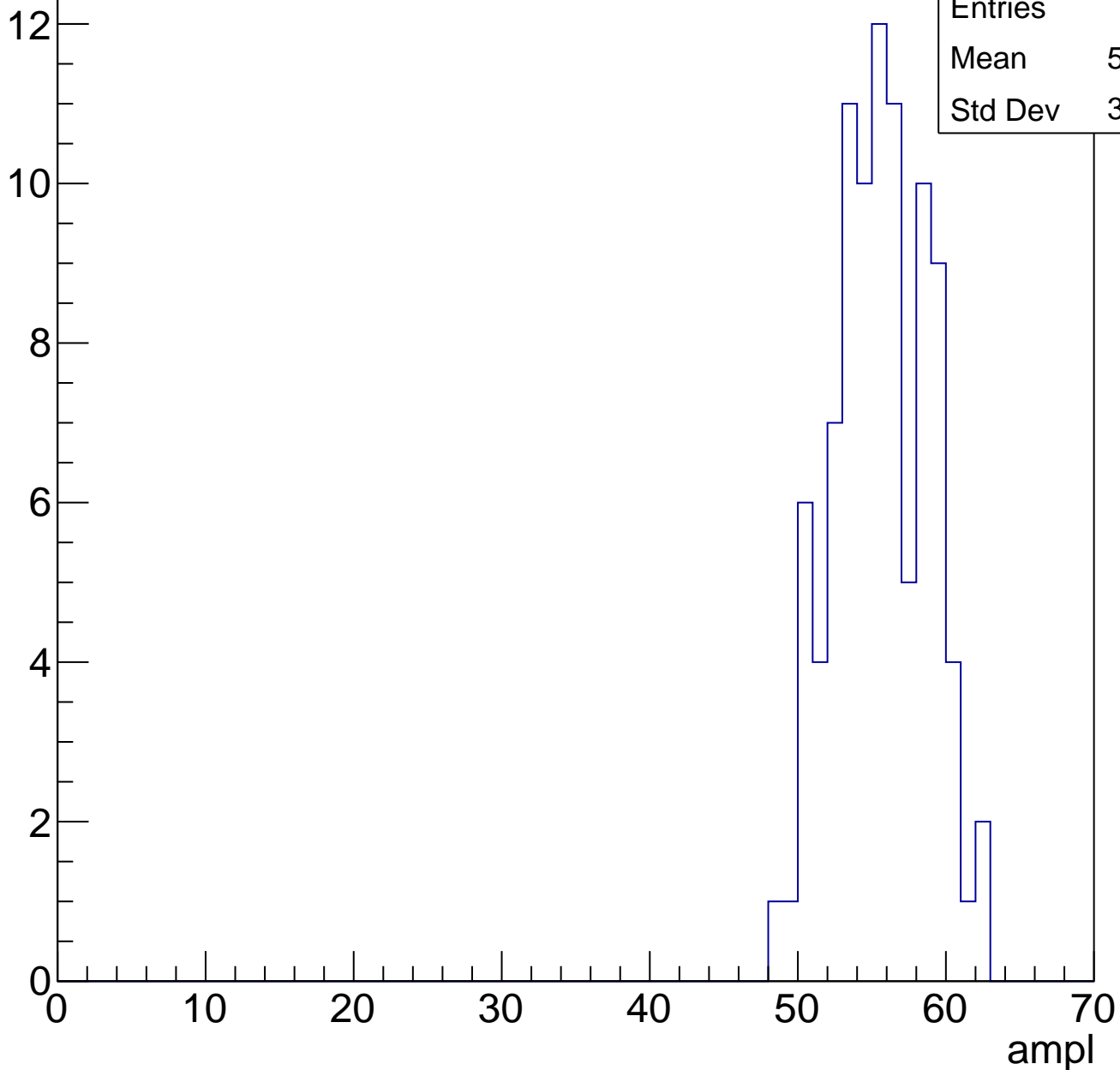


B1L001S, U20-ch30, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	94
Mean	55.16
Std Dev	3.116

Entry



B1L001S, U20-ch30, adc5

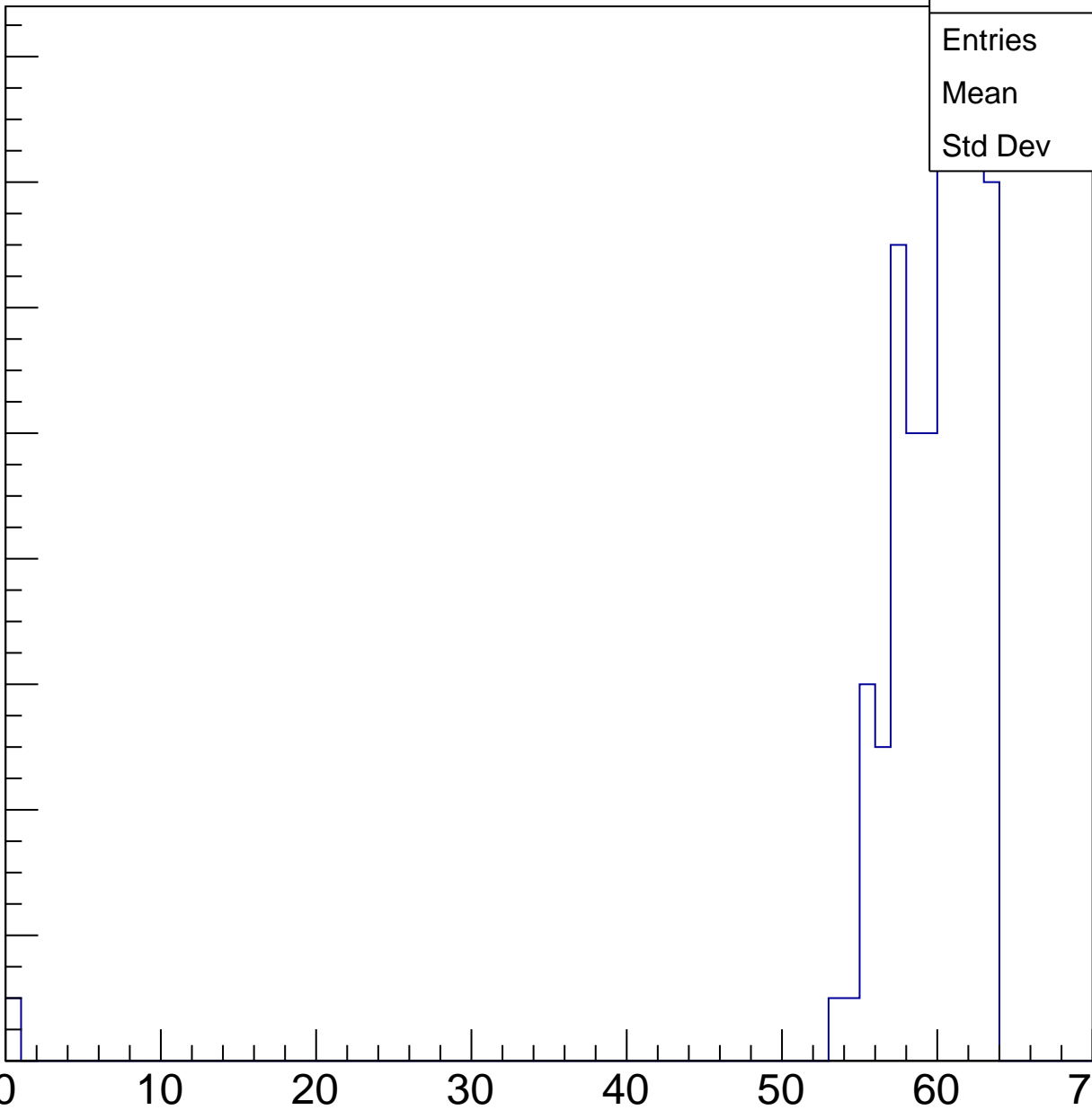
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	108
Mean	59.04
Std Dev	6.224

ampl

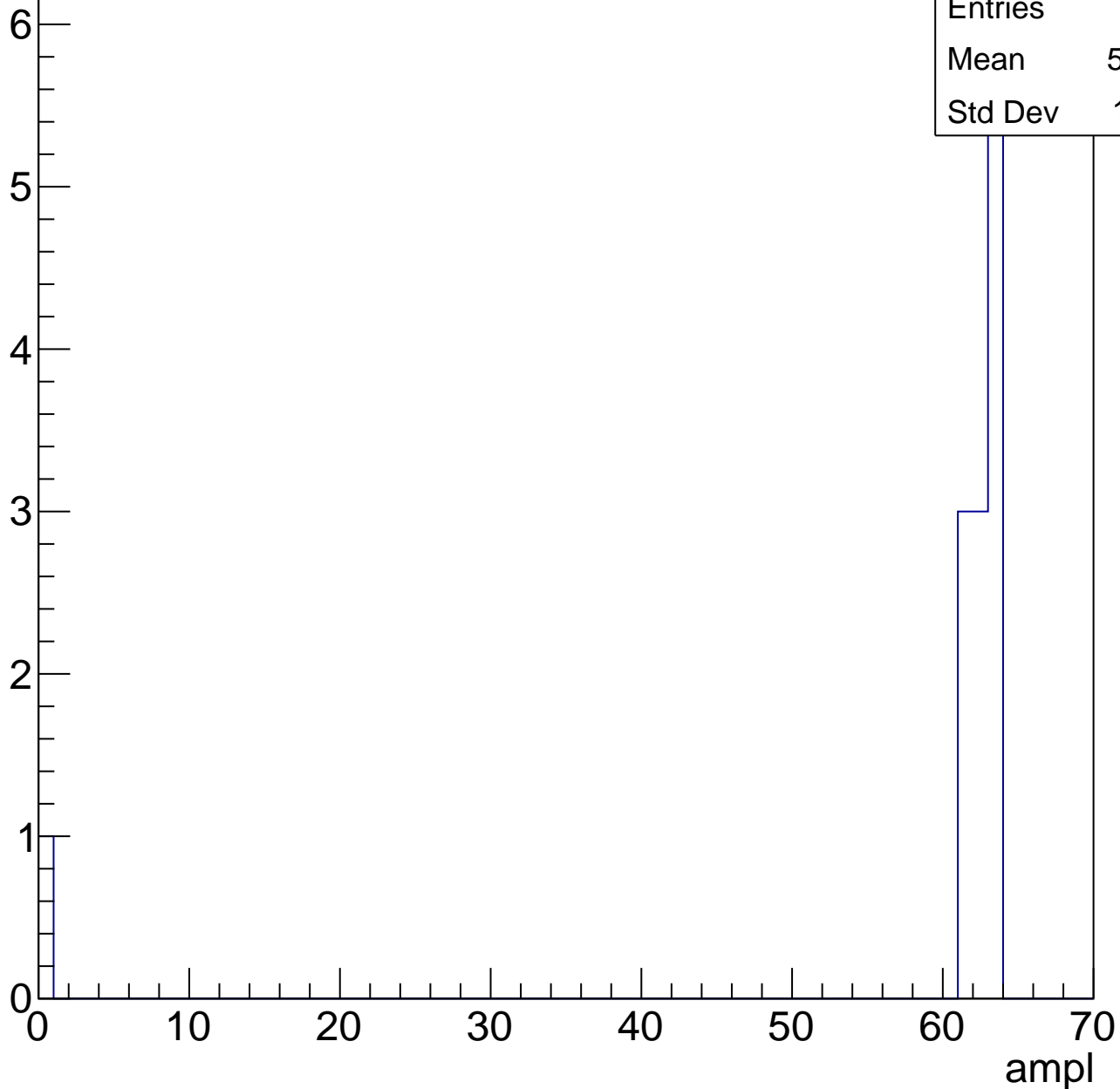


B1L001S, U20-ch30, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

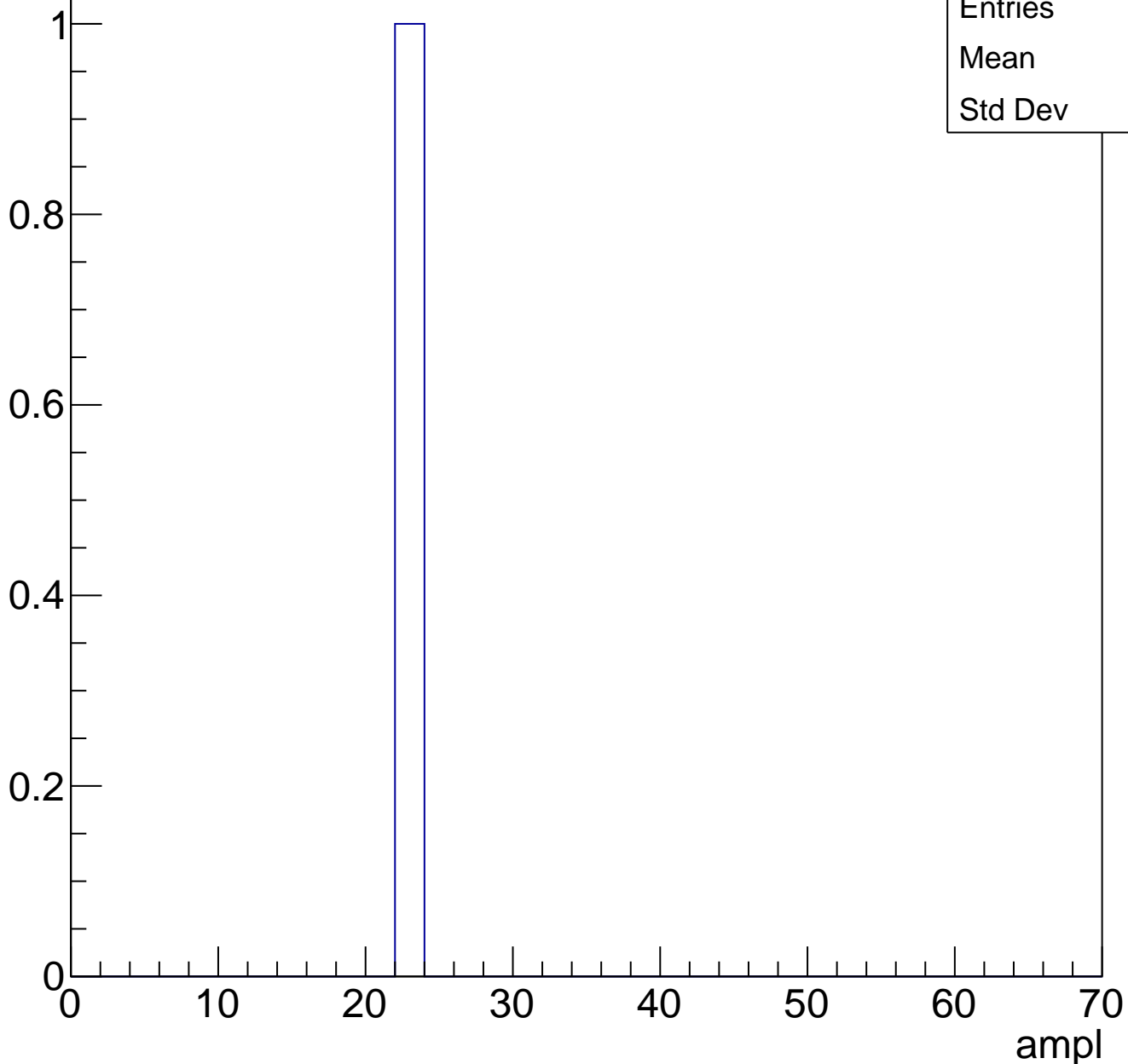
Entries	13
Mean	57.46
Std Dev	16.61



B1L001S, U20-ch30, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	22.5
Std Dev	0.5

B1L001S, U20-ch31, adc0

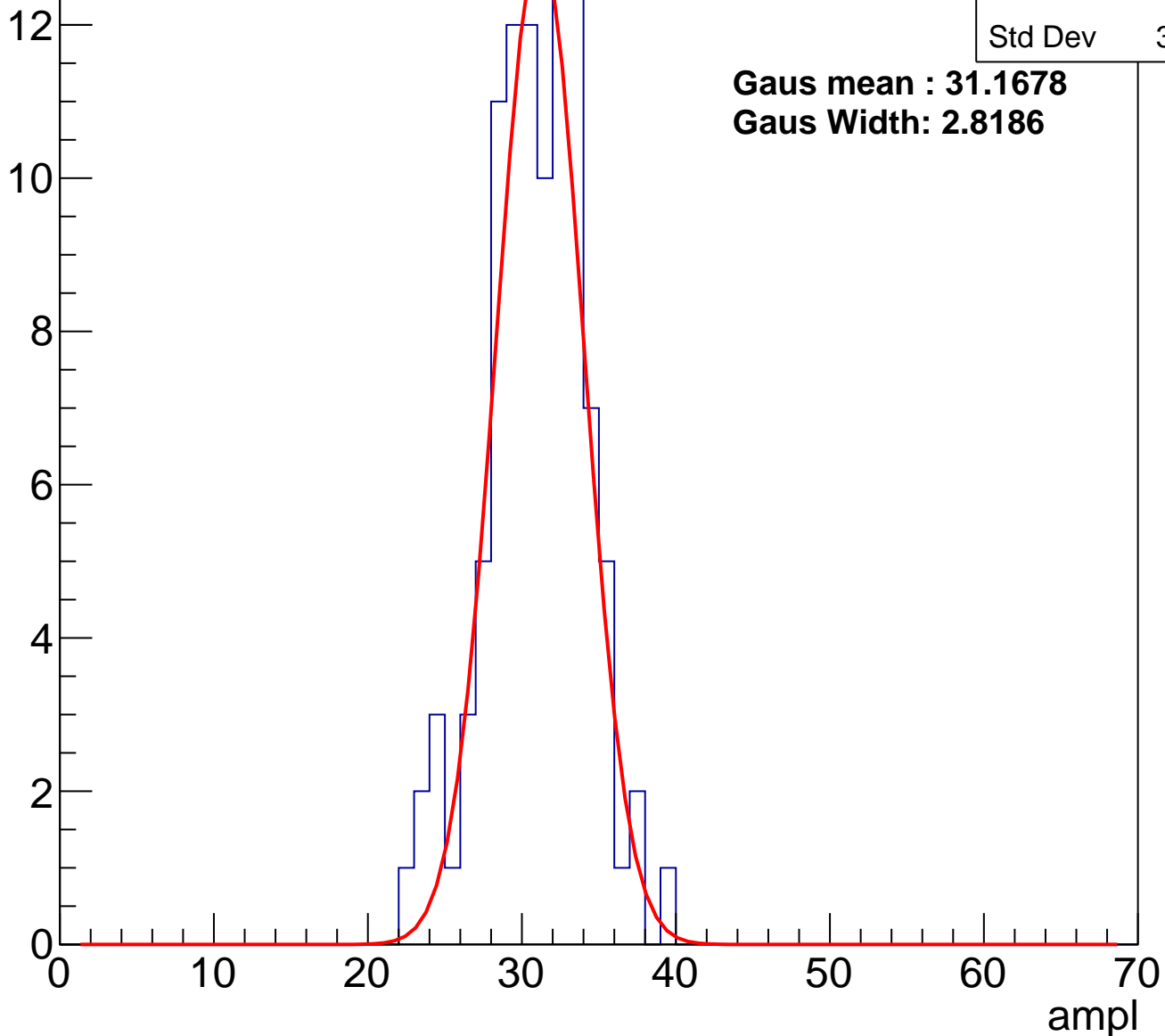
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	30.5
Std Dev	3.208

Gaus mean : 31.1678

Gaus Width: 2.8186

Entry

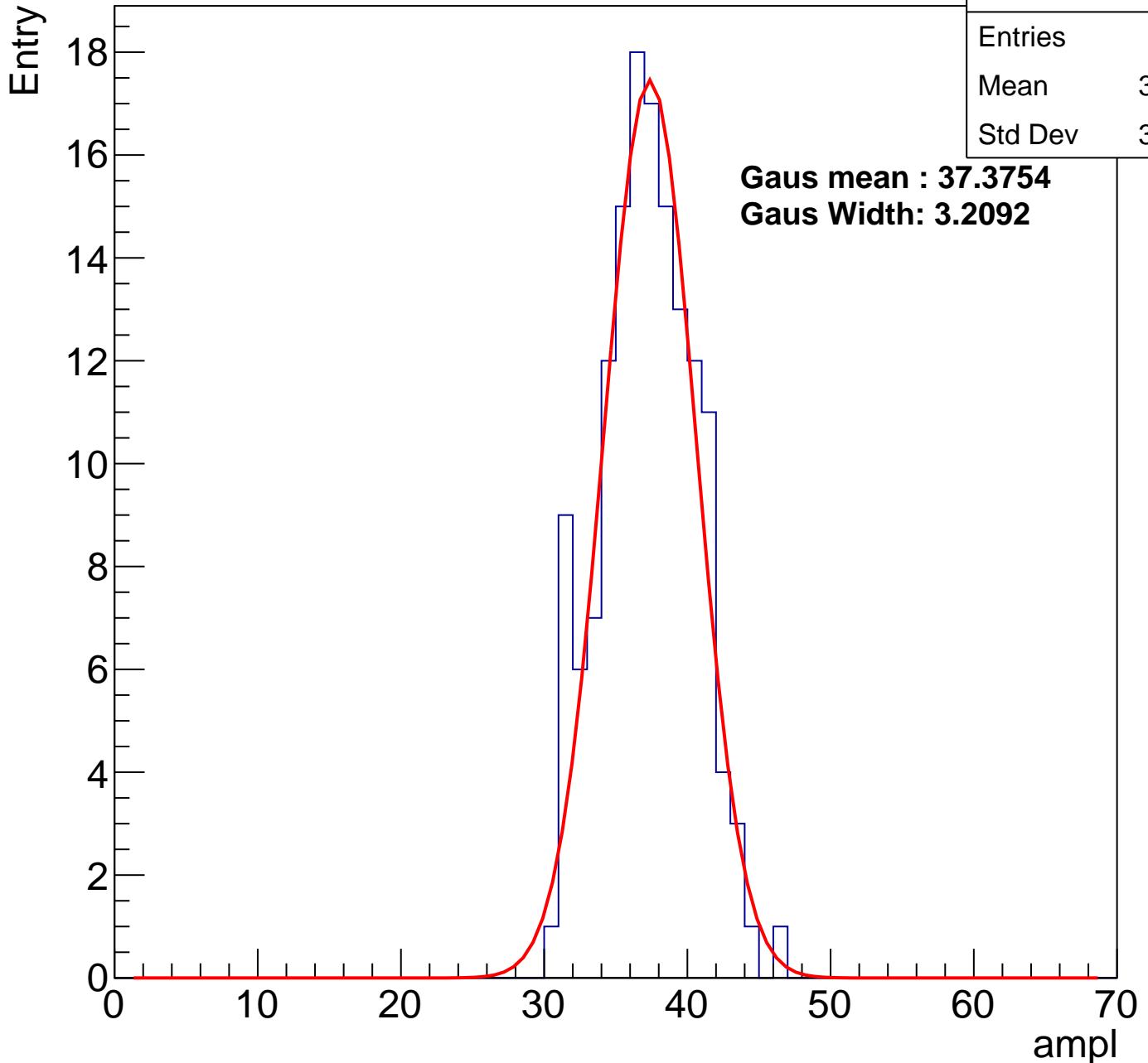


B1L001S, U20-ch31, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	36.81
Std Dev	3.221

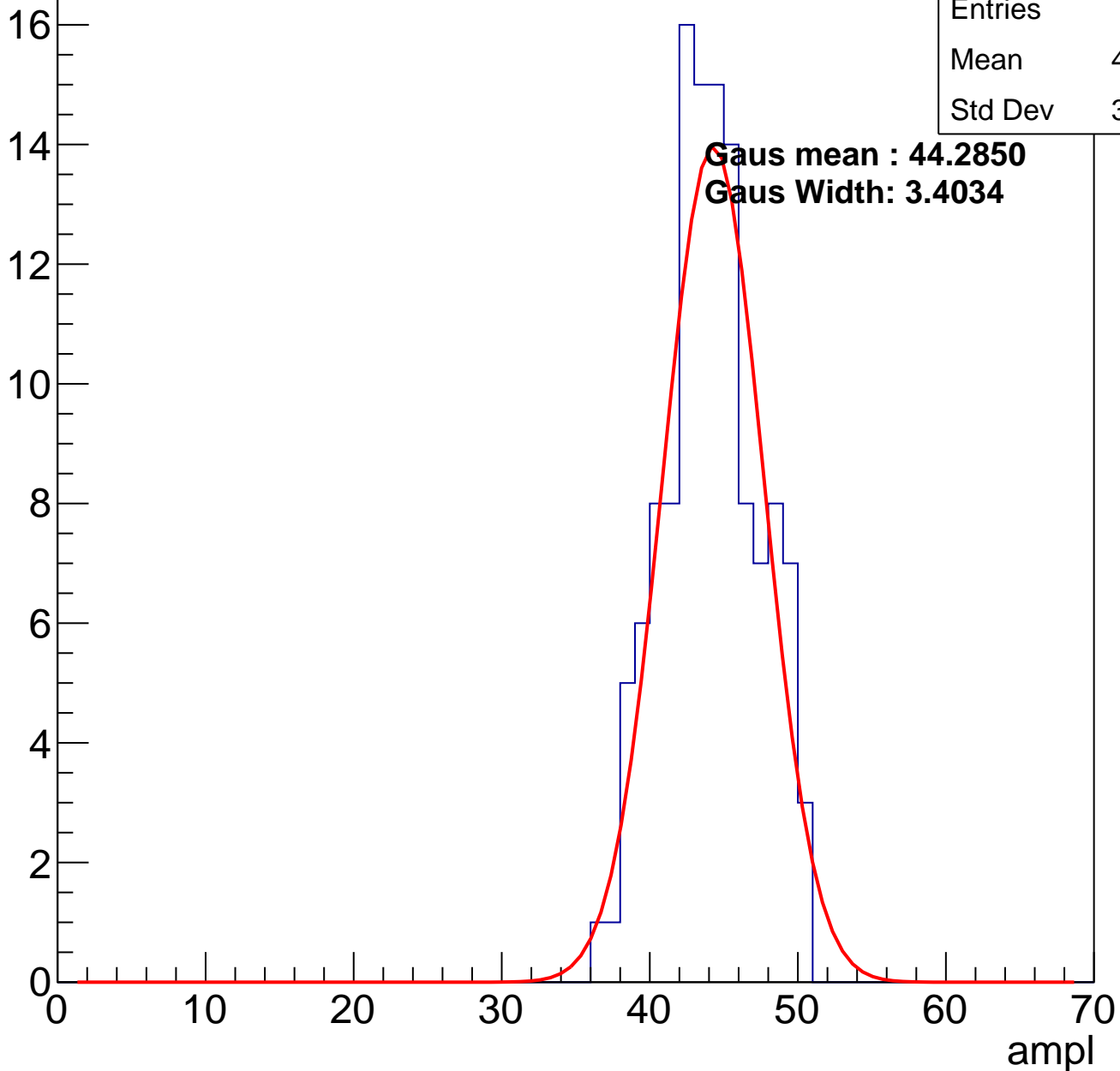
Gaus mean : 37.3754
Gaus Width: 3.2092



B1L001S, U20-ch31, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

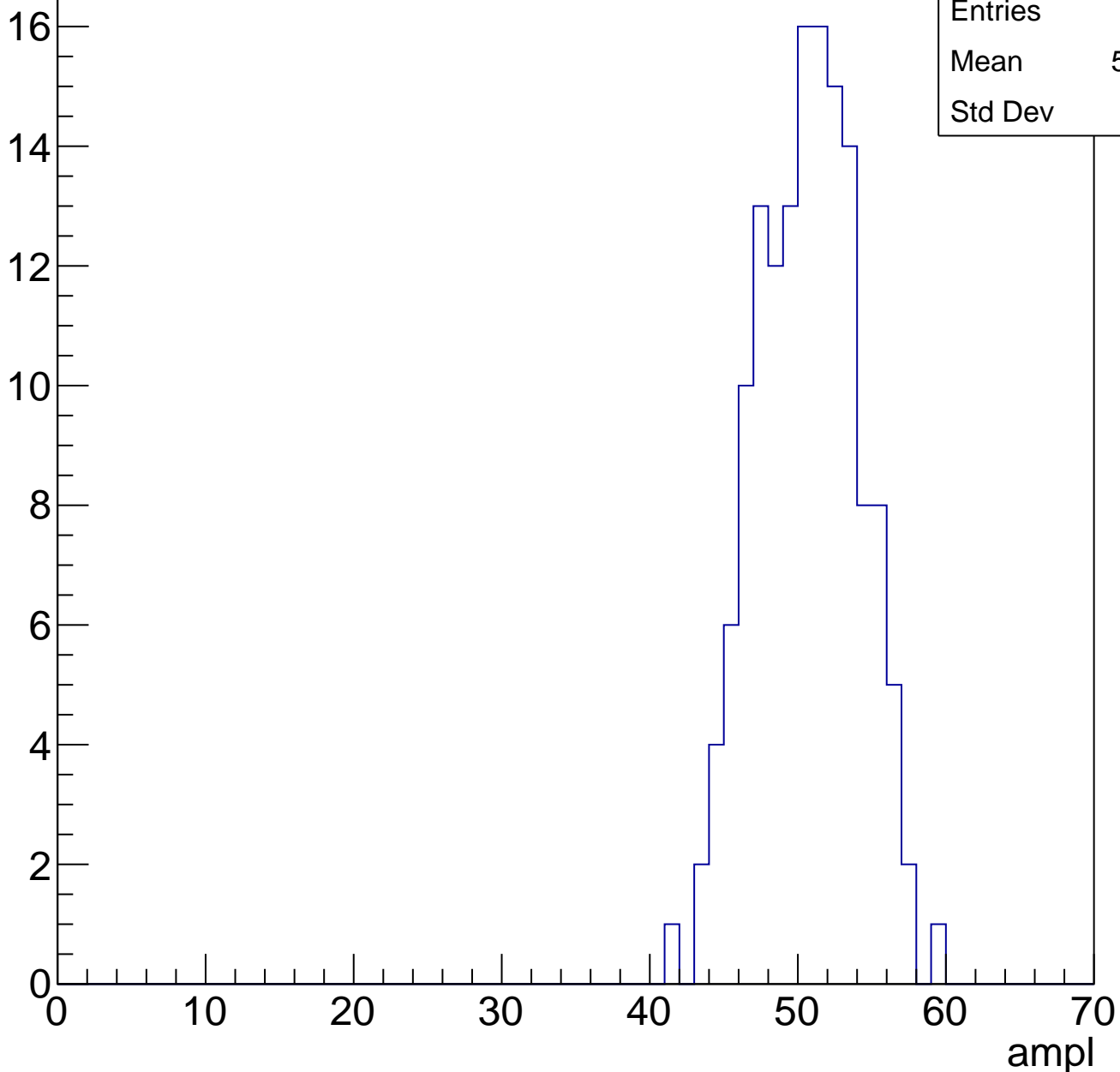


B1L001S, U20-ch31, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	50.14
Std Dev	3.41

Entry



B1L001S, U20-ch31, adc4

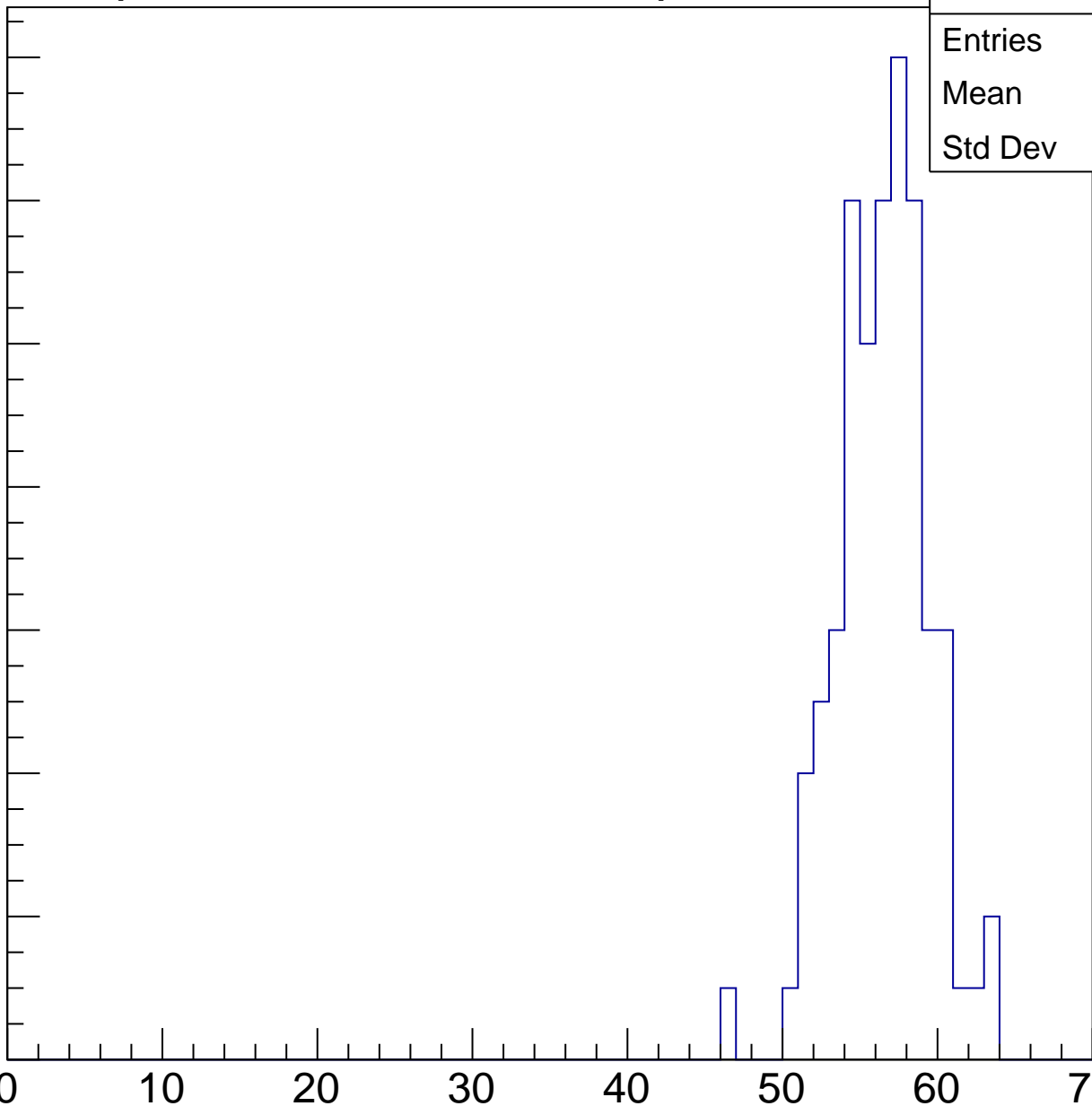
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	55.97
Std Dev	2.931

ampl

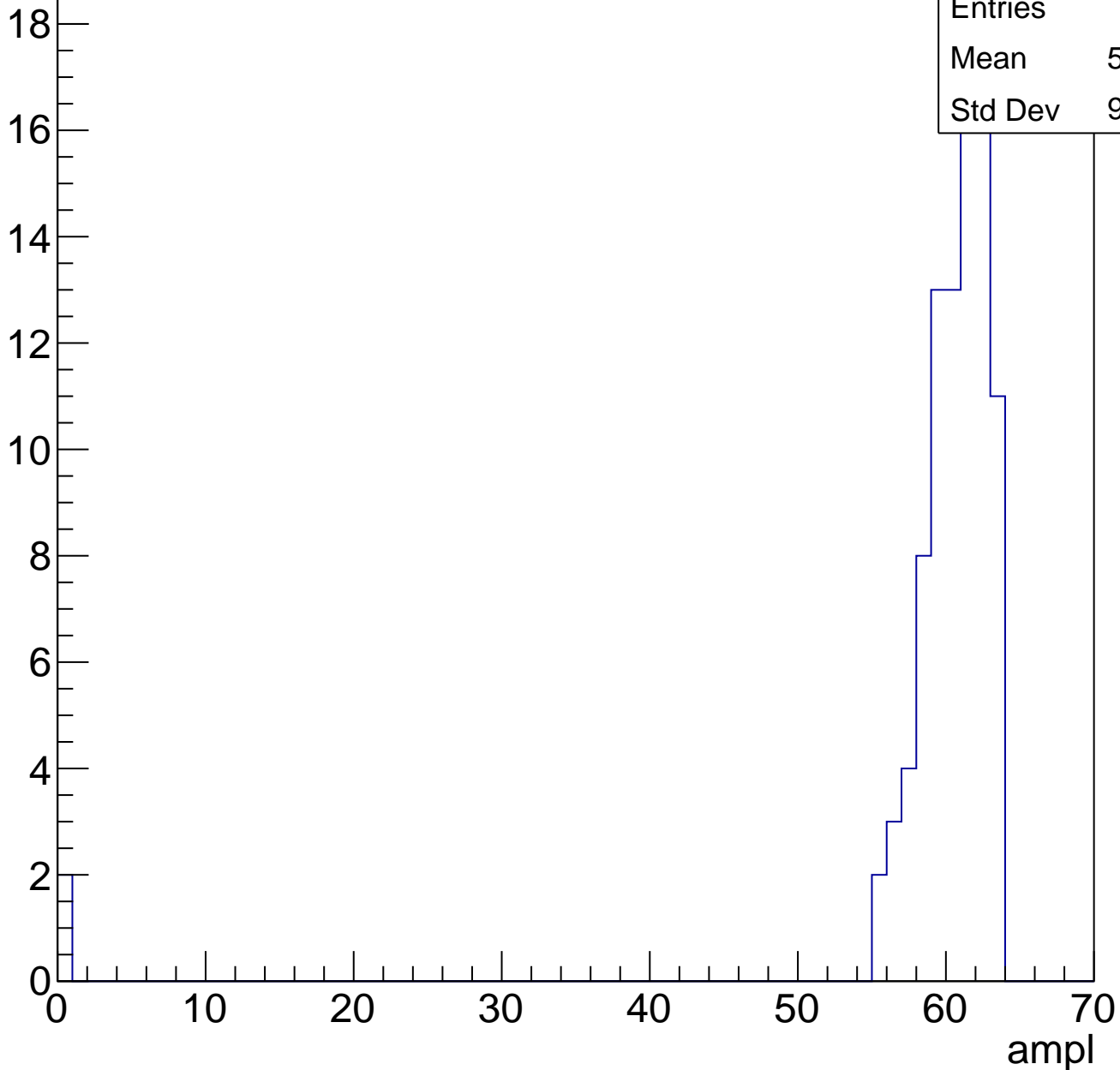


B1L001S, U20-ch31, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	90
Mean	58.89
Std Dev	9.098

Entry

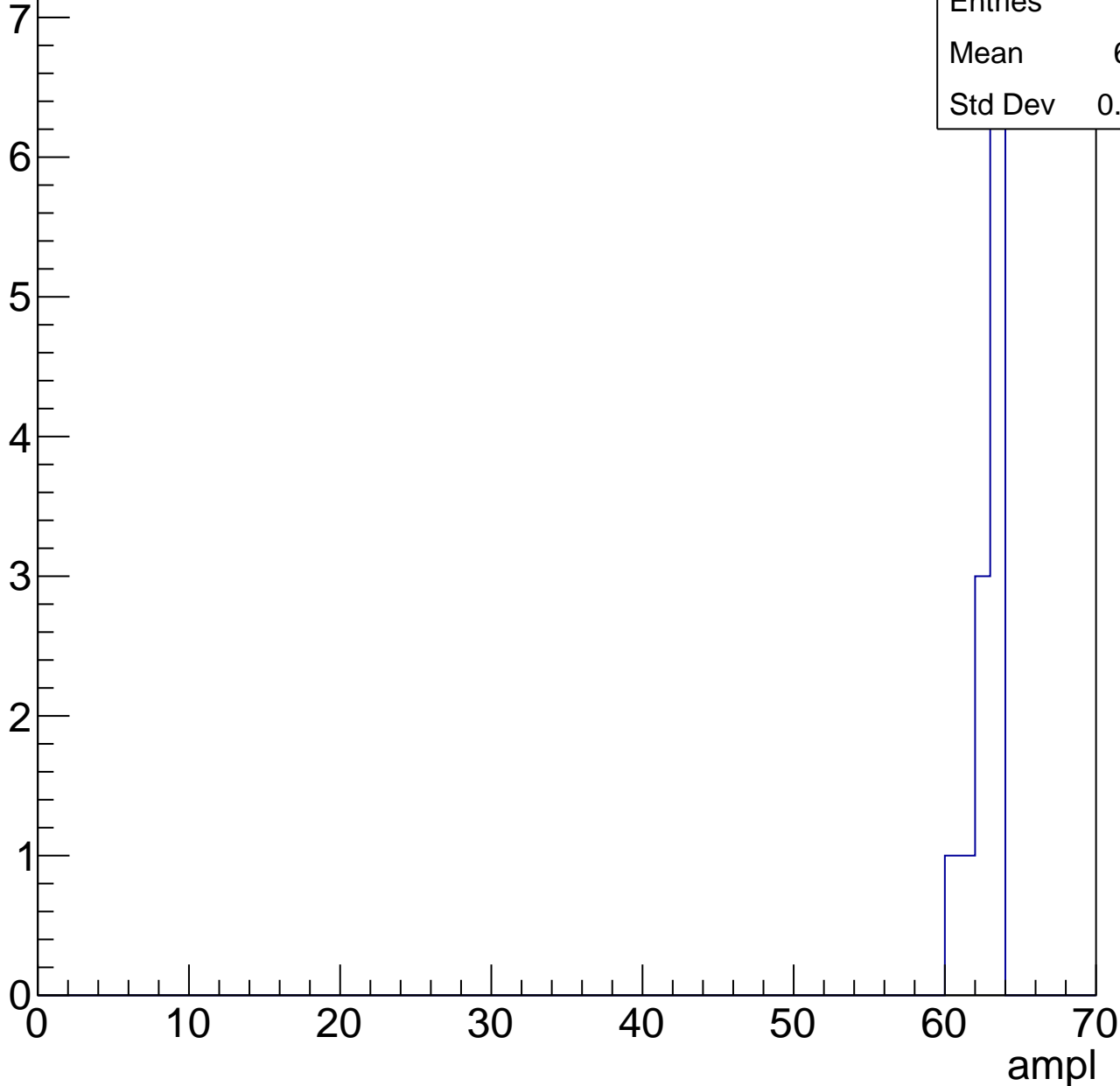


B1L001S, U20-ch31, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	62.33
Std Dev	0.9428



B1L001S, U20-ch31, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U20-ch32, adc0

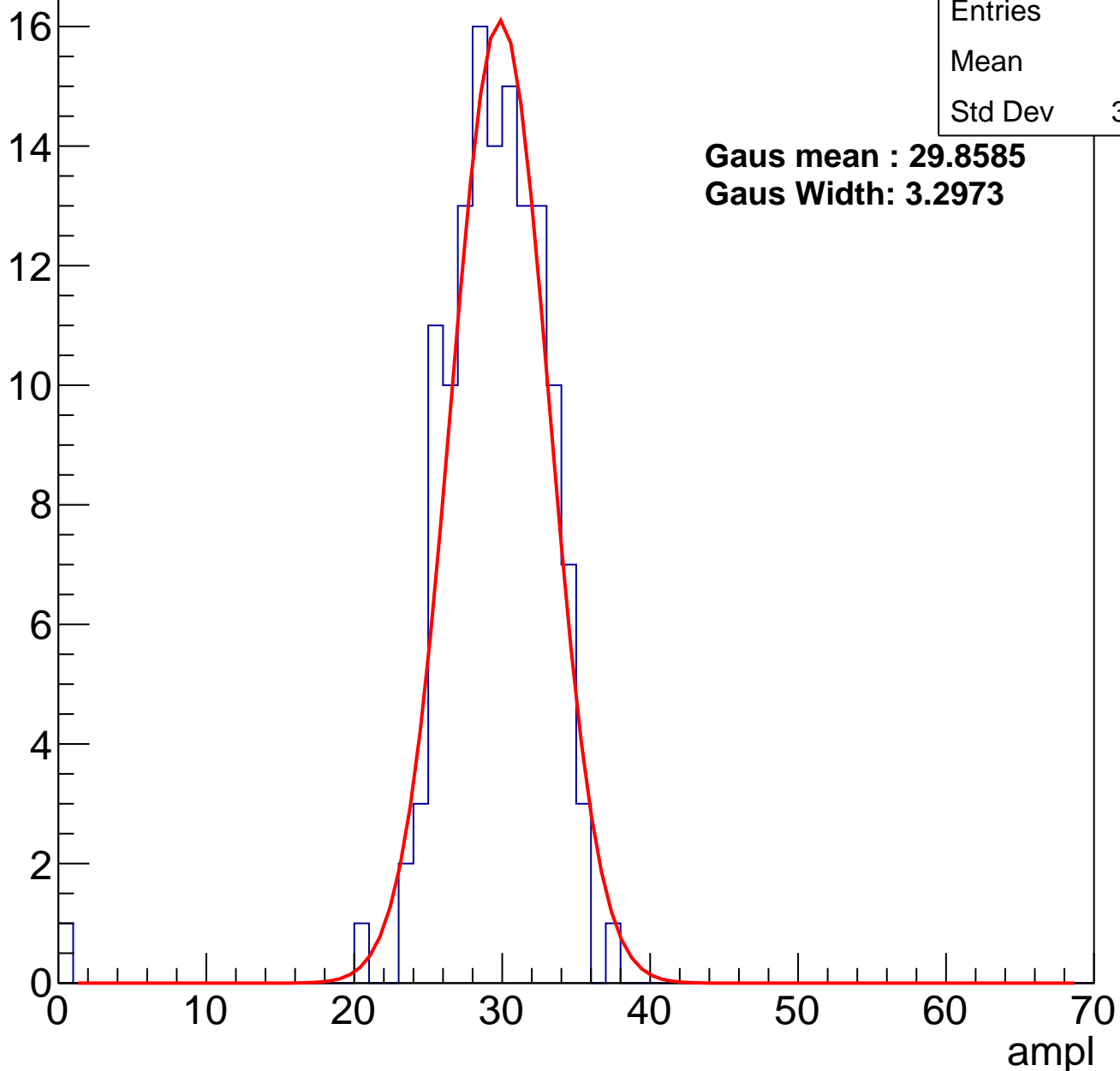
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	29
Std Dev	3.968

Gaus mean : 29.8585

Gaus Width: 3.2973

Entry



B1L001S, U20-ch32, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	35.91
Std Dev	3.278

Gaus mean : 36.2183

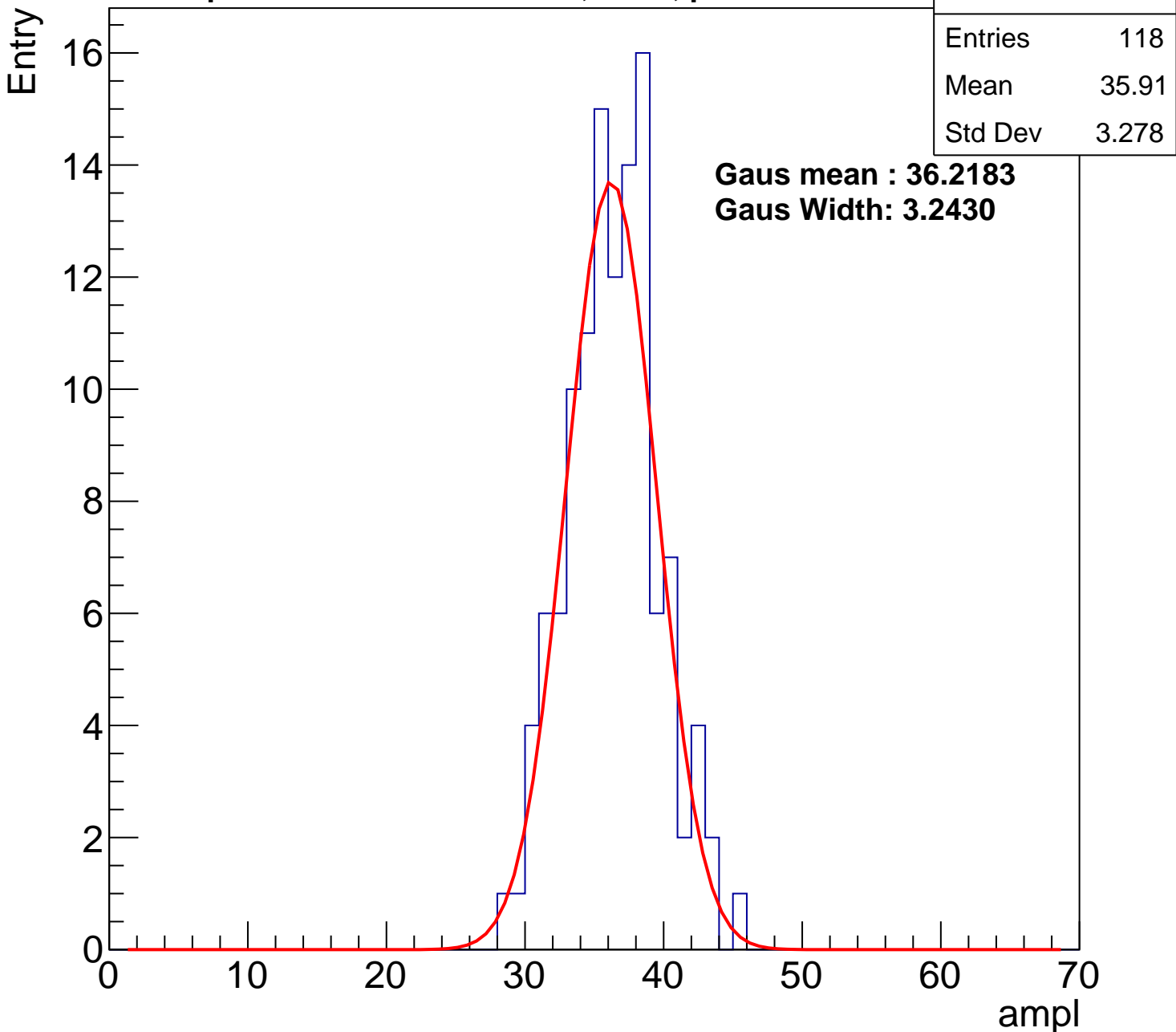
Gaus Width: 3.2430

Entry

16
14
12
10
8
6
4
2
0

ampl

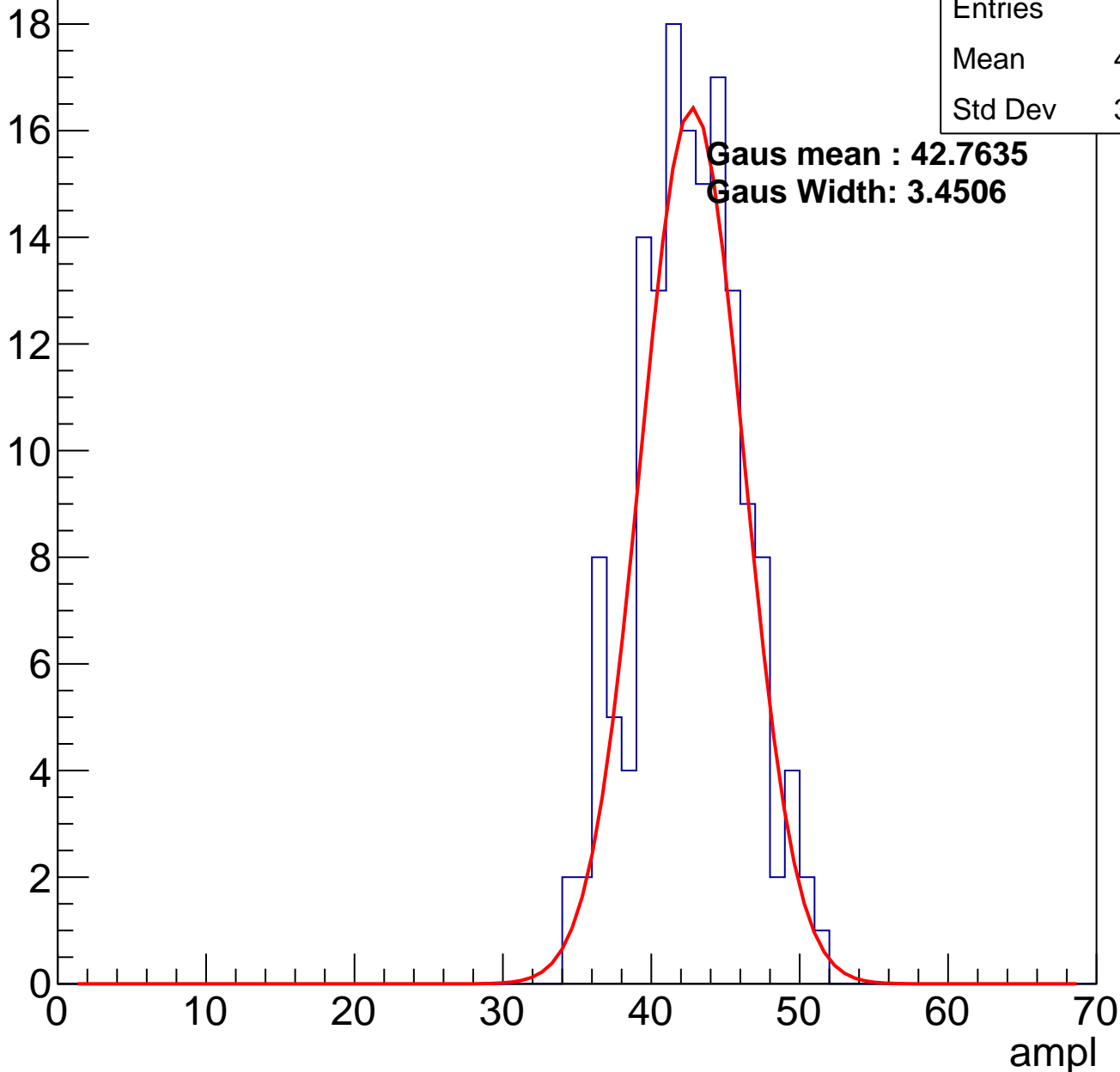
0 10 20 30 40 50 60 70



B1L001S, U20-ch32, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch32, adc3

calib_packv5_042523_0143.root, FC#2, port C2

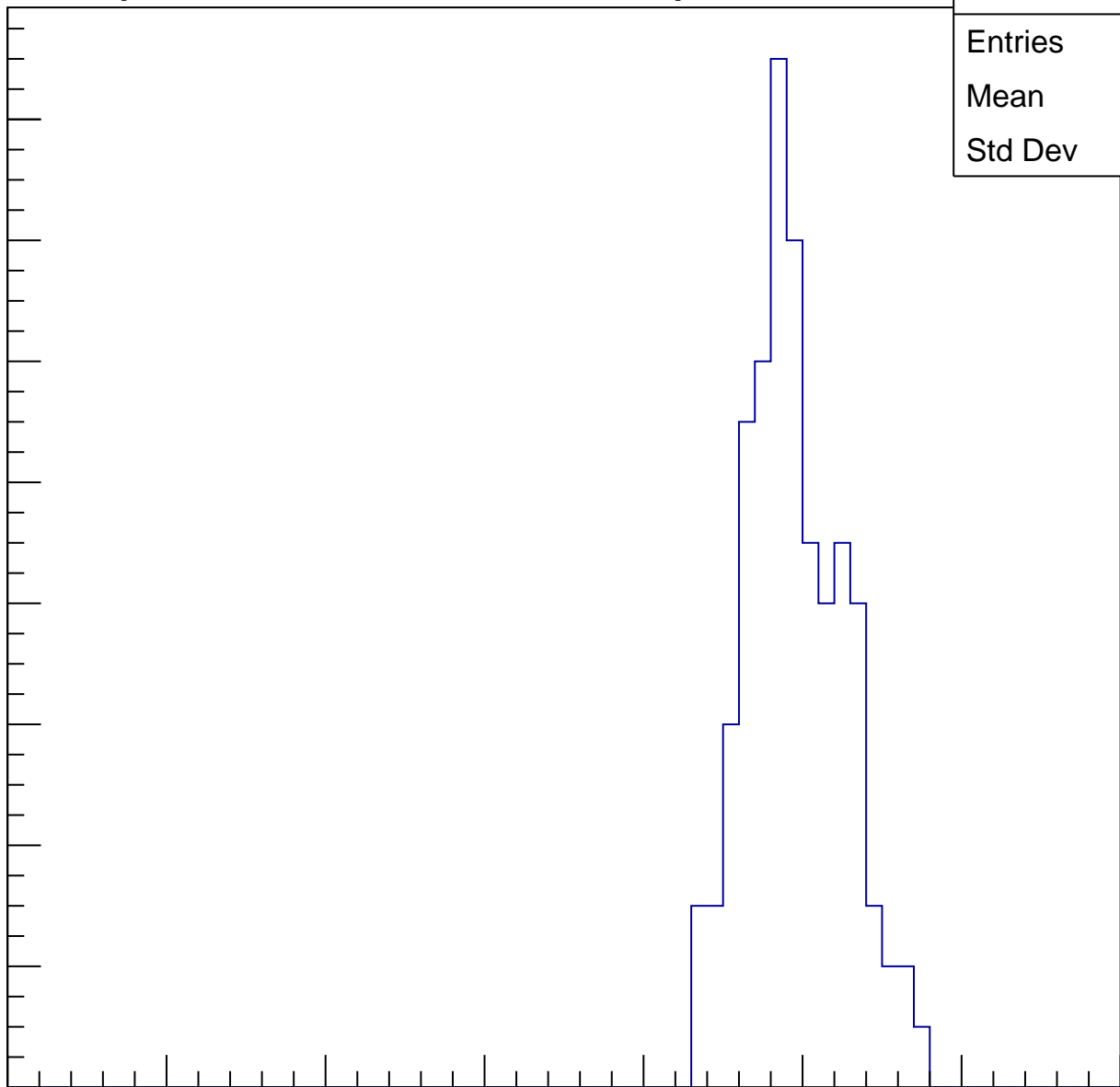
Entries	108
Mean	49.02
Std Dev	3.046

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch32, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

129

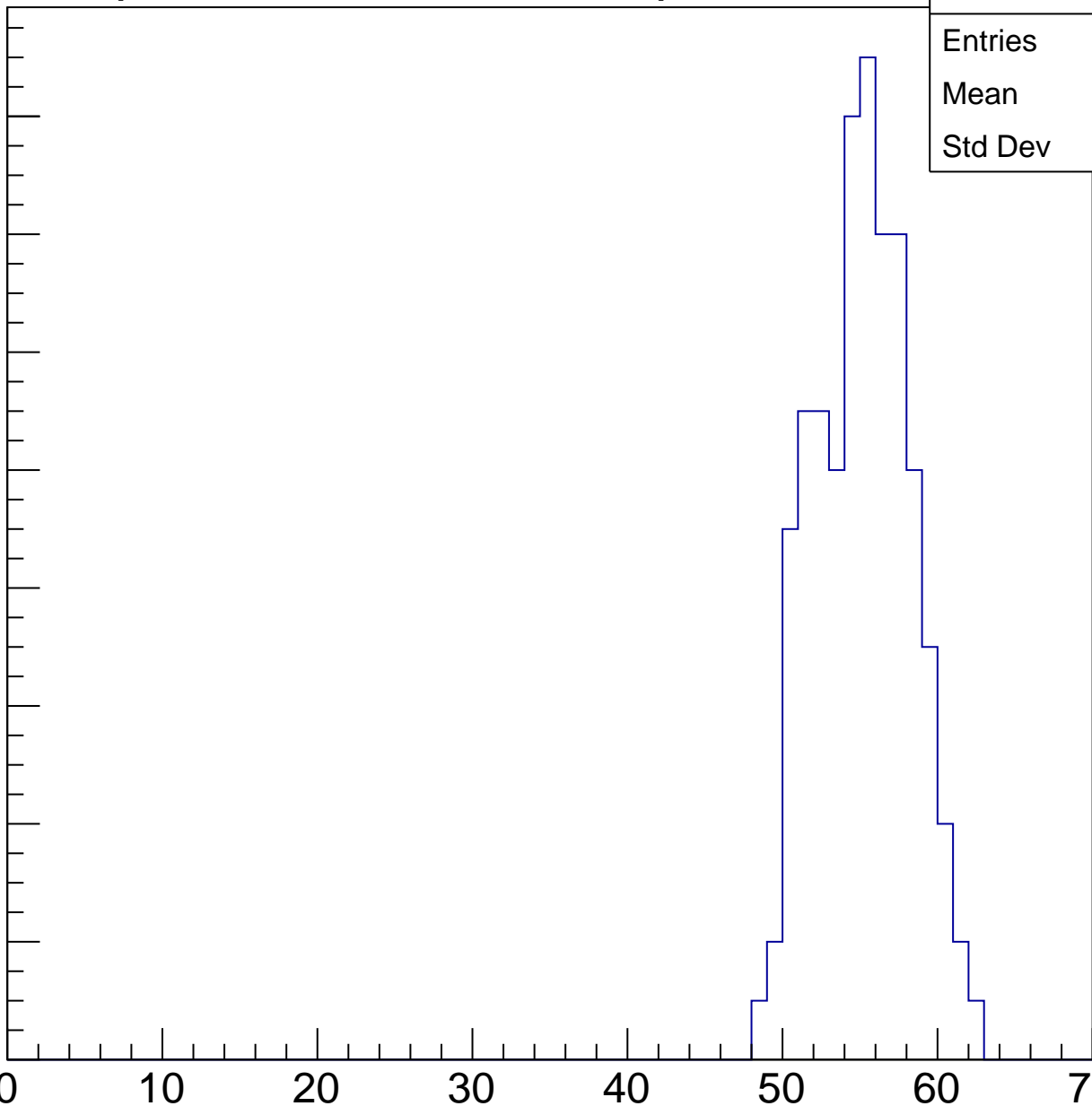
Mean

54.71

Std Dev

2.997

ampl



B1L001S, U20-ch32, adc5

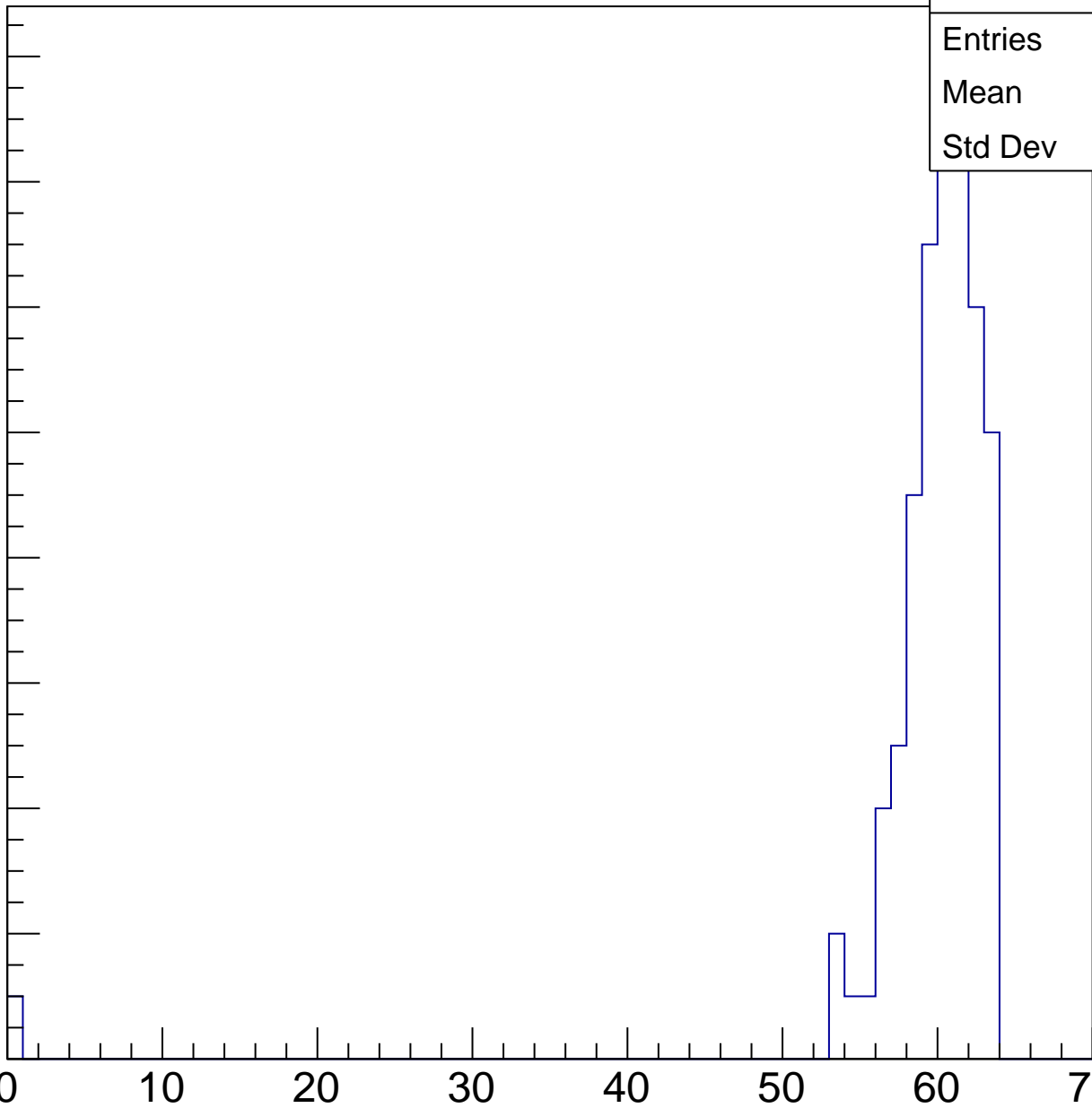
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	59.13
Std Dev	6.708

ampl

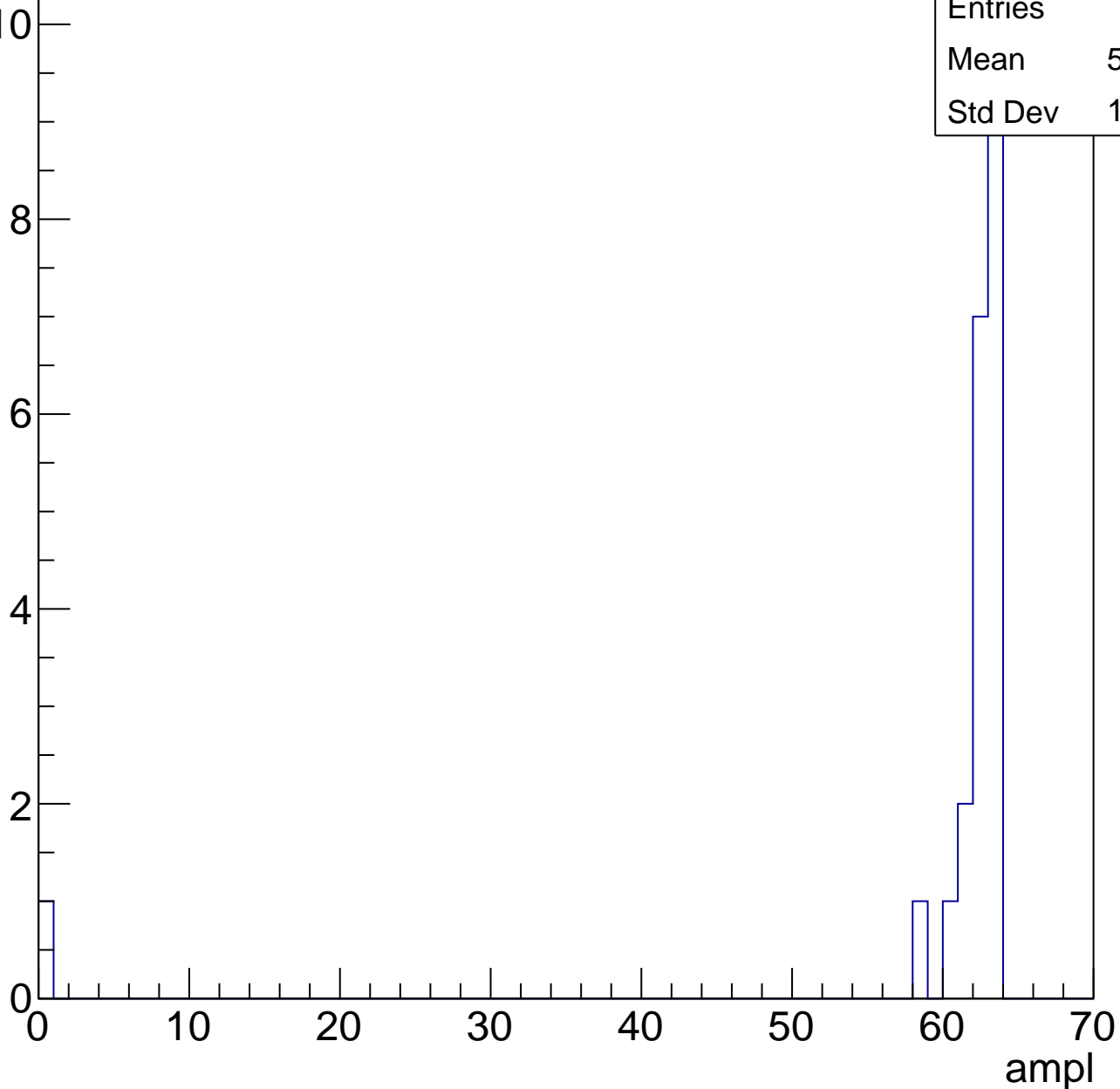


B1L001S, U20-ch32, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	22
Mean	59.27
Std Dev	12.99



B1L001S, U20-ch32, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch33, adc0

calib_packv5_042523_0143.root, FC#2, port C2

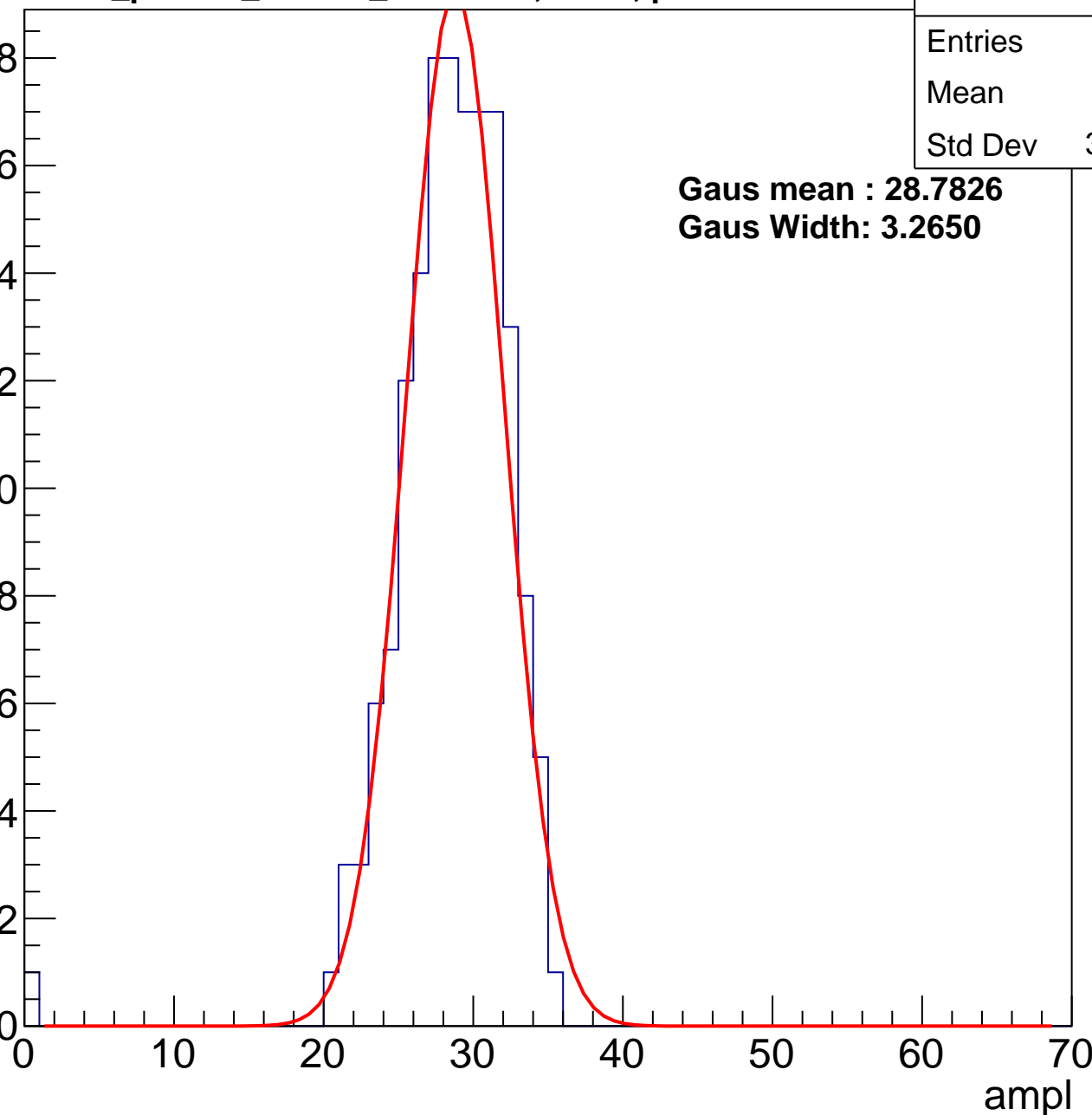
Entry

18
16
14
12
10
8
6
4
2
0

Entries	161
Mean	28.1
Std Dev	3.877

Gaus mean : 28.7826

Gaus Width: 3.2650



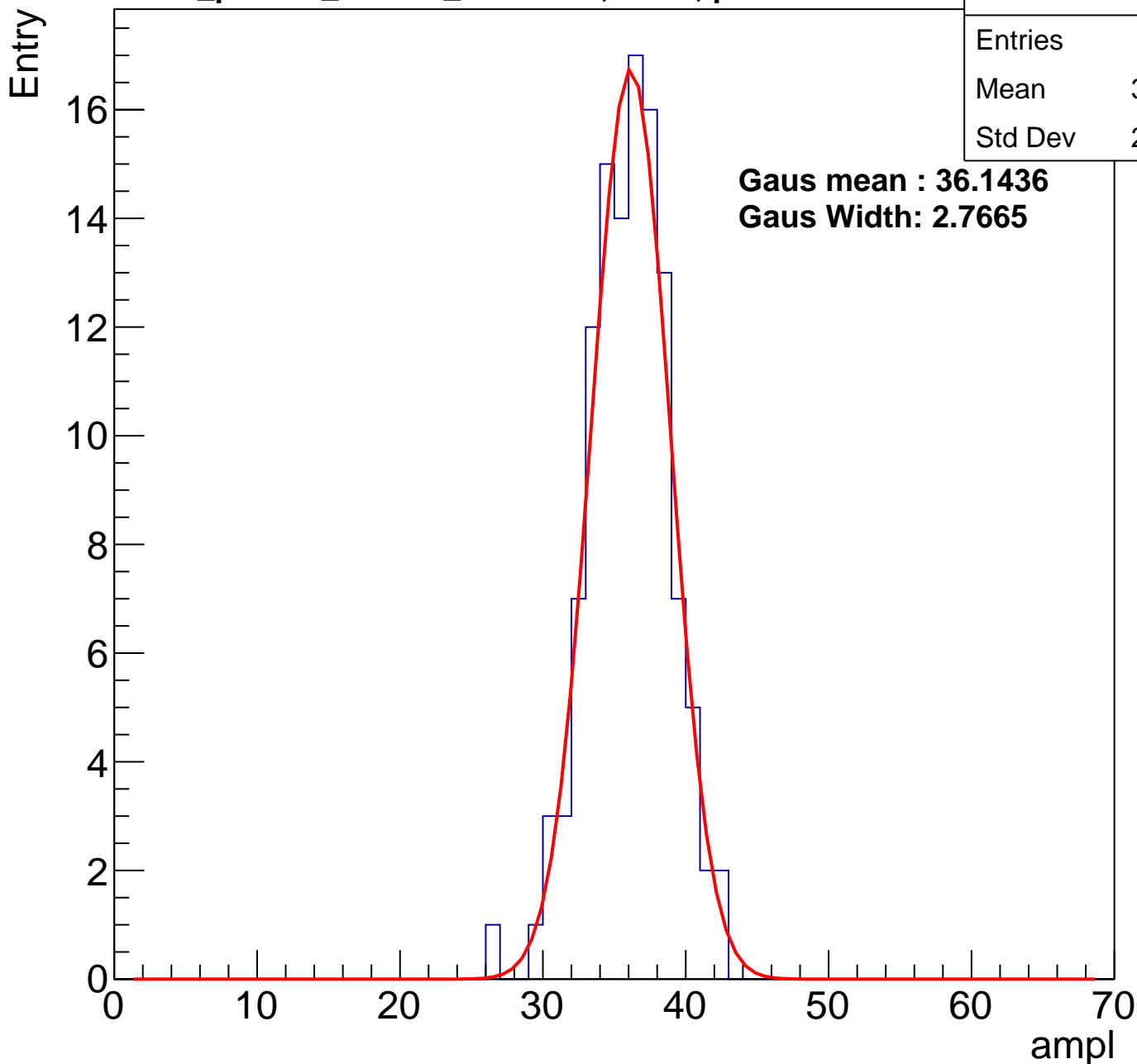
B1L001S, U20-ch33, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	35.55
Std Dev	2.815

Gaus mean : 36.1436

Gaus Width: 2.7665



B1L001S, U20-ch33, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

132

Mean

41.7

Std Dev

3.094

Gaus mean : 42.2197

Gaus Width: 3.1225

0

10

20

30

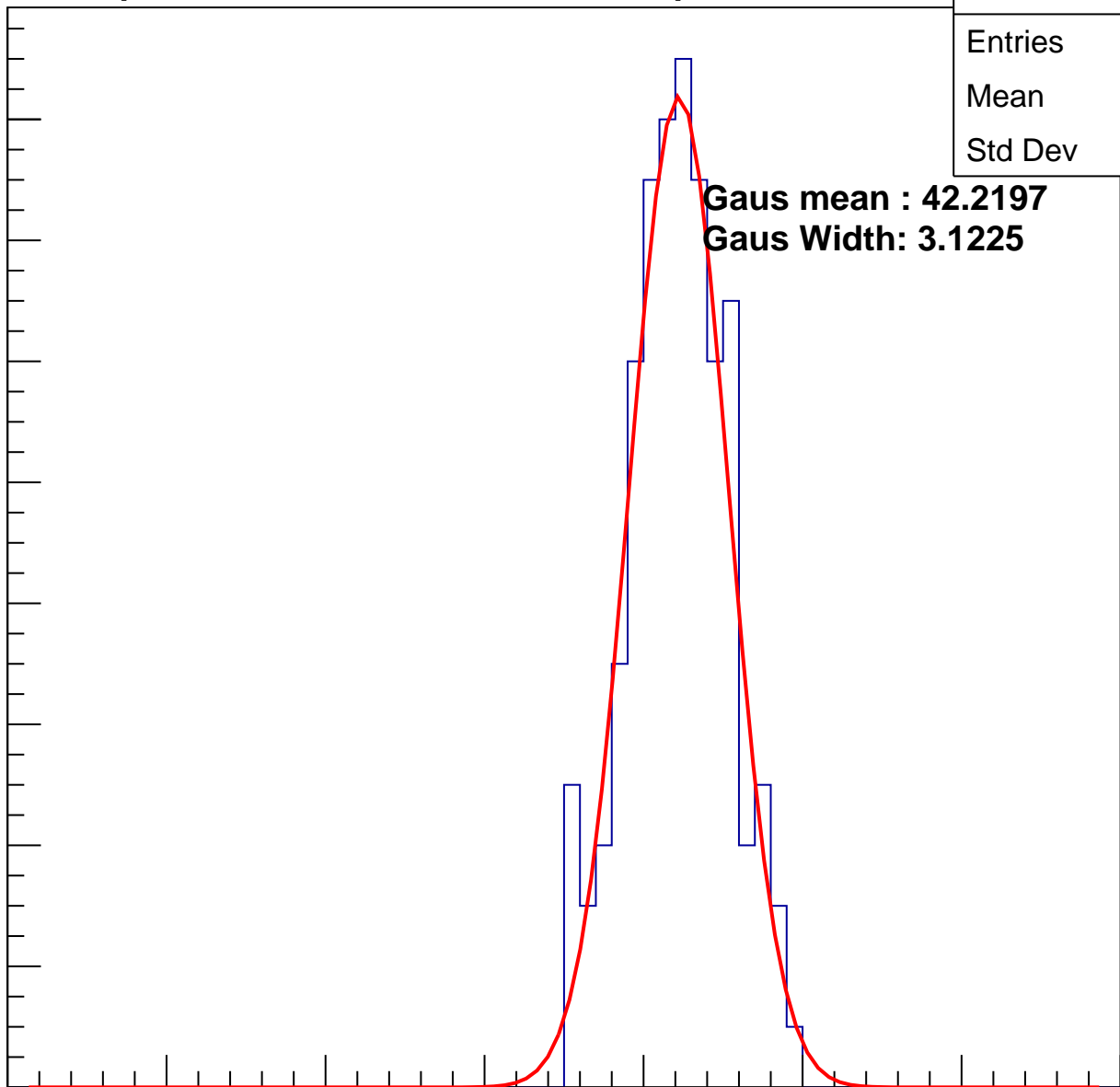
40

50

60

70

ampl



B1L001S, U20-ch33, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	48.98
Std Dev	3.621

Entry

16

14

12

10

8

6

4

2

0

0

10

20

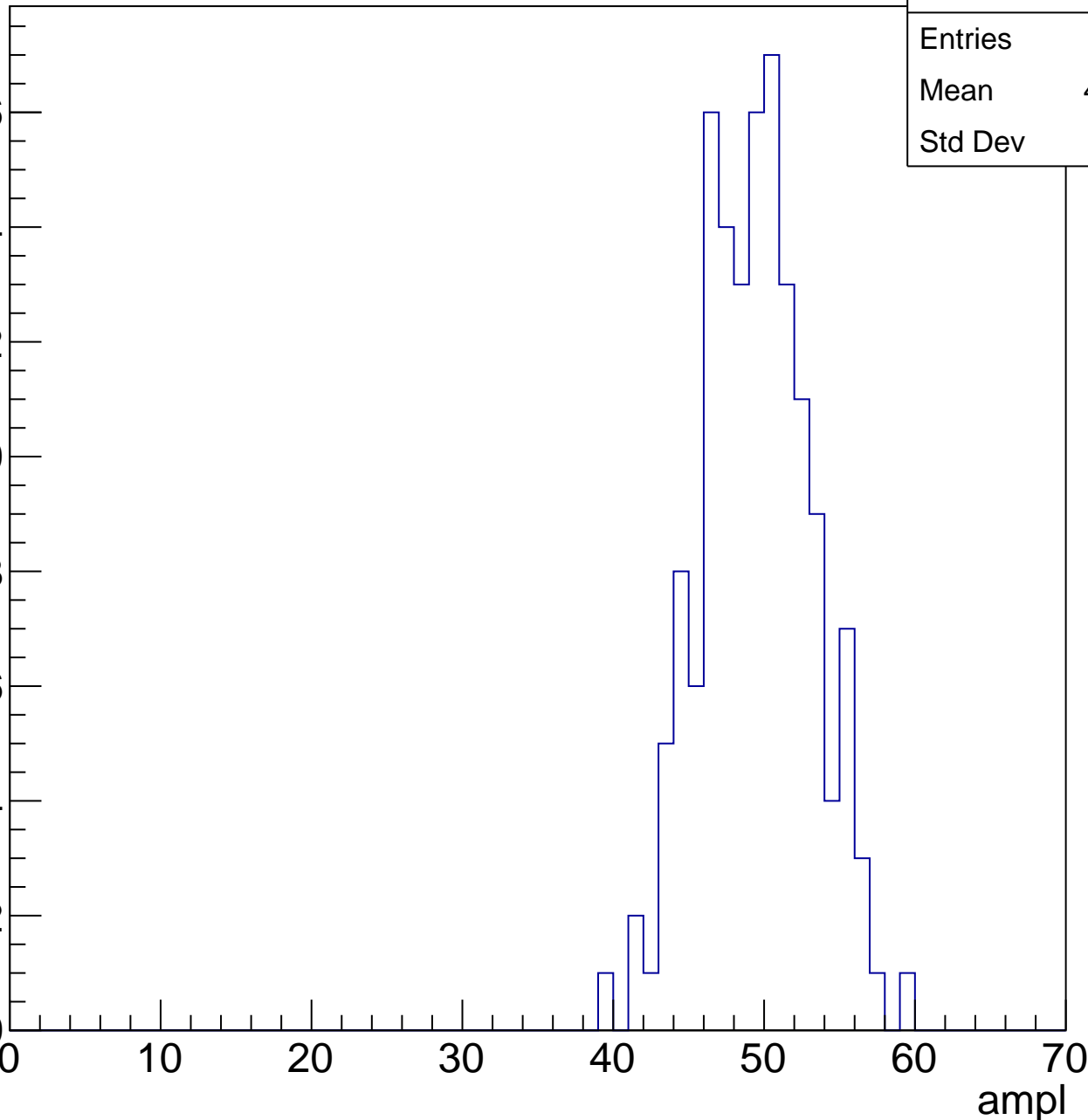
30

40

50

60

ampl



B1L001S, U20-ch33, adc4

calib_packv5_042523_0143.root, FC#2, port C2

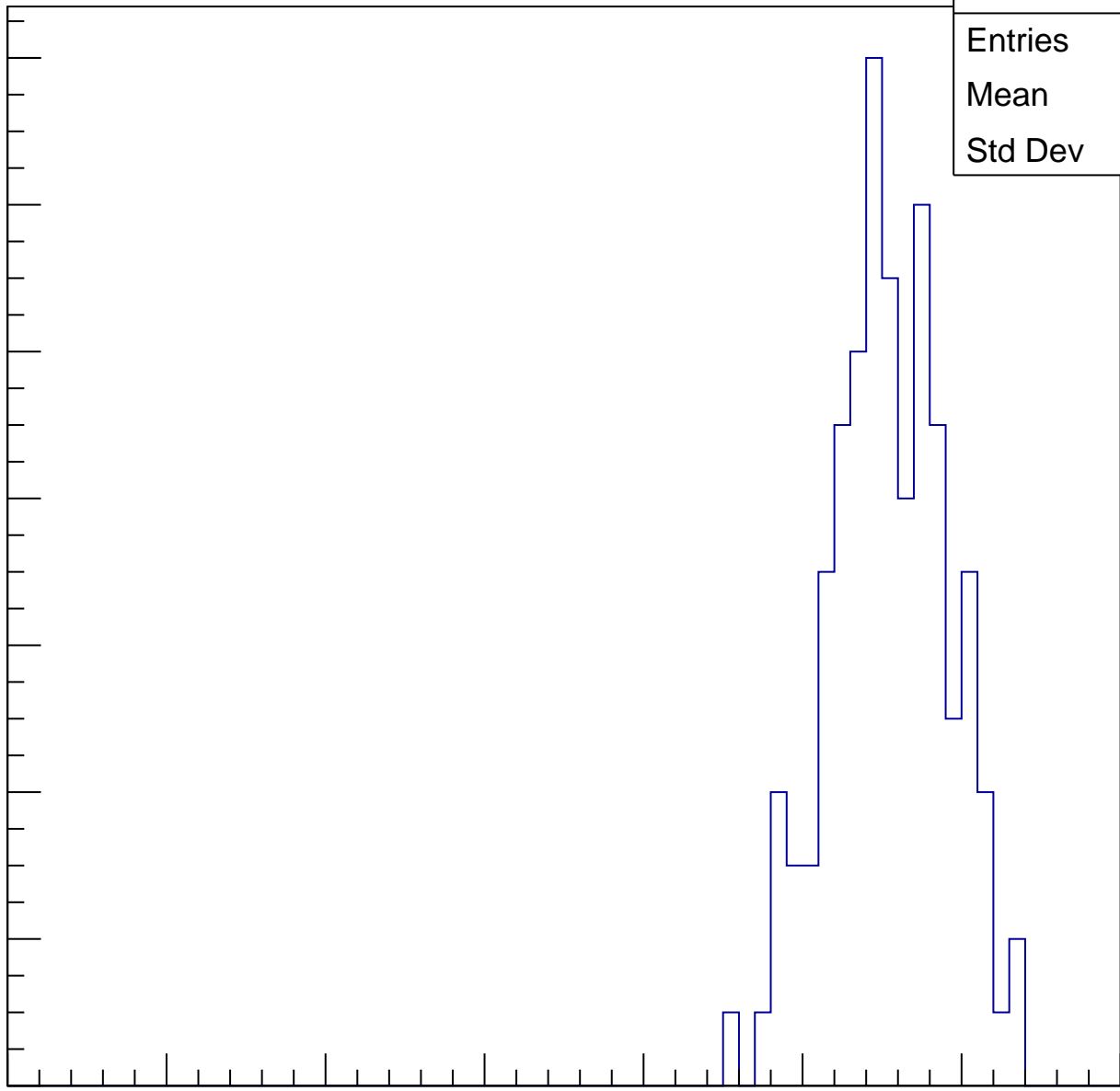
Entries	111
Mean	54.94
Std Dev	3.679

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch33, adc5

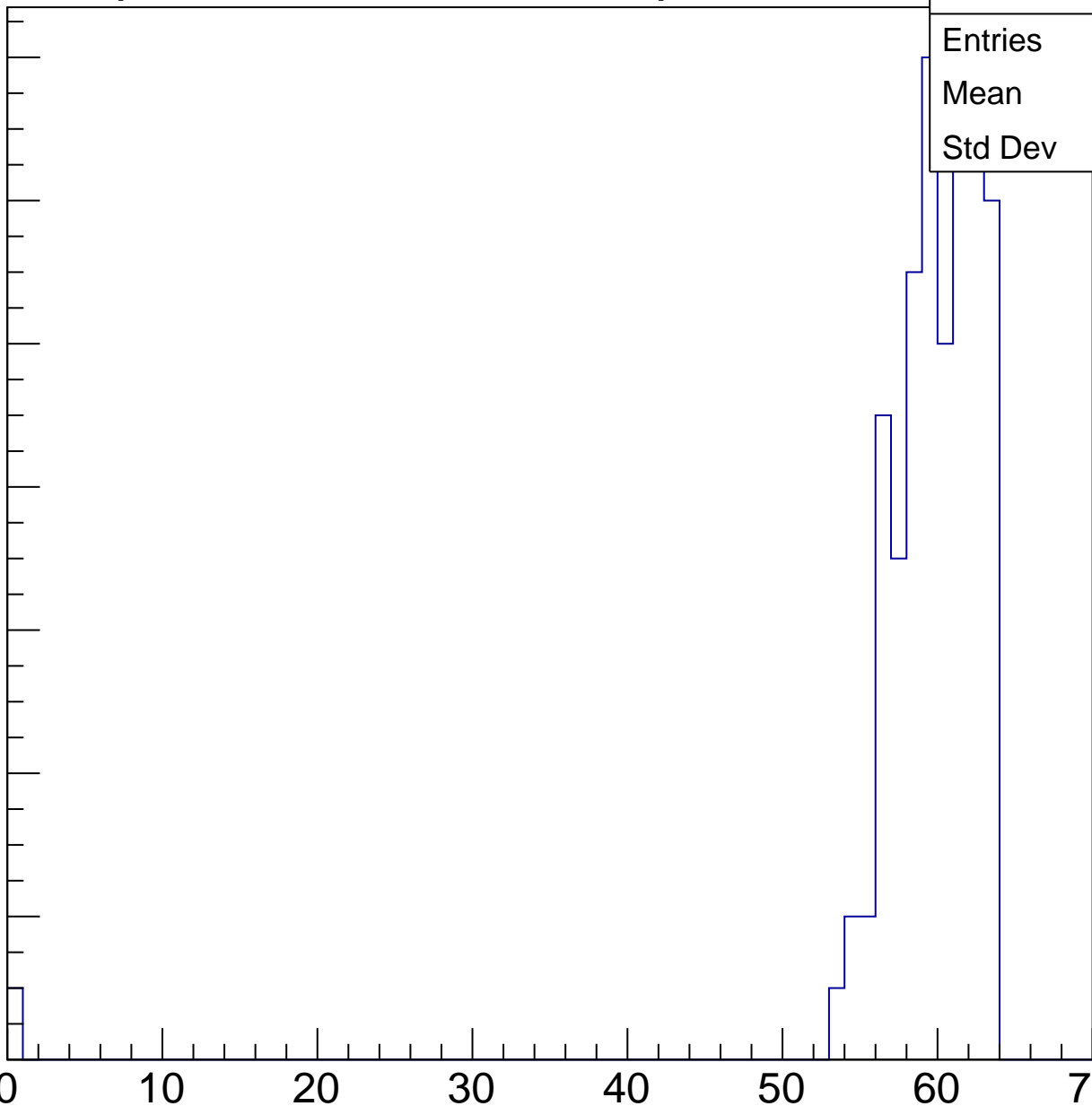
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	58.93
Std Dev	6.503

ampl

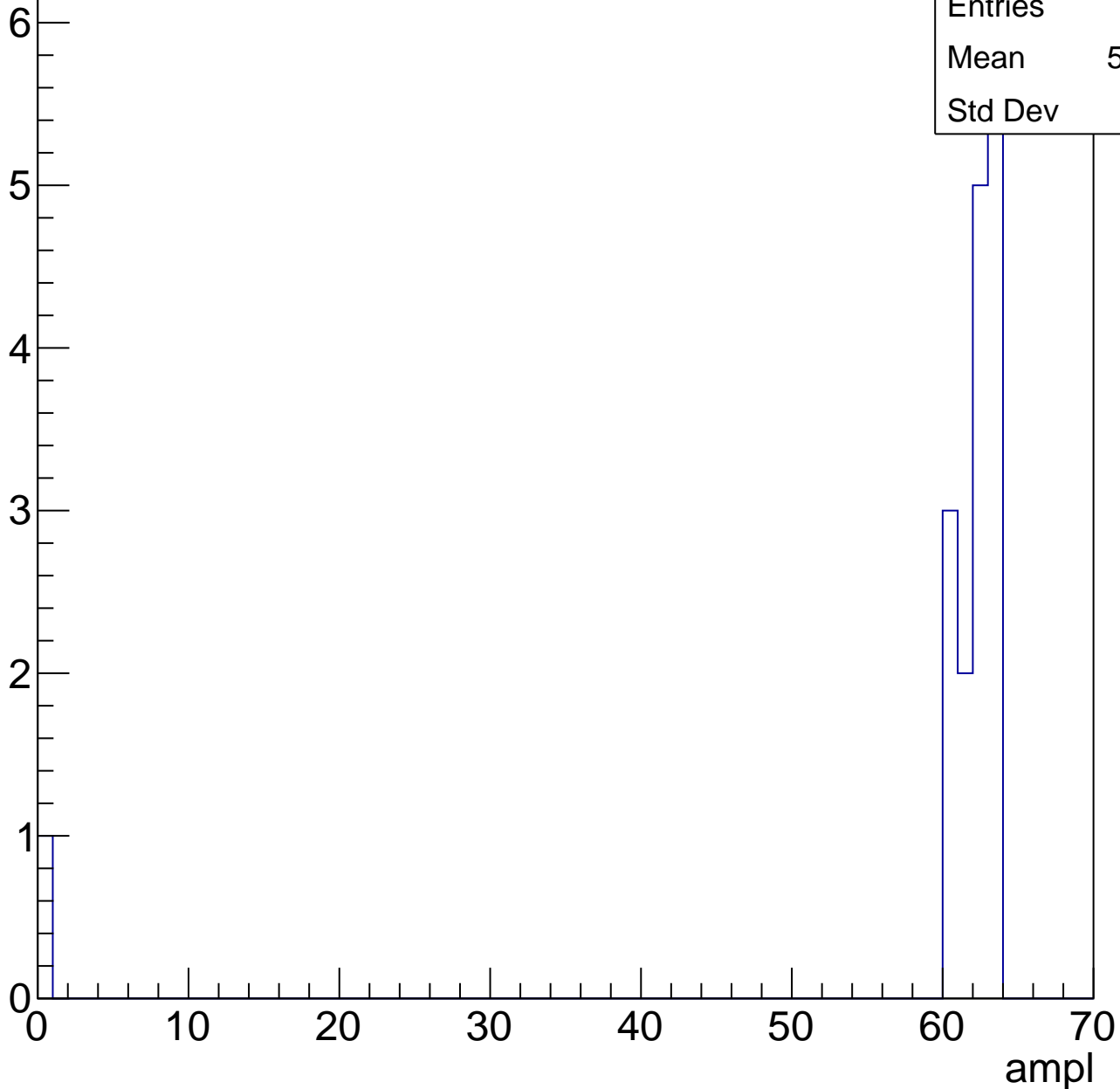


B1L001S, U20-ch33, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	58.24
Std Dev	14.6



B1L001S, U20-ch33, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U20-ch34, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	28.68
Std Dev	2.931

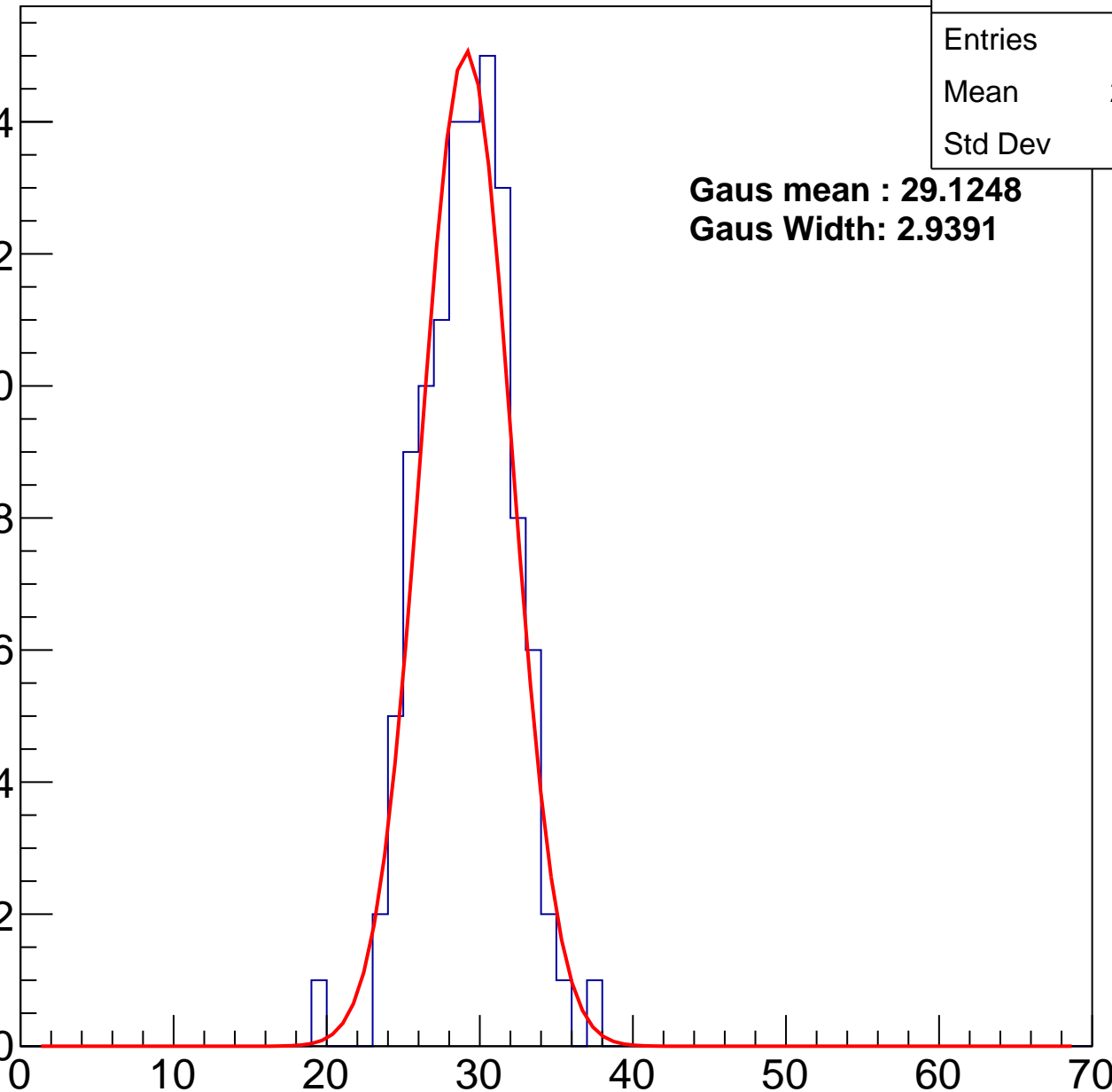
Gaus mean : 29.1248

Gaus Width: 2.9391

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch34, adc1

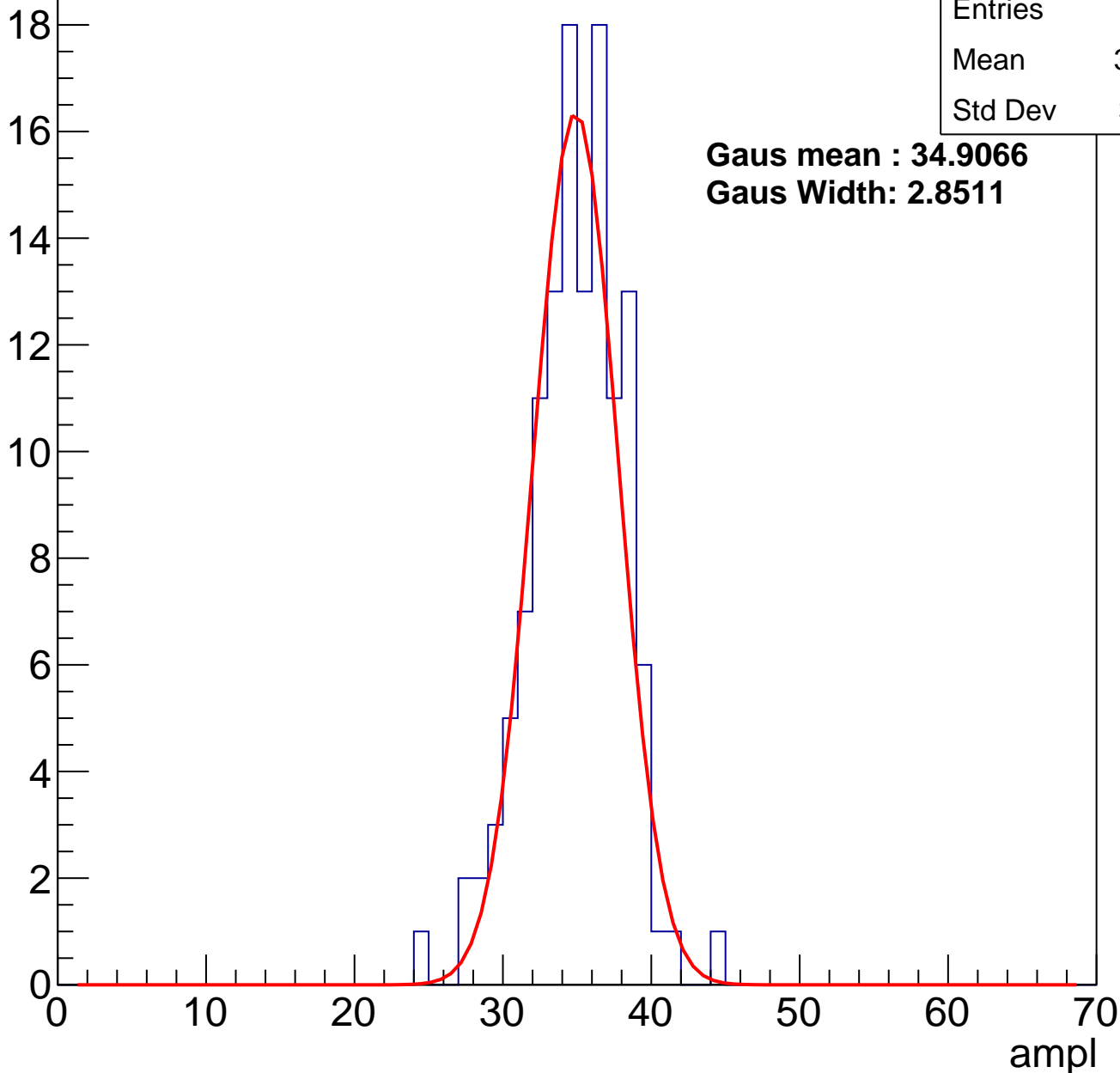
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	34.48
Std Dev	3.141

Gaus mean : 34.9066

Gaus Width: 2.8511

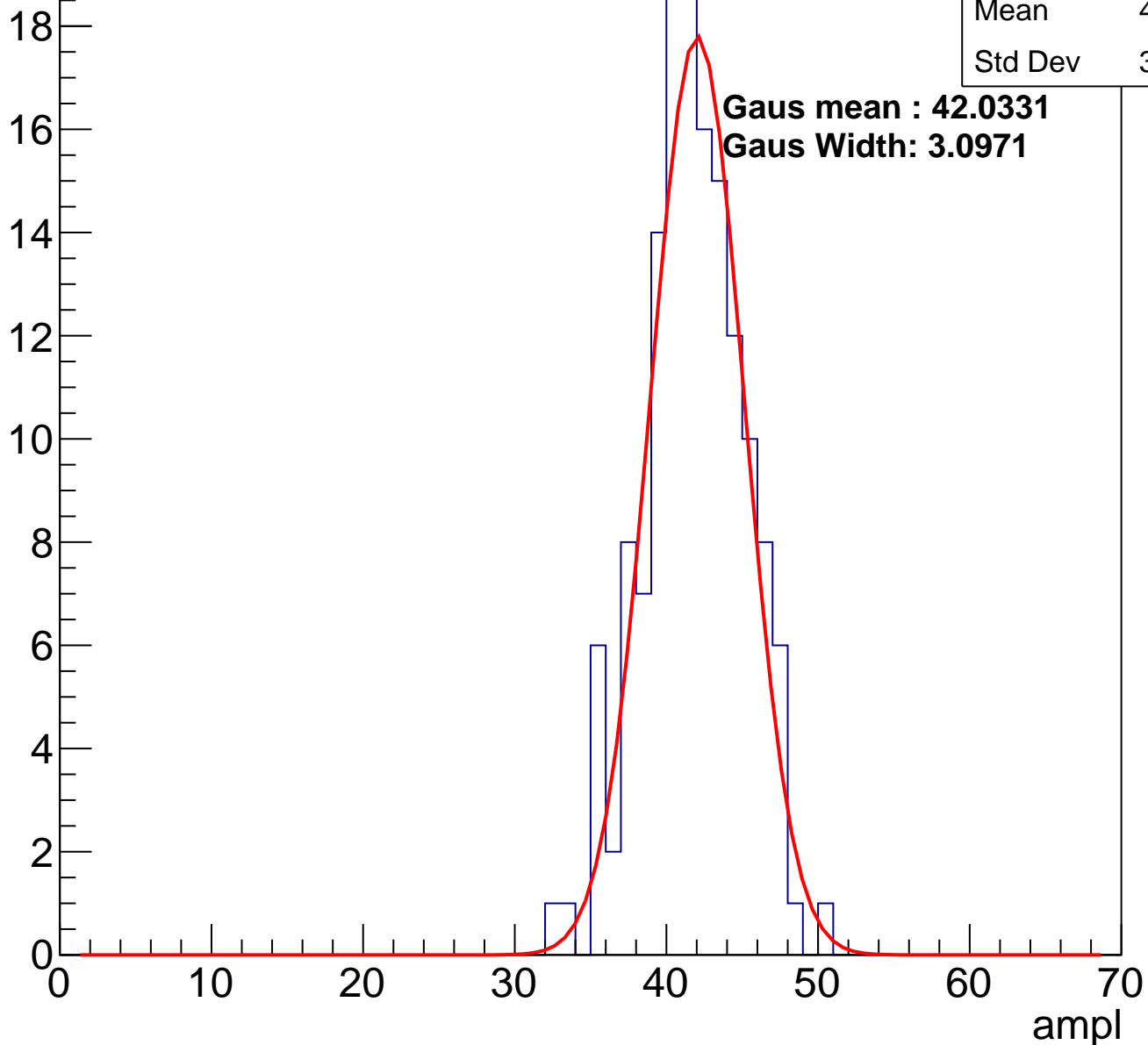
Entry



B1L001S, U20-ch34, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	146
Mean	41.35
Std Dev	3.257

B1L001S, U20-ch34, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	48.81
Std Dev	3.351

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

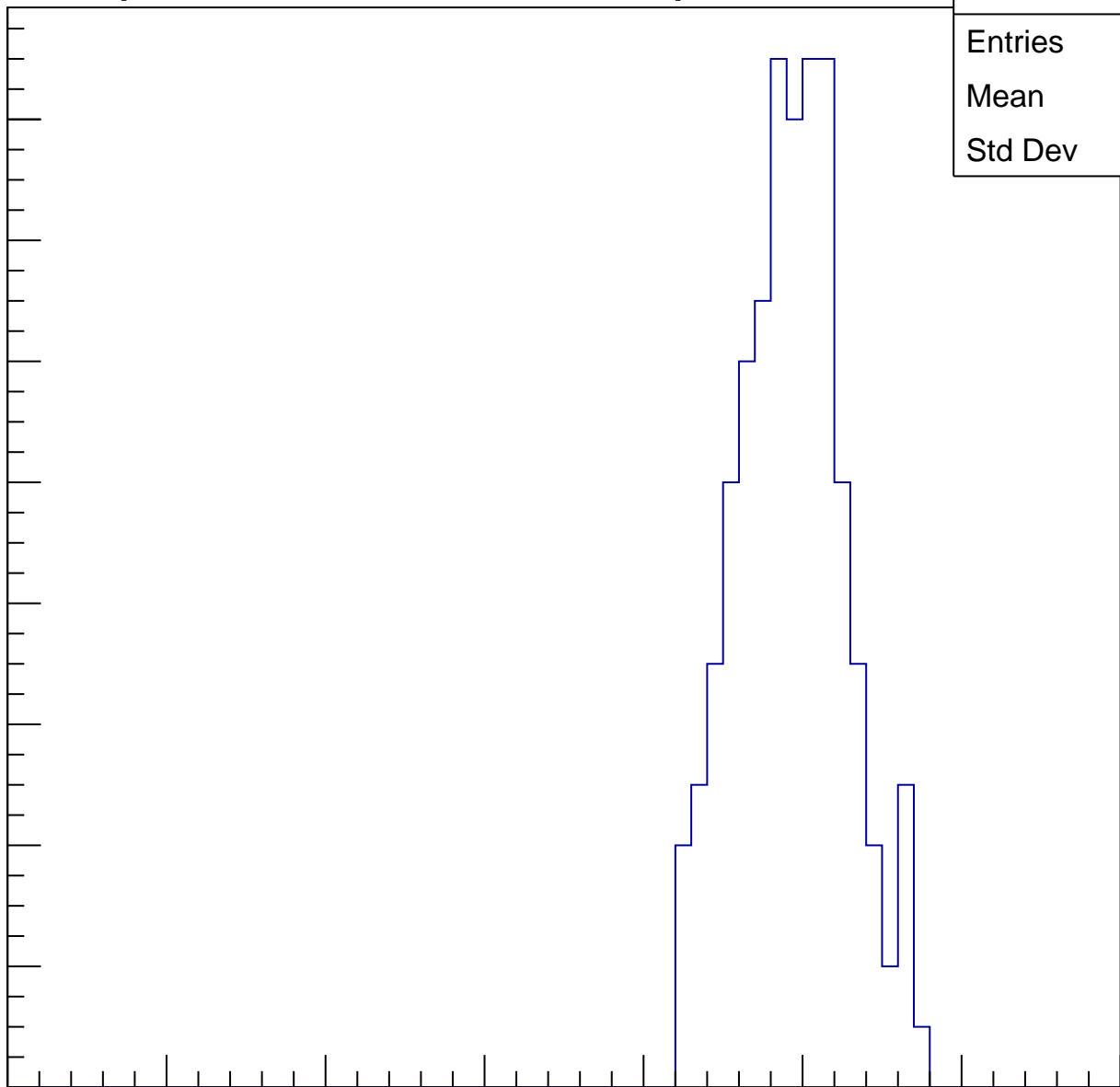
40

50

60

70

ampl



B1L001S, U20-ch34, adc4

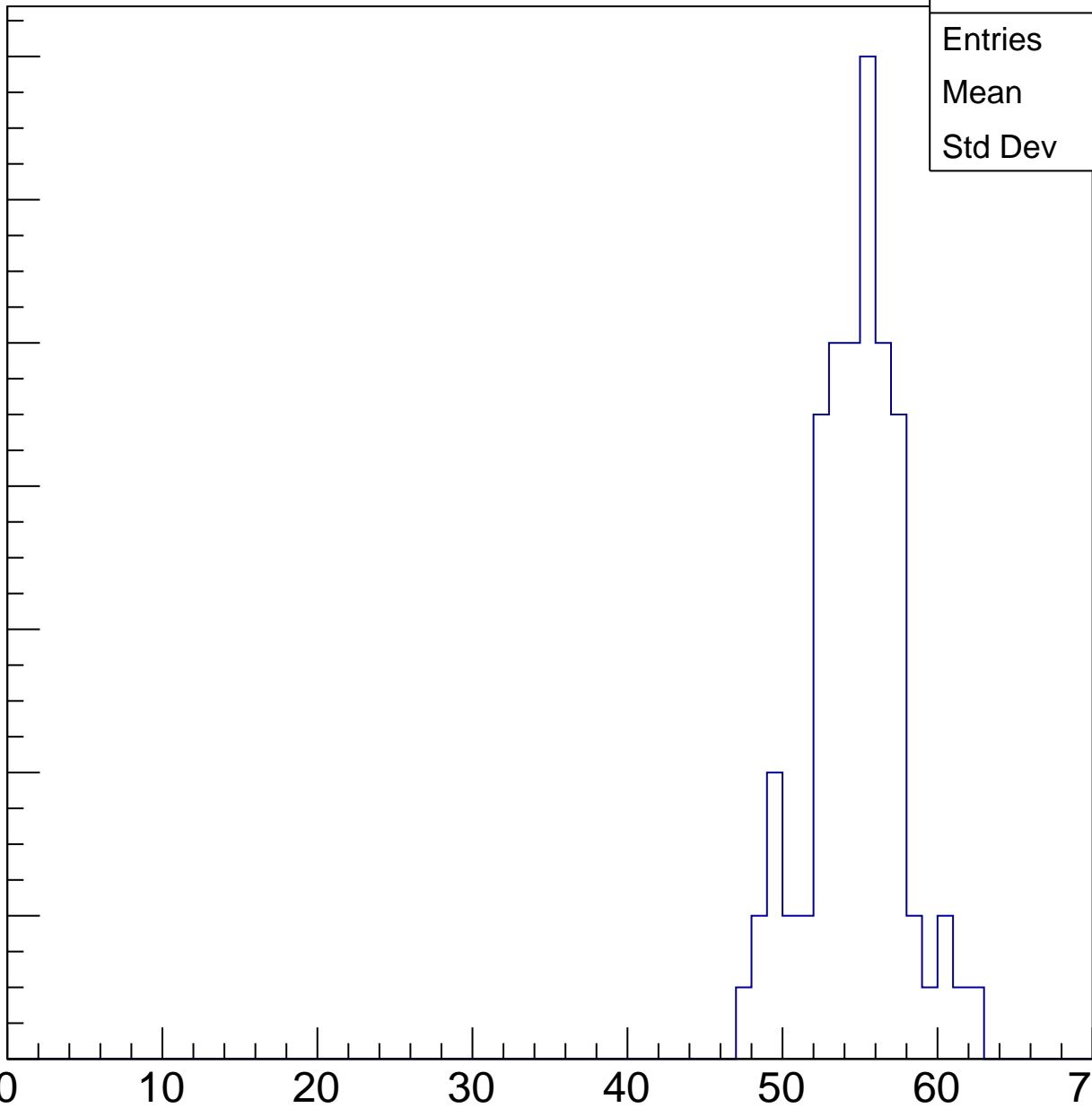
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	80
Mean	54.25
Std Dev	2.935

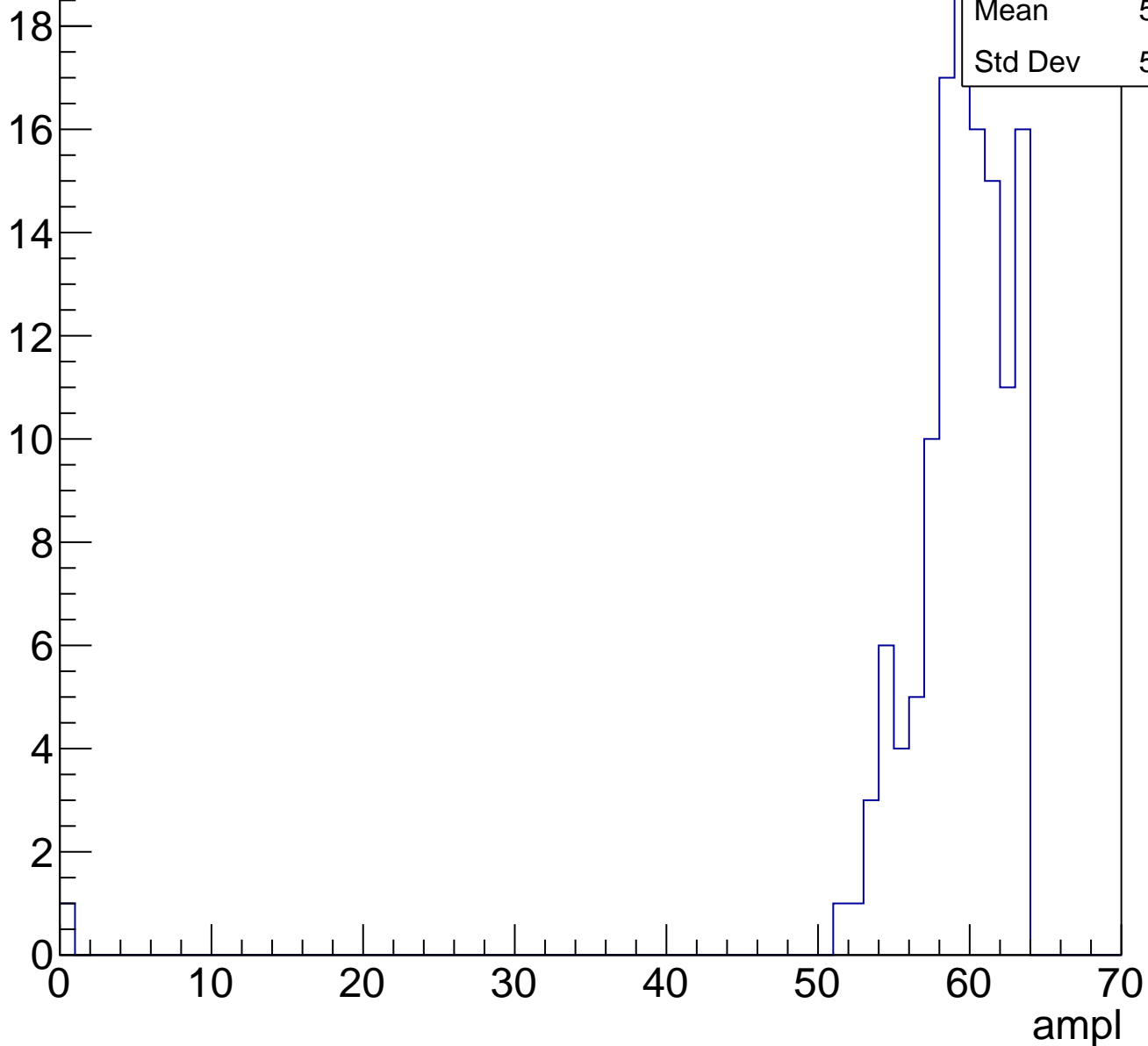
ampl



B1L001S, U20-ch34, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



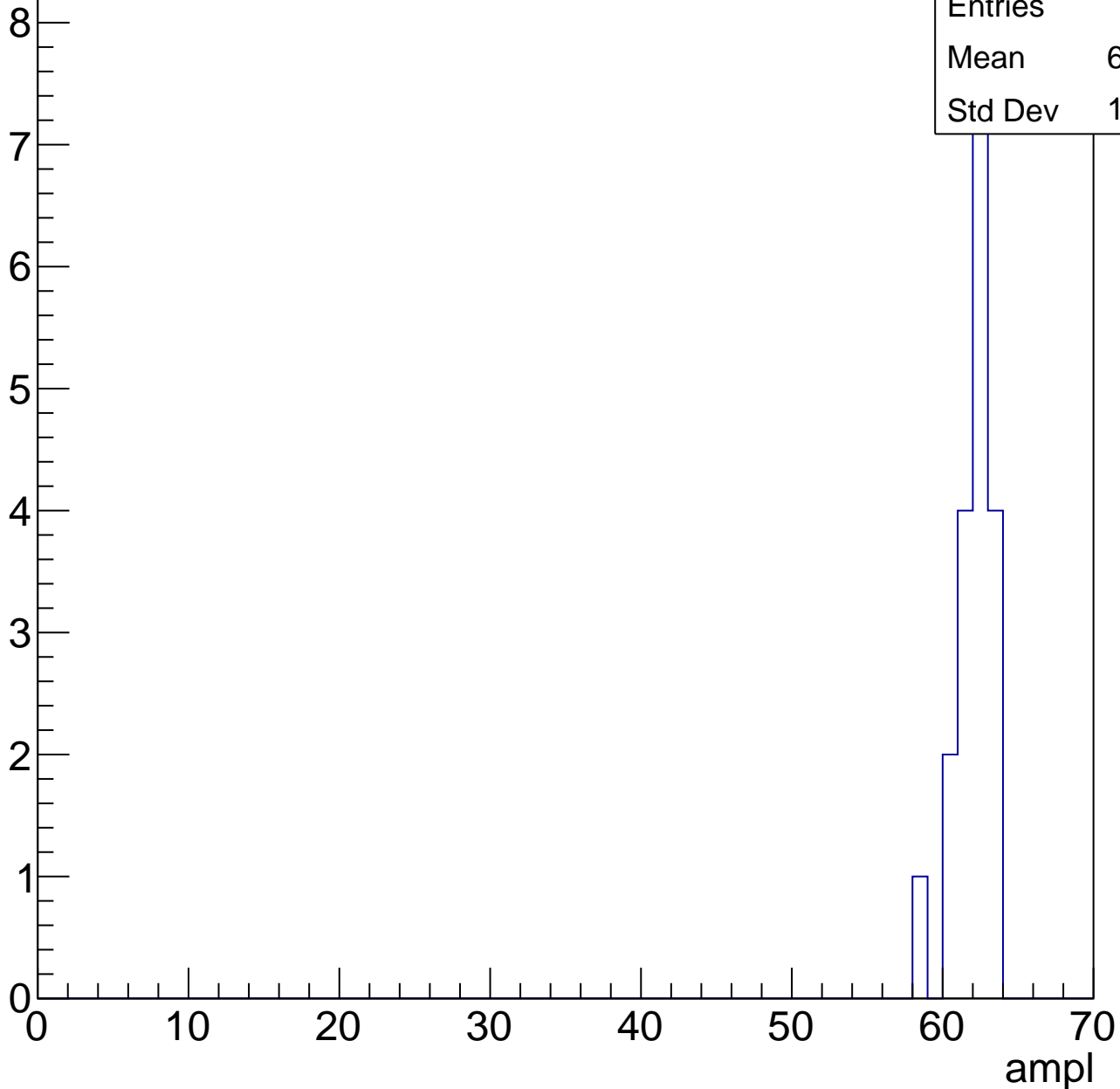
Entries	125
Mean	58.62
Std Dev	5.954

B1L001S, U20-ch34, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	61.58
Std Dev	1.228



B1L001S, U20-ch34, adc7

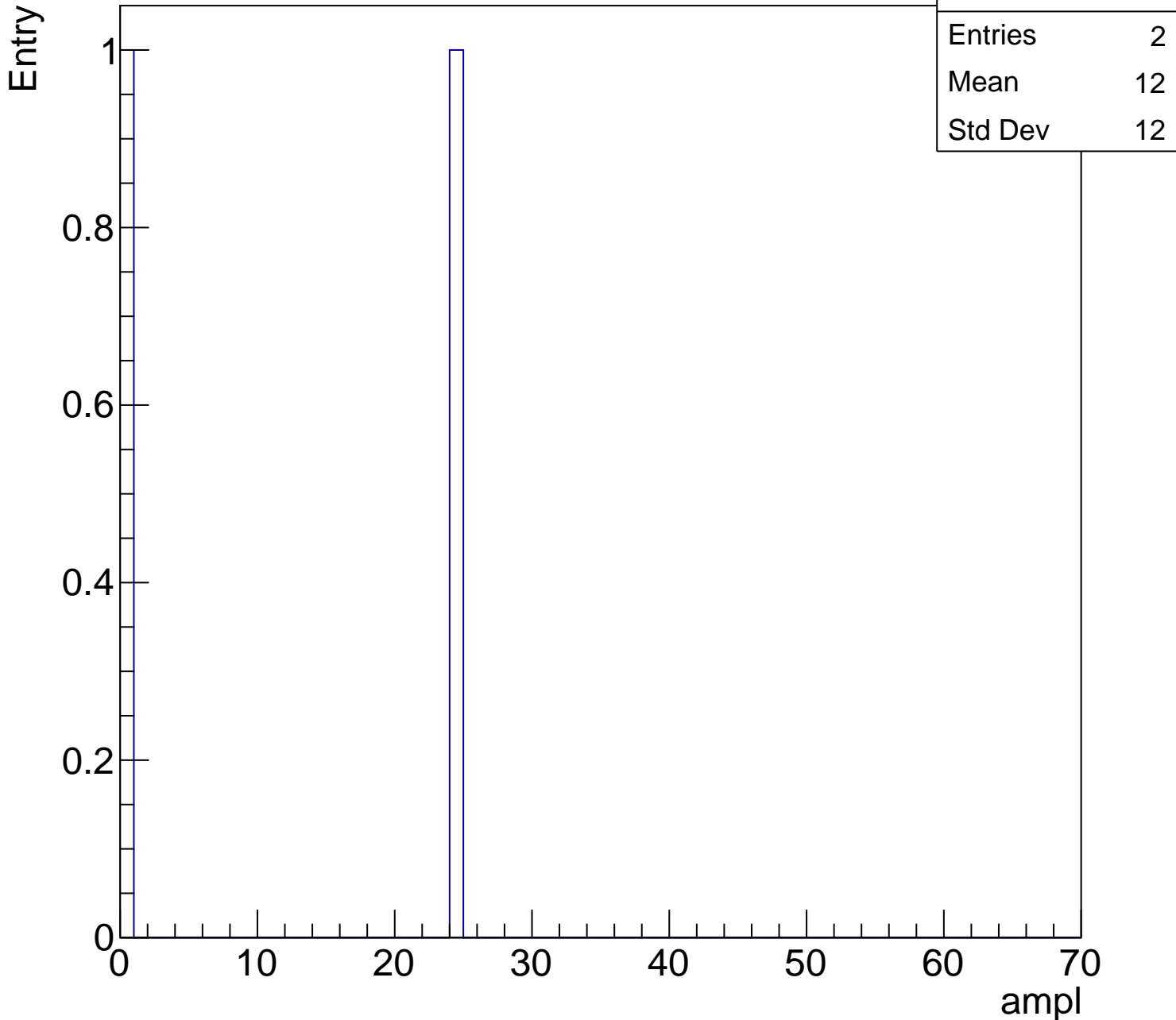
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12
Std Dev	12

ampl



B1L001S, U20-ch35, adc0

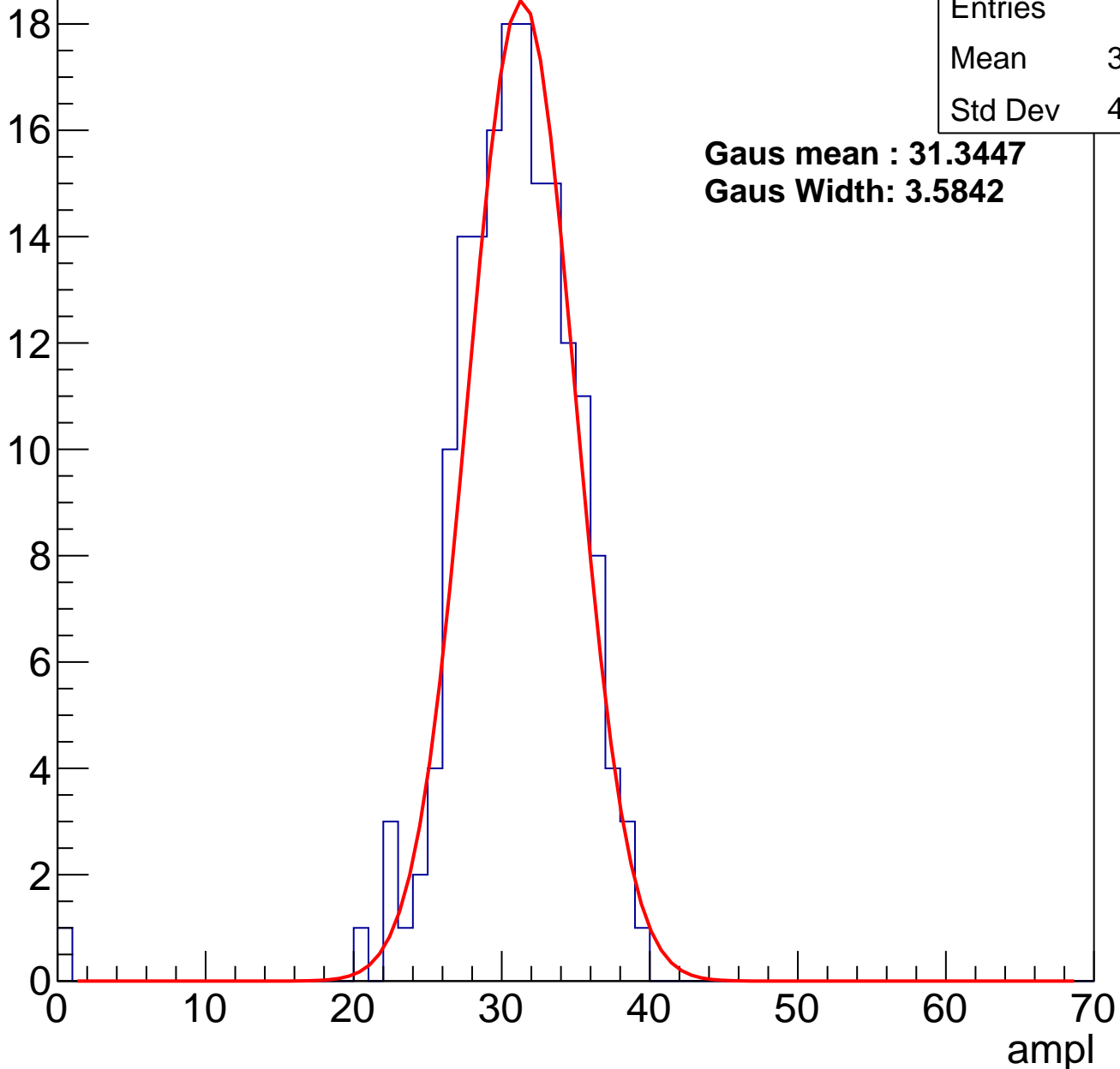
calib_packv5_042523_0143.root, FC#2, port C2

Entries	171
Mean	30.44
Std Dev	4.308

Gaus mean : 31.3447

Gaus Width: 3.5842

Entry



B1L001S, U20-ch35, adc1

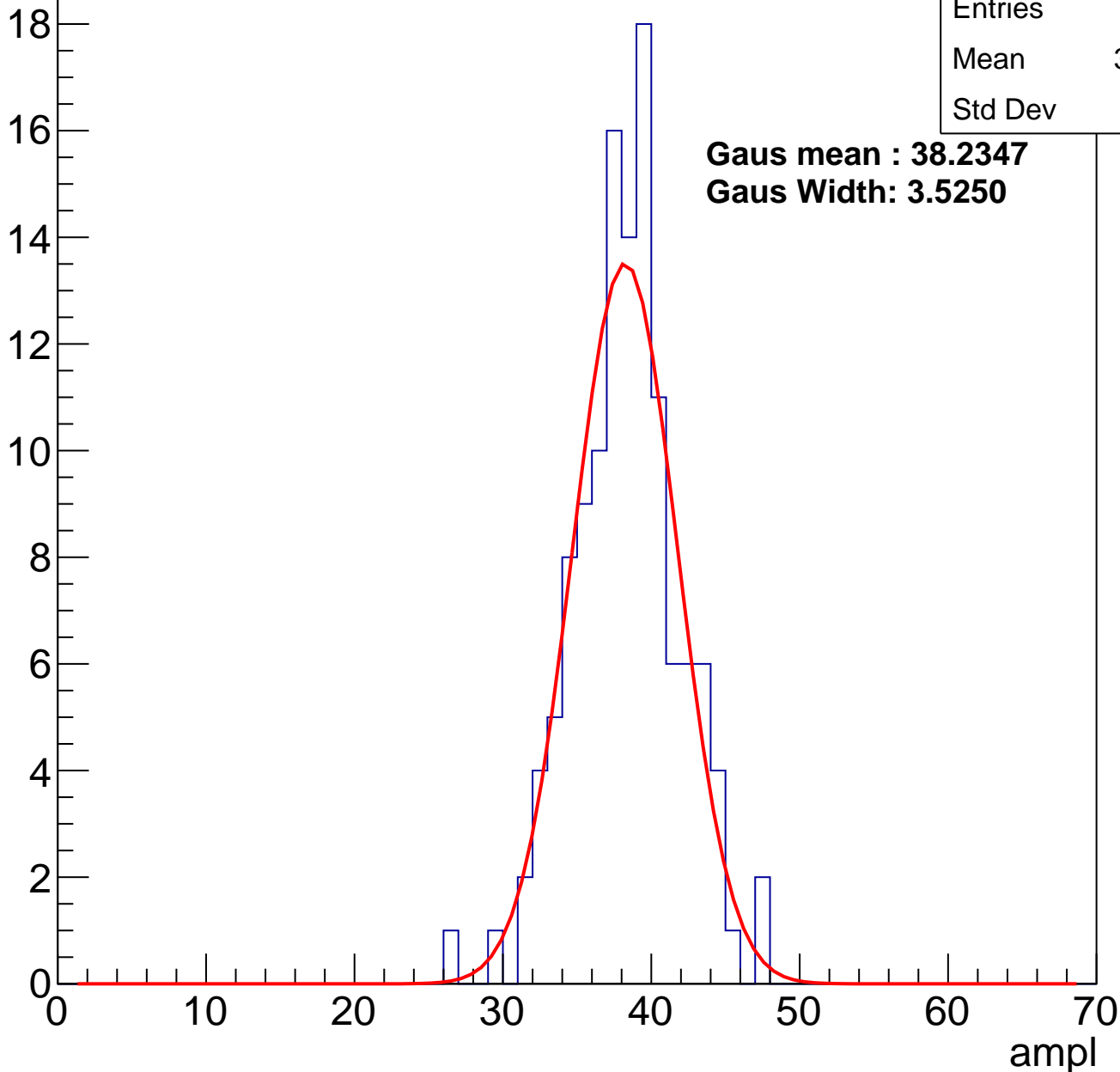
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	37.85
Std Dev	3.56

Gaus mean : 38.2347

Gaus Width: 3.5250

Entry



B1L001S, U20-ch35, adc2

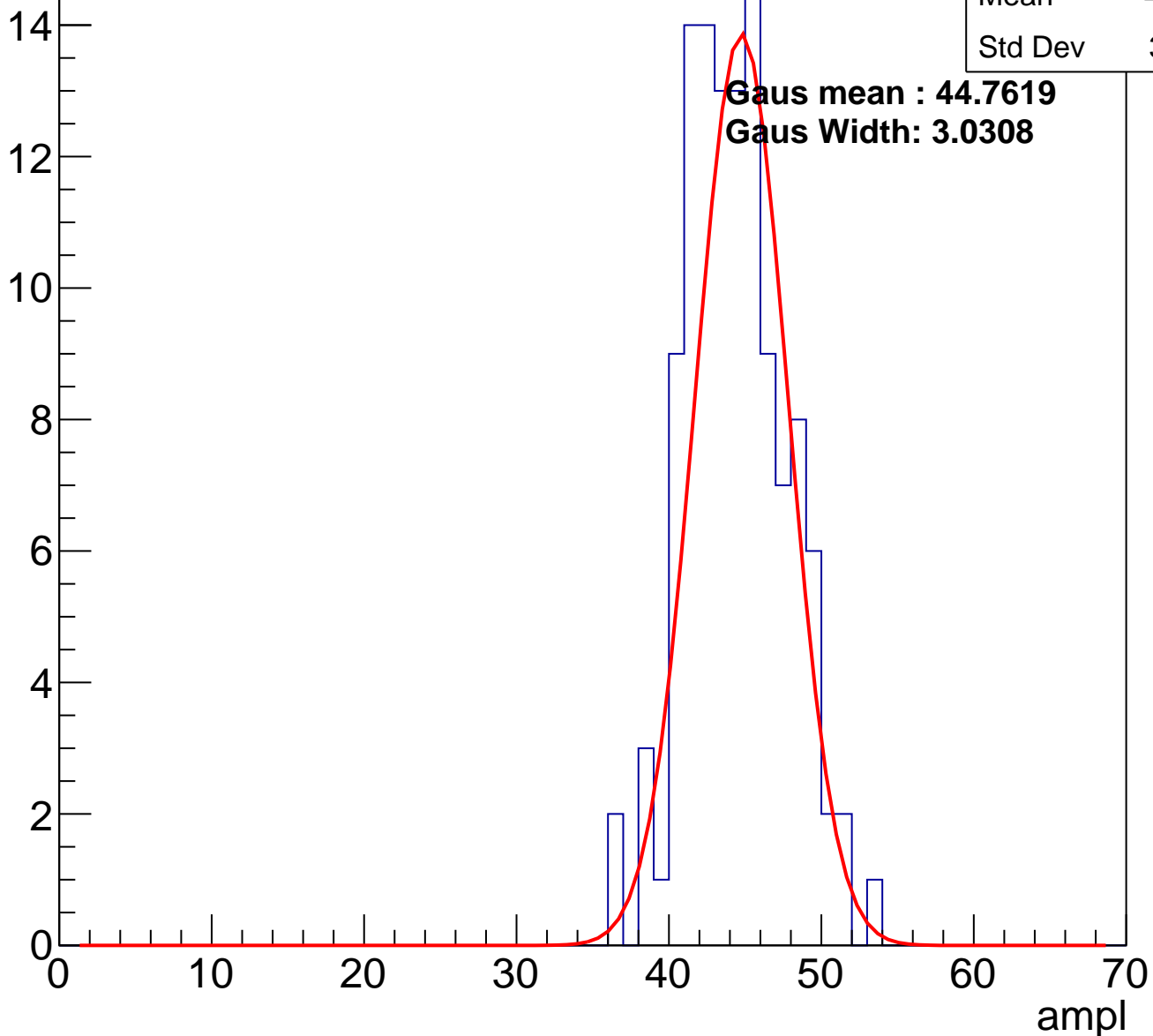
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	43.94
Std Dev	3.231

Gaus mean : 44.7619

Gaus Width: 3.0308

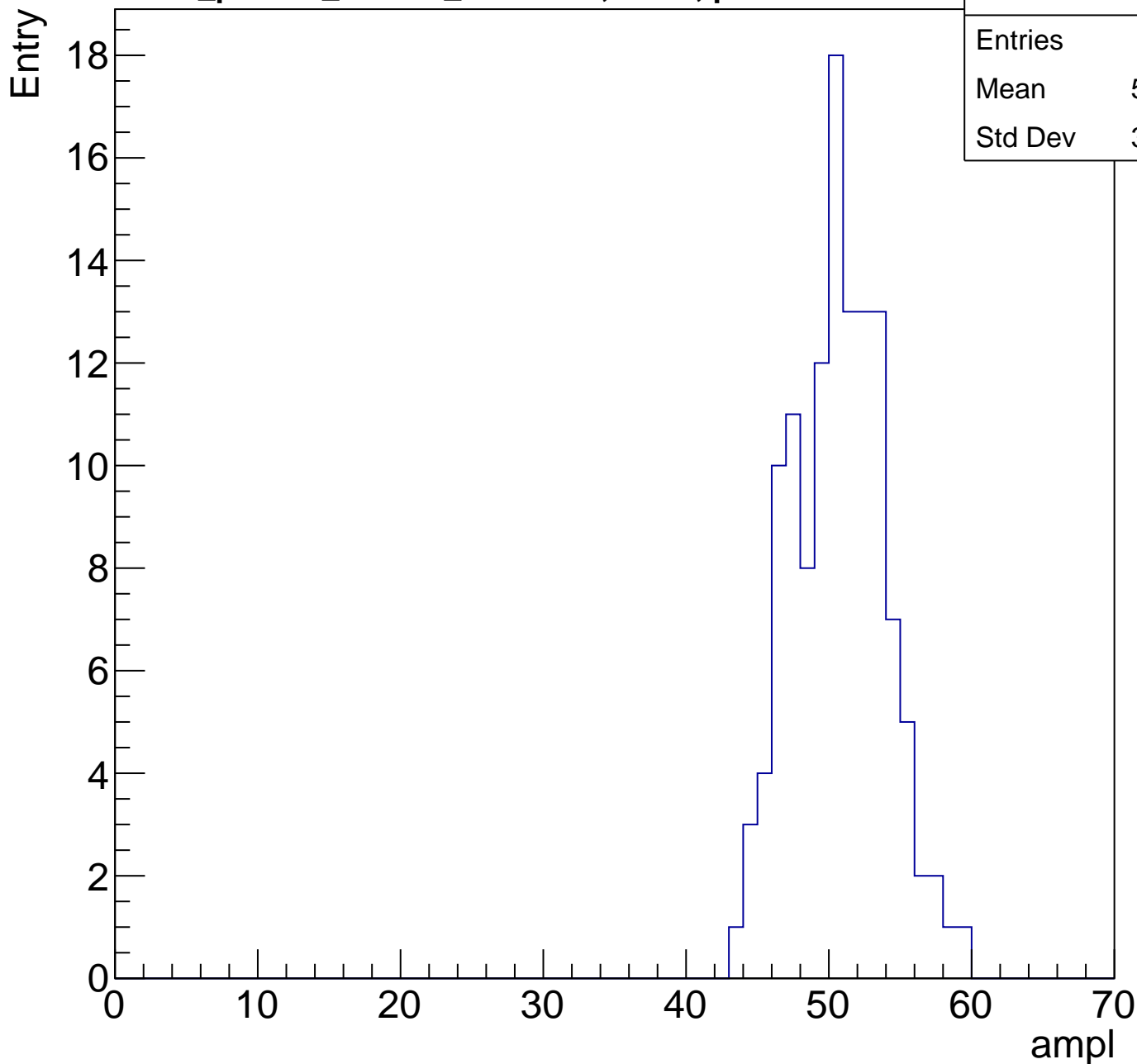
Entry



B1L001S, U20-ch35, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	50.23
Std Dev	3.222

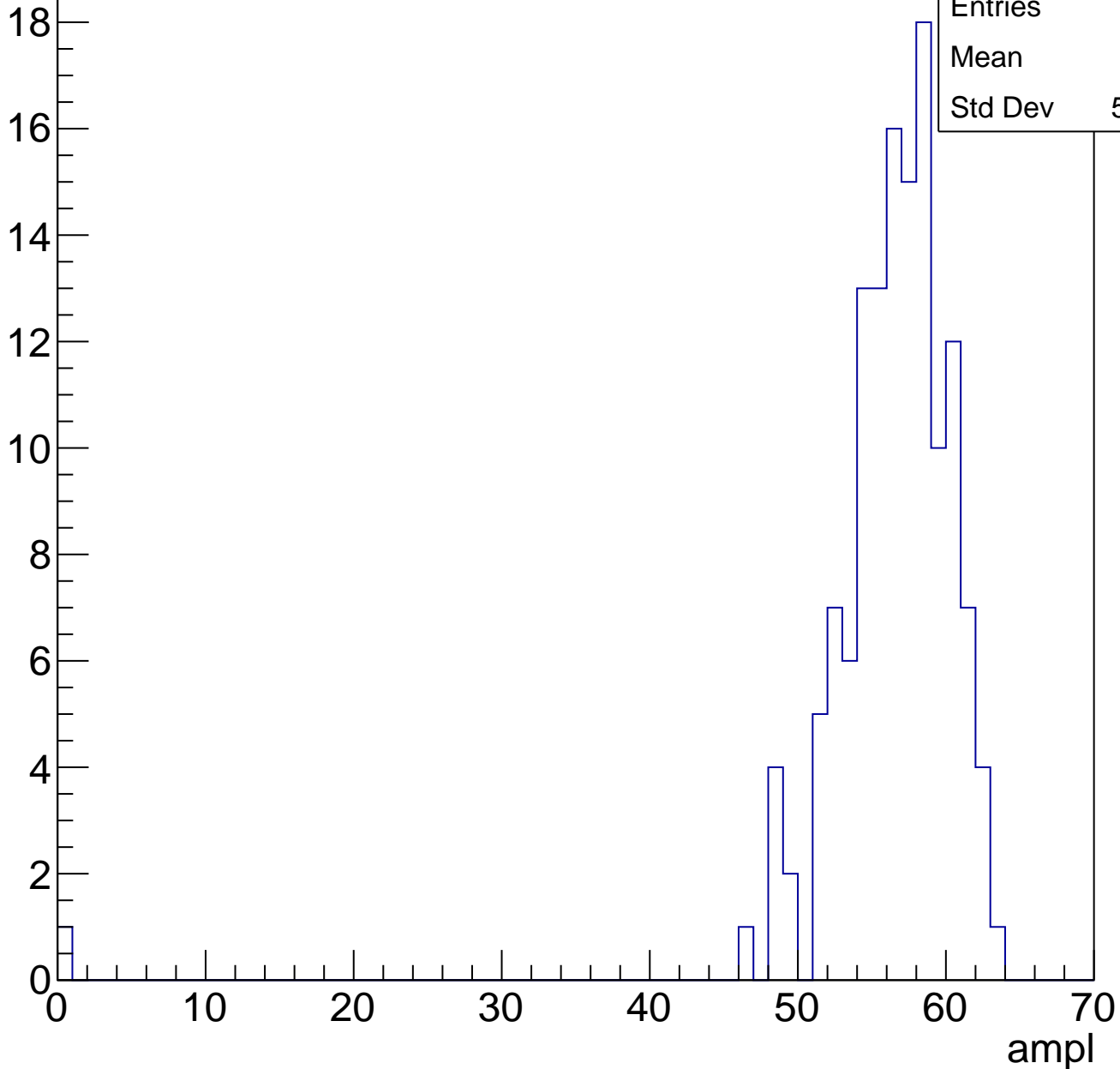


B1L001S, U20-ch35, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	55.8
Std Dev	5.886

Entry



B1L001S, U20-ch35, adc5

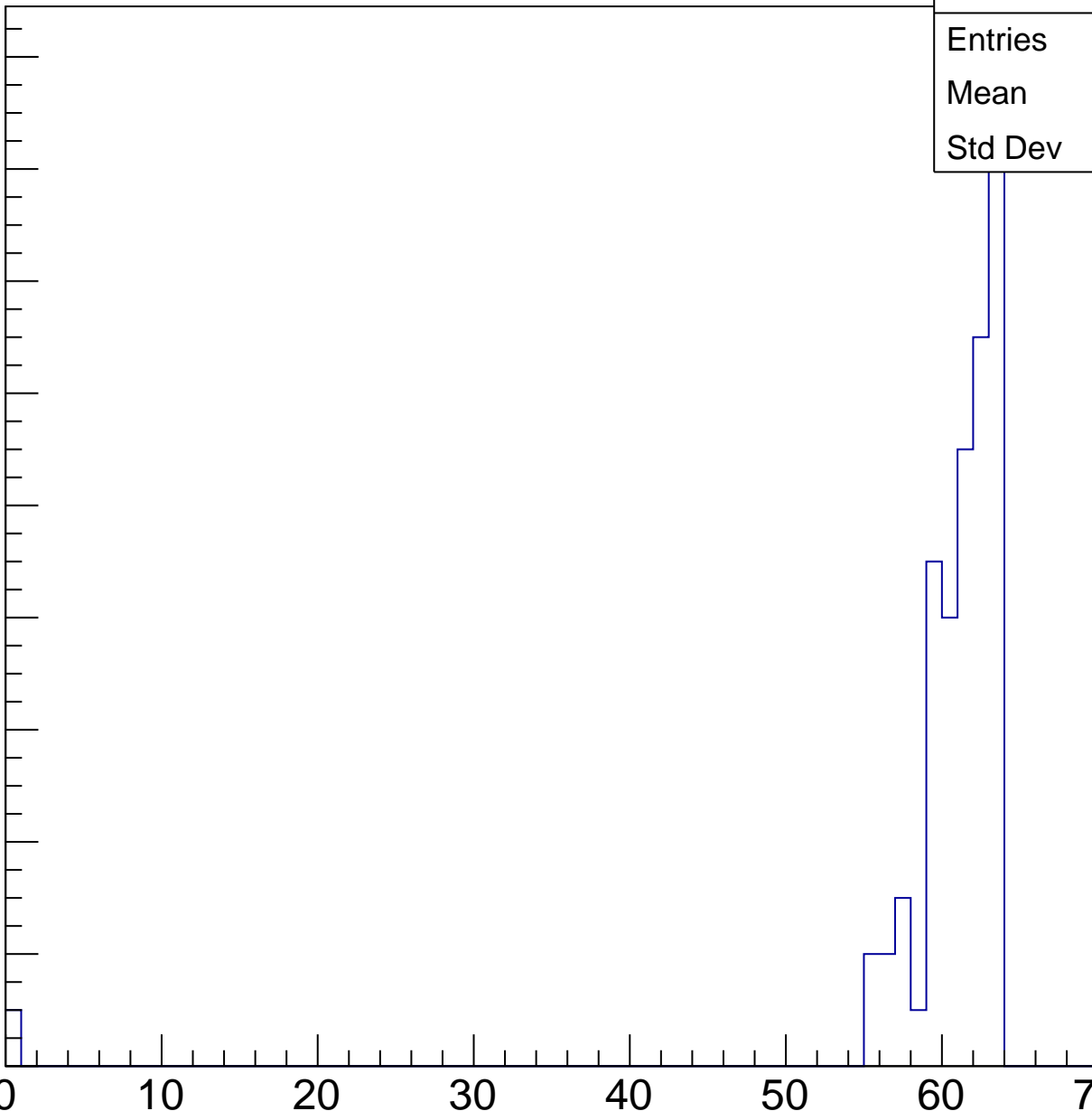
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	68
Mean	59.9
Std Dev	7.618

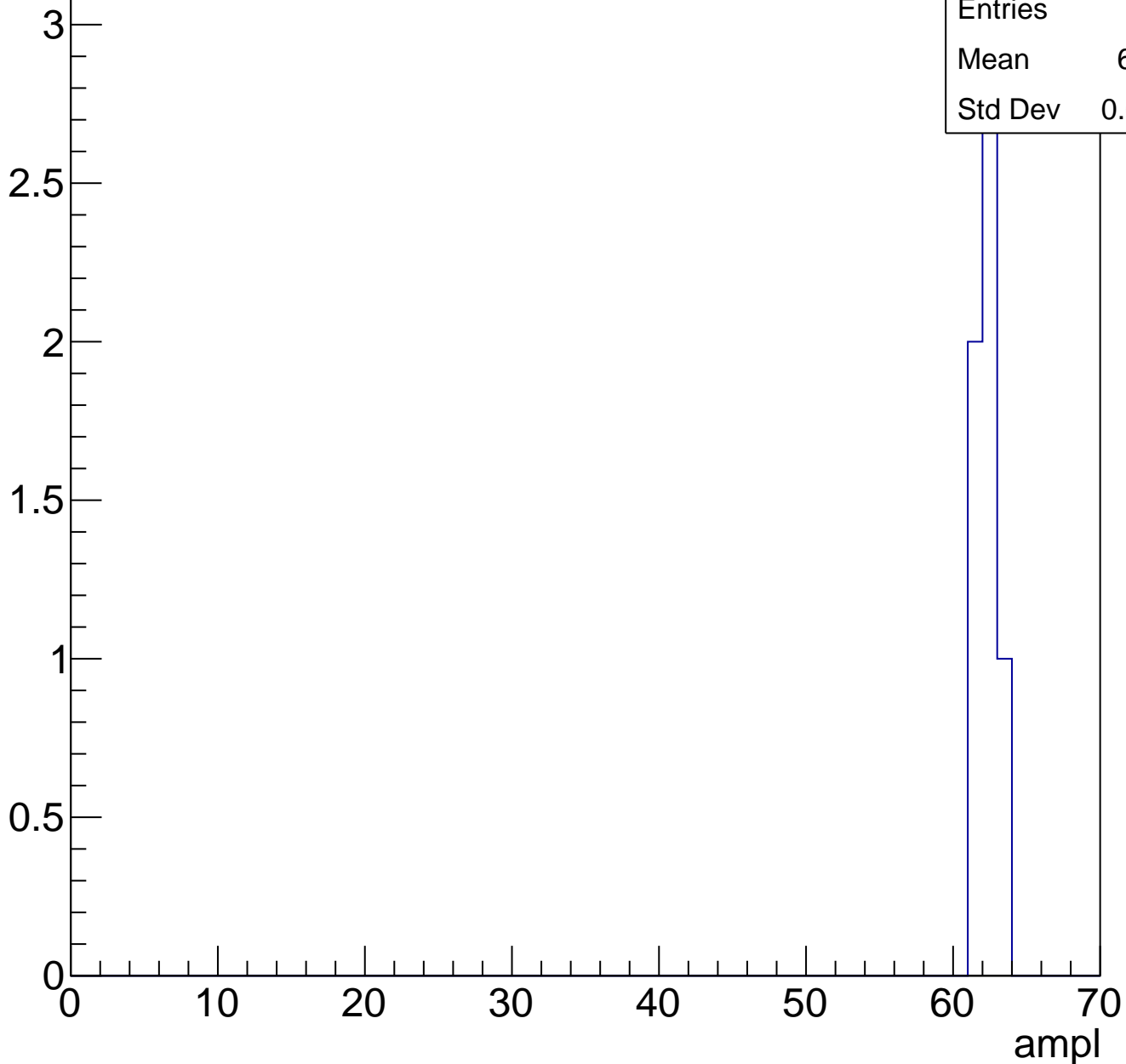
ampl



B1L001S, U20-ch35, adc6

calib_packv5_042523_0143.root, FC#2, port C2

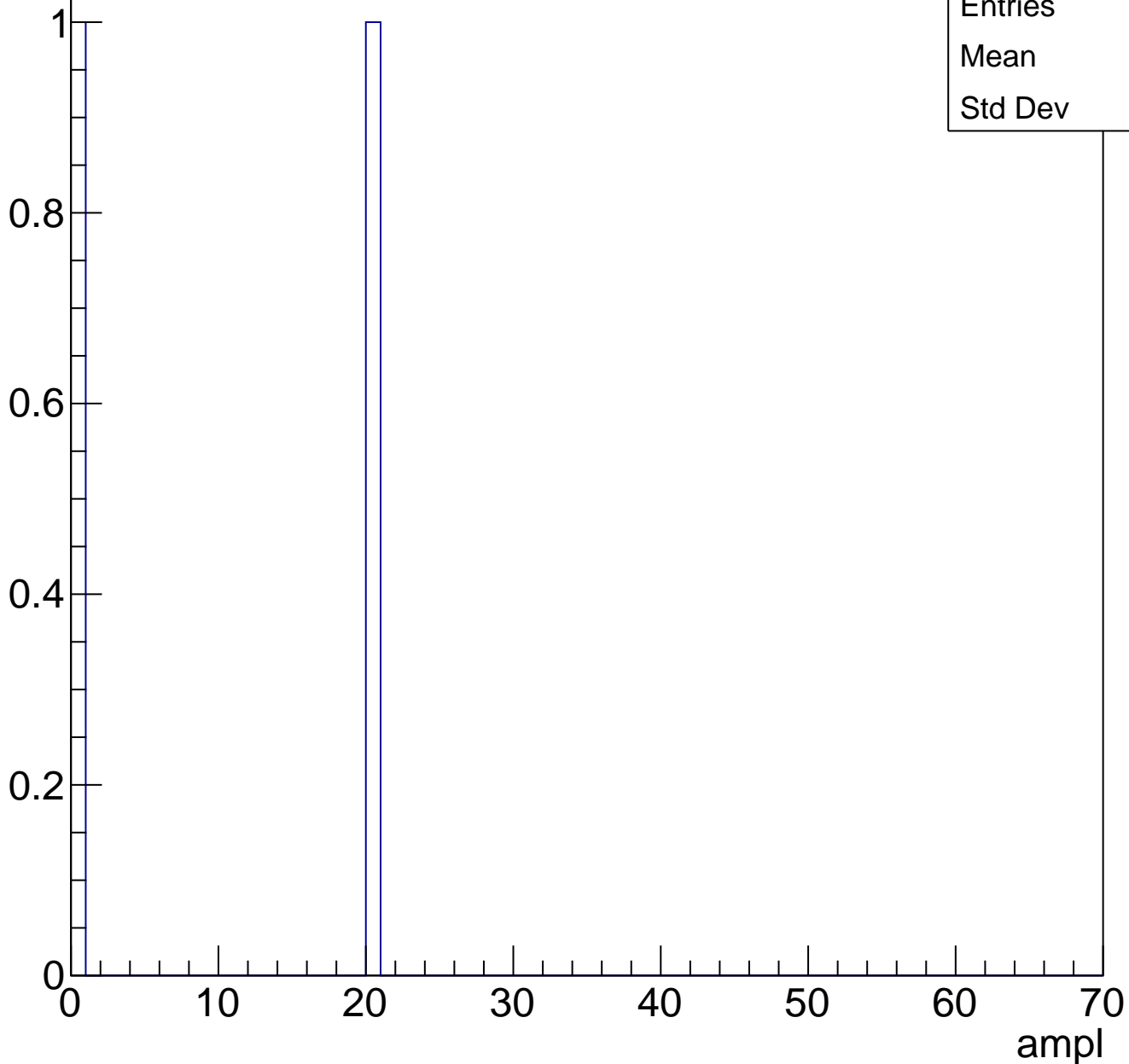
Entry



B1L001S, U20-ch35, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch36, adc0

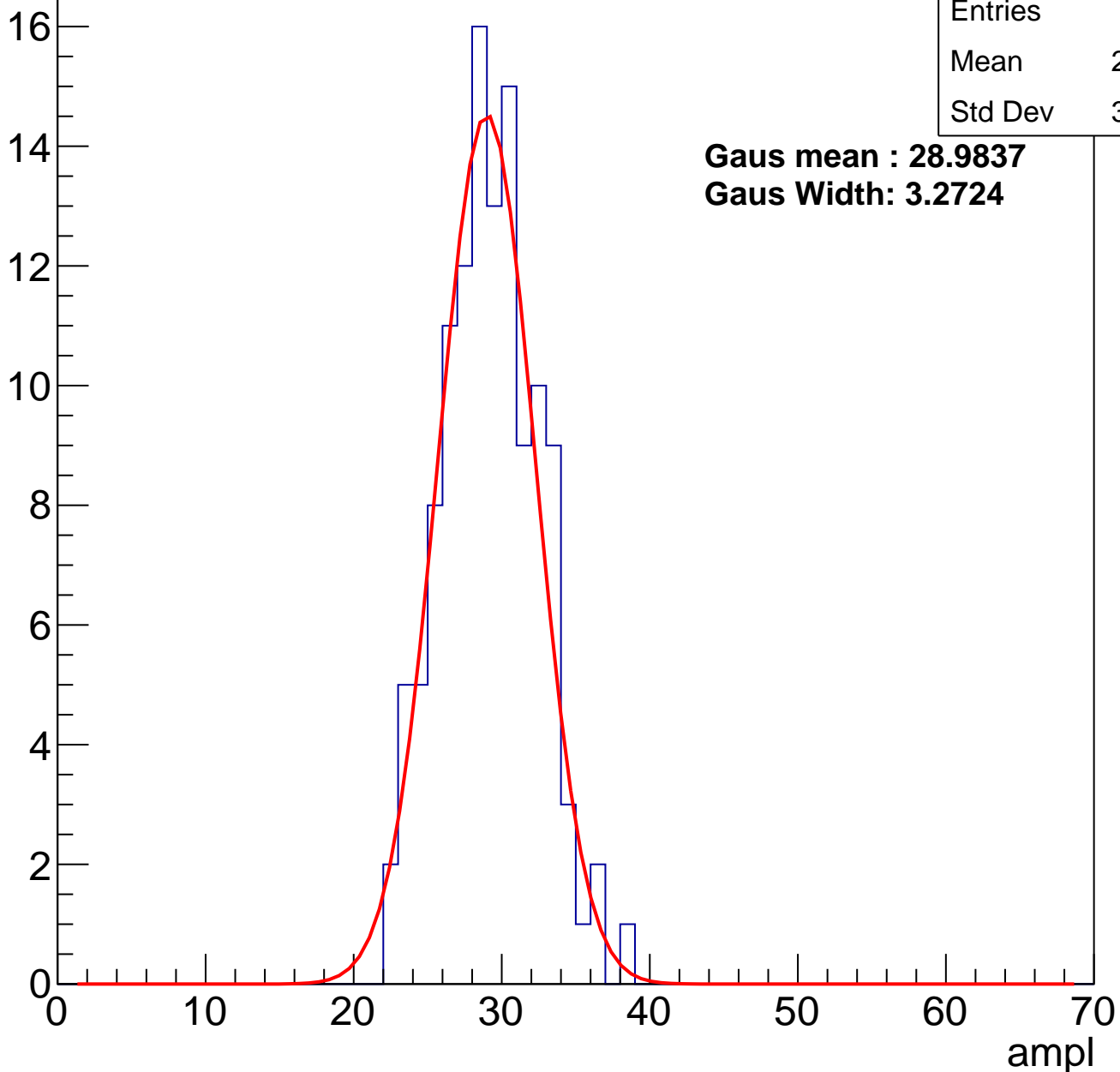
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	28.75
Std Dev	3.215

Gaus mean : 28.9837

Gaus Width: 3.2724

Entry



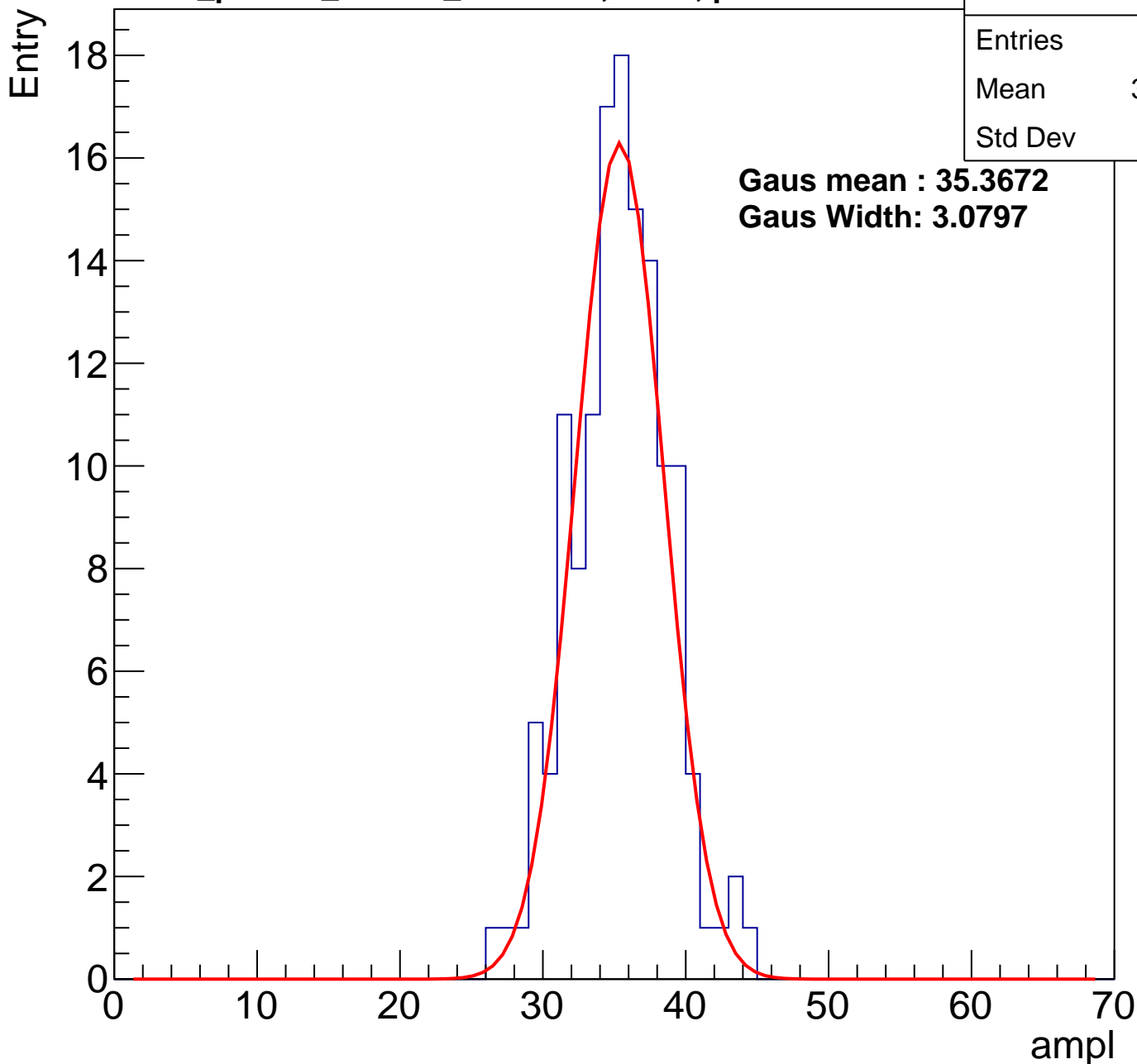
B1L001S, U20-ch36, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	34.93
Std Dev	3.33

Gaus mean : 35.3672

Gaus Width: 3.0797



B1L001S, U20-ch36, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

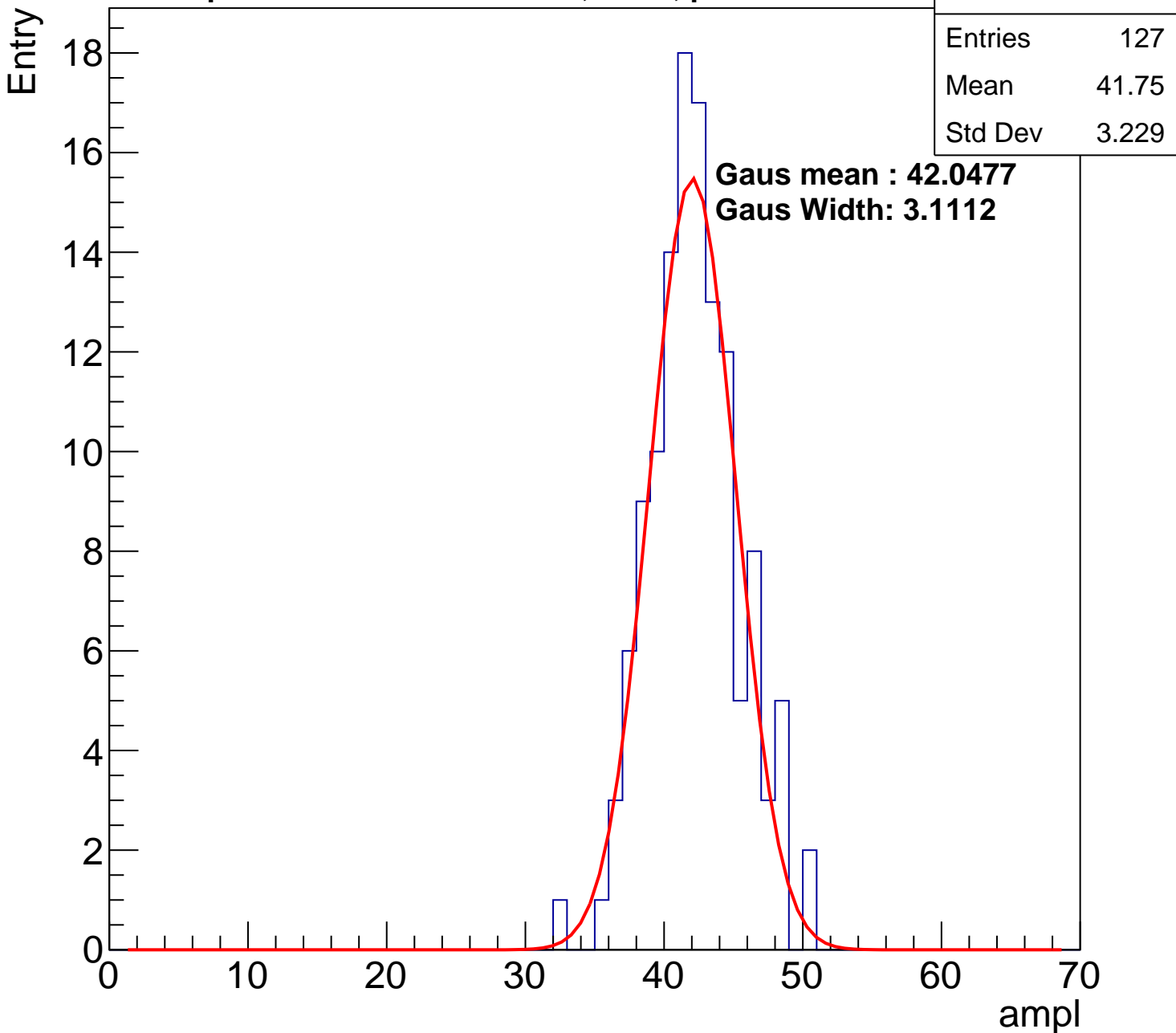
Entries	127
Mean	41.75
Std Dev	3.229

Gaus mean : 42.0477

Gaus Width: 3.1112

0 10 20 30 40 50 60 70

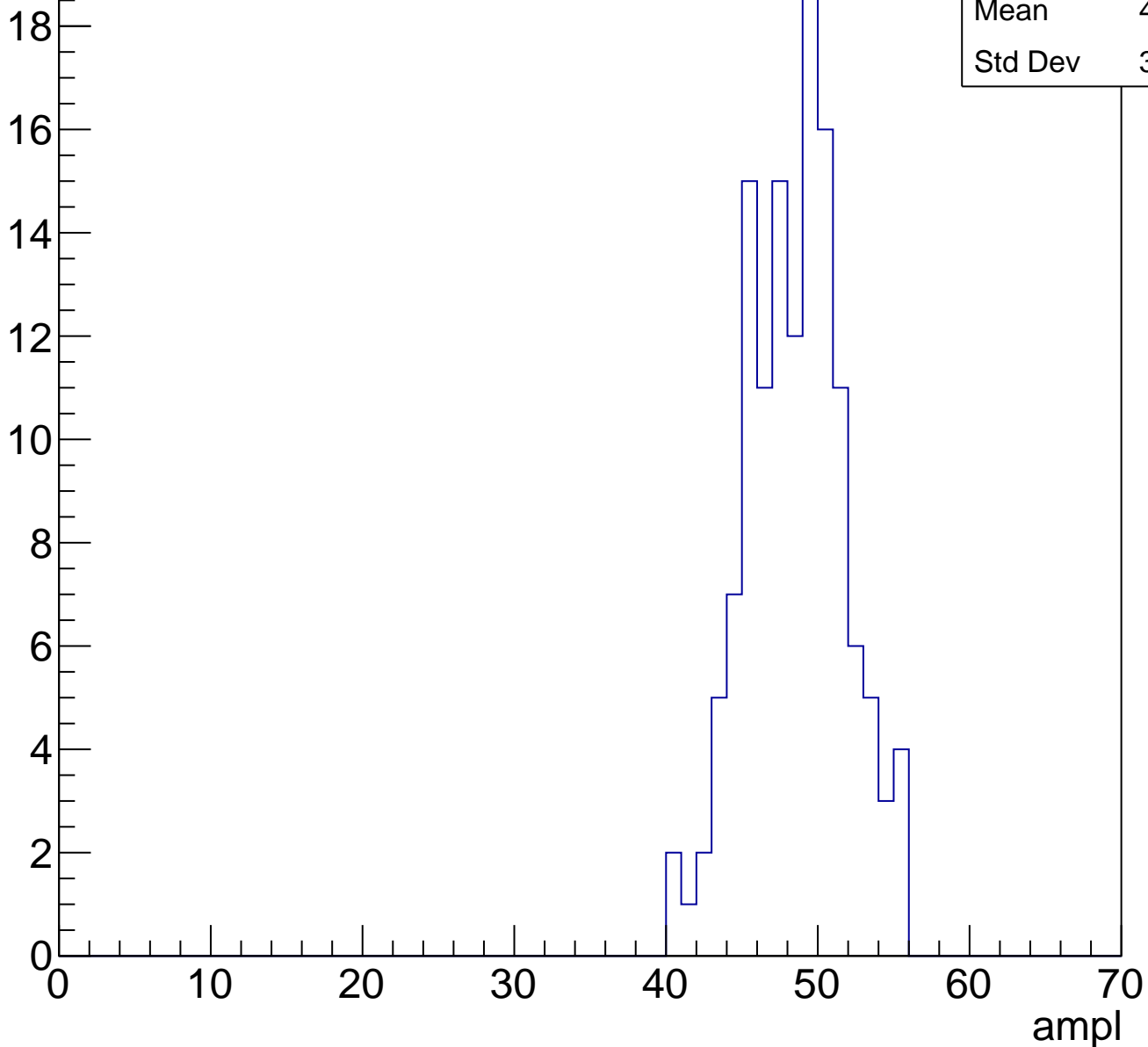
ampl



B1L001S, U20-ch36, adc3

calib_packv5_042523_0143.root, FC#2, port C2

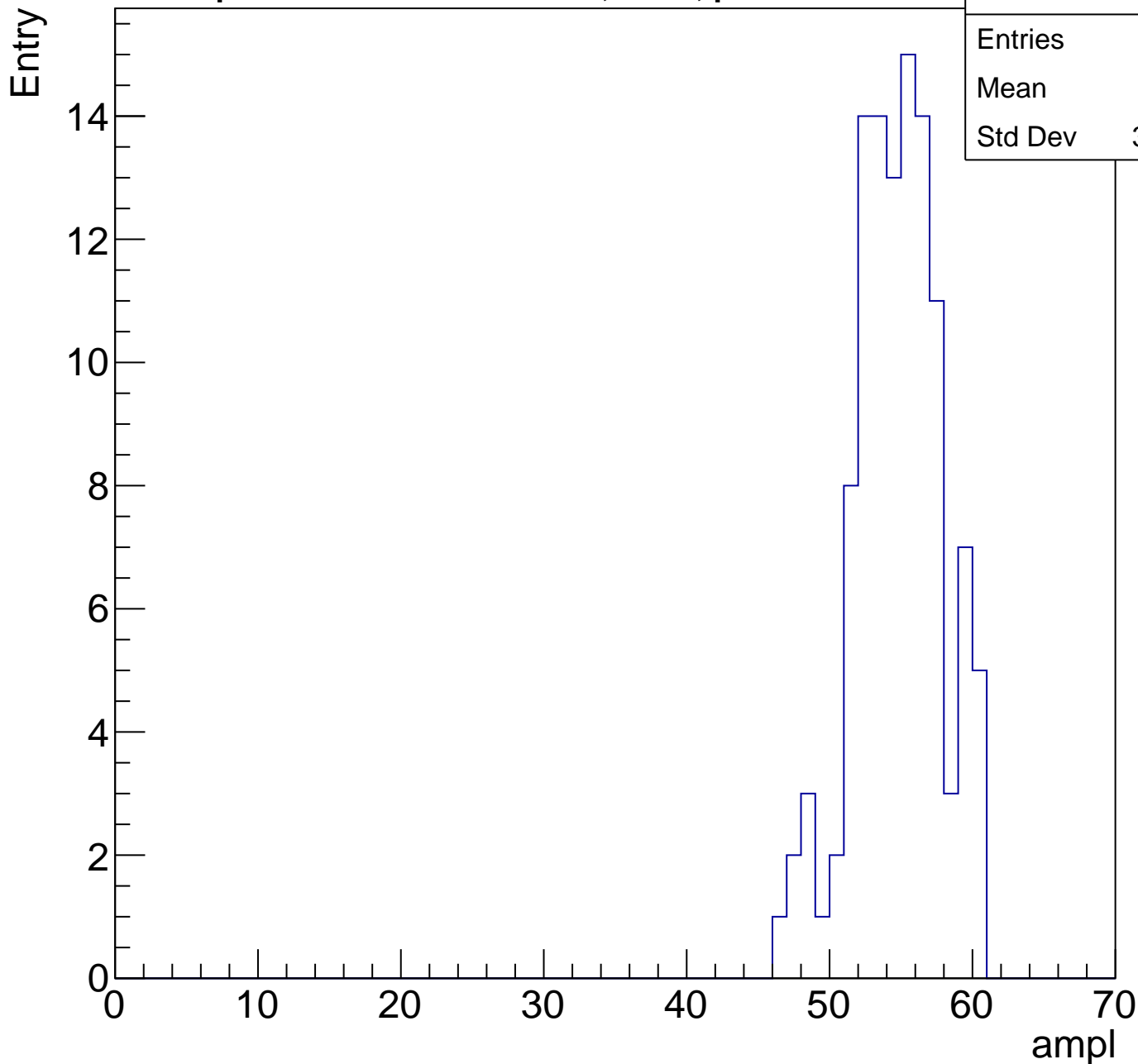
Entry



B1L001S, U20-ch36, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	54.3
Std Dev	3.022

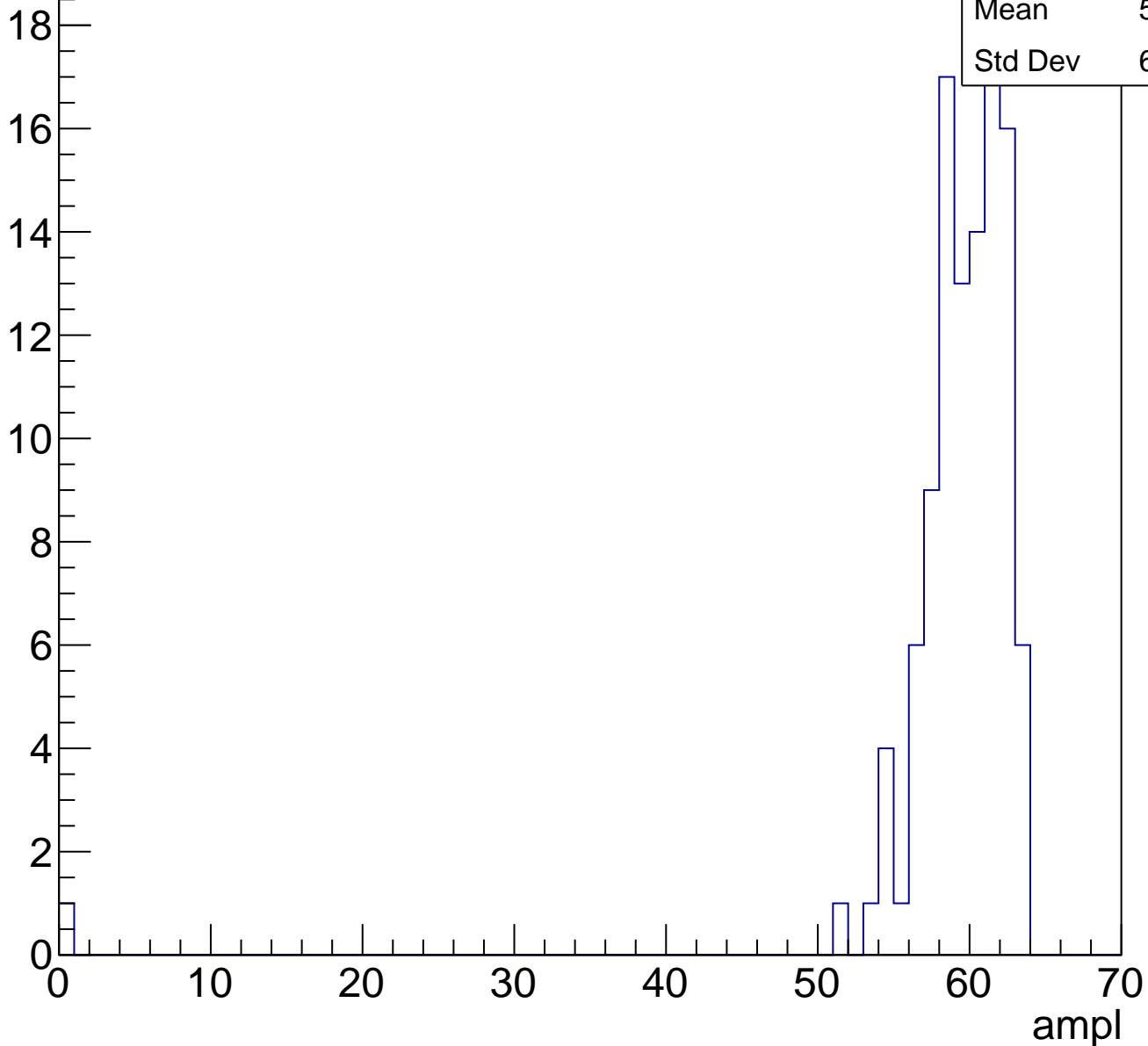


B1L001S, U20-ch36, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	58.76
Std Dev	6.182

Entry



B1L001S, U20-ch36, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	20
Mean	59.4
Std Dev	13.65

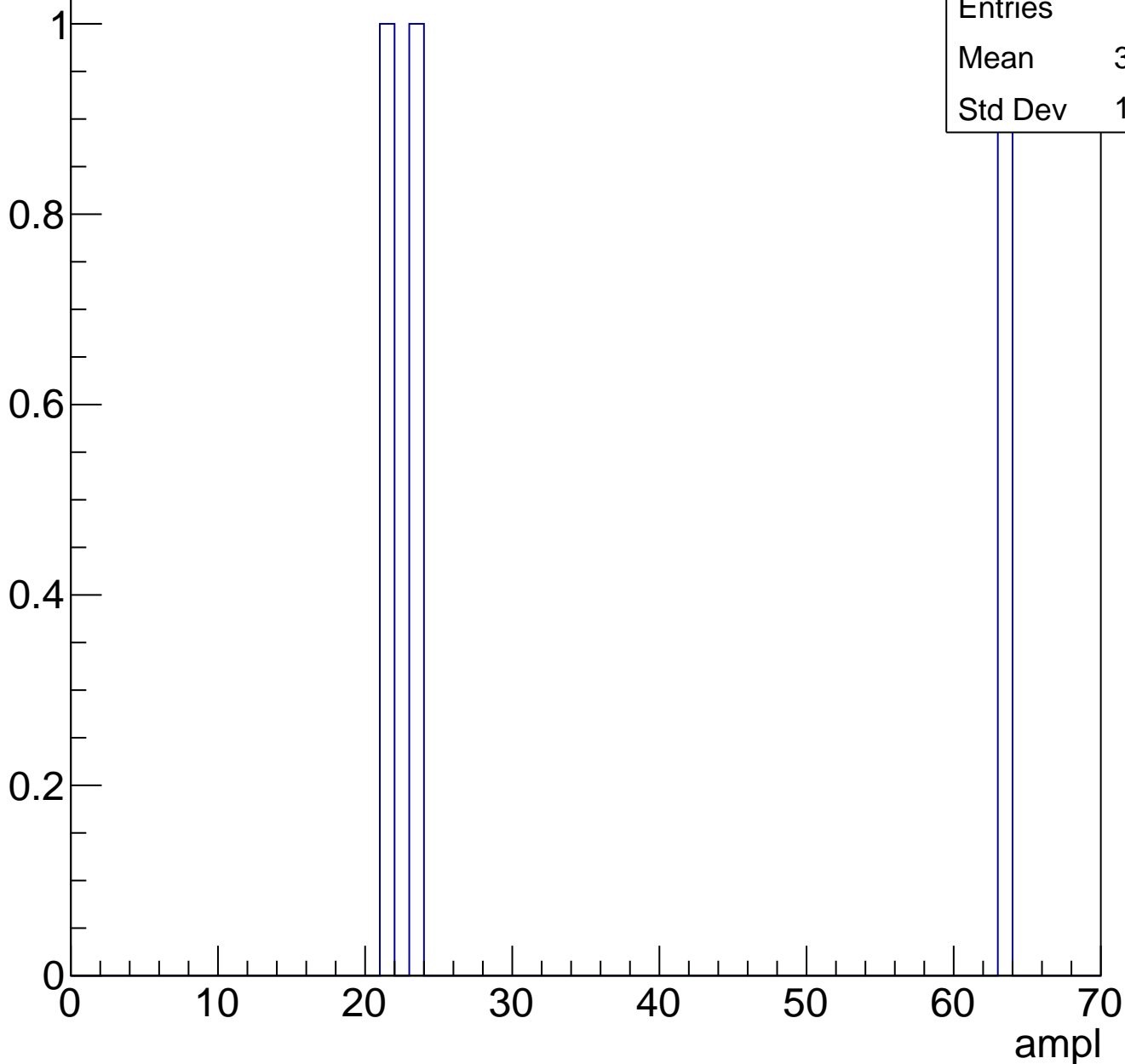
ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch36, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch37, adc0

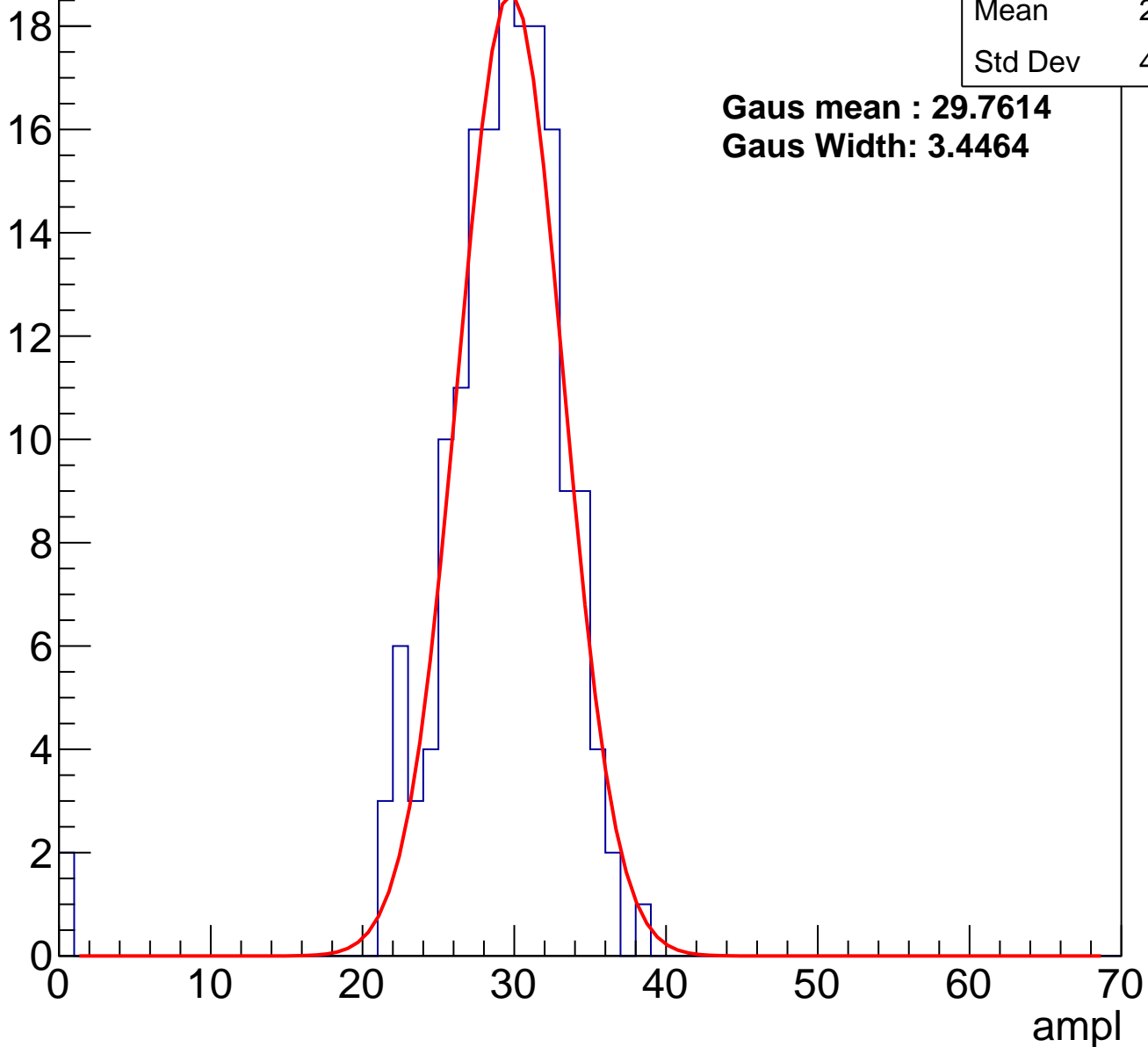
calib_packv5_042523_0143.root, FC#2, port C2

Entries	167
Mean	28.68
Std Dev	4.659

Gaus mean : 29.7614

Gaus Width: 3.4464

Entry



B1L001S, U20-ch37, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	36.3
Std Dev	3.012

Gaus mean : 36.6613

Gaus Width: 2.5942

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

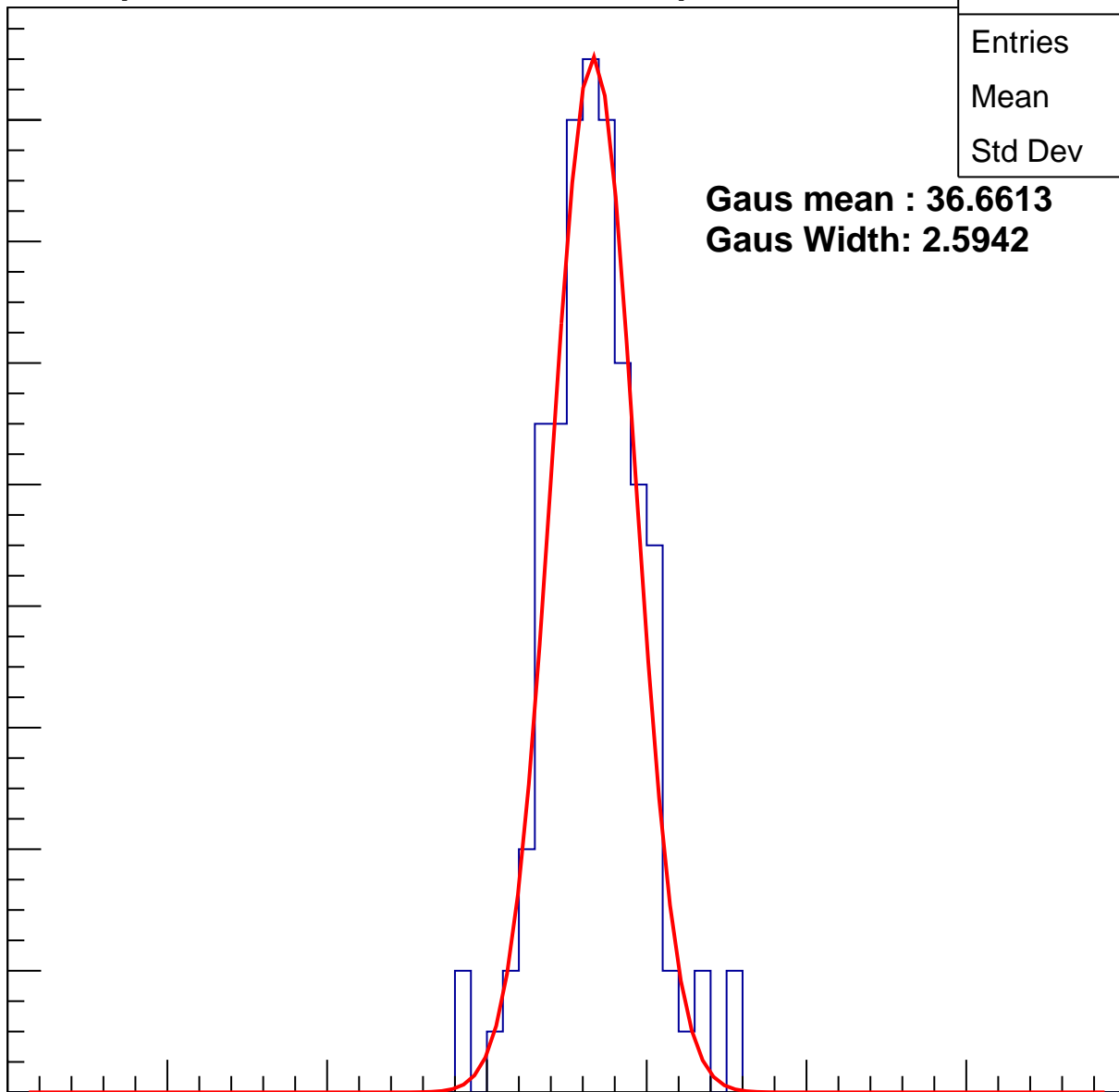
40

50

60

70

ampl



B1L001S, U20-ch37, adc2

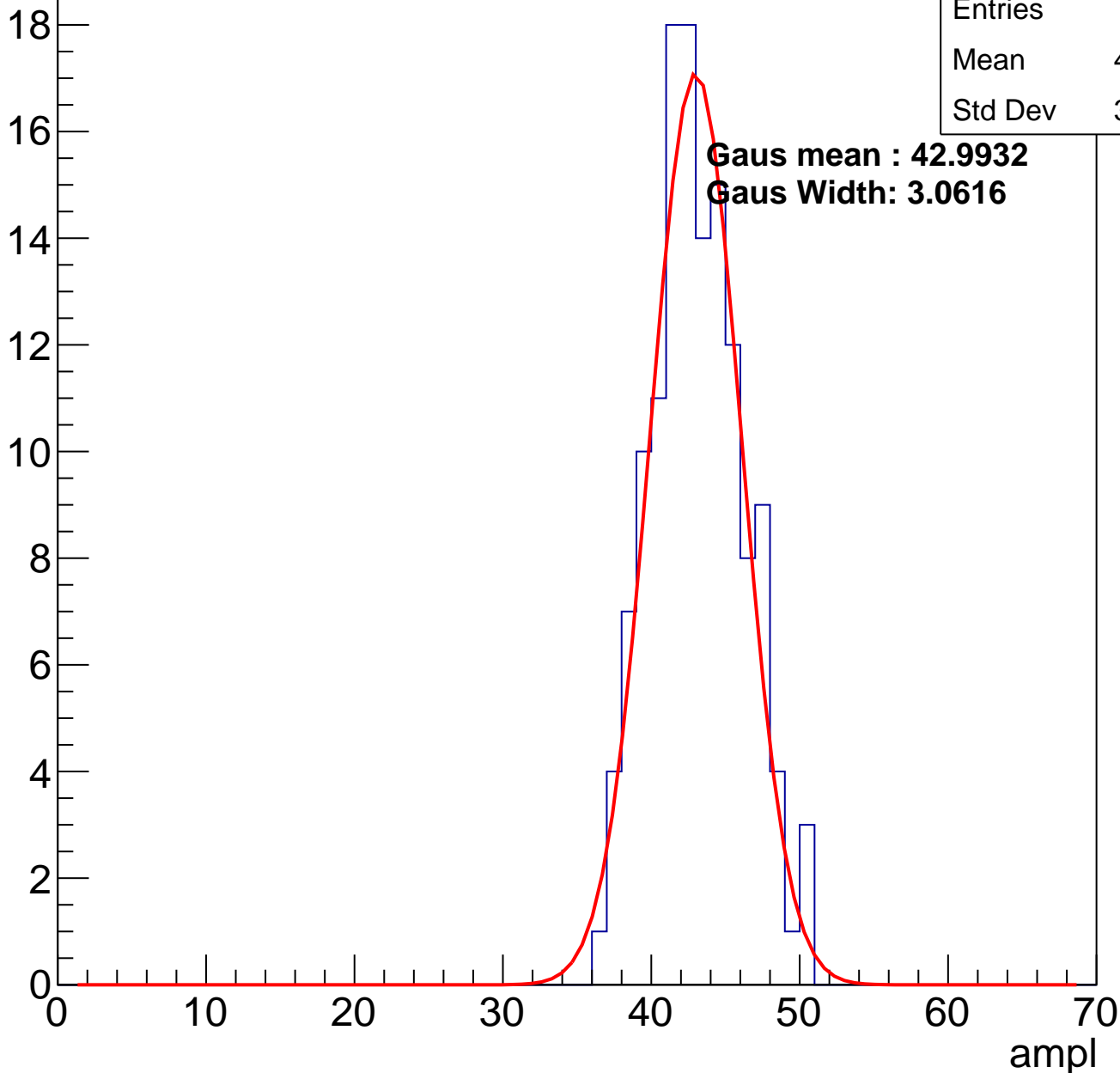
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	42.65
Std Dev	3.058

Gaus mean : 42.9932

Gaus Width: 3.0616

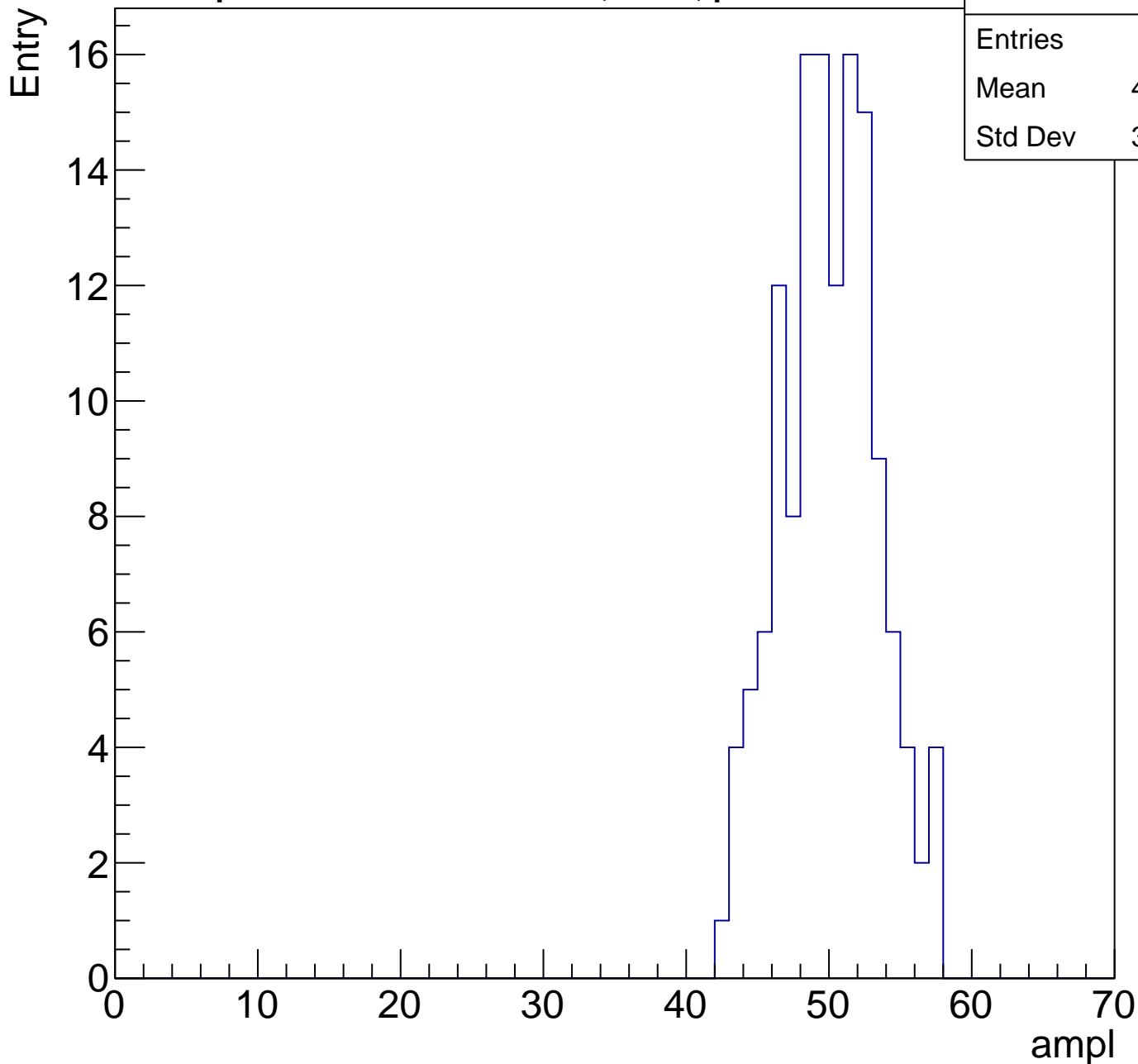
Entry



B1L001S, U20-ch37, adc3

calib_packv5_042523_0143.root, FC#2, port C2

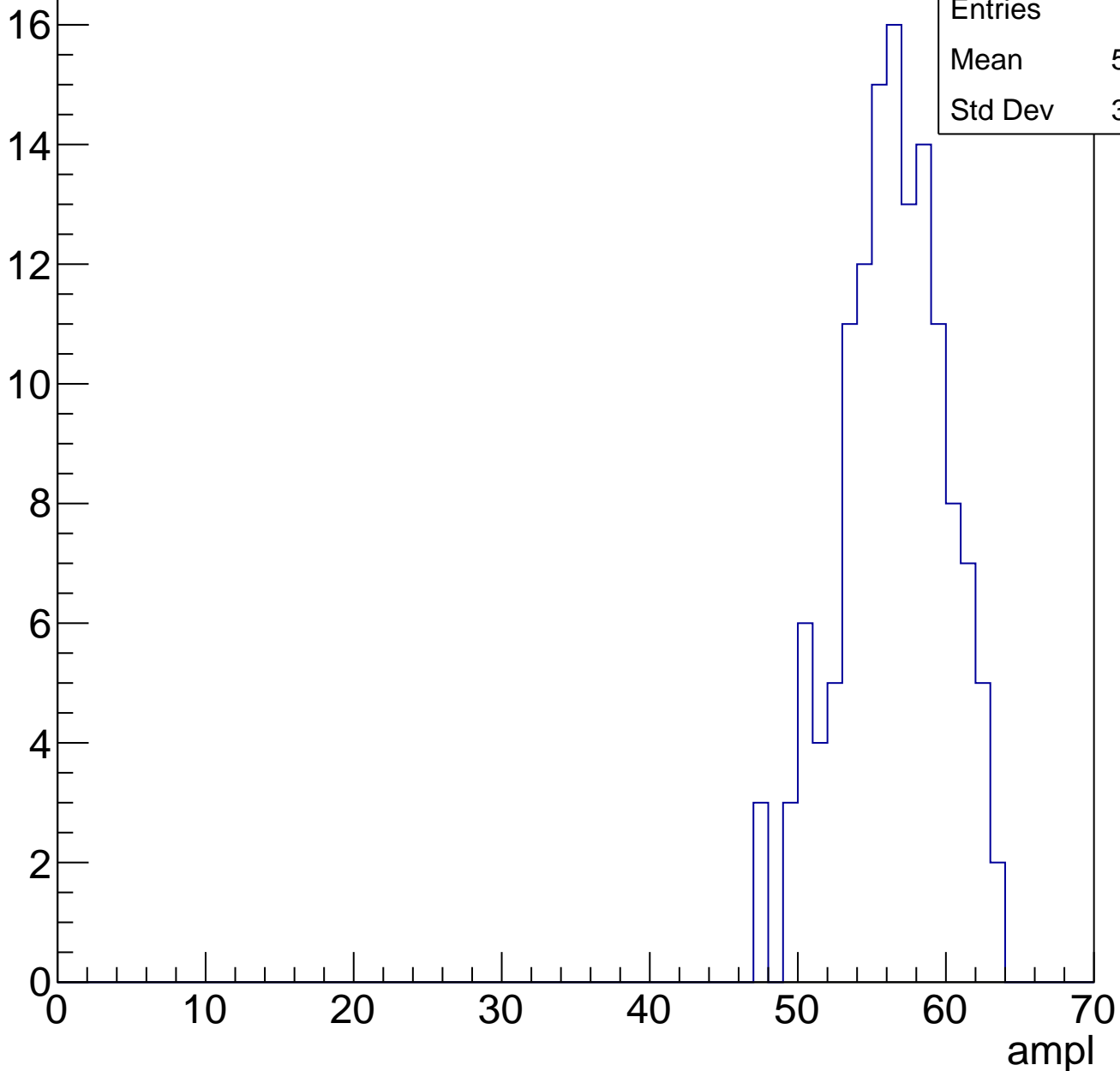
Entries	136
Mean	49.57
Std Dev	3.353



B1L001S, U20-ch37, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch37, adc5

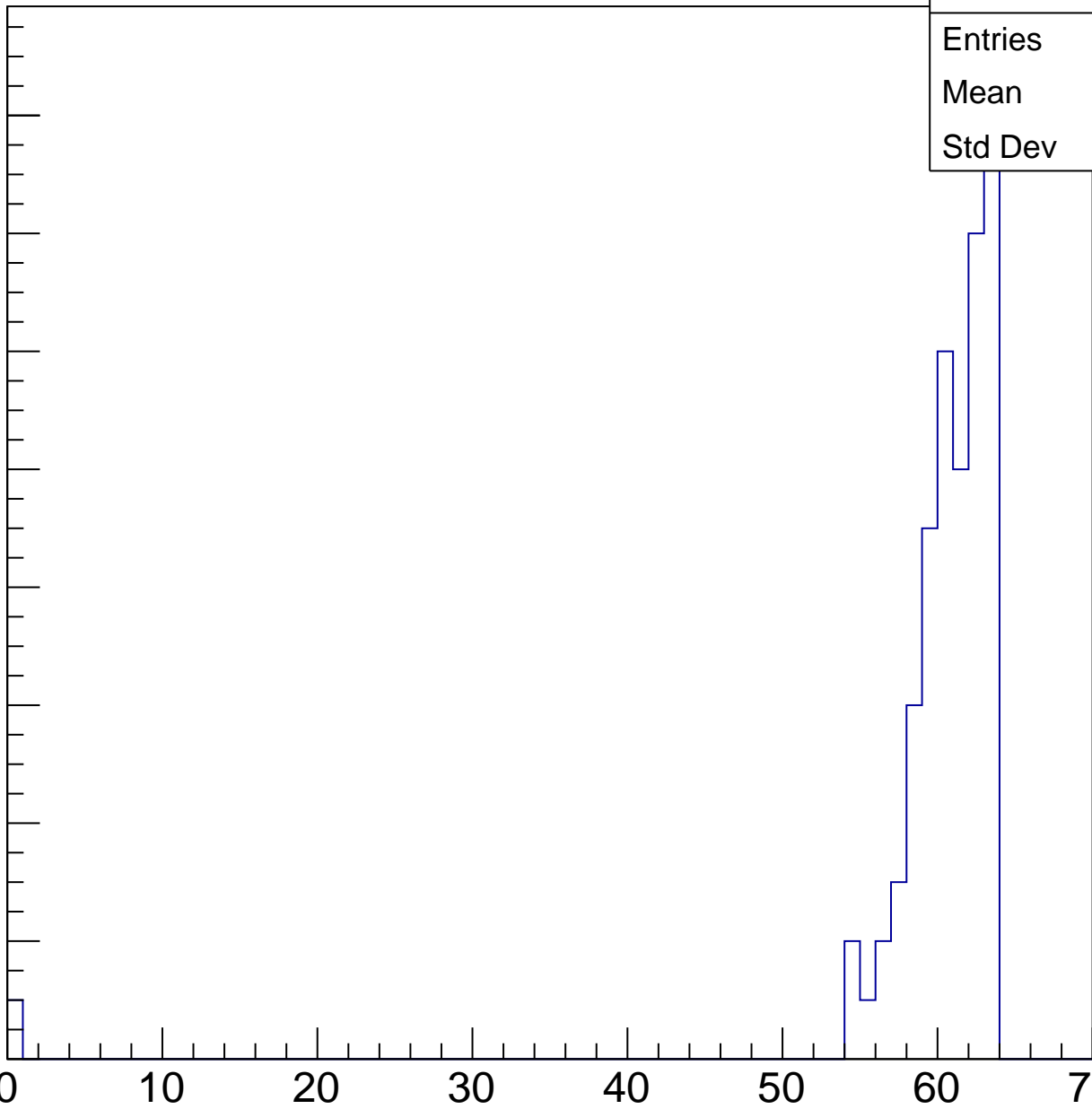
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	77
Mean	59.66
Std Dev	7.207

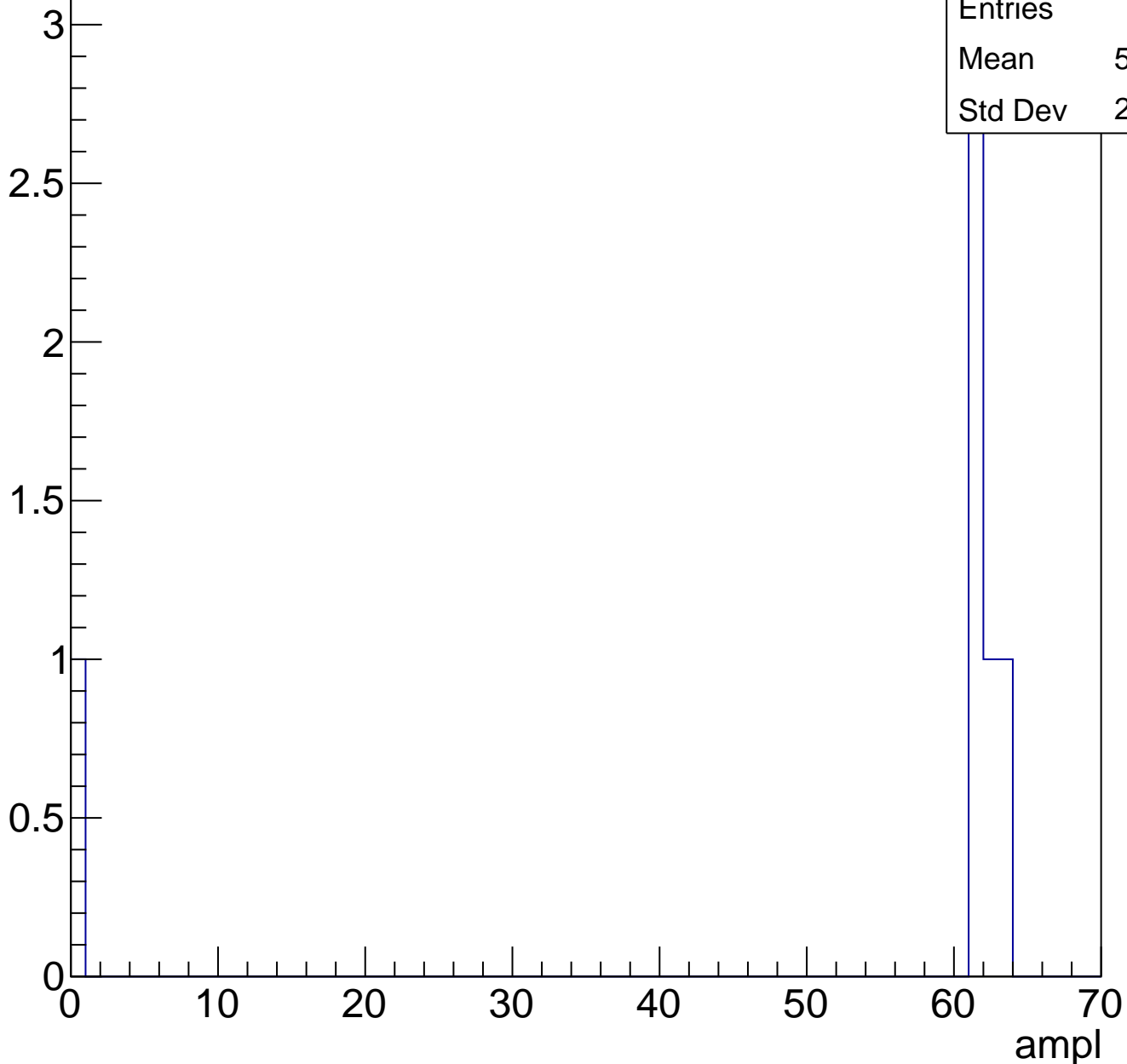
ampl



B1L001S, U20-ch37, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch37, adc7

calib_packv5_042523_0143.root, FC#2, port C2

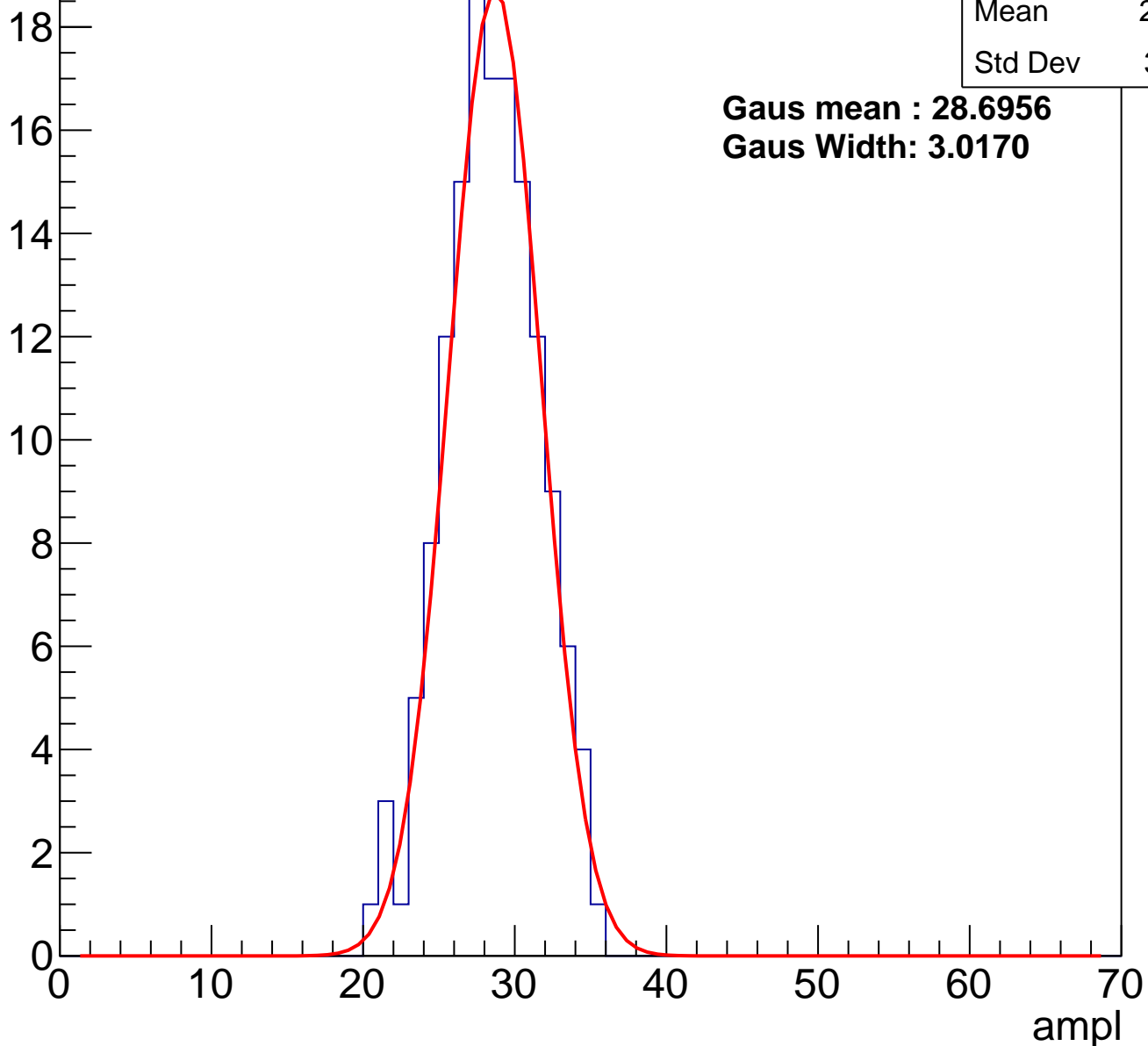
Entry



B1L001S, U20-ch38, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

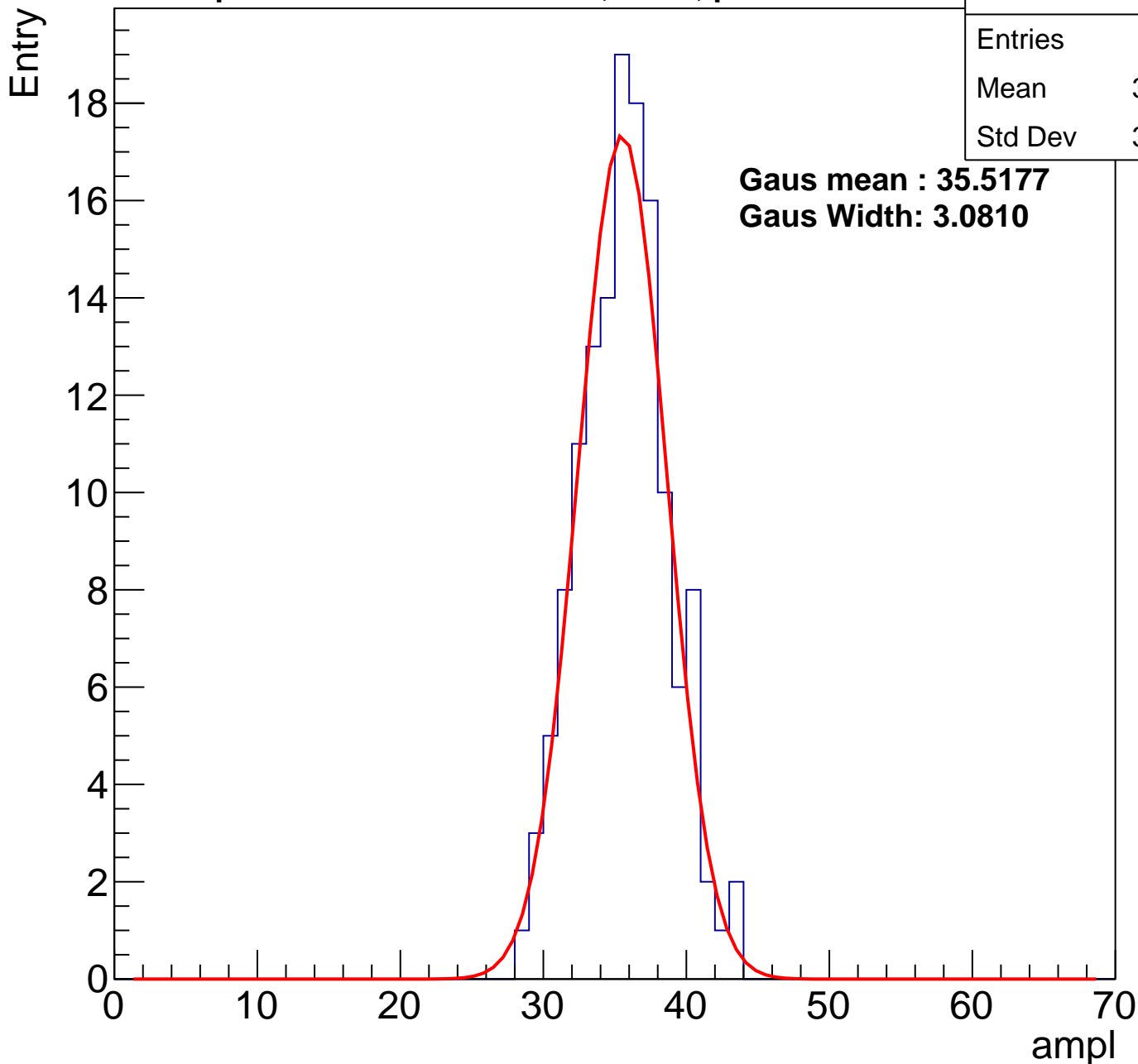


B1L001S, U20-ch38, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.18
Std Dev	3.066

Gaus mean : 35.5177
Gaus Width: 3.0810



B1L001S, U20-ch38, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	41.18
Std Dev	2.922

Gaus mean : 41.8352

Gaus Width: 2.9690

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

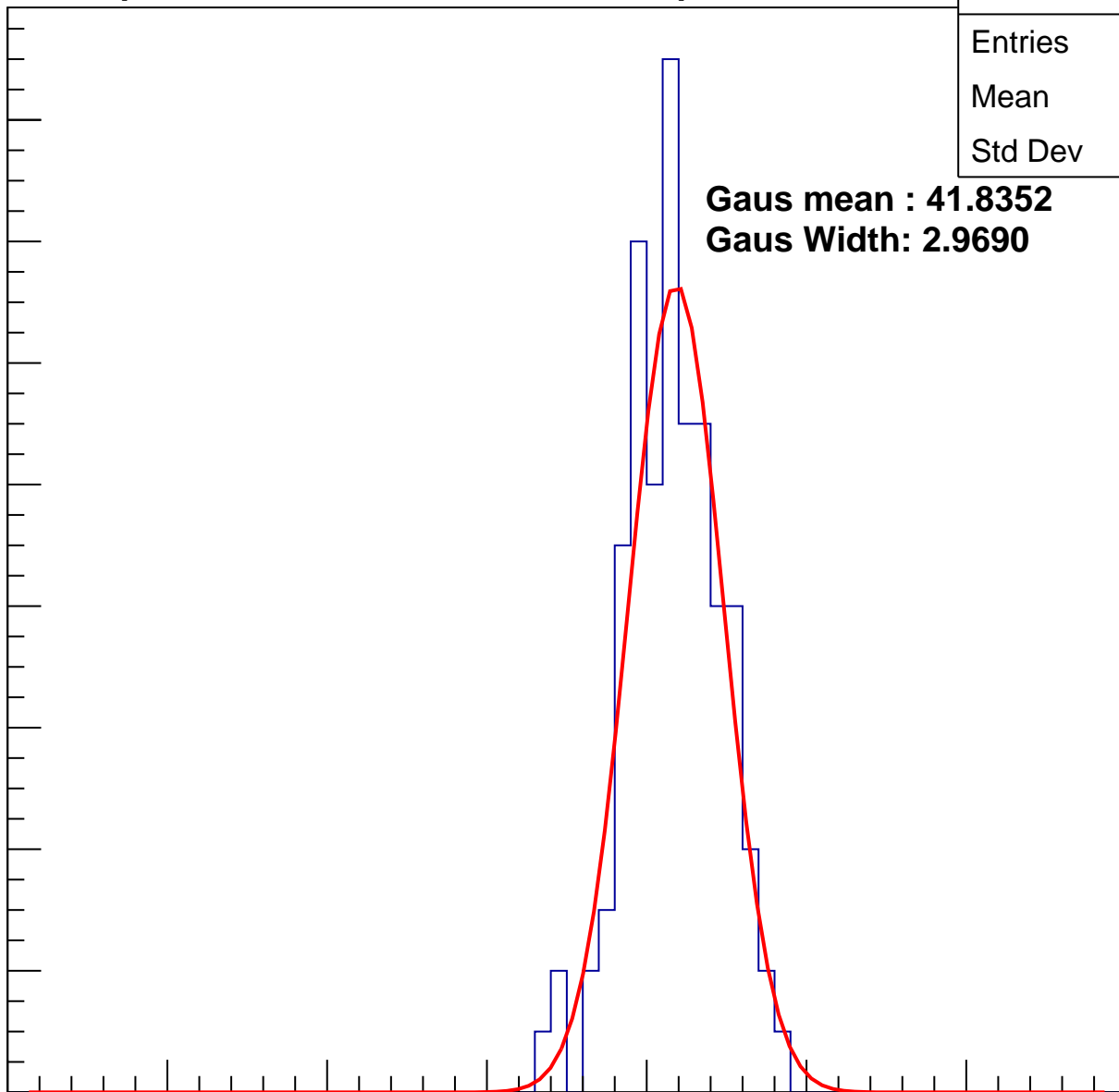
40

50

60

70

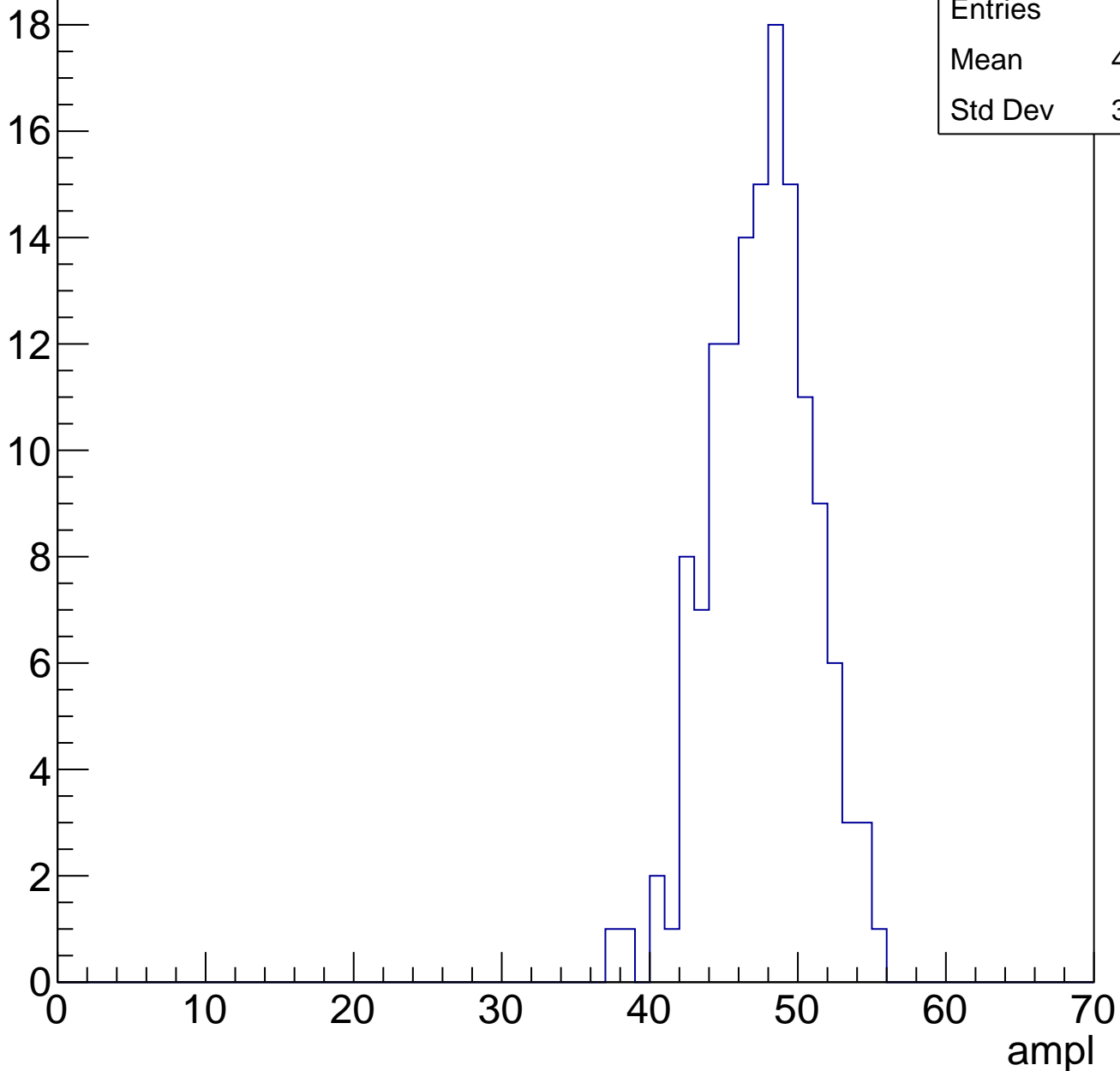
ampl



B1L001S, U20-ch38, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

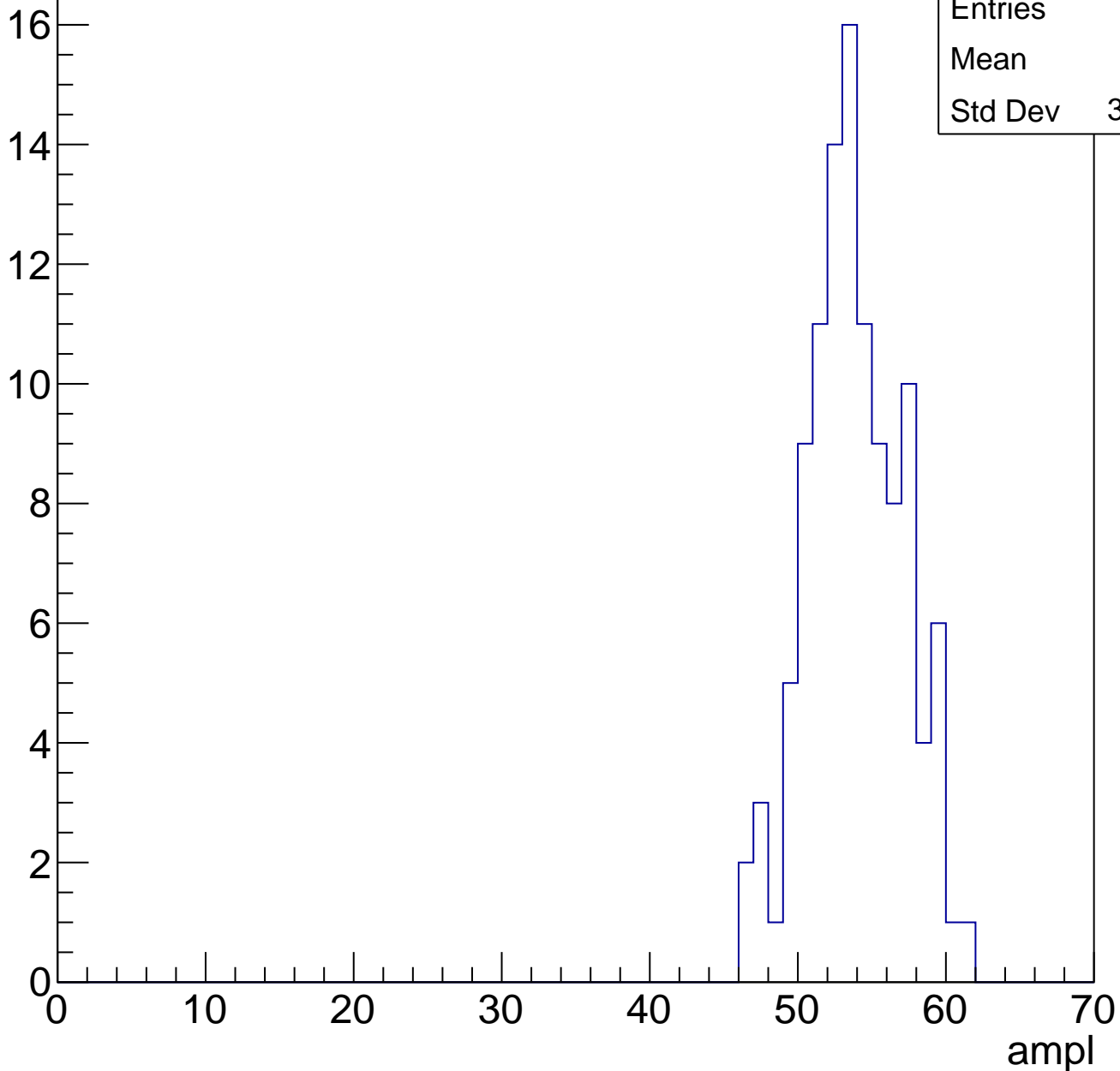


Entries	139
Mean	47.09
Std Dev	3.378

B1L001S, U20-ch38, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch38, adc5

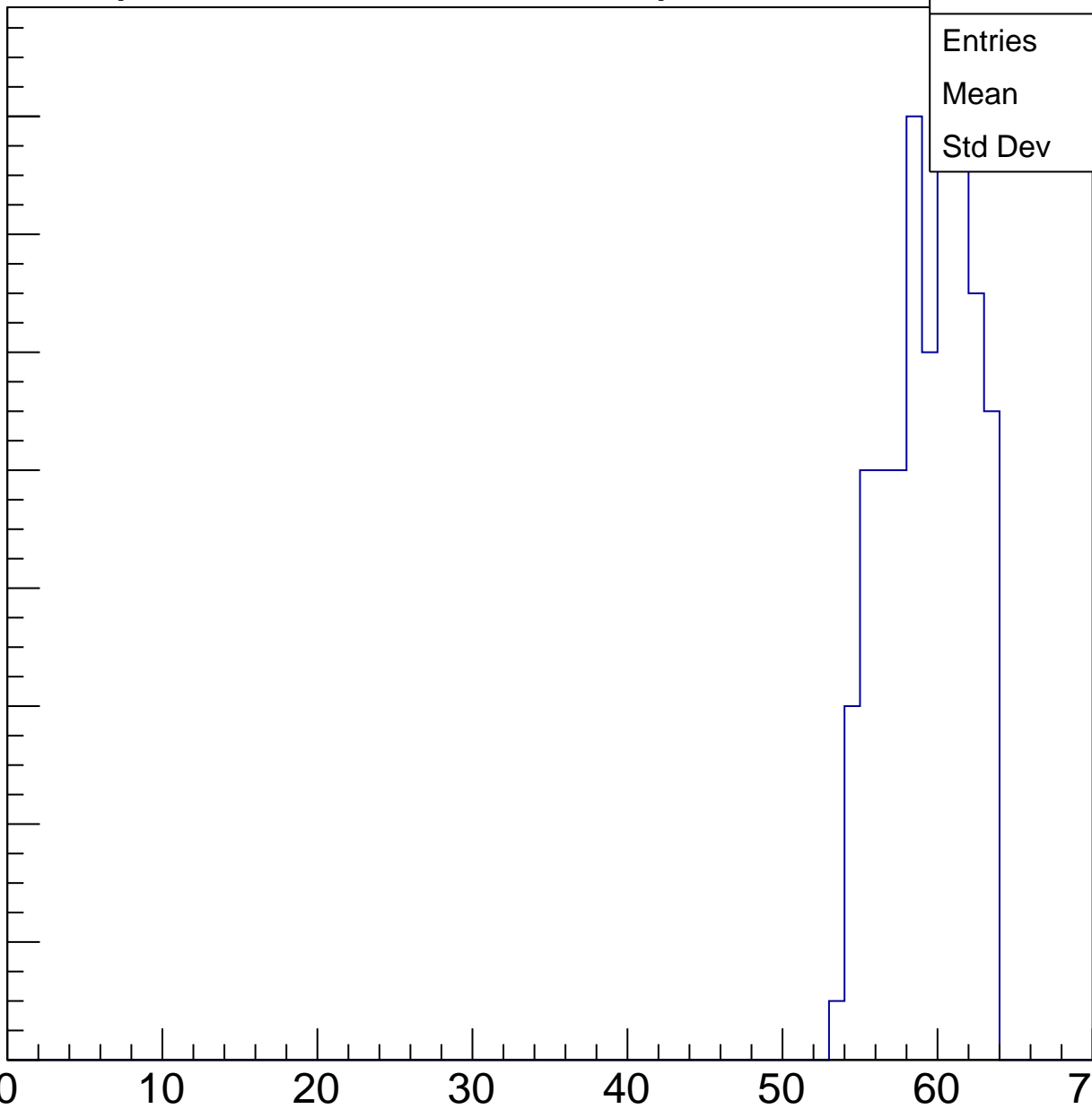
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	123
Mean	58.93
Std Dev	2.659

ampl

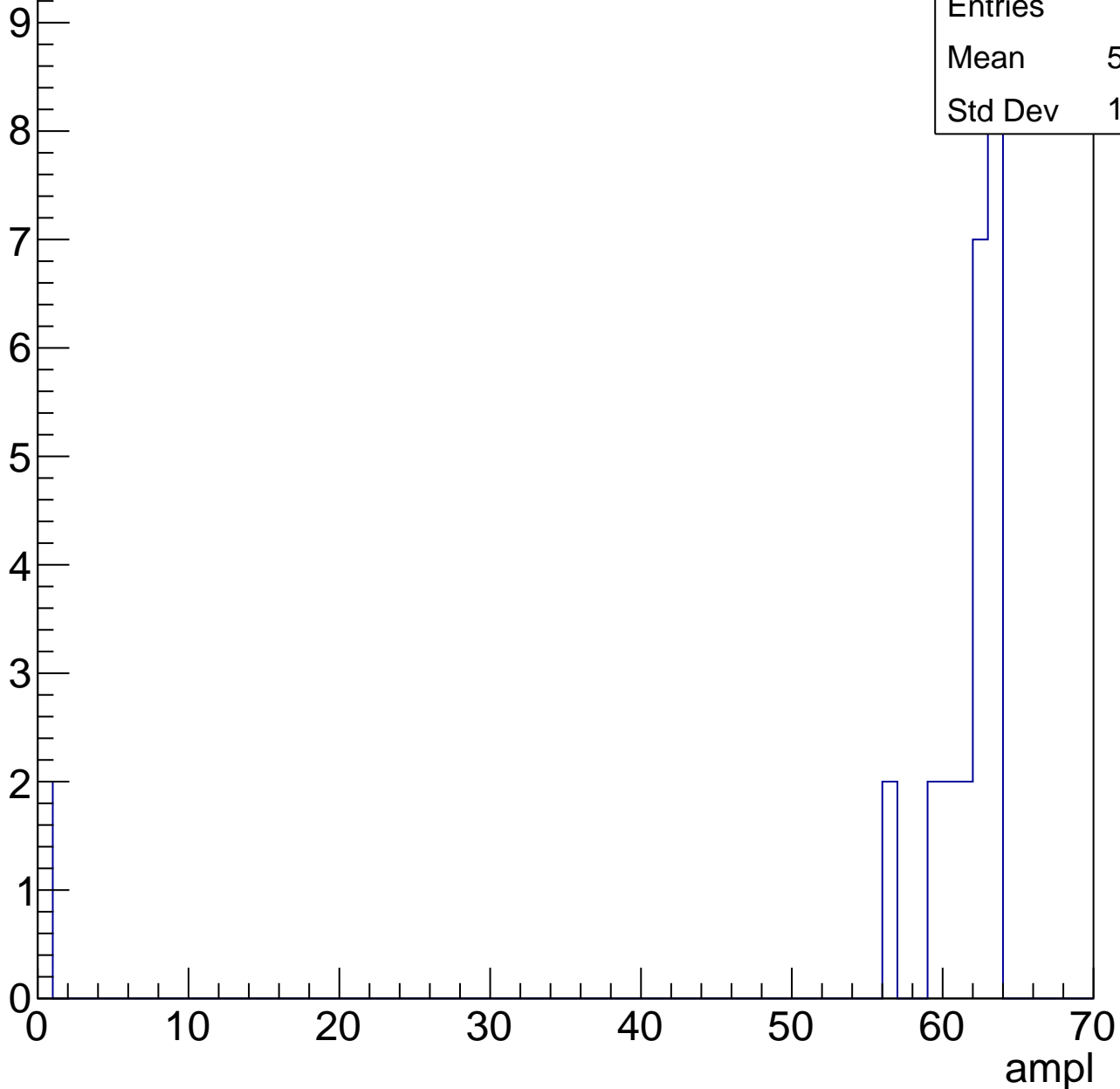


B1L001S, U20-ch38, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	56.65
Std Dev	16.47



B1L001S, U20-ch38, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch39, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	30.23
Std Dev	3.483

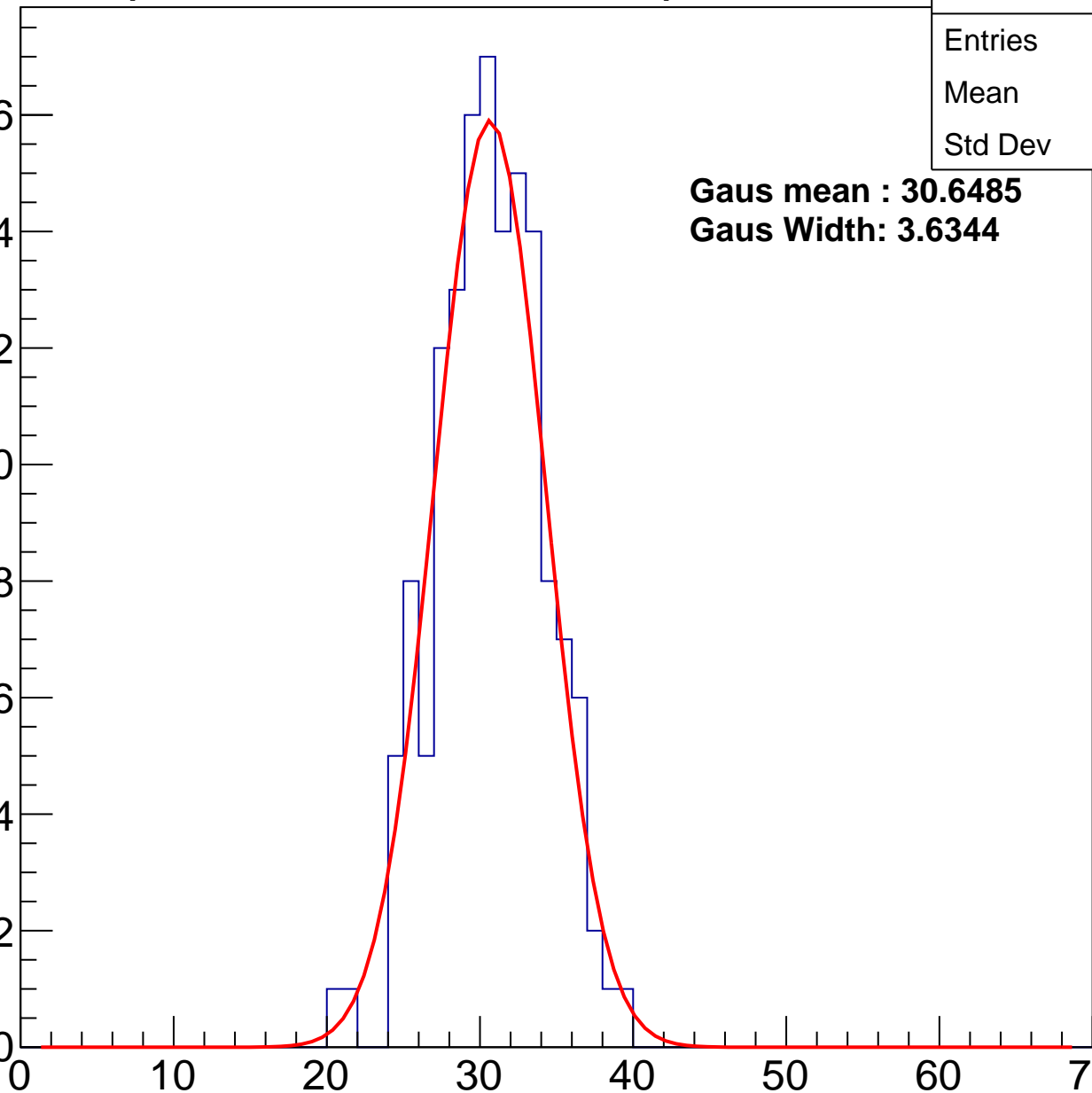
Gaus mean : 30.6485

Gaus Width: 3.6344

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch39, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	36.93
Std Dev	3.313

Gaus mean : 37.5318

Gaus Width: 3.1856

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

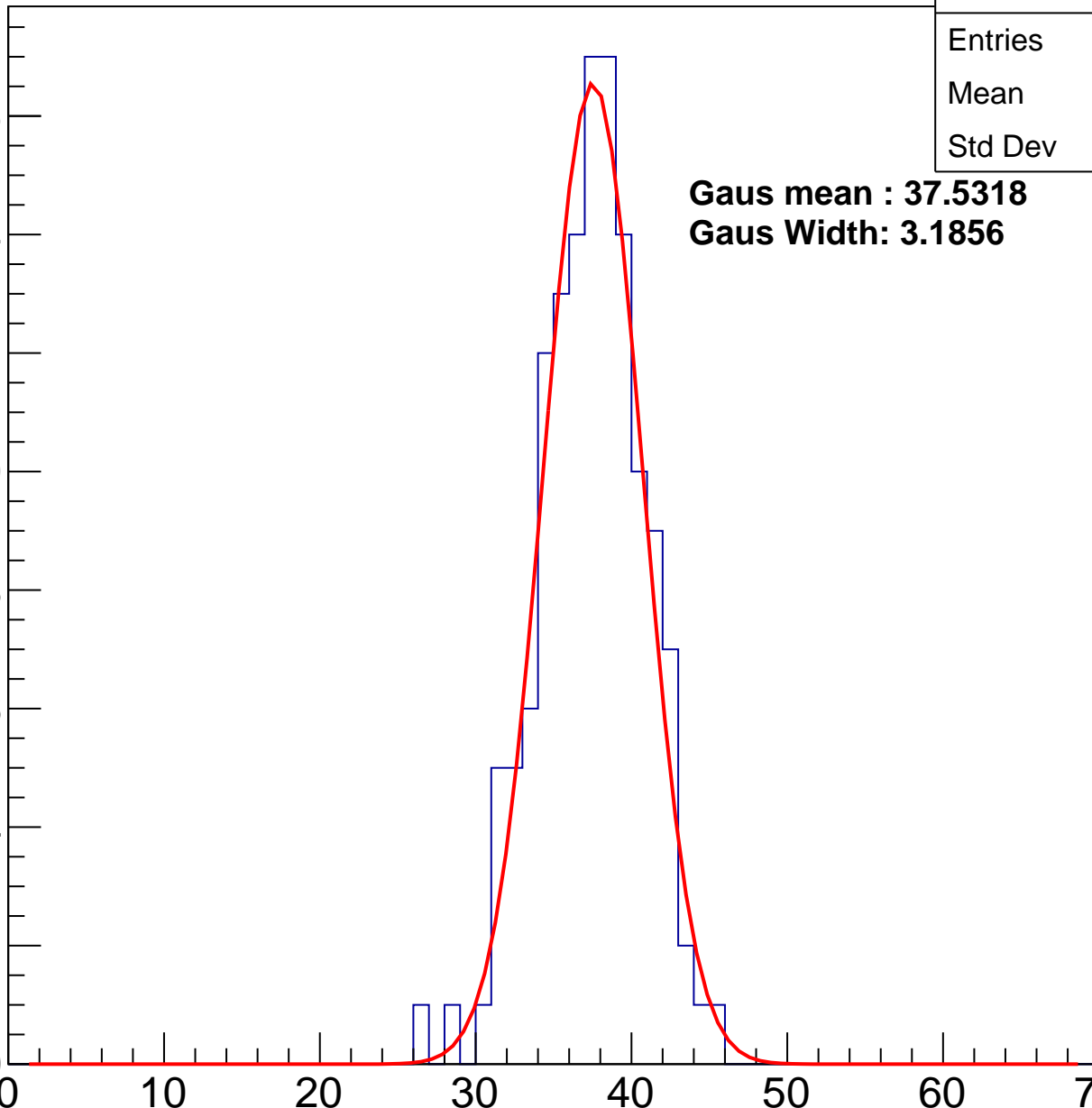
40

50

60

70

ampl



B1L001S, U20-ch39, adc2

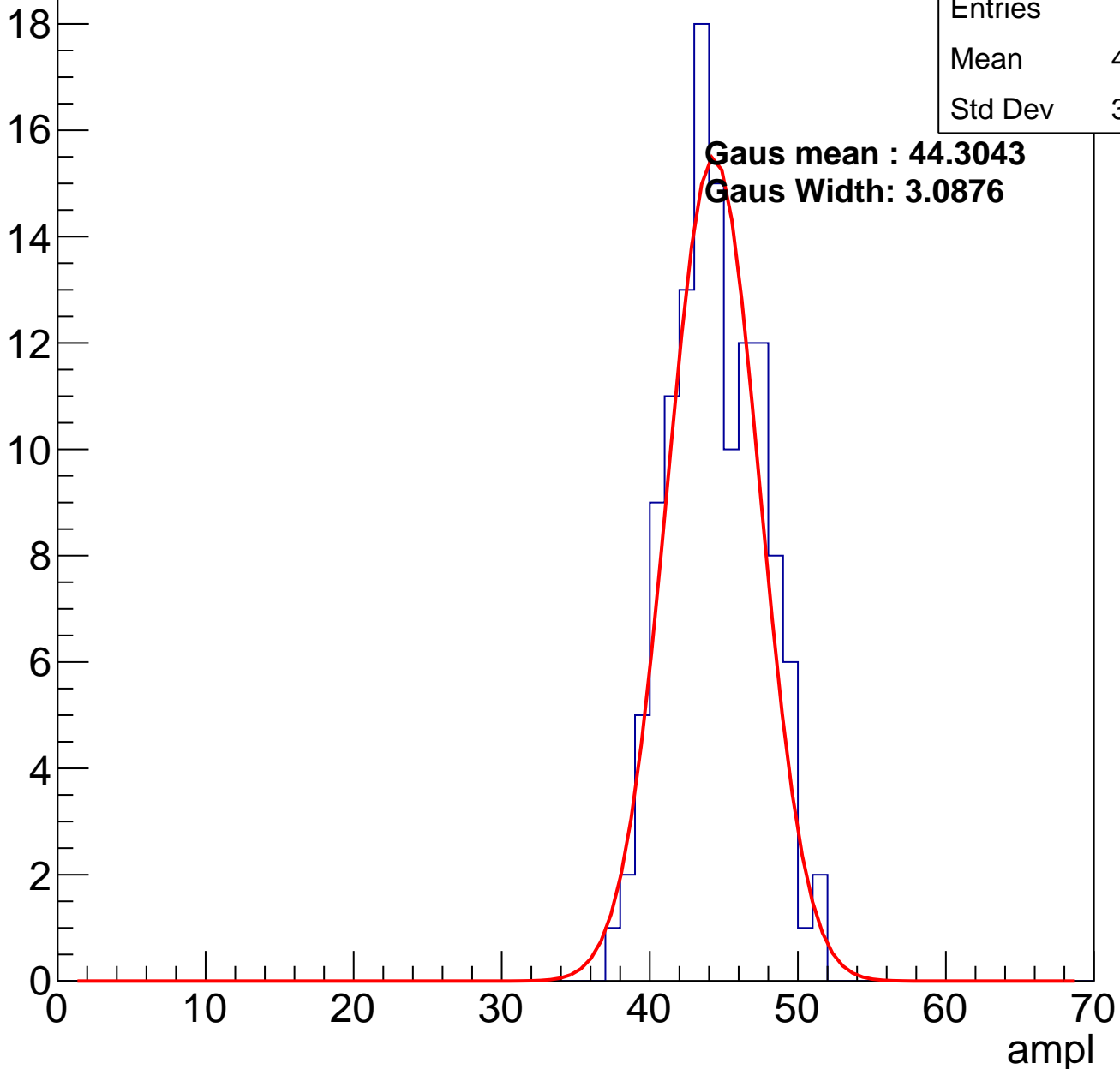
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	43.96
Std Dev	3.016

Gaus mean : 44.3043

Gaus Width: 3.0876

Entry



B1L001S, U20-ch39, adc3

calib_packv5_042523_0143.root, FC#2, port C2

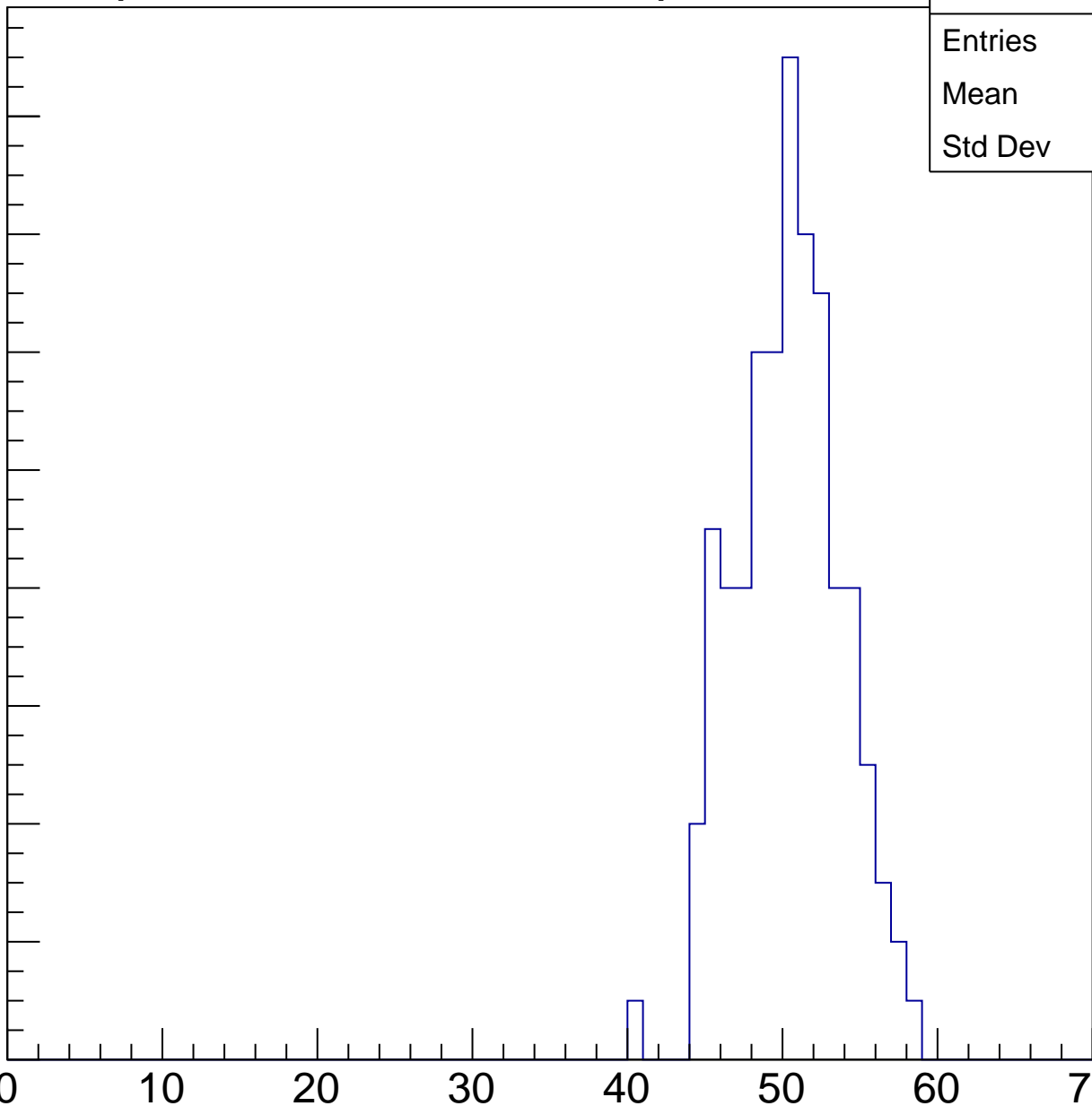
Entries	125
Mean	49.92
Std Dev	3.334

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

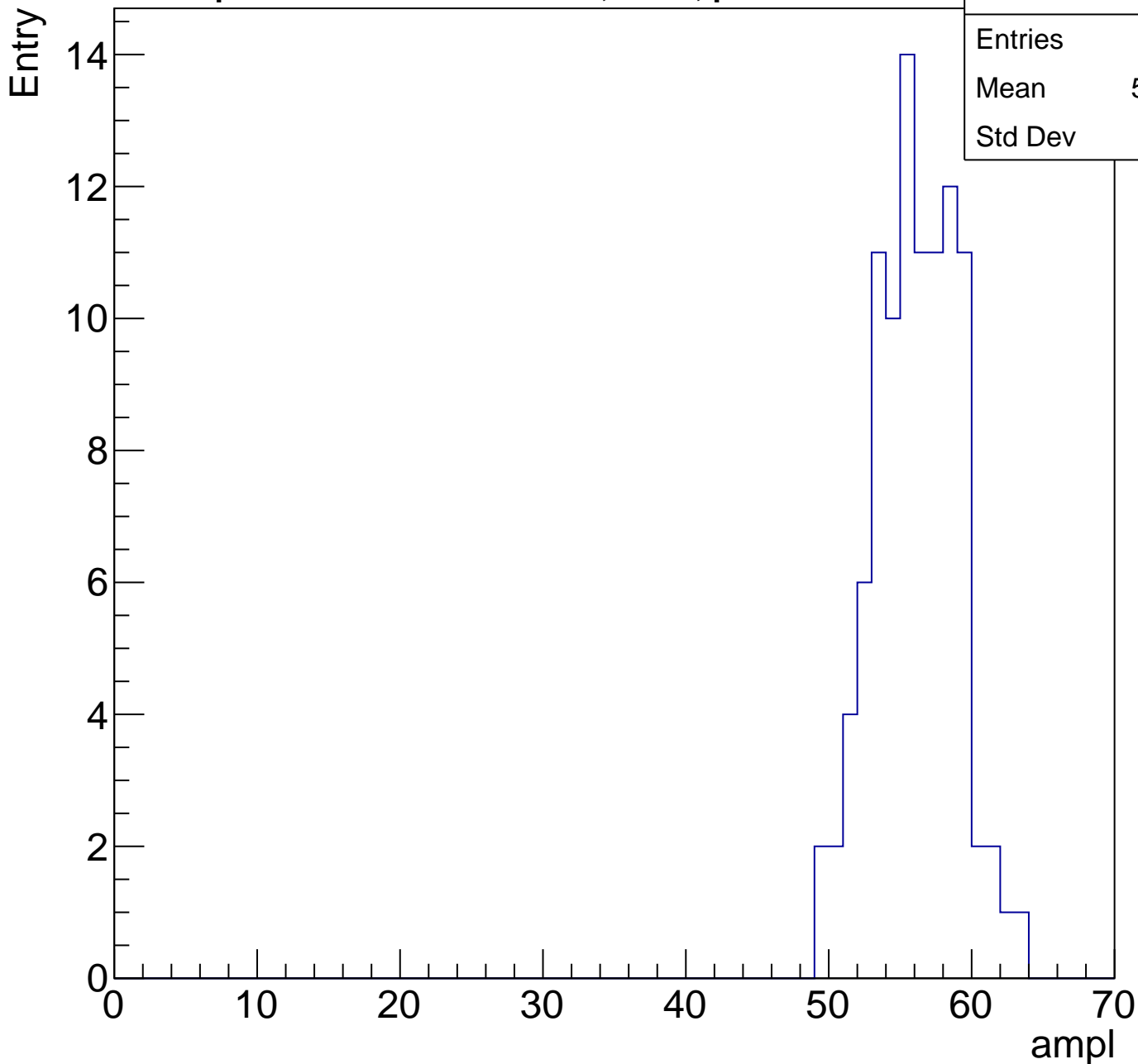
ampl



B1L001S, U20-ch39, adc4

calib_packv5_042523_0143.root, FC#2, port C2

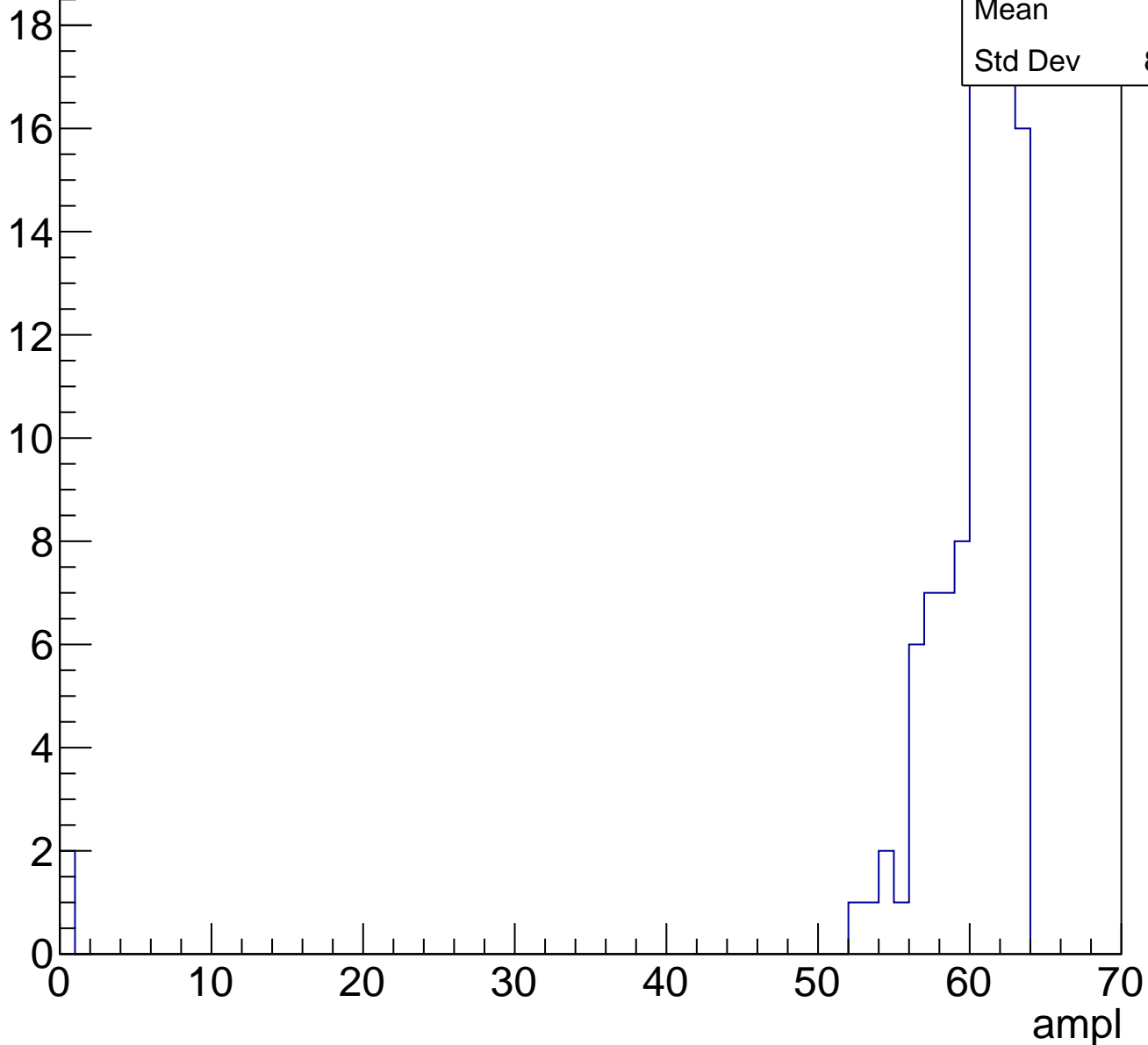
Entries	100
Mean	55.62
Std Dev	2.88



B1L001S, U20-ch39, adc5

calib_packv5_042523_0143.root, FC#2, port C2

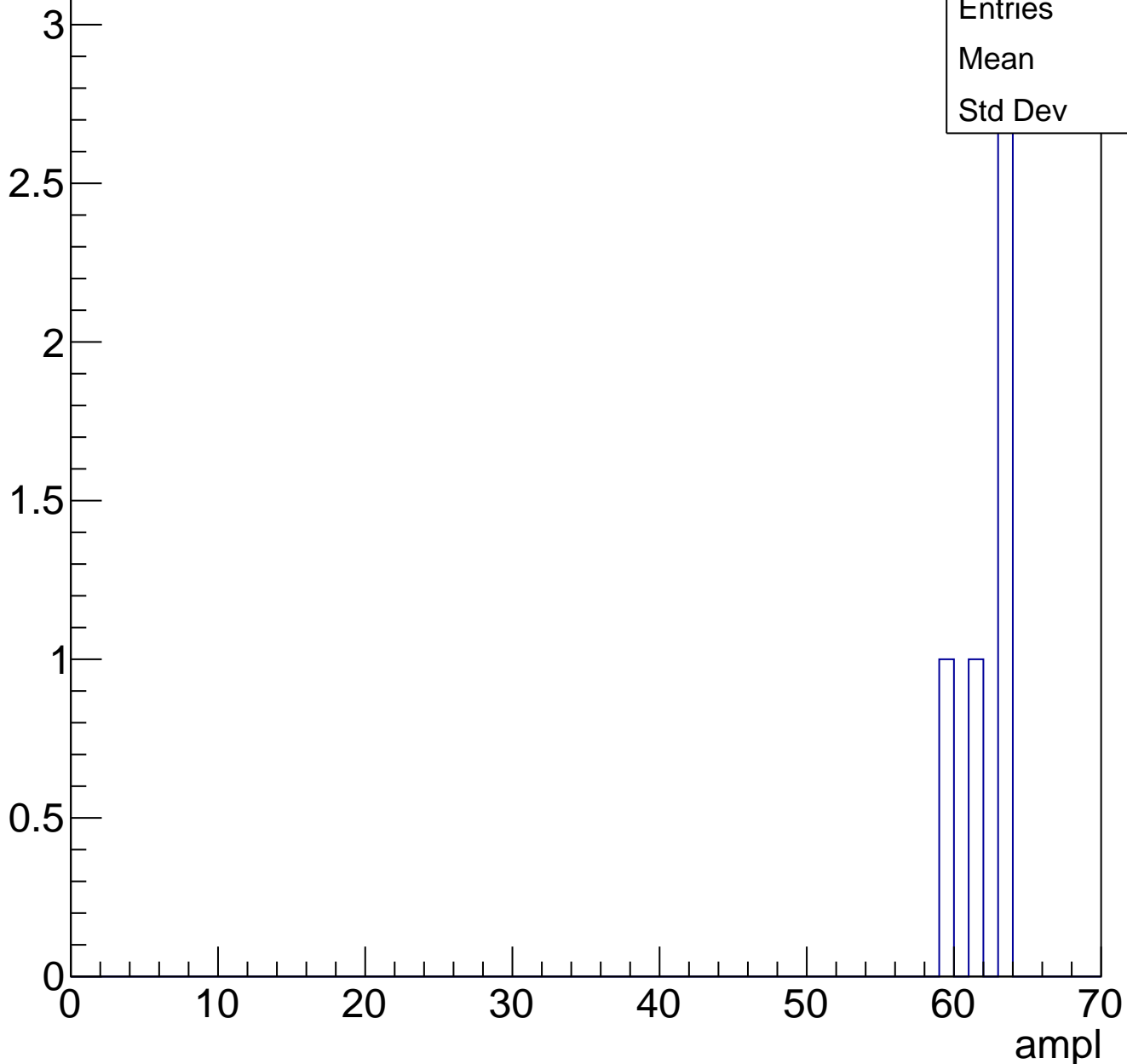
Entry



B1L001S, U20-ch39, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	61.8
Std Dev	1.6

B1L001S, U20-ch39, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U20-ch40, adc0

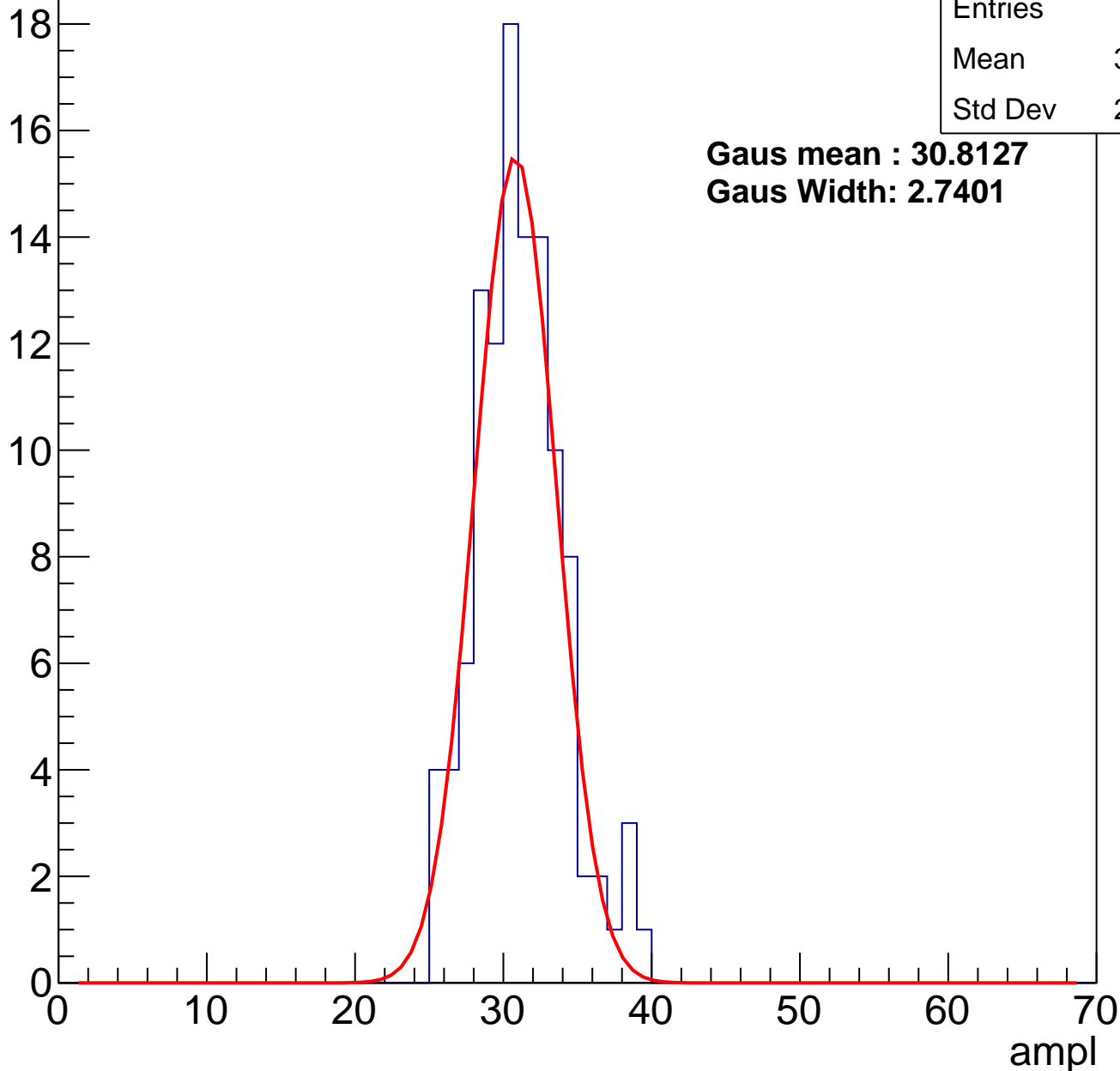
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	30.66
Std Dev	2.935

Gaus mean : 30.8127

Gaus Width: 2.7401

Entry



B1L001S, U20-ch40, adc1

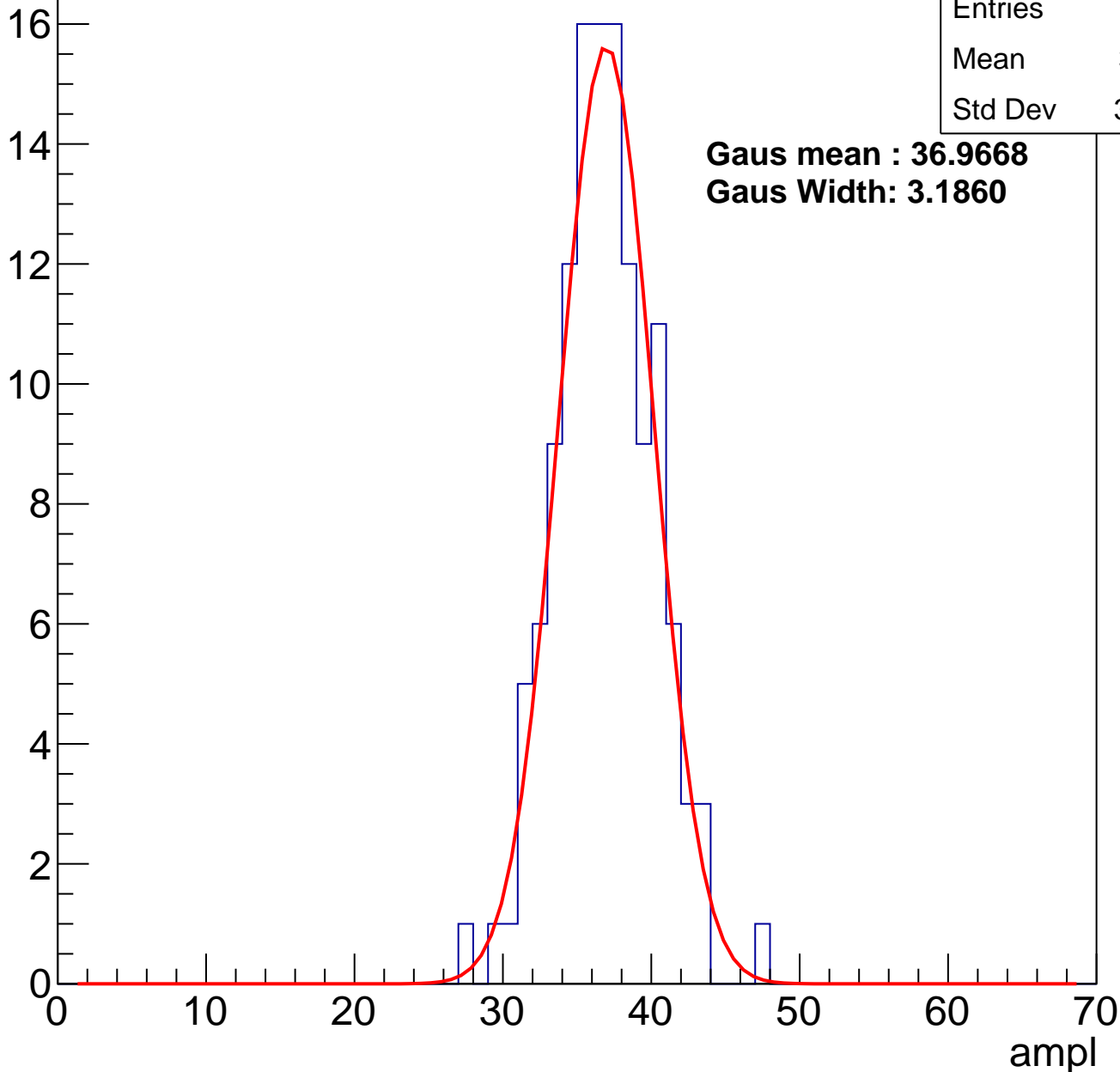
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.41
Std Dev	3.256

Gaus mean : 36.9668

Gaus Width: 3.1860

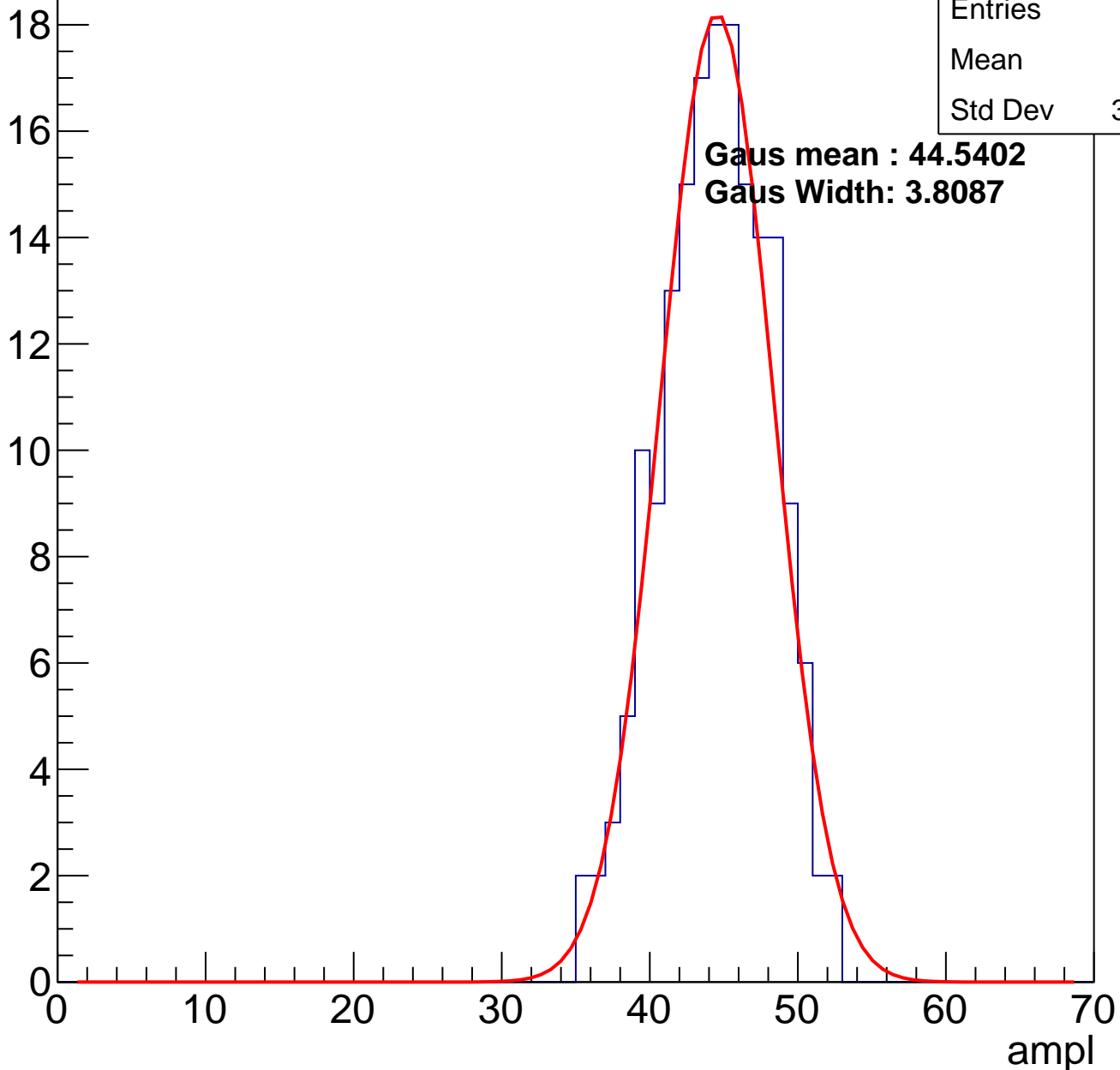
Entry



B1L001S, U20-ch40, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch40, adc3

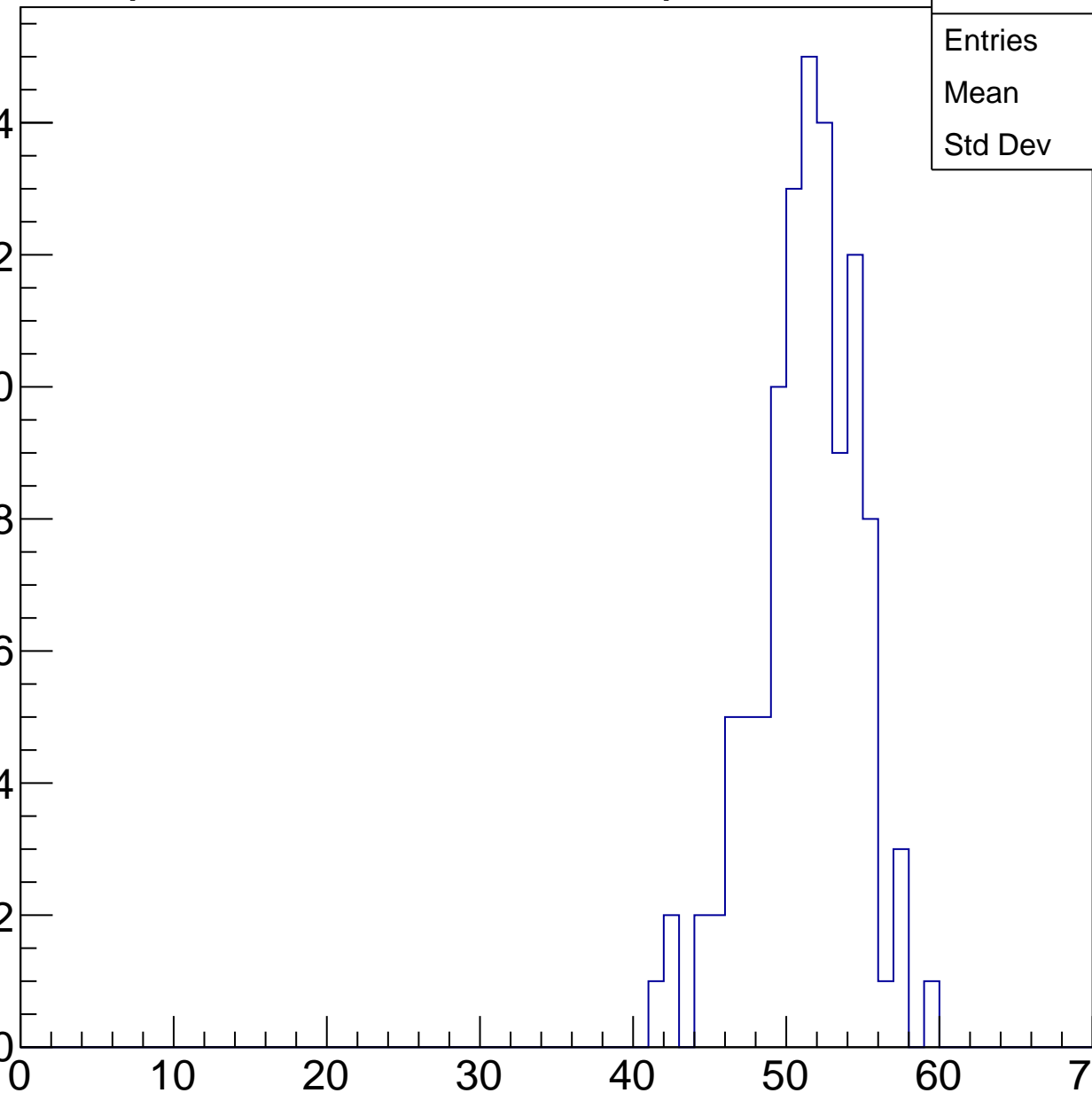
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	50.85
Std Dev	3.355

ampl



B1L001S, U20-ch40, adc4

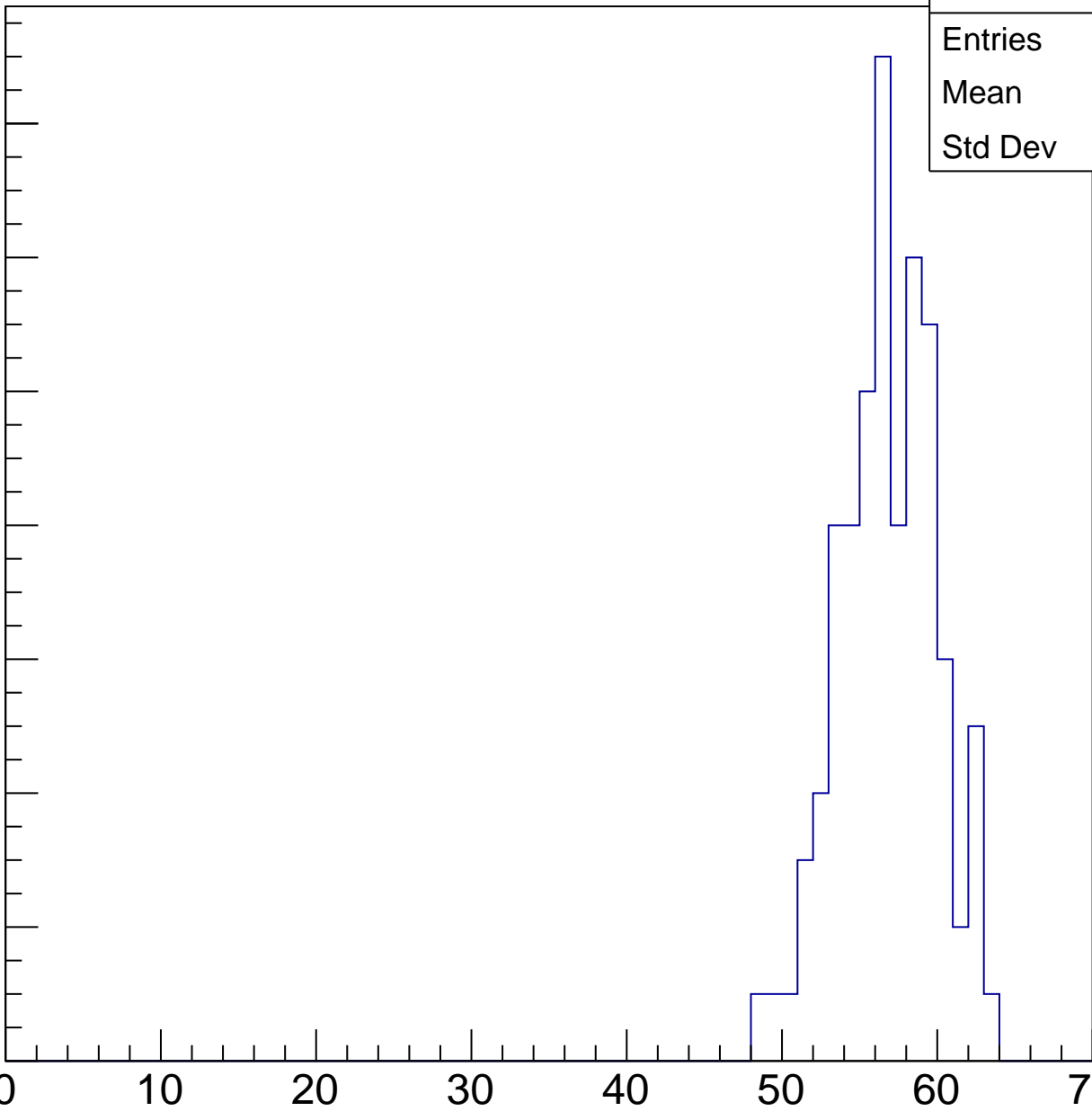
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	56.35
Std Dev	3.096

ampl



B1L001S, U20-ch40, adc5

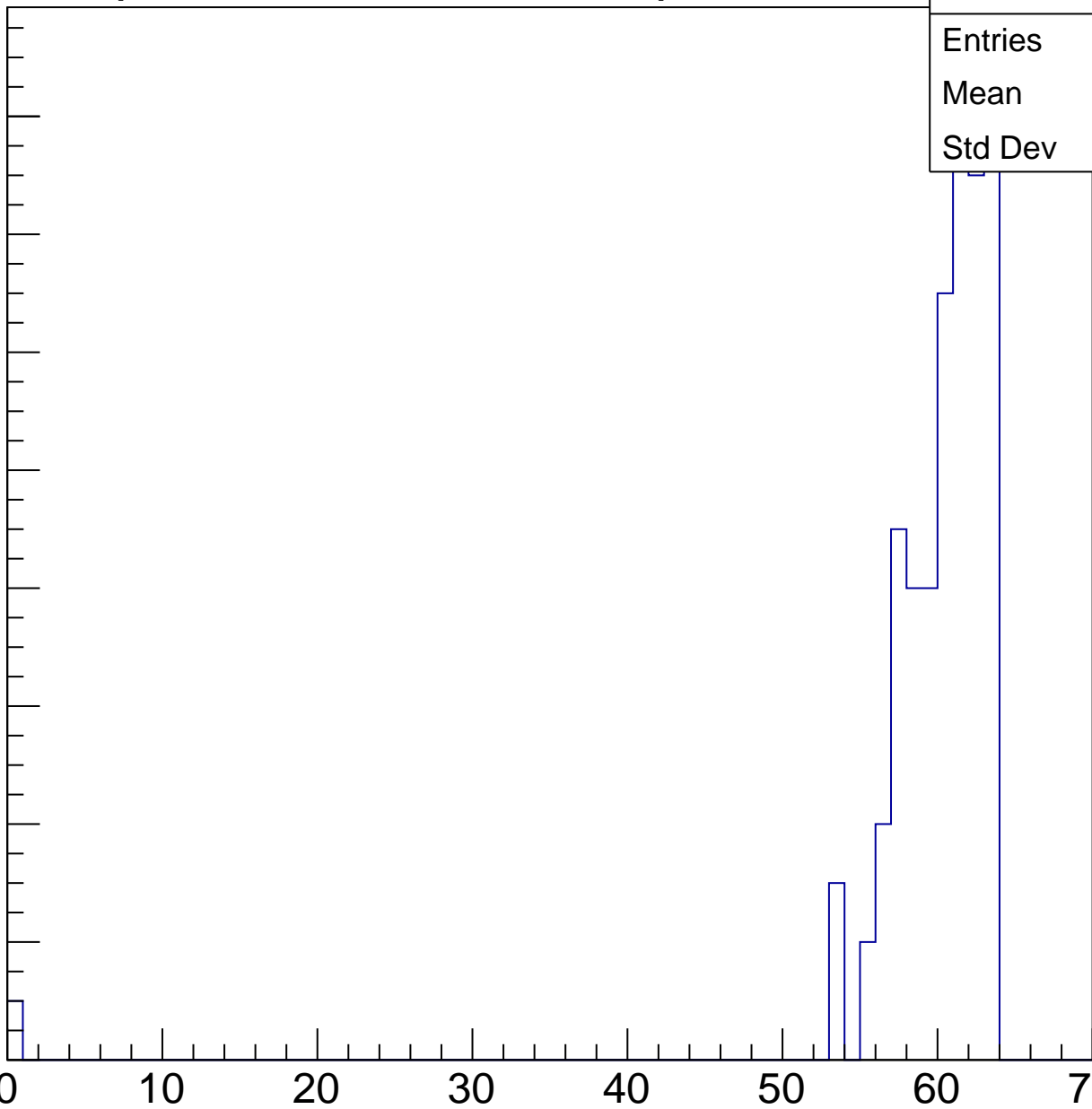
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	59.38
Std Dev	6.56

ampl



B1L001S, U20-ch40, adc6

calib_packv5_042523_0143.root, FC#2, port C2

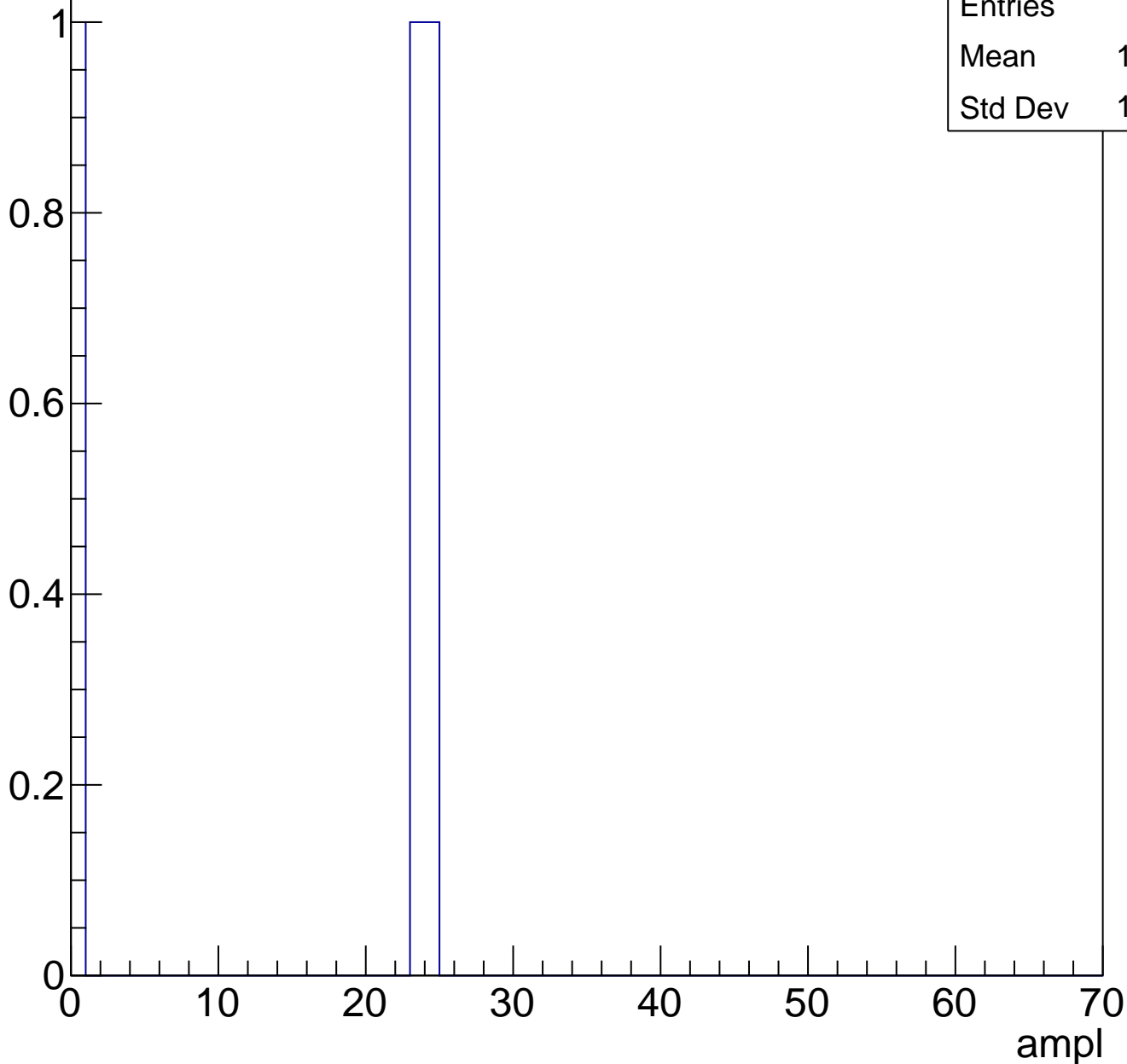
Entry



B1L001S, U20-ch40, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

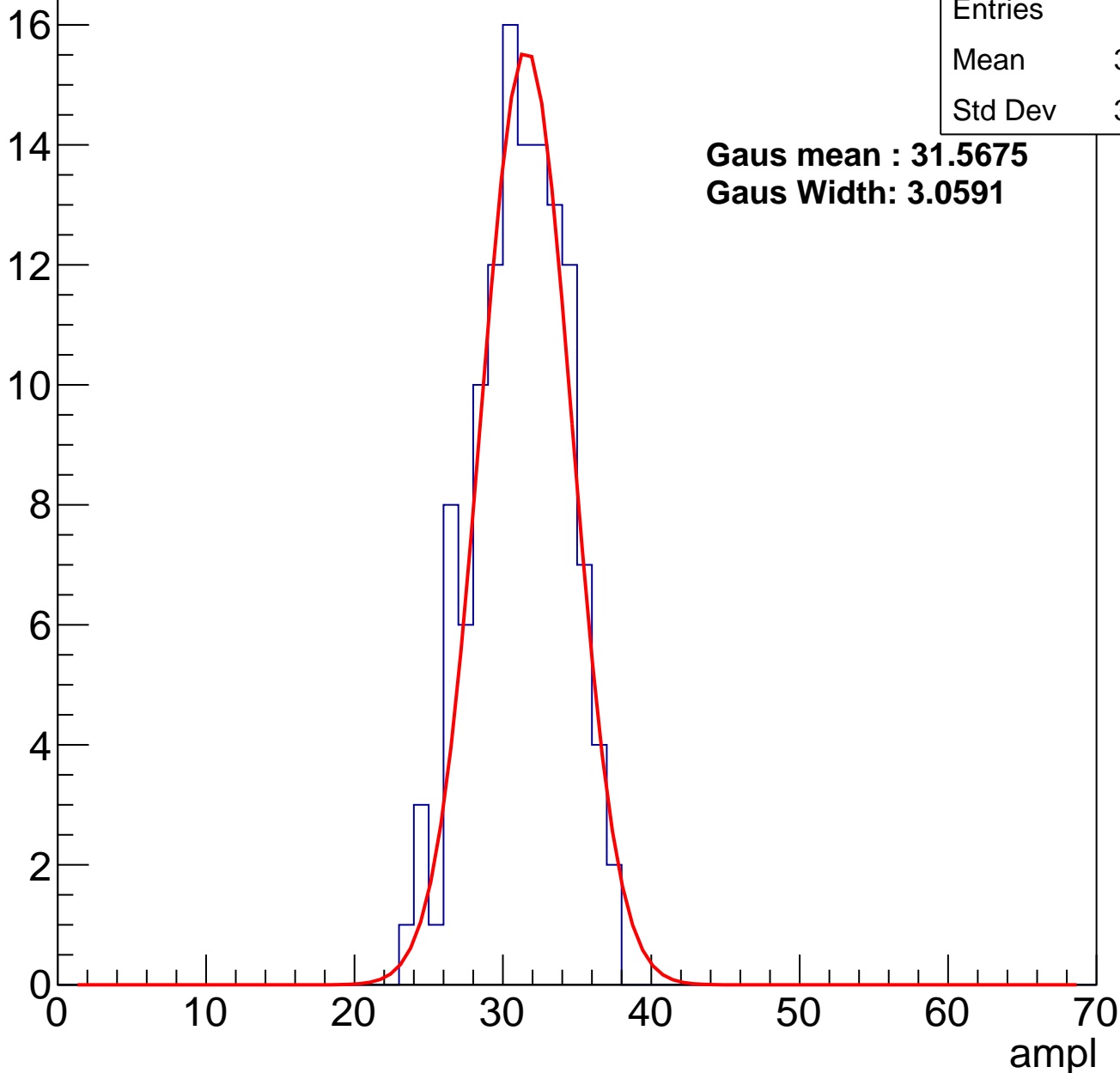


Entries	3
Mean	15.67
Std Dev	11.09

B1L001S, U20-ch41, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch41, adc1

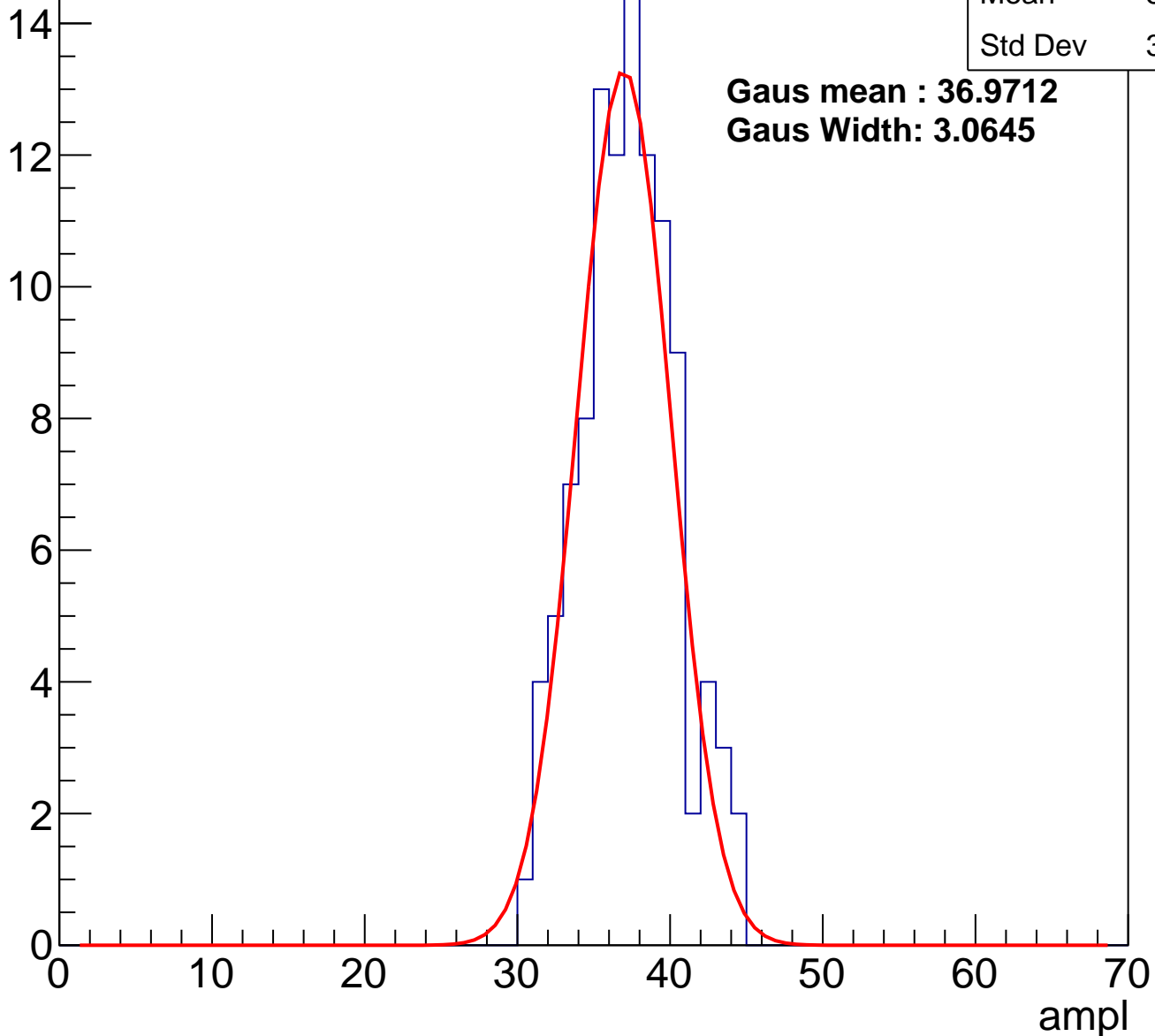
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	36.77
Std Dev	3.096

Gaus mean : 36.9712

Gaus Width: 3.0645

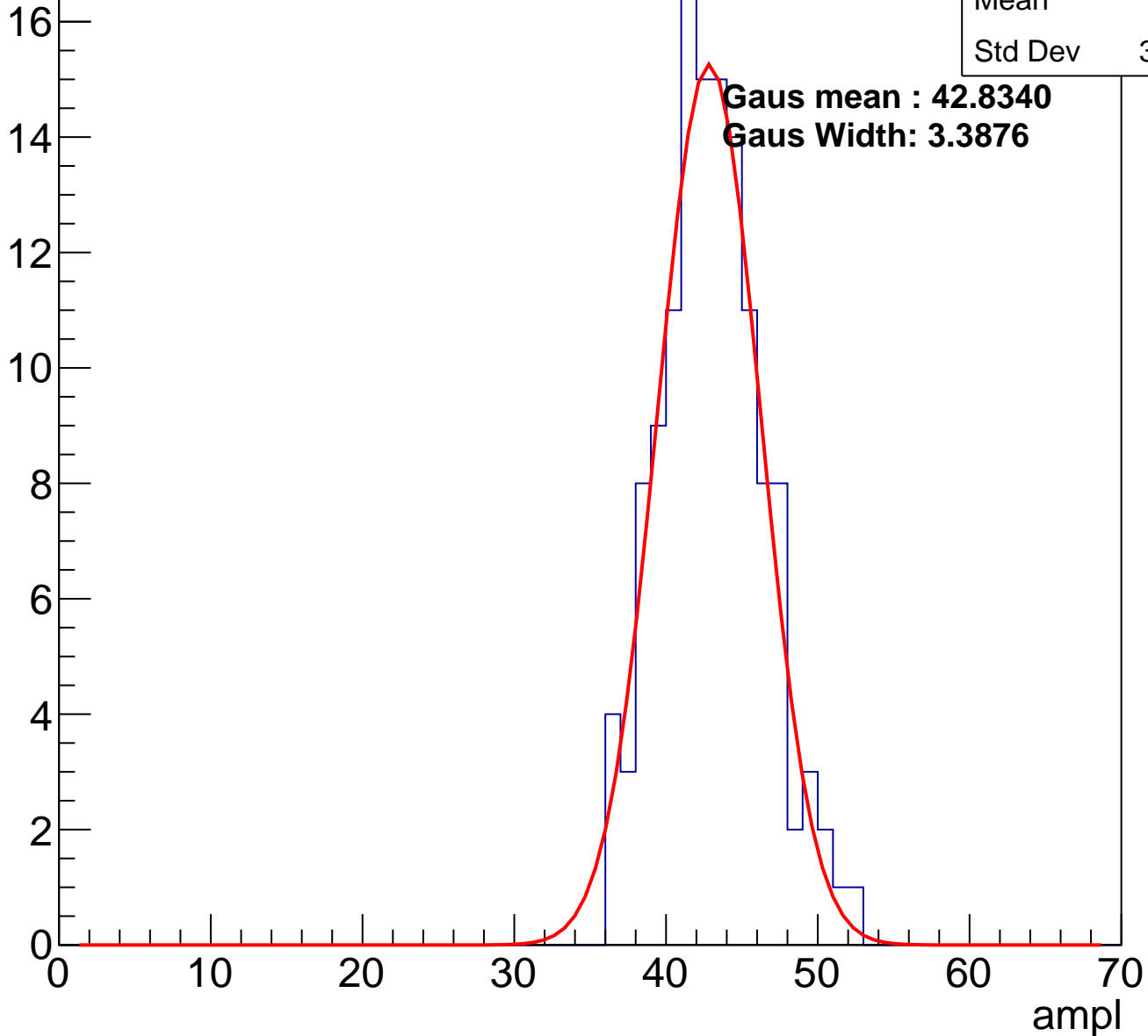
Entry



B1L001S, U20-ch41, adc2

calib_packv5_042523_0143.root, FC#2, port C2

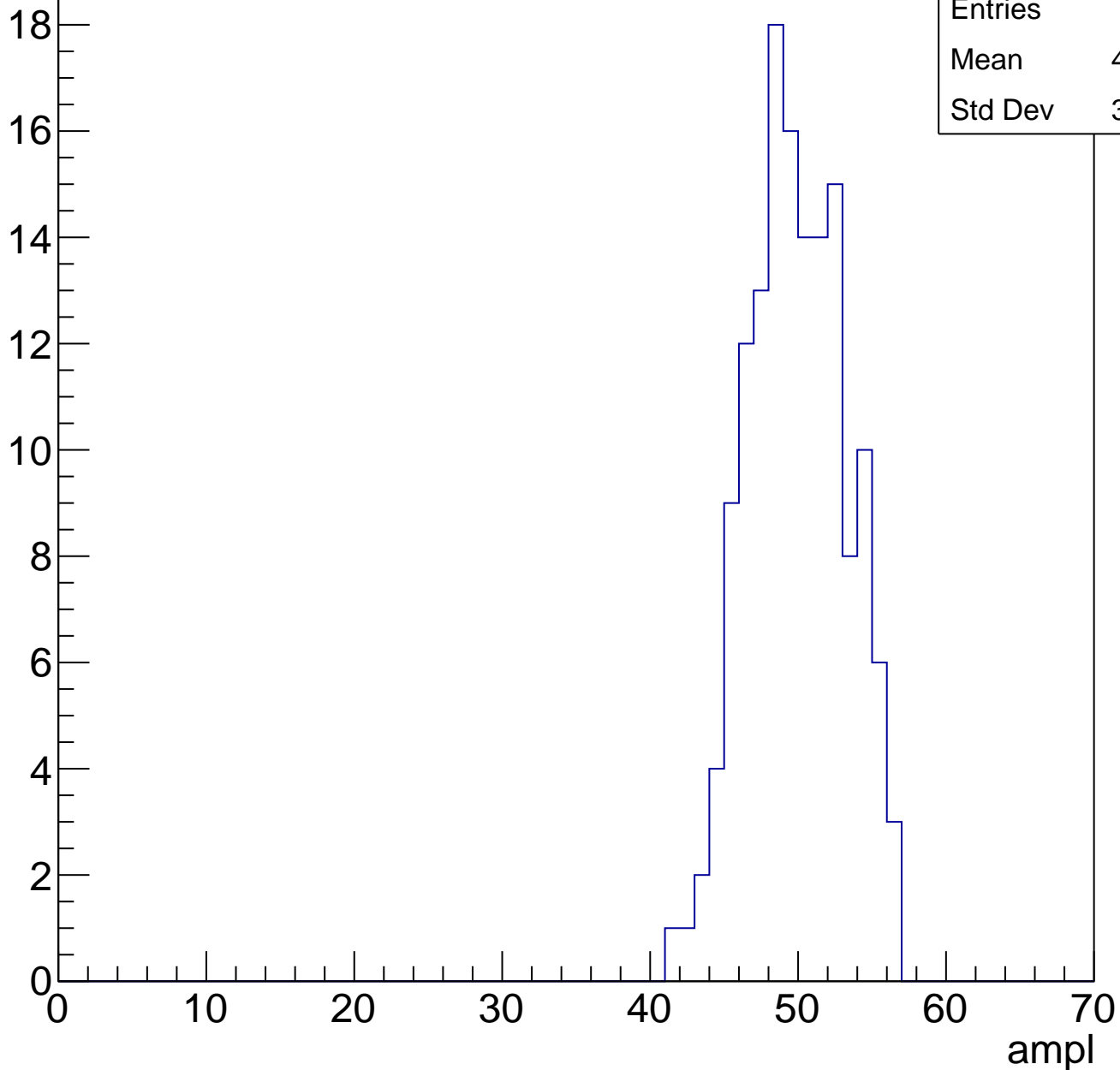
Entry



B1L001S, U20-ch41, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	146
Mean	49.43
Std Dev	3.233

B1L001S, U20-ch41, adc4

calib_packv5_042523_0143.root, FC#2, port C2

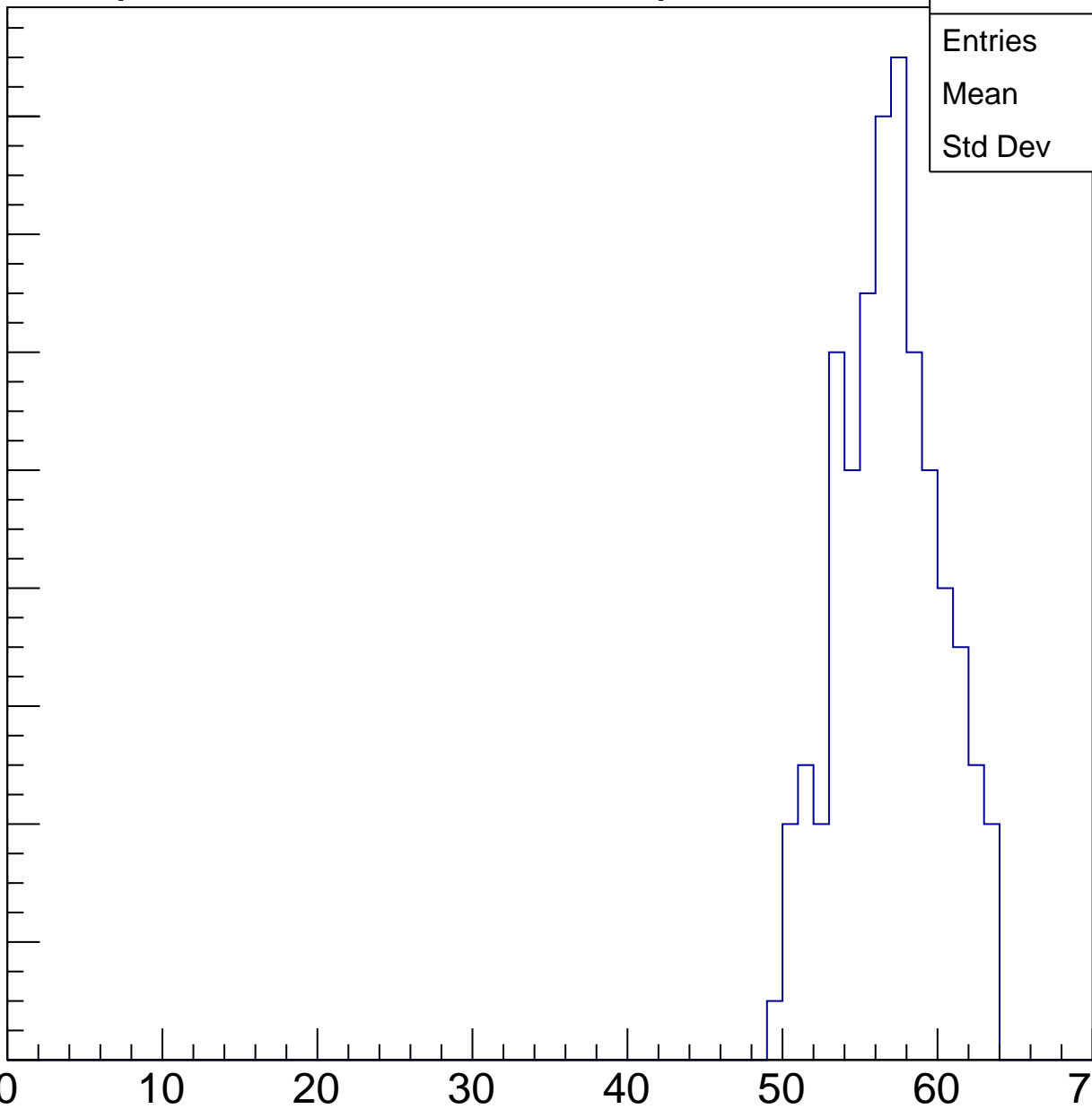
Entries	128
Mean	56.43
Std Dev	3.252

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch41, adc5

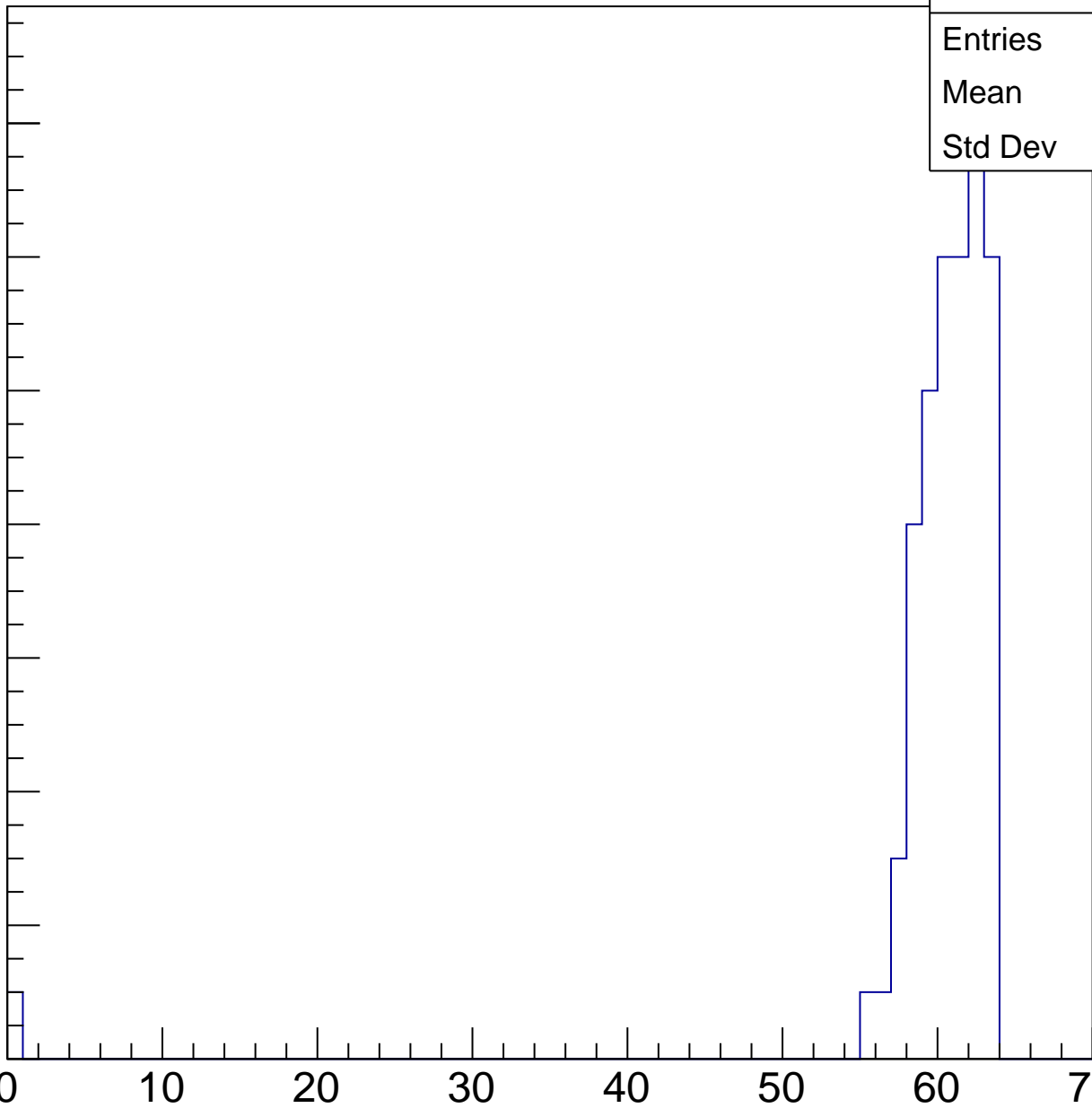
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	75
Mean	59.65
Std Dev	7.194

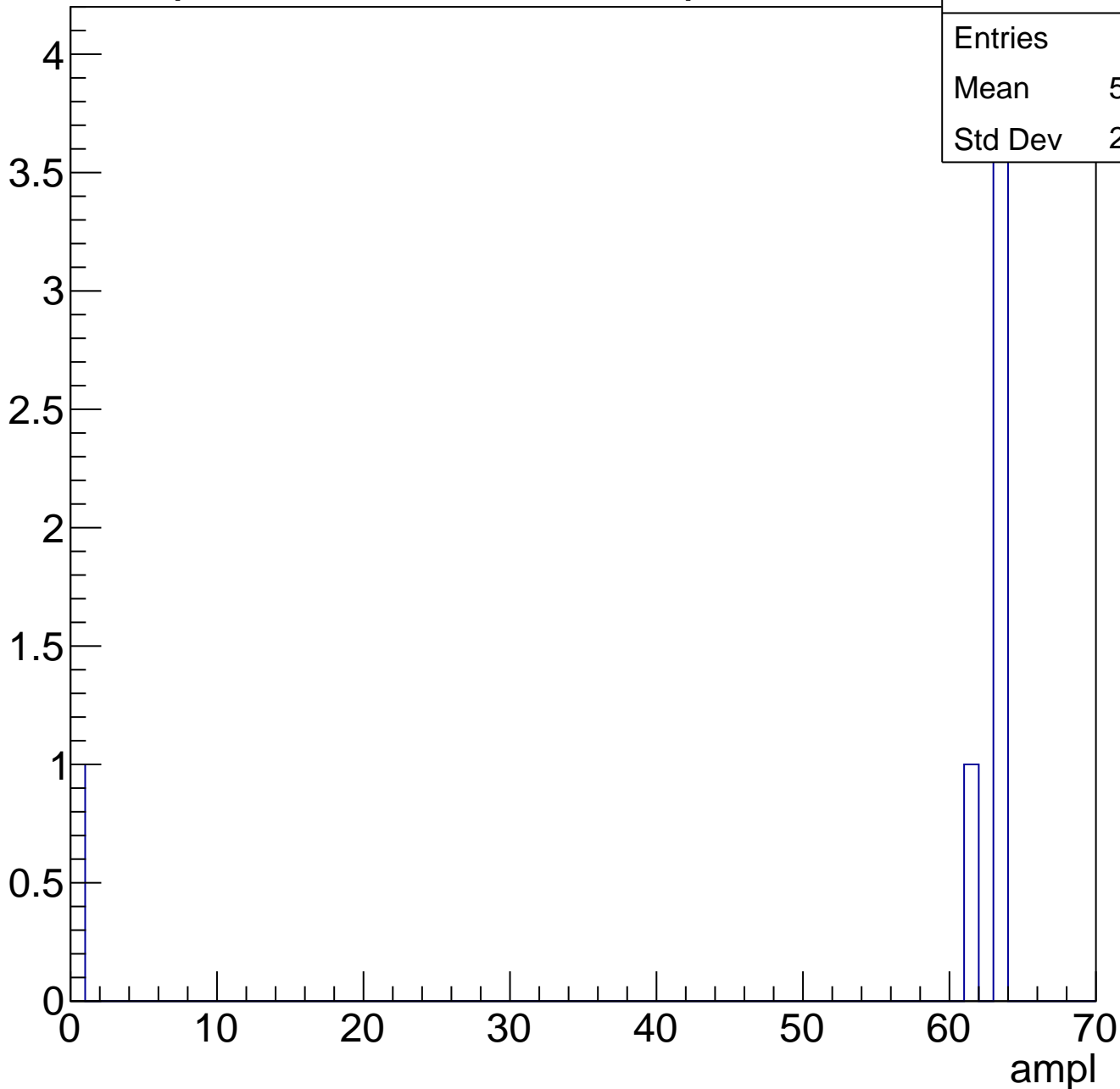
ampl



B1L001S, U20-ch41, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch41, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	10
Std Dev	10

B1L001S, U20-ch42, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

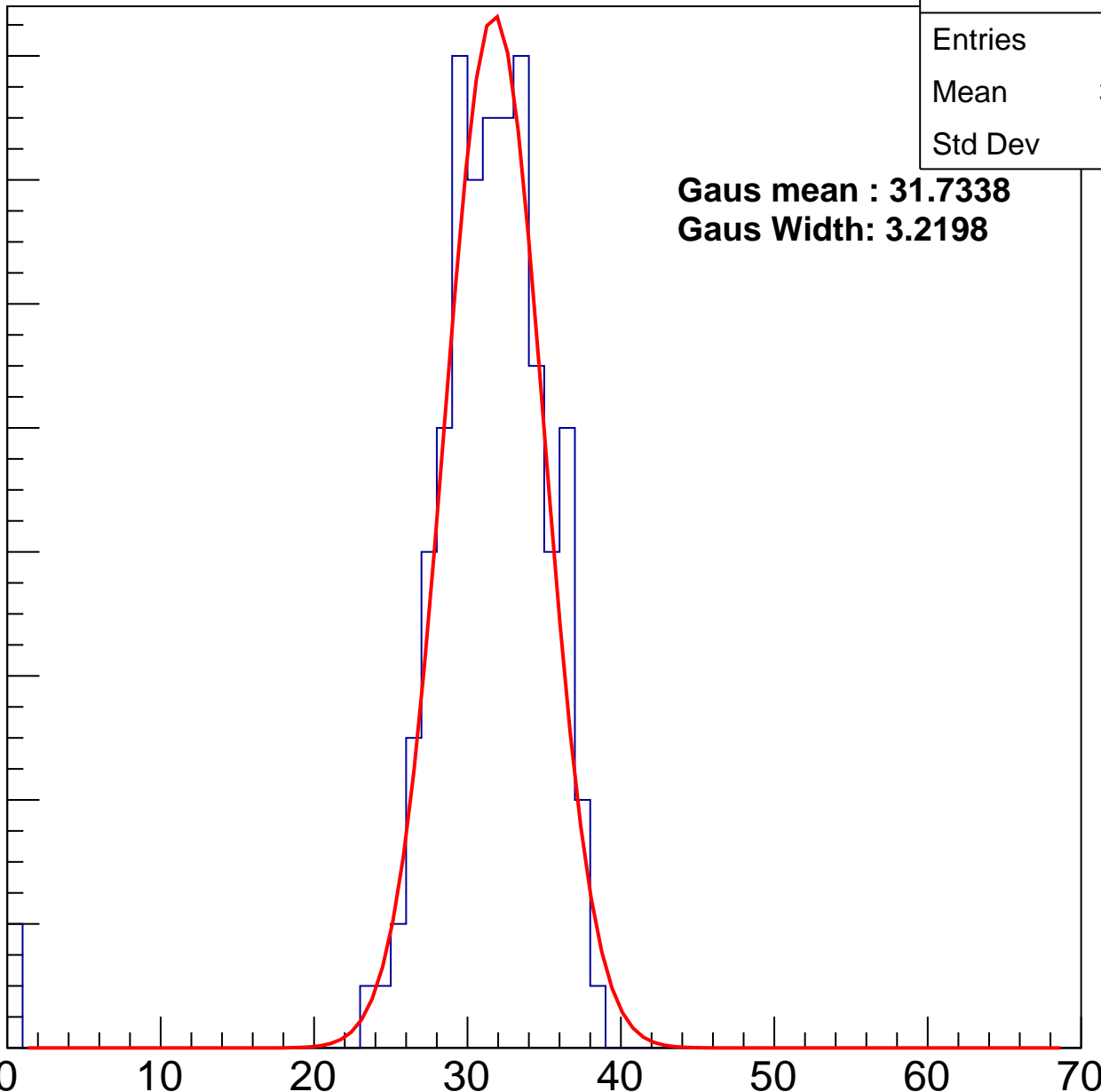
16
14
12
10
8
6
4
2
0

Entries	139
Mean	30.79
Std Dev	4.851

Gaus mean : 31.7338

Gaus Width: 3.2198

ampl



B1L001S, U20-ch42, adc1

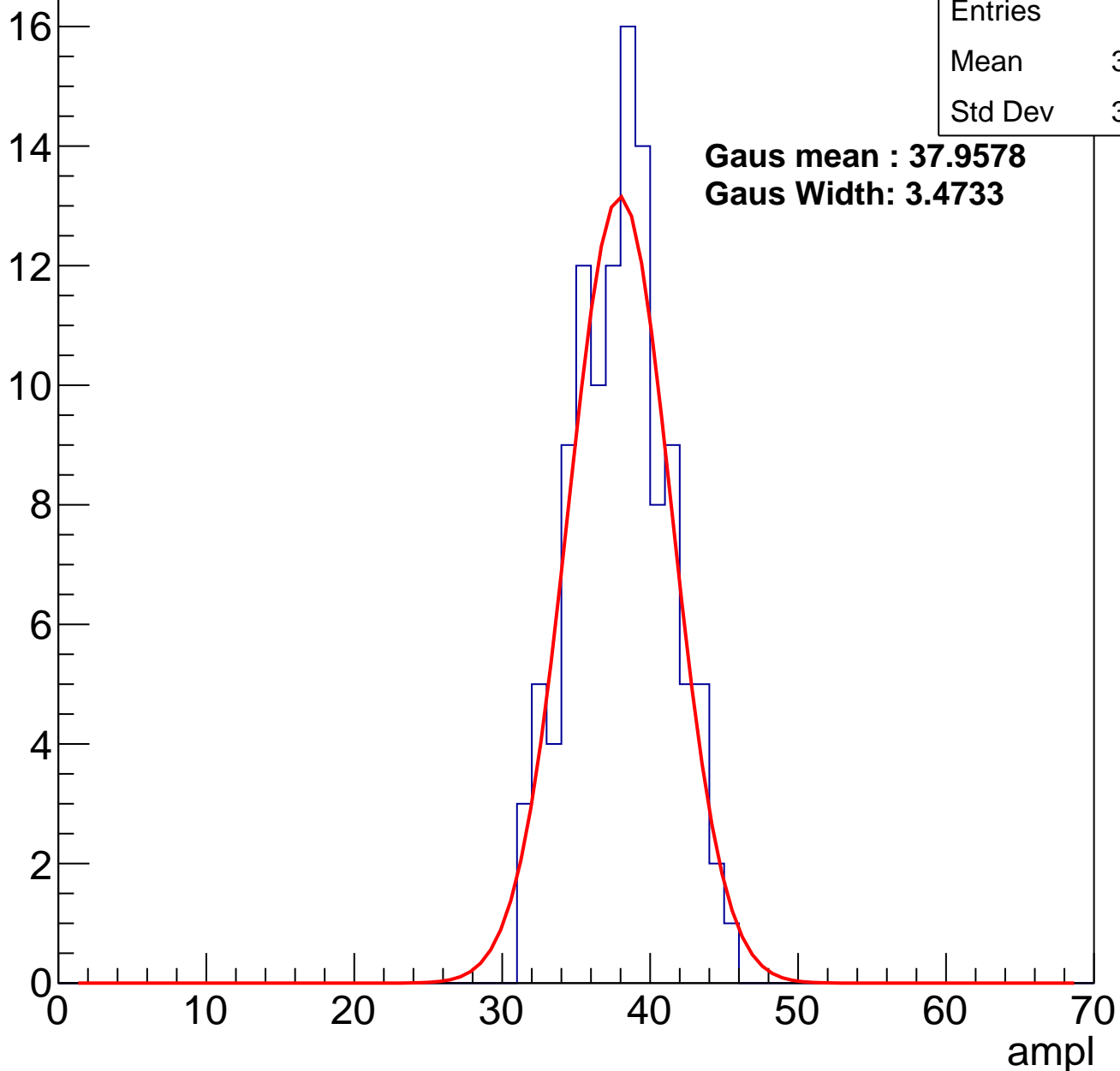
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	37.53
Std Dev	3.174

Gaus mean : 37.9578

Gaus Width: 3.4733

Entry



B1L001S, U20-ch42, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	43.94
Std Dev	3.493

Gaus mean : 44.2569

Gaus Width: 3.6588

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

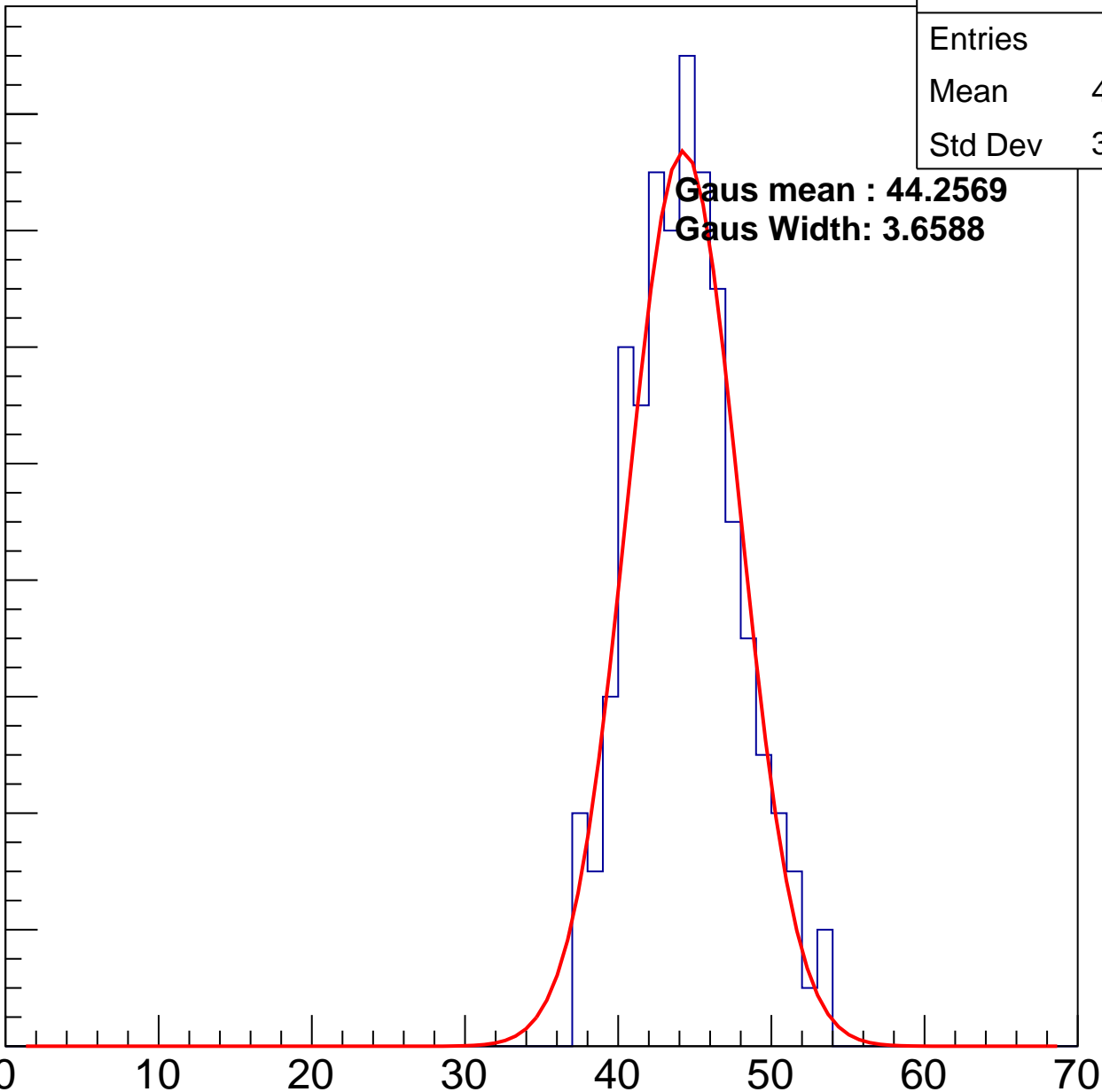
40

50

60

ampl

70



B1L001S, U20-ch42, adc3

calib_packv5_042523_0143.root, FC#2, port C2

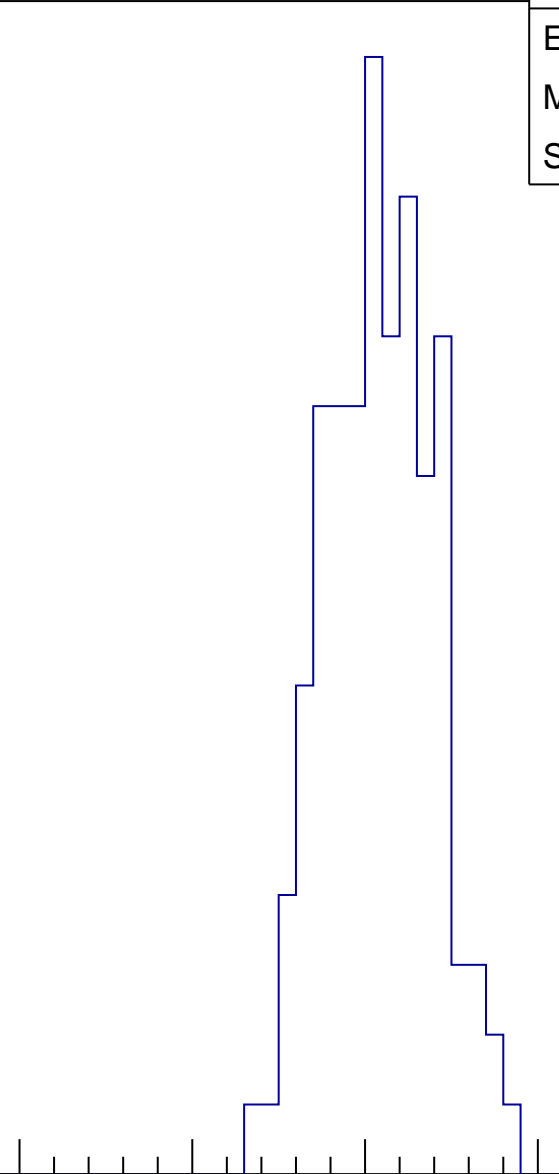
Entries	119
Mean	50.39
Std Dev	3.079

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch42, adc4

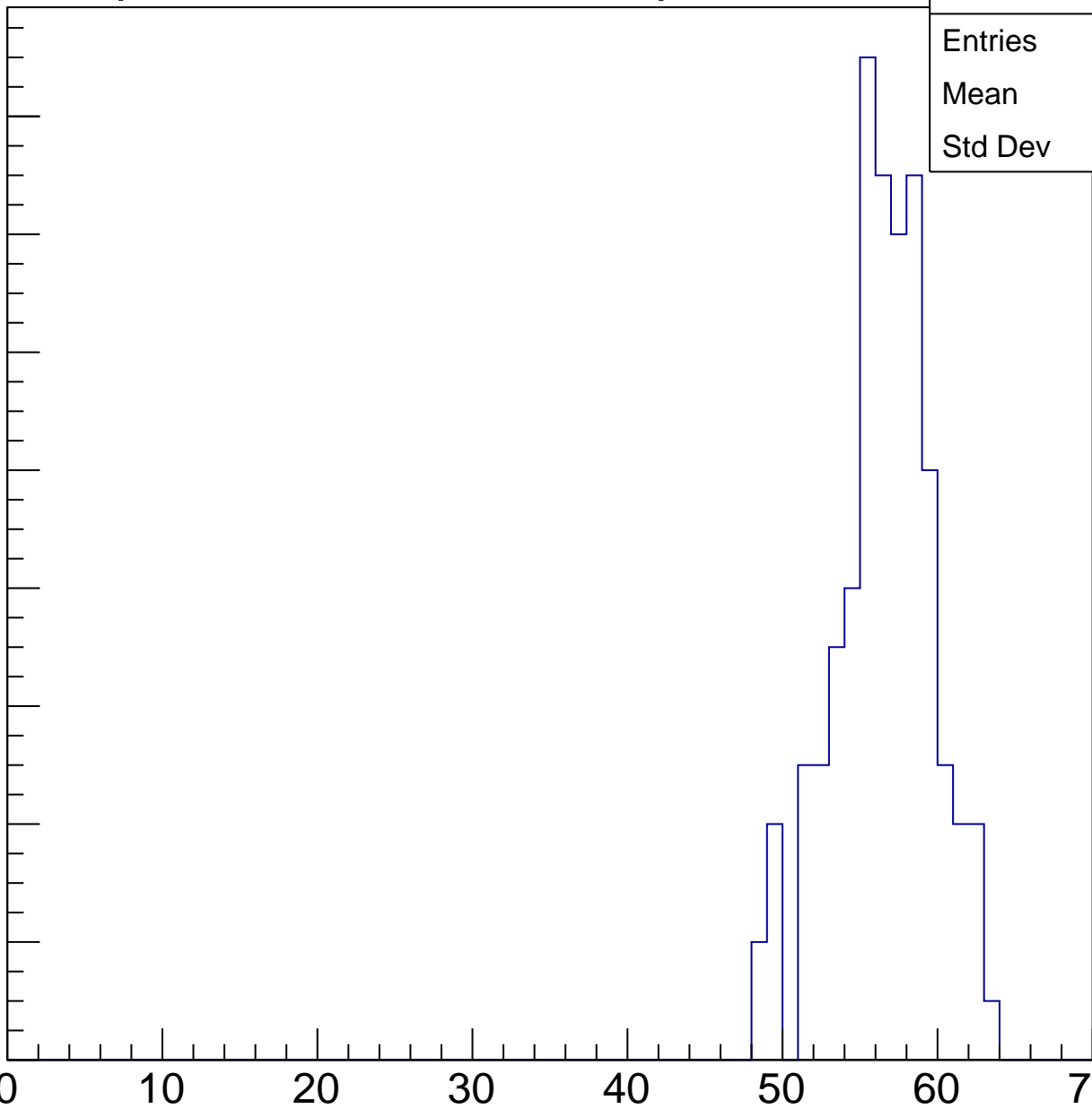
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	56.02
Std Dev	3.187

ampl



B1L001S, U20-ch42, adc5

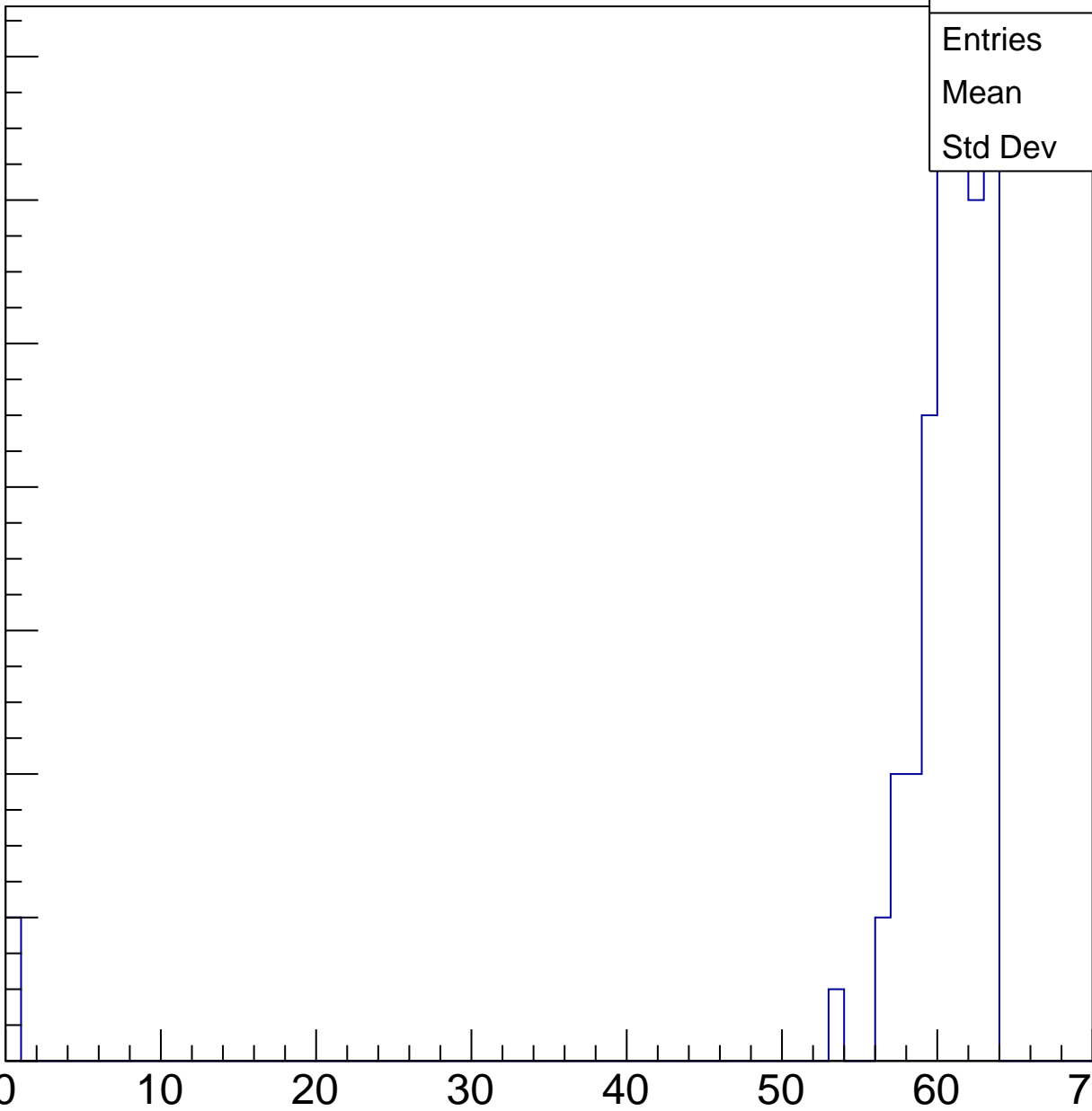
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	58.85
Std Dev	10.02

ampl

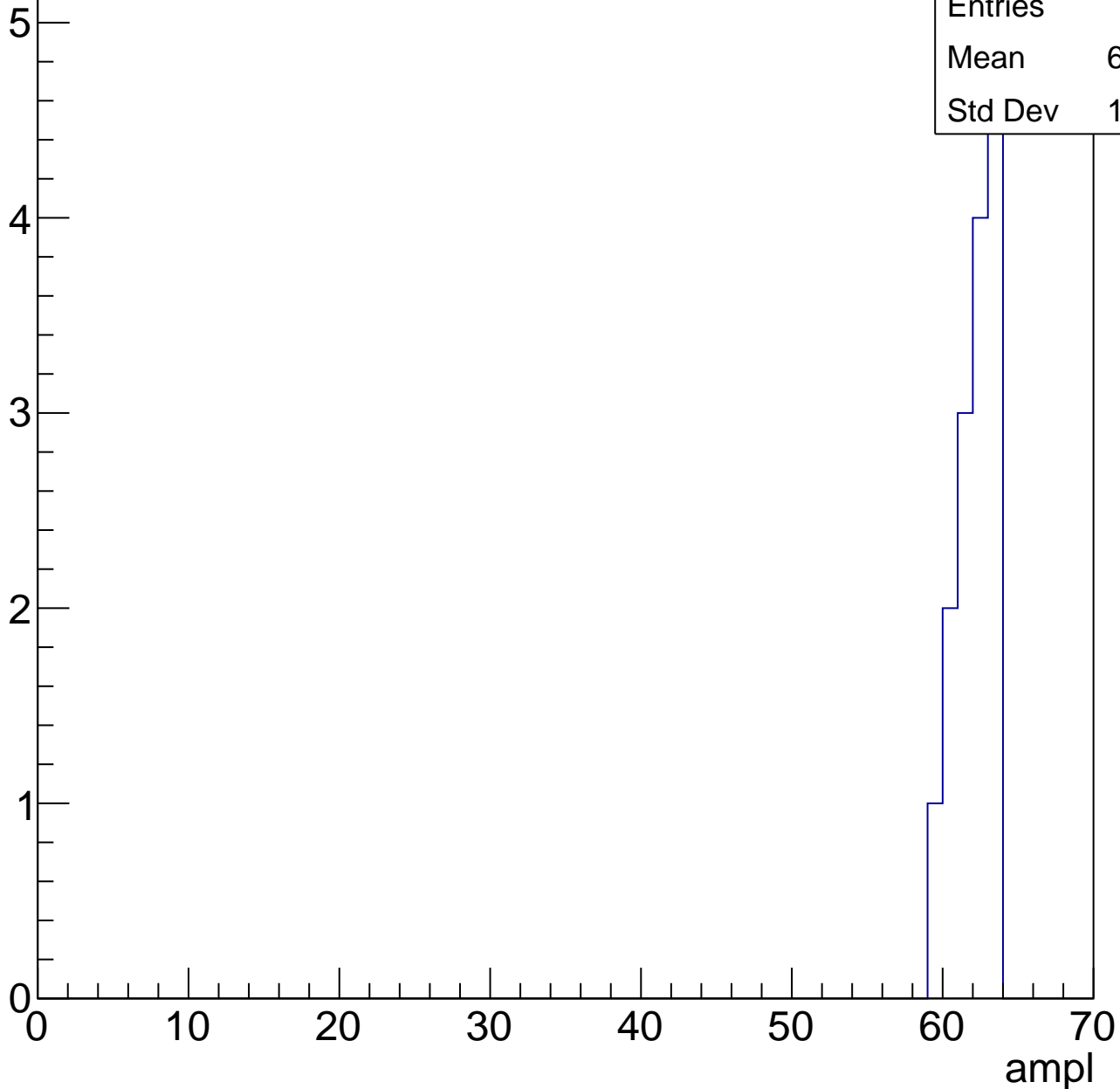


B1L001S, U20-ch42, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	61.67
Std Dev	1.247



B1L001S, U20-ch42, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U20-ch43, adc0

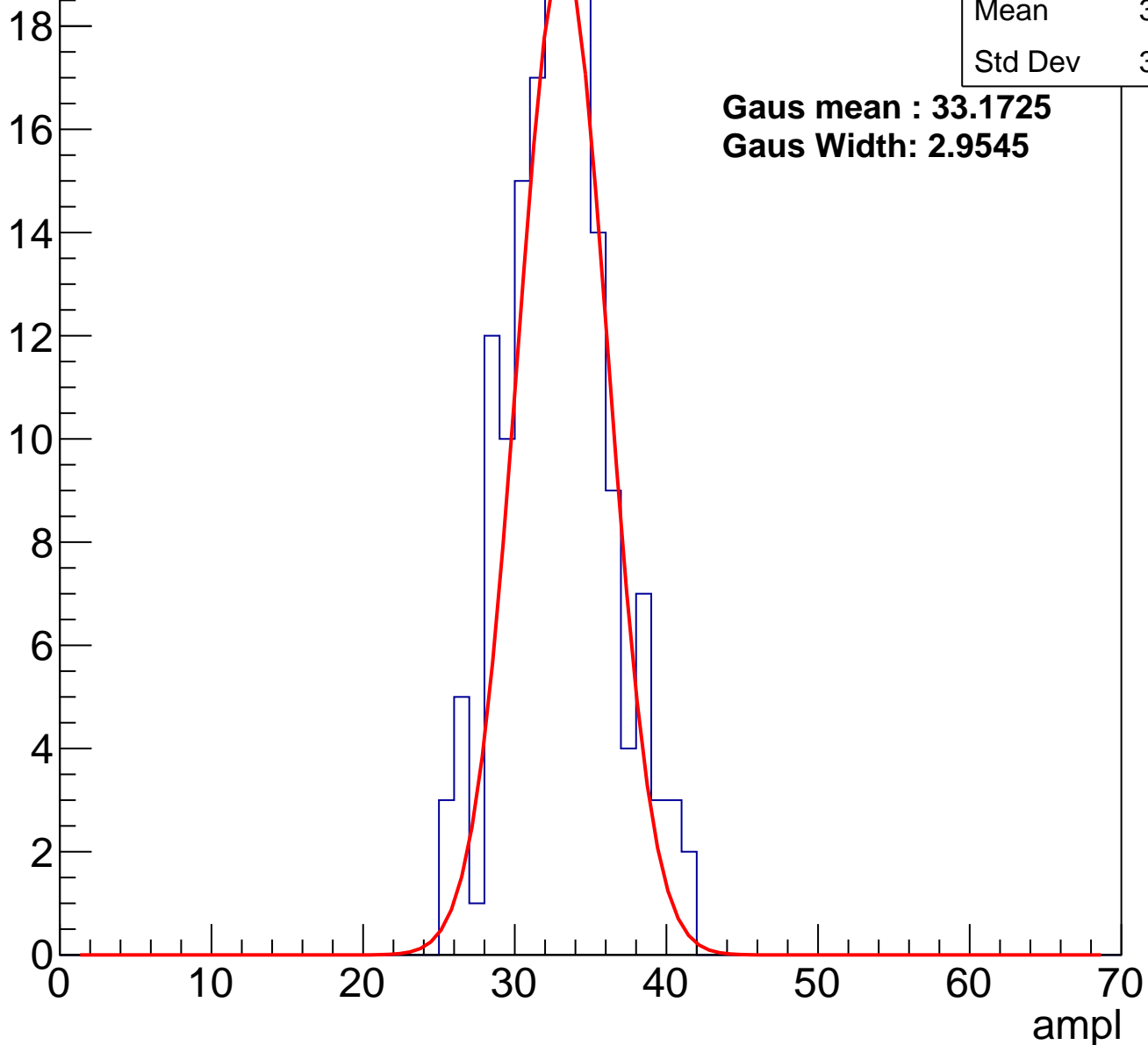
calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	32.49
Std Dev	3.416

Gaus mean : 33.1725

Gaus Width: 2.9545

Entry



B1L001S, U20-ch43, adc1

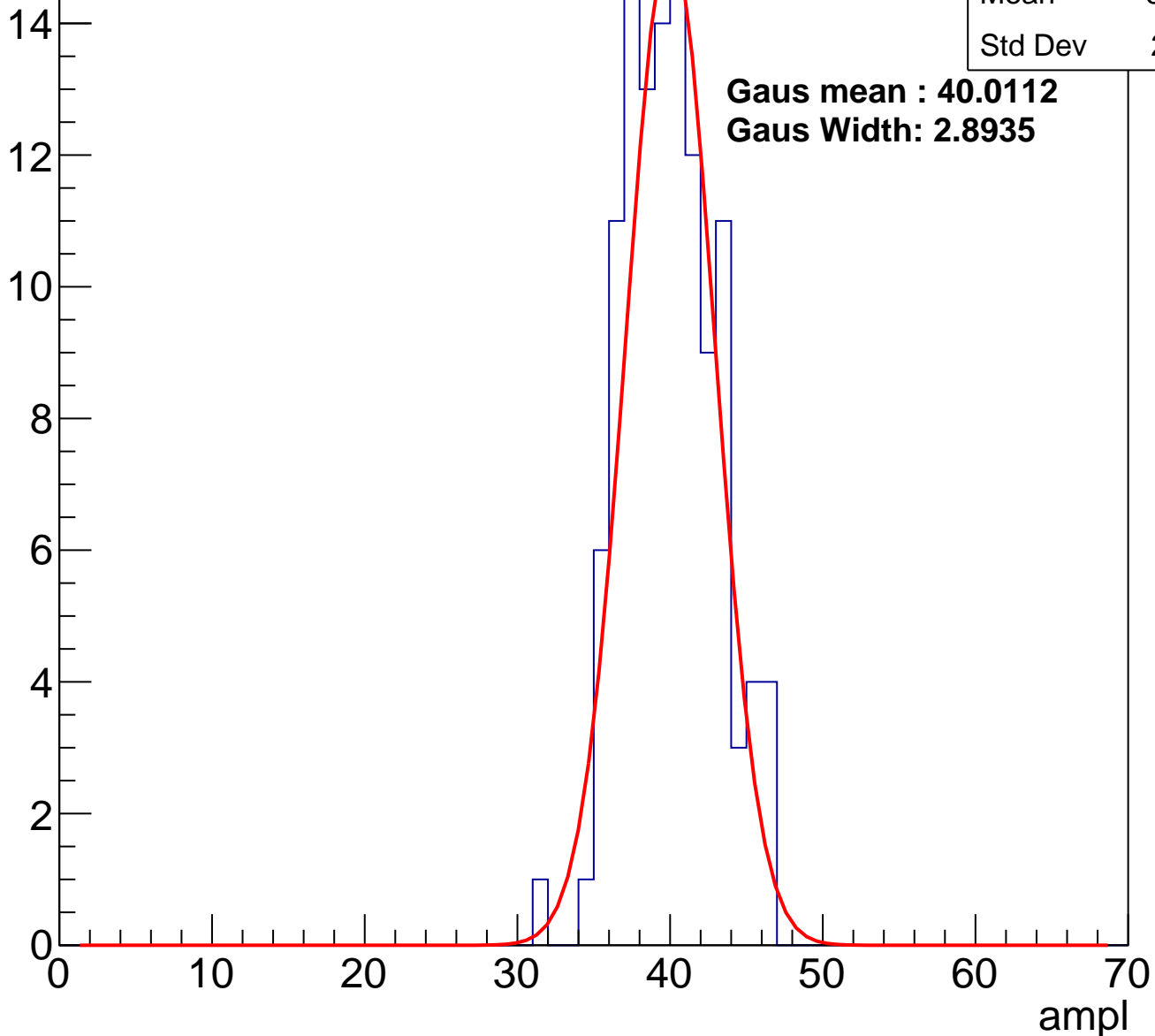
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	39.54
Std Dev	2.981

Gaus mean : 40.0112

Gaus Width: 2.8935

Entry



B1L001S, U20-ch43, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

107

Mean

45.48

Std Dev

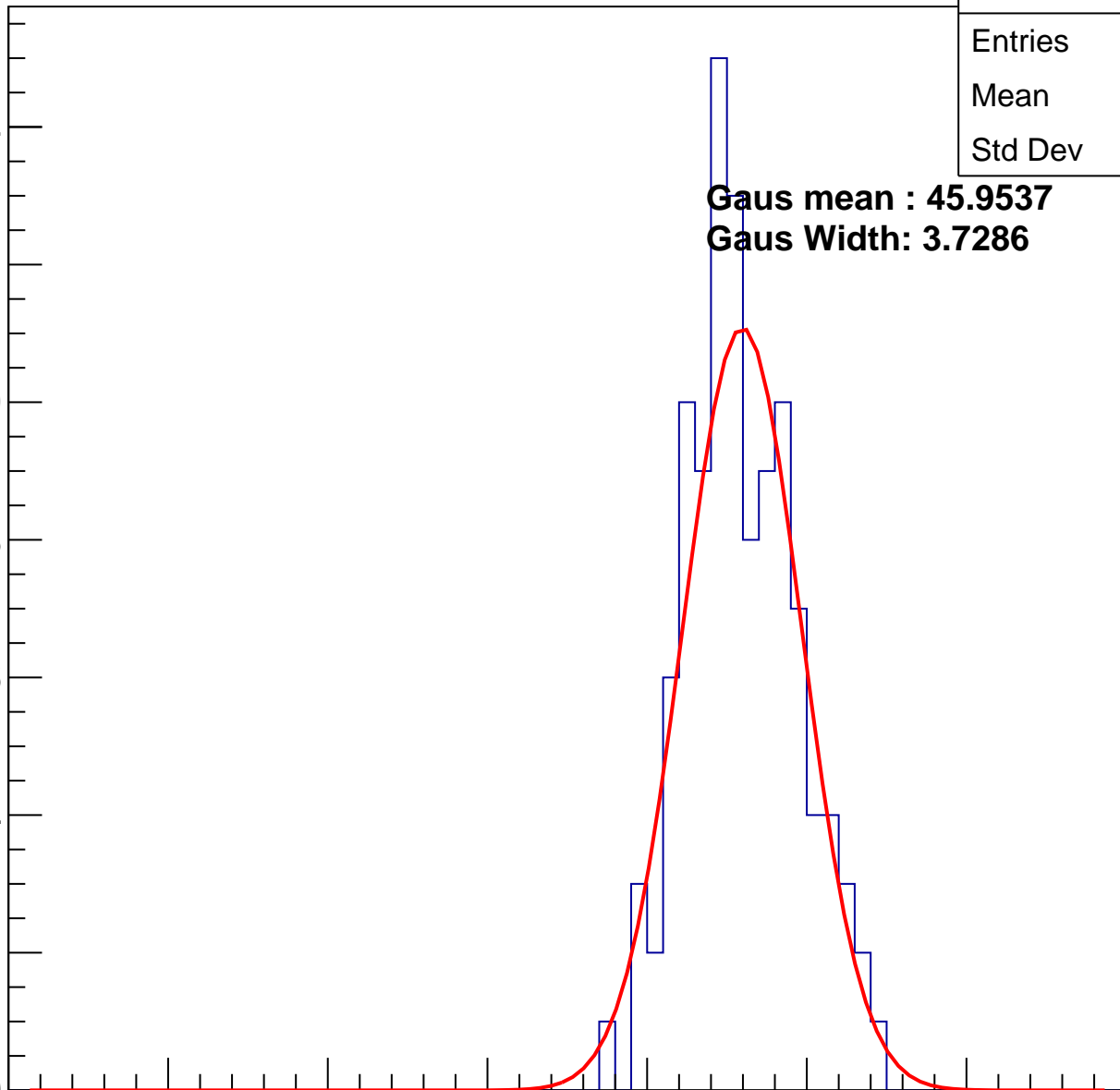
3.46

Gaus mean : 45.9537

Gaus Width: 3.7286

0 10 20 30 40 50 60 70

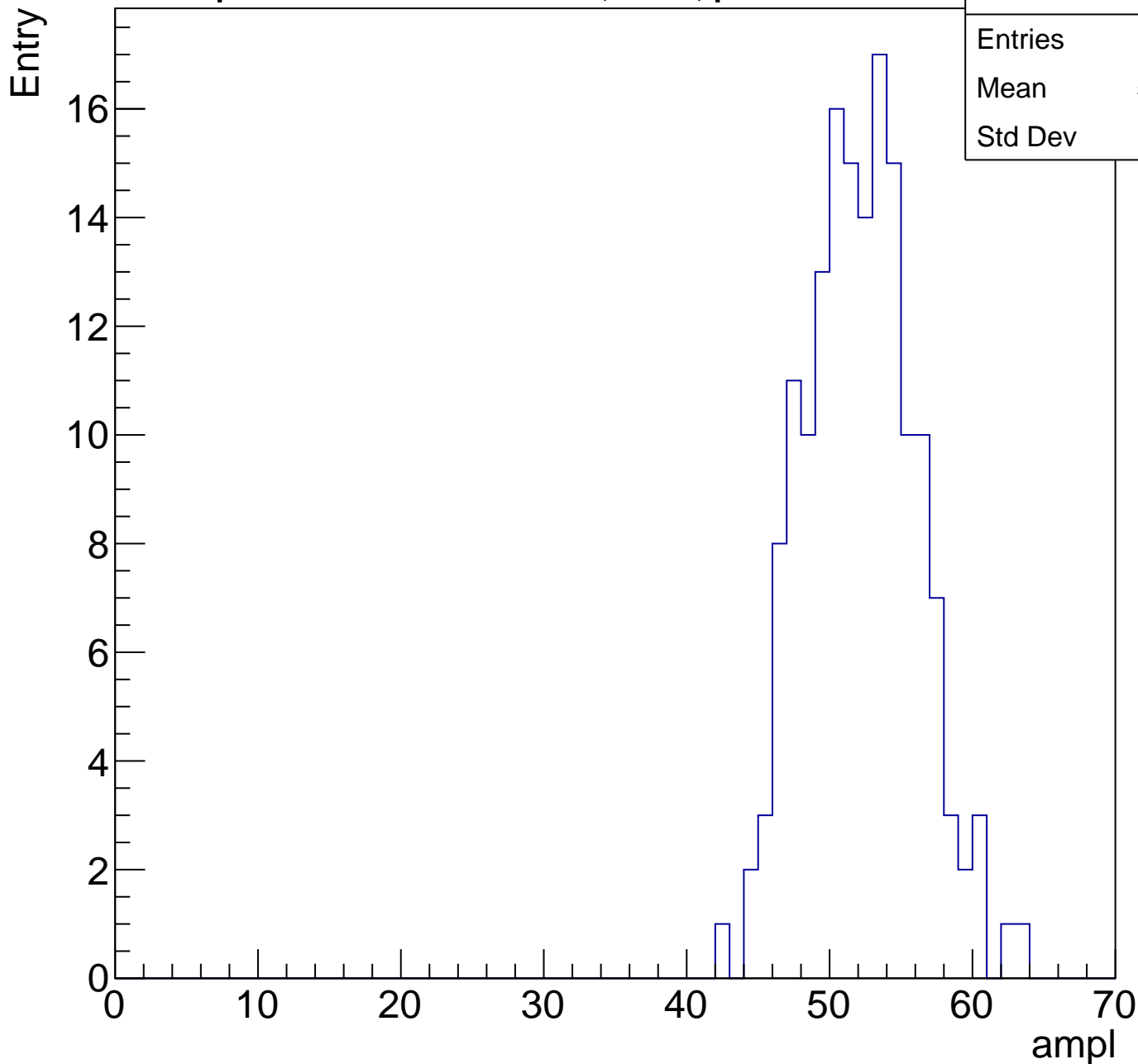
ampl



B1L001S, U20-ch43, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	51.71
Std Dev	3.85



B1L001S, U20-ch43, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

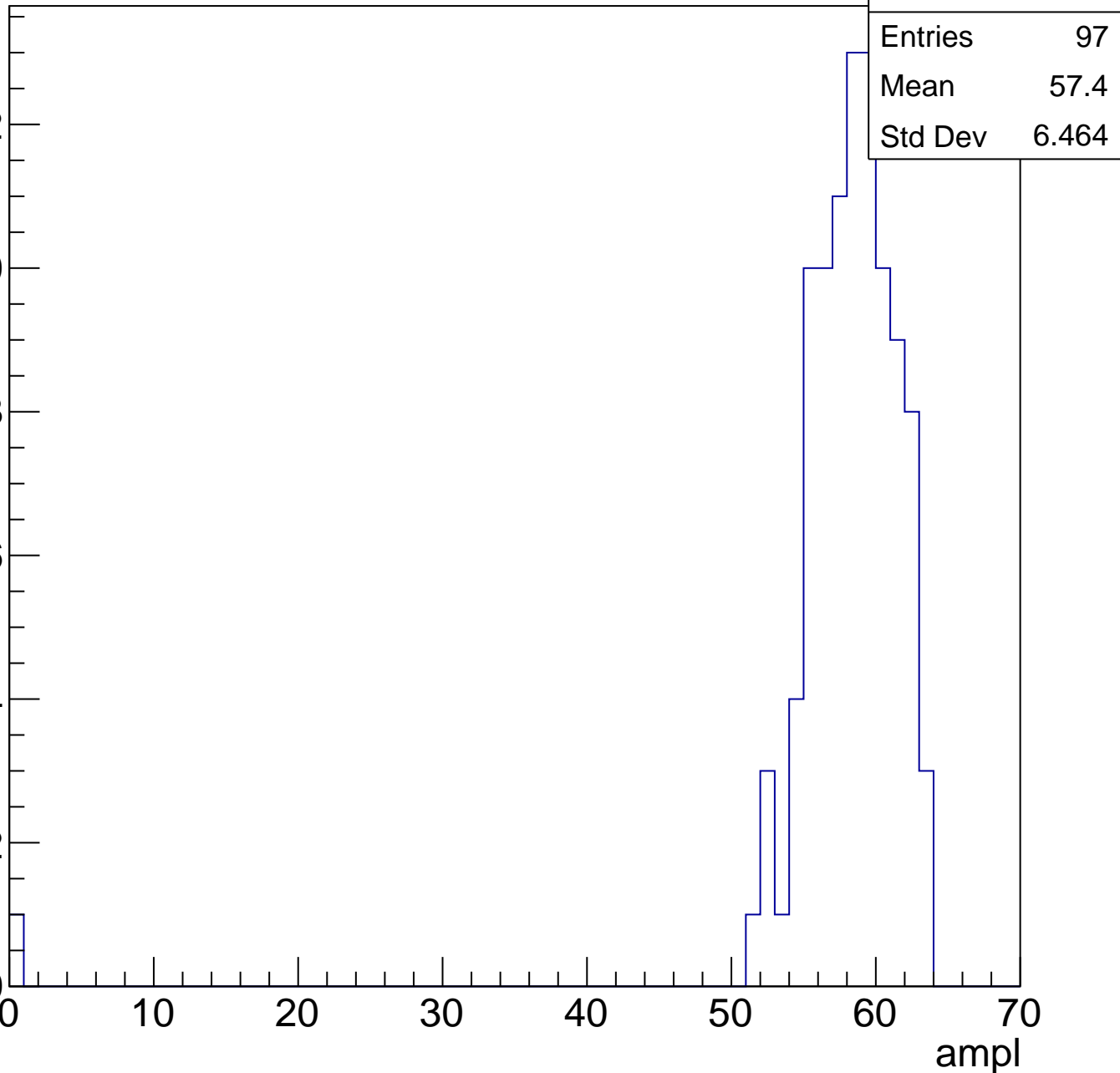
50

60

70

ampl

Entries	97
Mean	57.4
Std Dev	6.464



B1L001S, U20-ch43, adc5

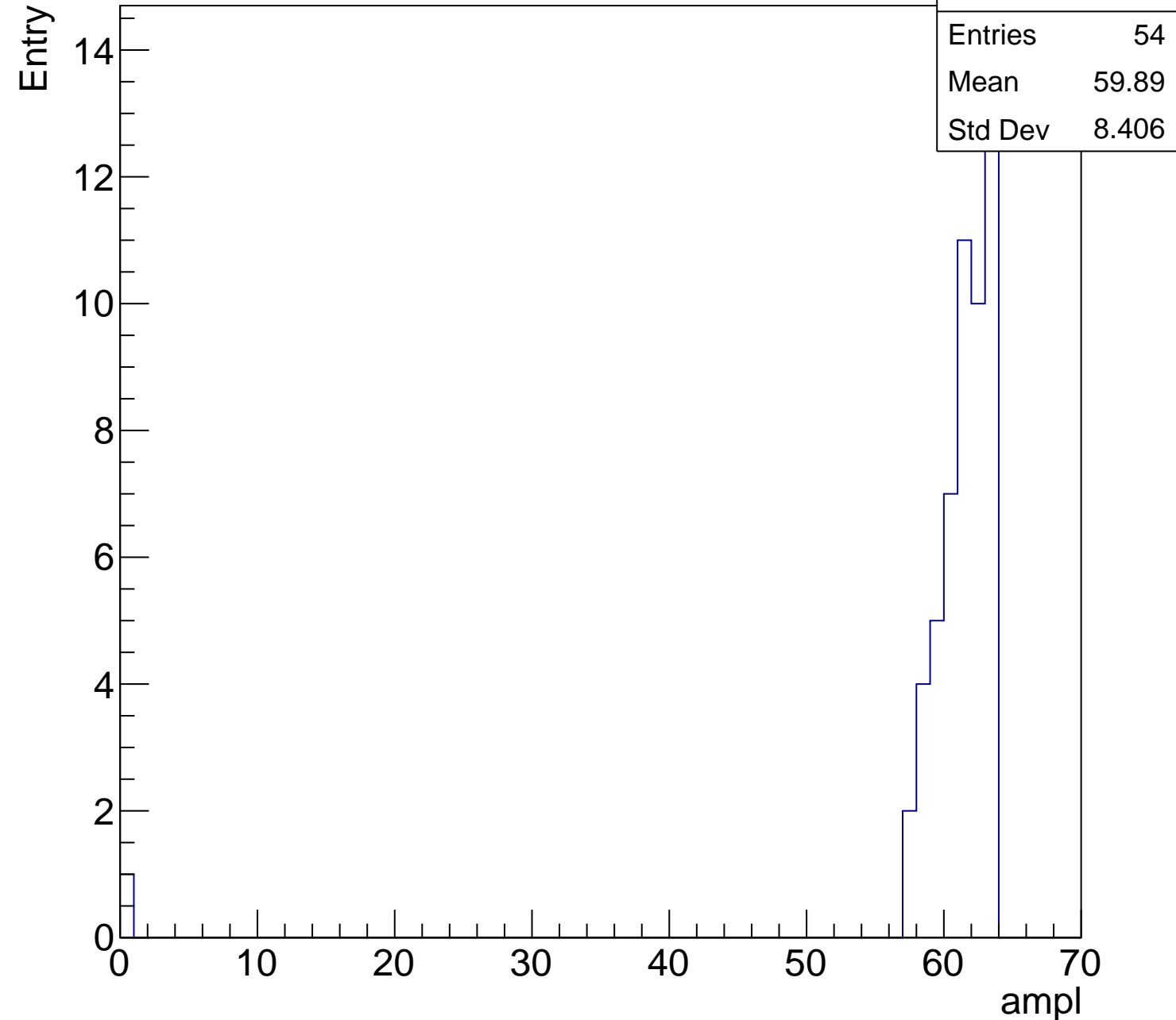
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	54
Mean	59.89
Std Dev	8.406

ampl



B1L001S, U20-ch43, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch43, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	12
Std Dev	12

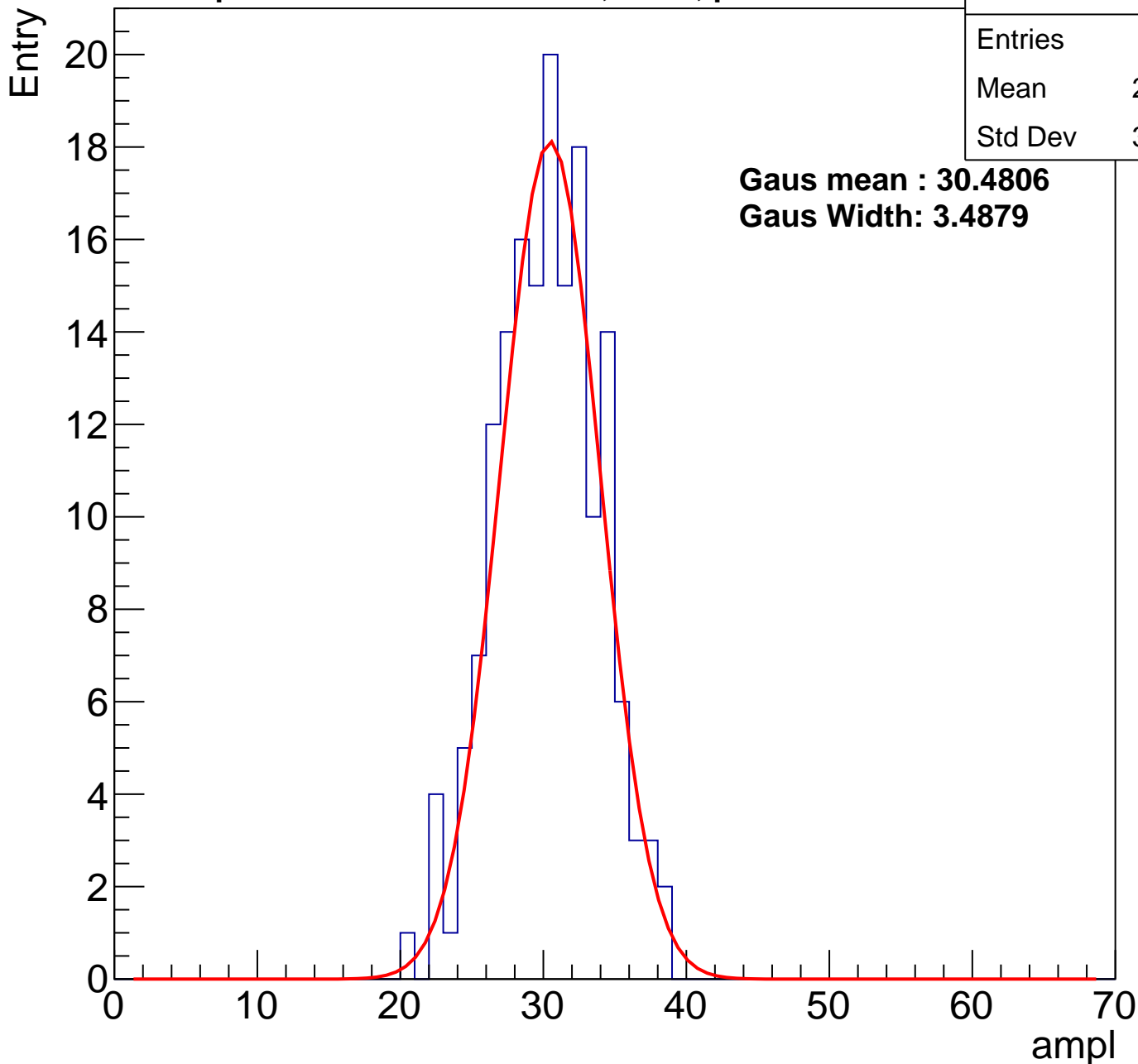
B1L001S, U20-ch44, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	166
Mean	29.83
Std Dev	3.545

Gaus mean : 30.4806

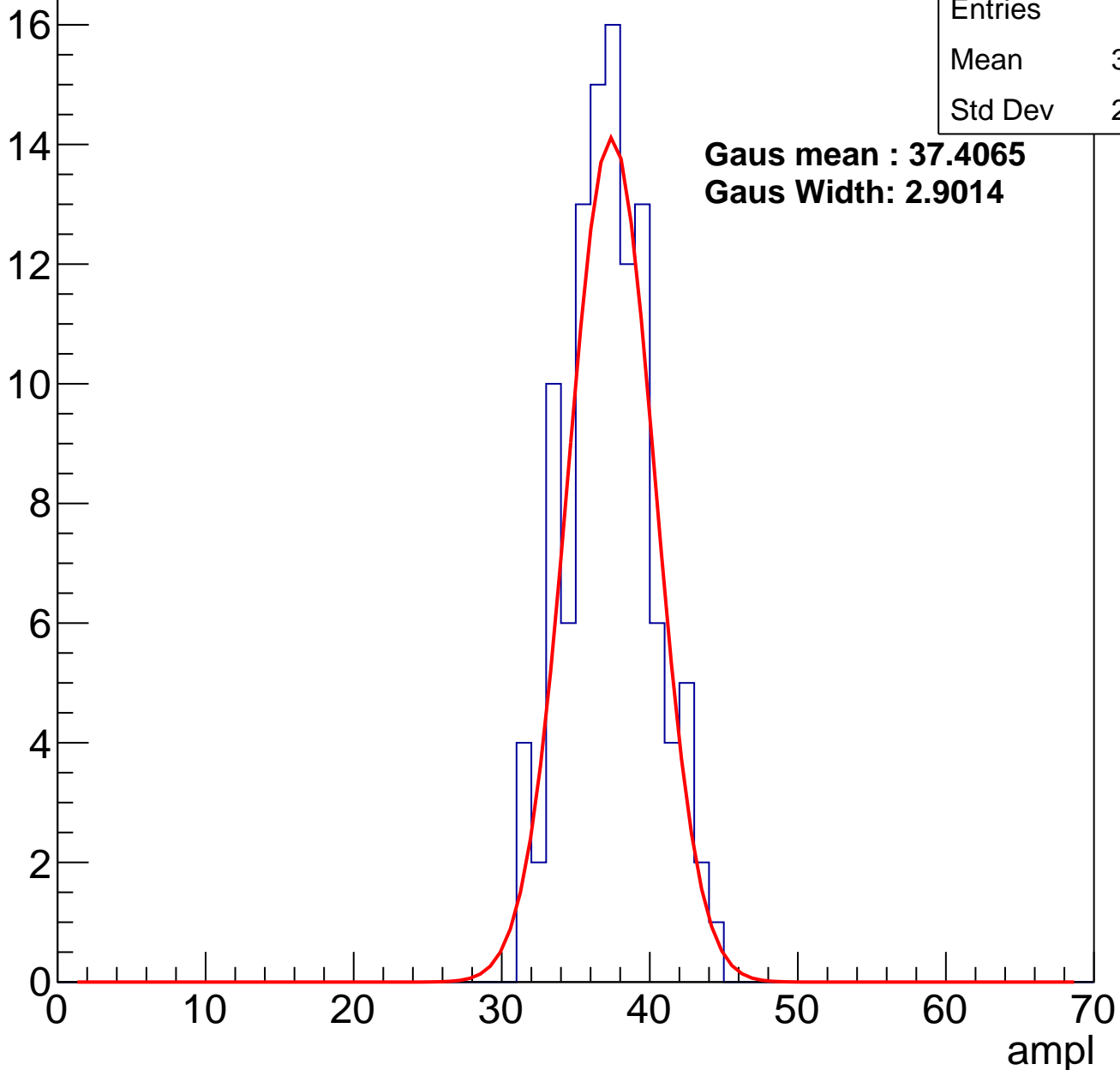
Gaus Width: 3.4879



B1L001S, U20-ch44, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch44, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

133

Mean

43.17

Std Dev

3.326

Gaus mean : 43.5362

Gaus Width: 3.1035

0

10

20

30

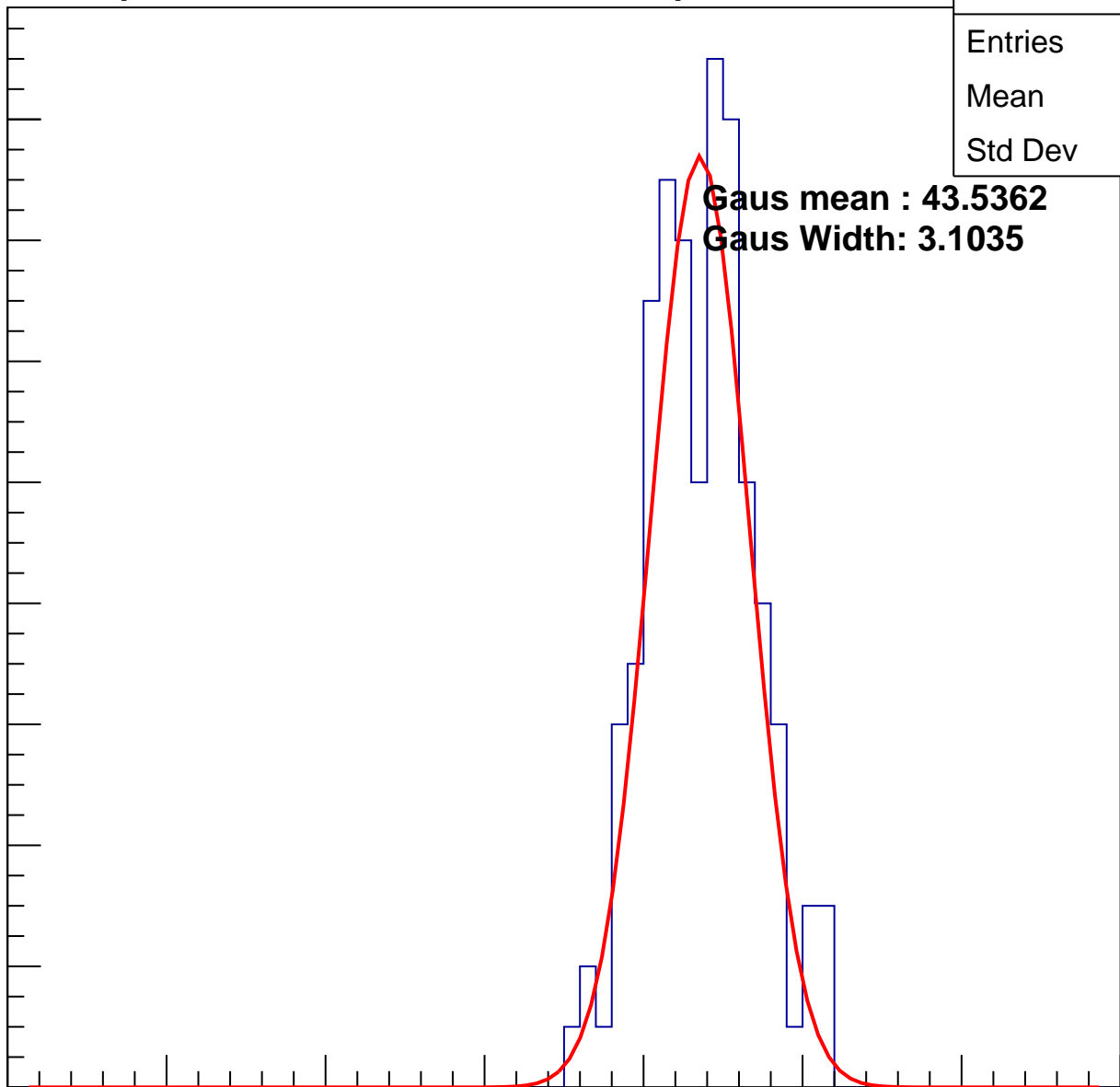
40

50

60

70

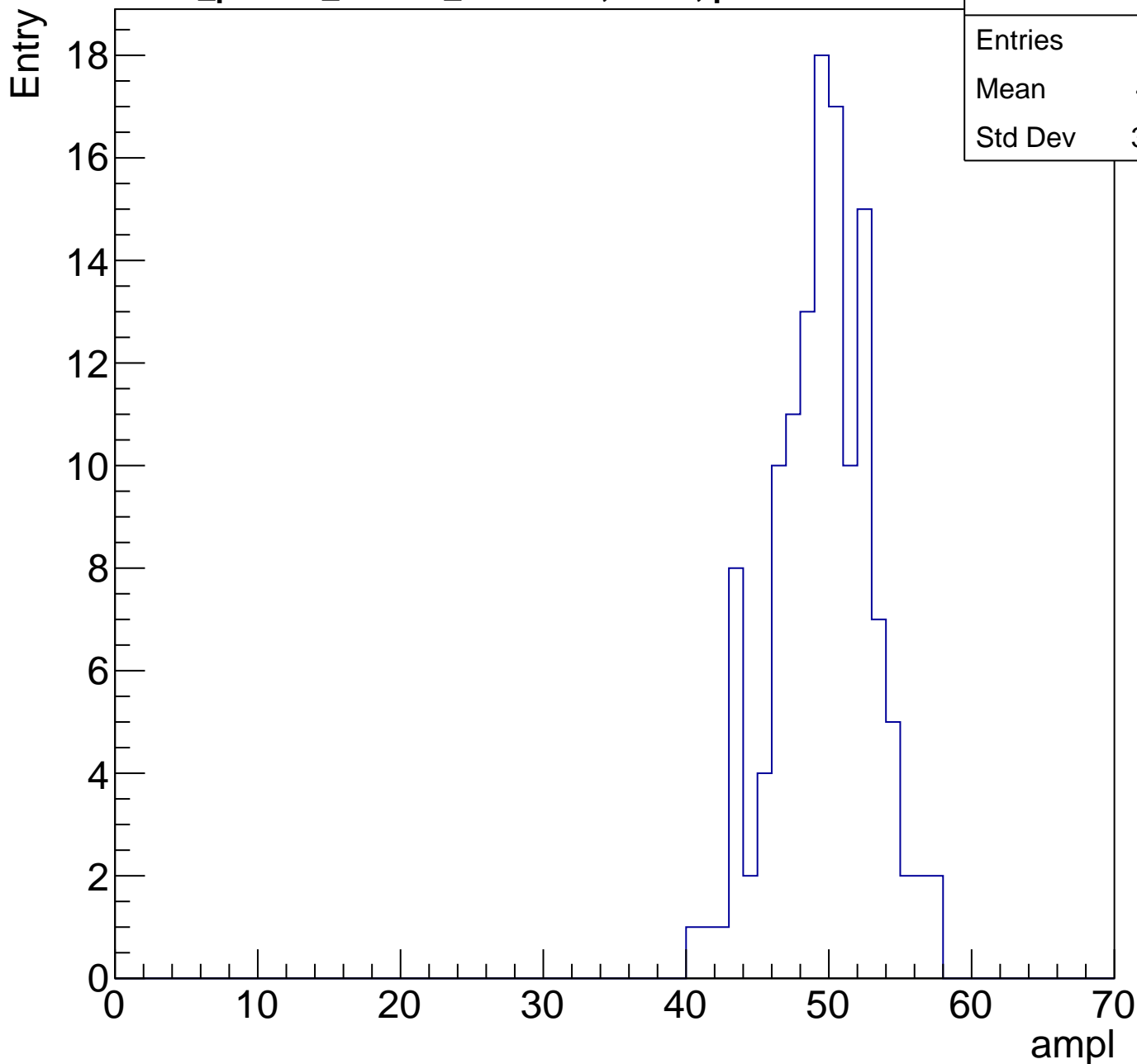
ampl



B1L001S, U20-ch44, adc3

calib_packv5_042523_0143.root, FC#2, port C2

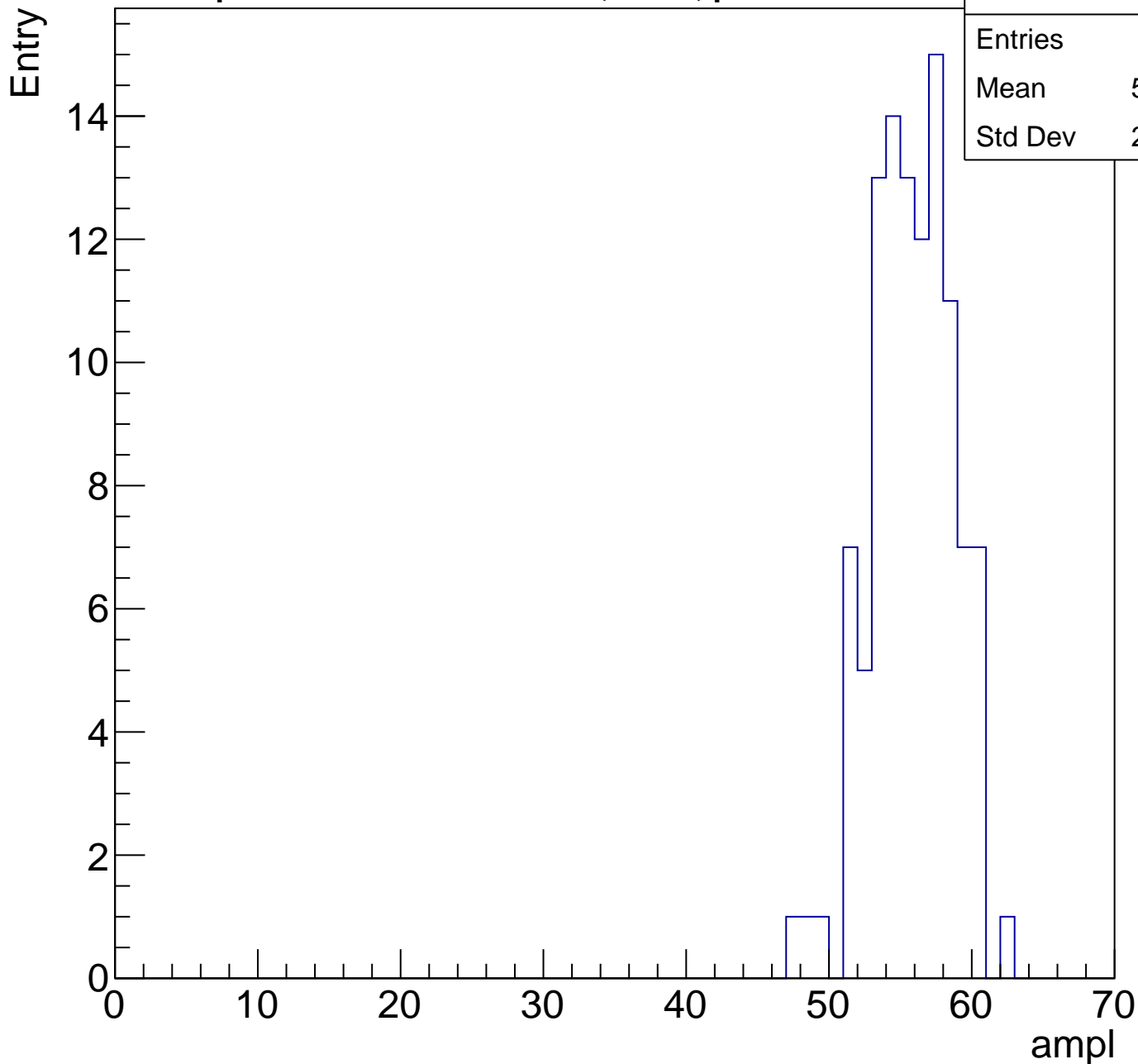
Entries	129
Mean	49.11
Std Dev	3.374



B1L001S, U20-ch44, adc4

calib_packv5_042523_0143.root, FC#2, port C2

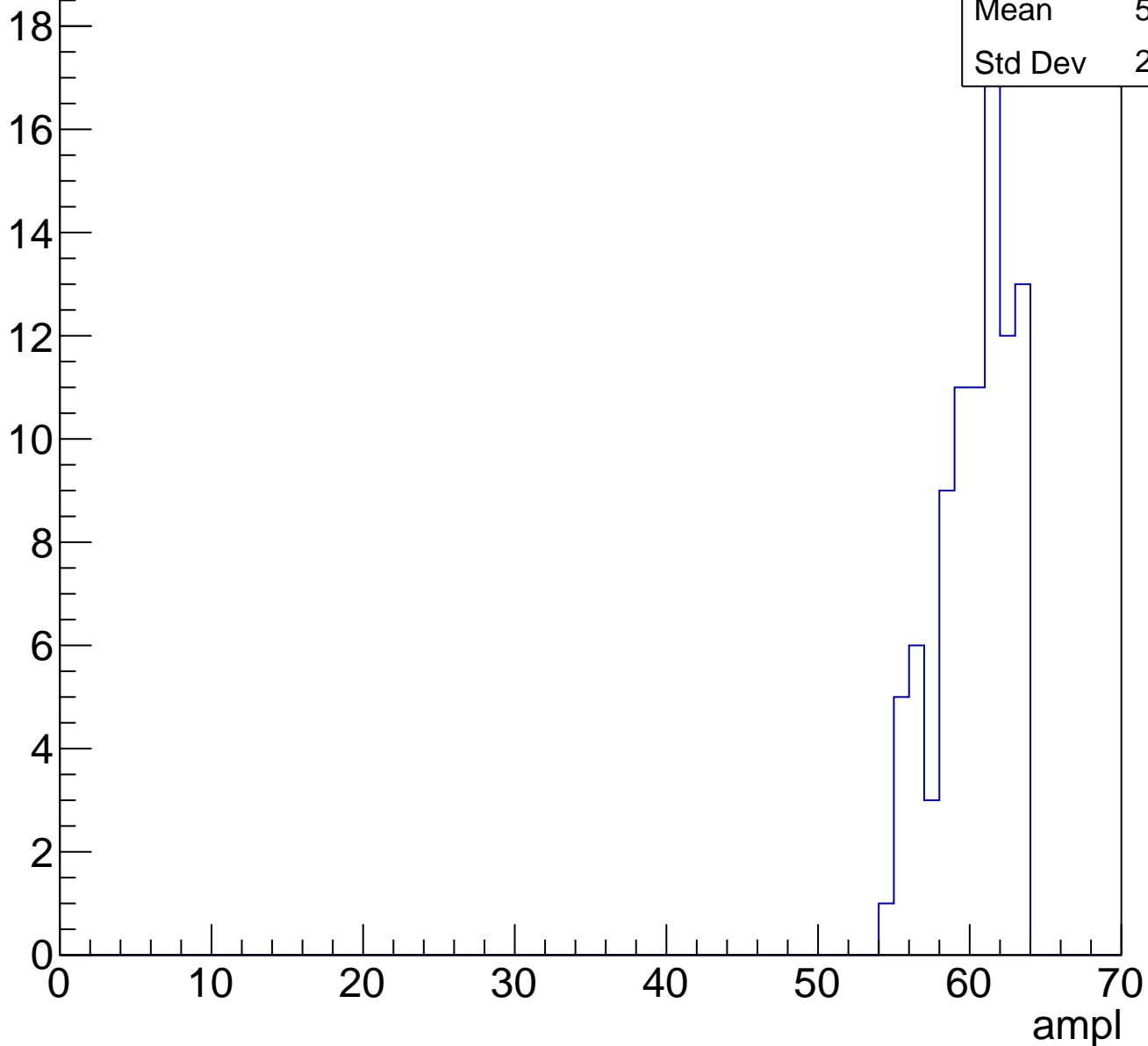
Entries	108
Mean	55.38
Std Dev	2.828



B1L001S, U20-ch44, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

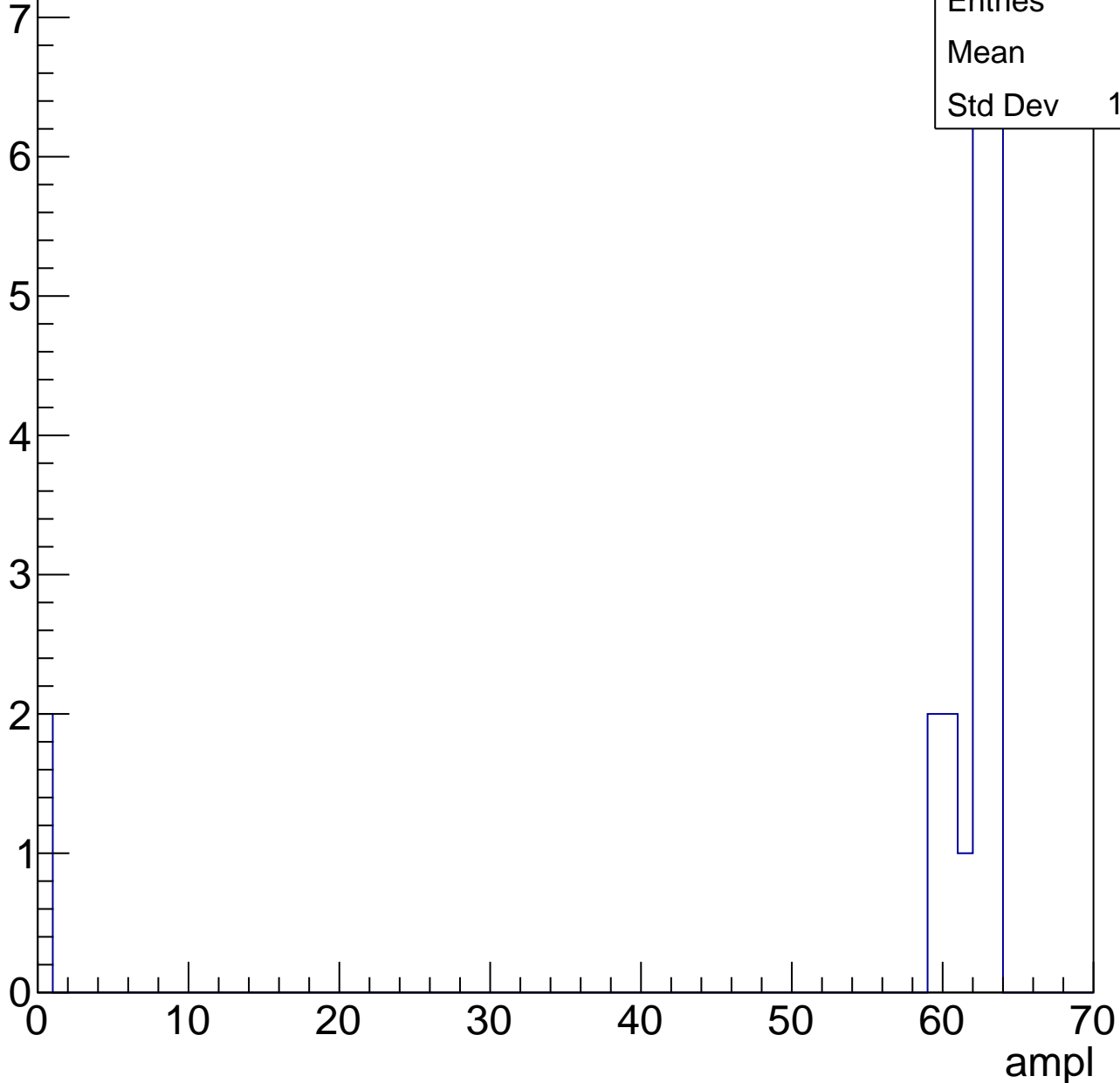


B1L001S, U20-ch44, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	21
Mean	55.9
Std Dev	18.18



B1L001S, U20-ch44, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U20-ch45, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.29
Std Dev	3.265

Gaus mean : 29.8396

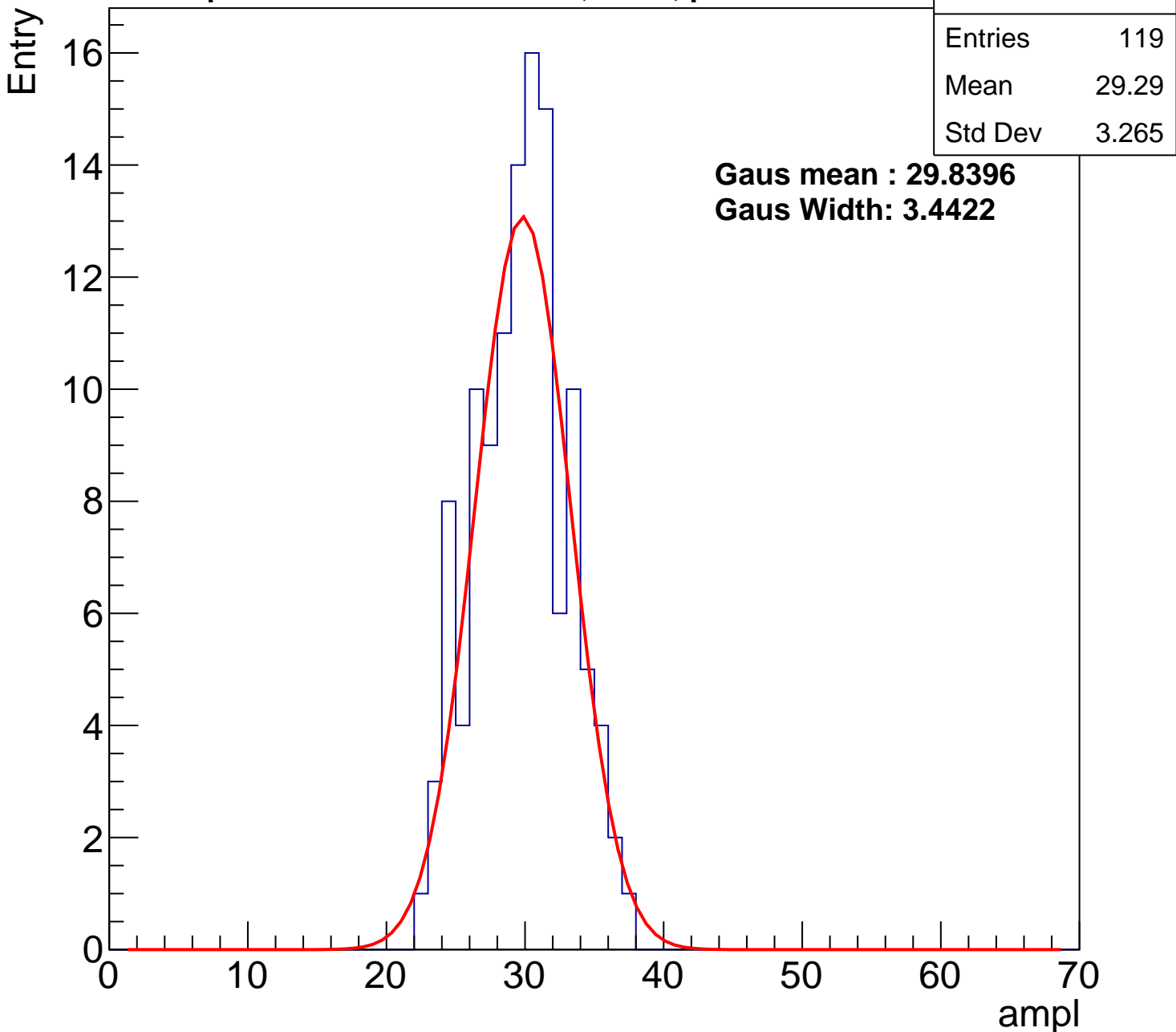
Gaus Width: 3.4422

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

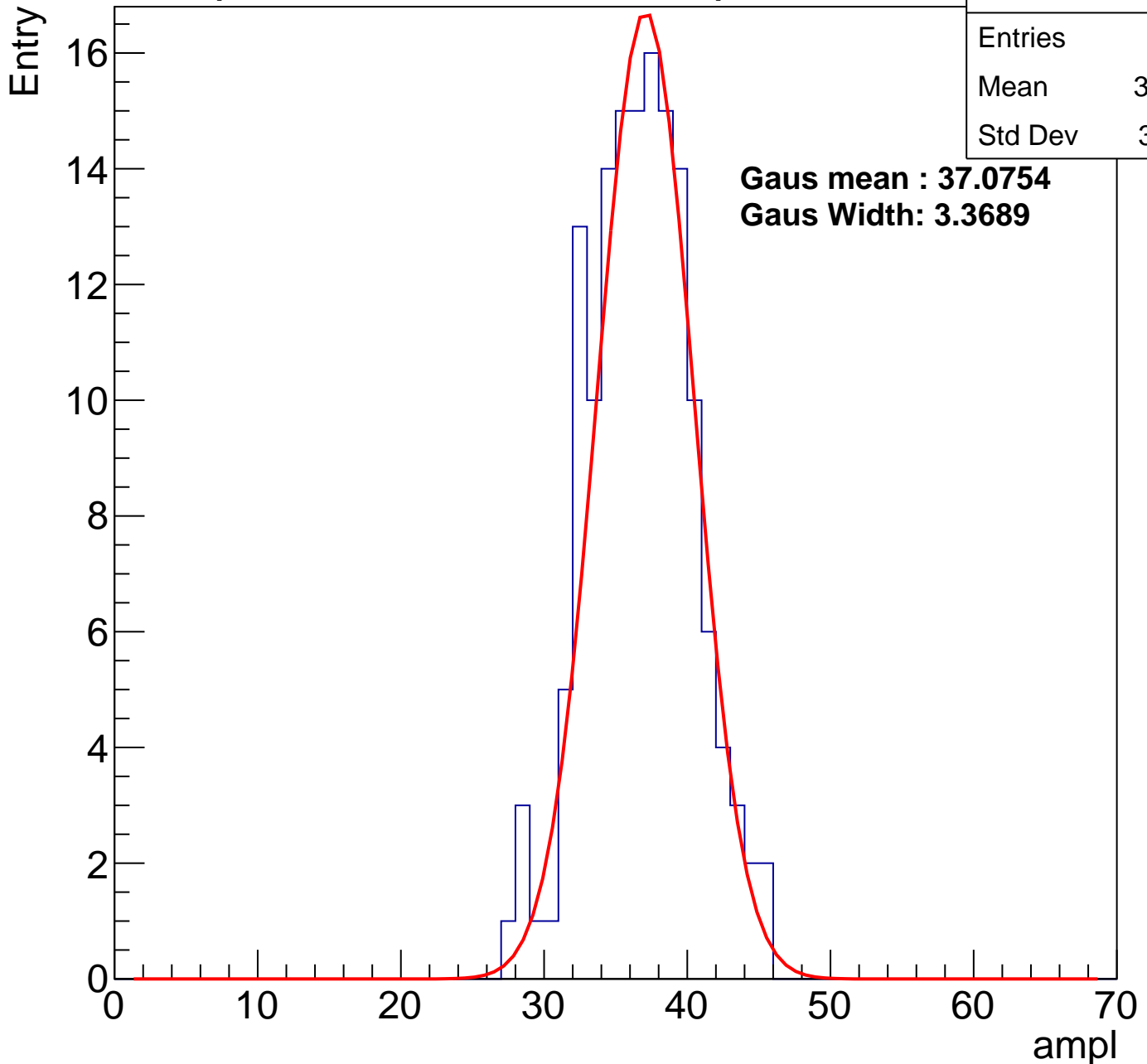


B1L001S, U20-ch45, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	36.27
Std Dev	3.591

Gaus mean : 37.0754
Gaus Width: 3.3689



B1L001S, U20-ch45, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	42.23
Std Dev	3.449

Entry

12

10

8

6

4

2

0

Gaus mean : 42.9664

Gaus Width: 3.6057

ampl

0

10

20

30

40

50

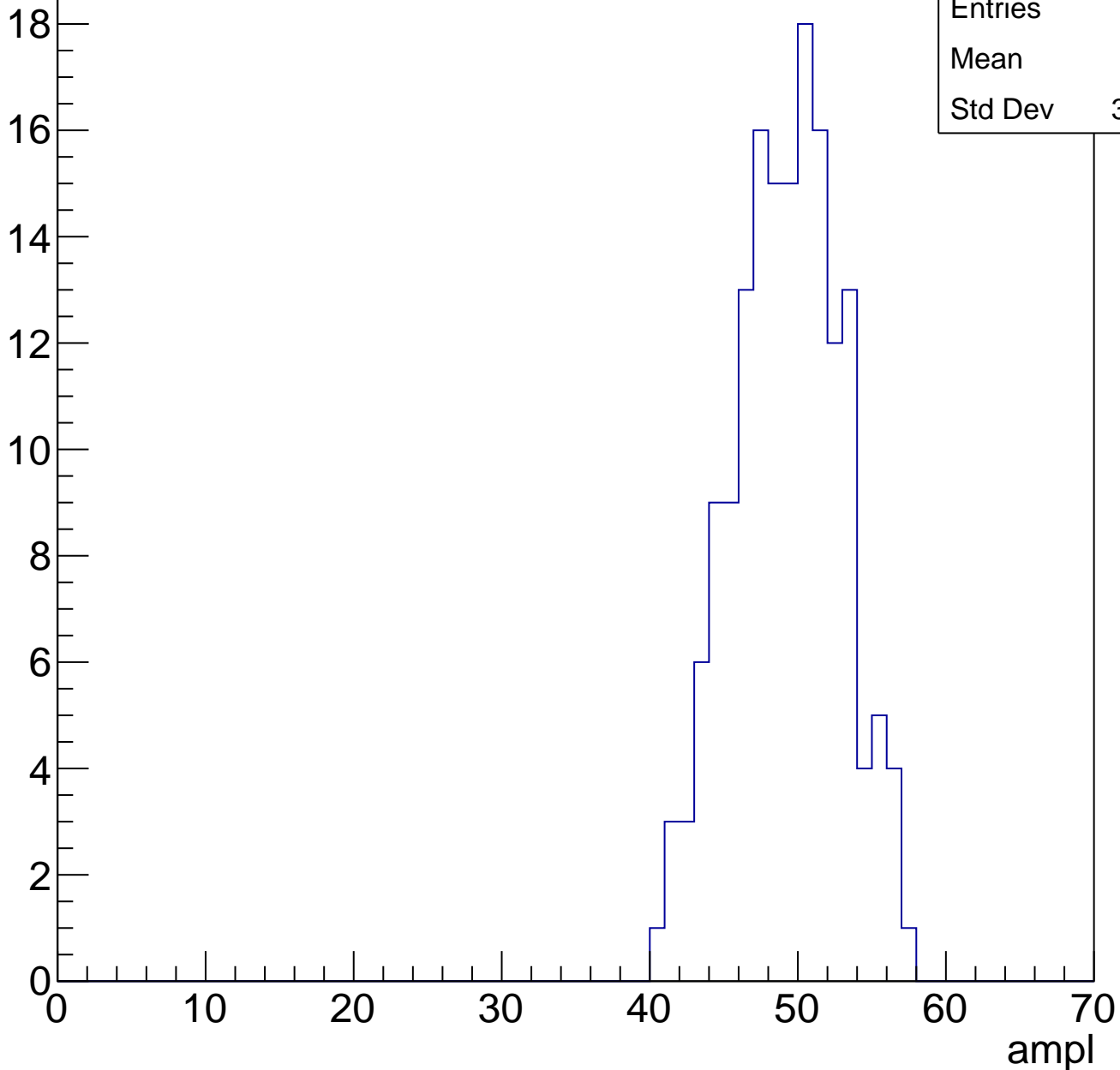
60

70

B1L001S, U20-ch45, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch45, adc4

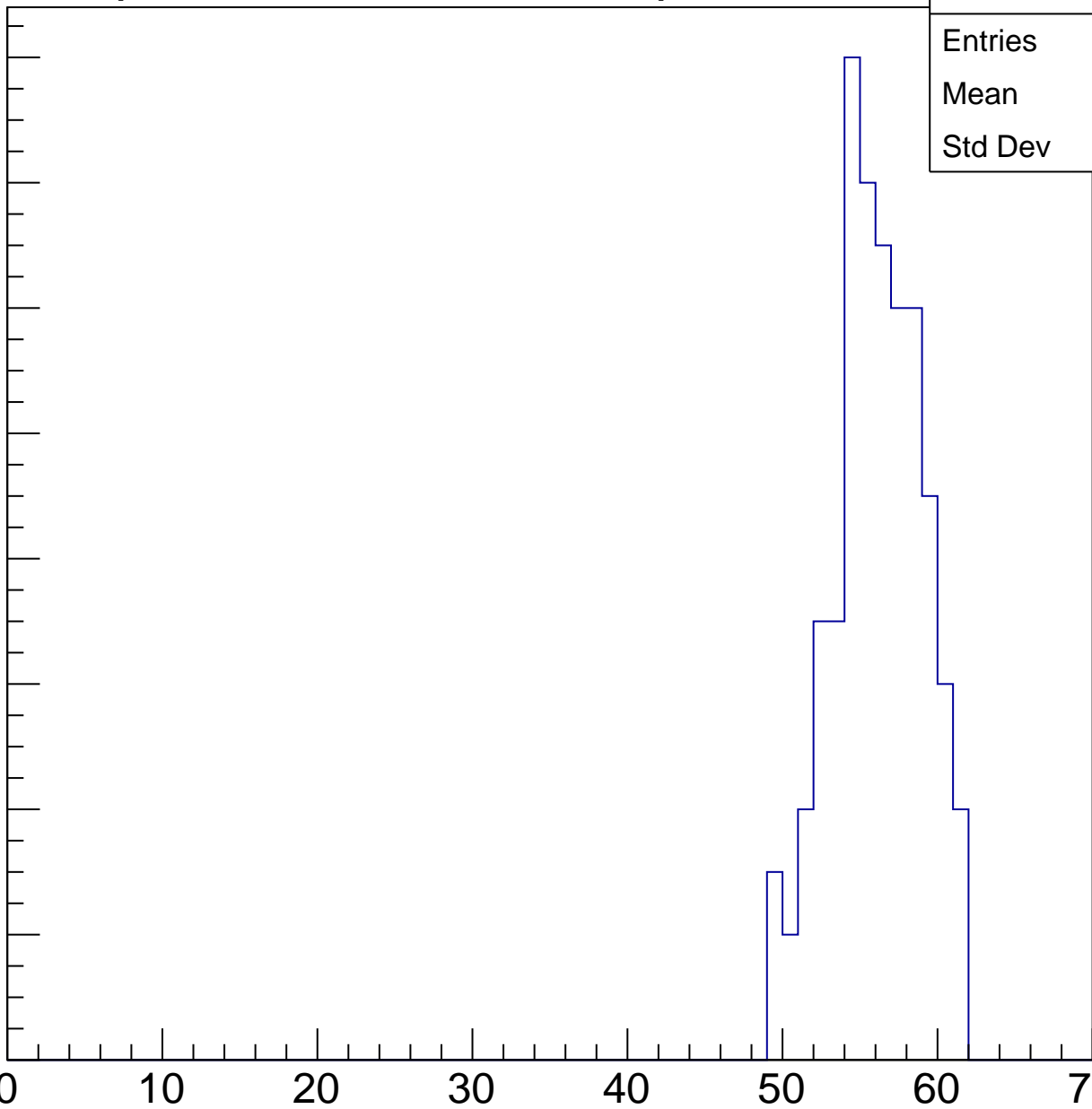
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	55.62
Std Dev	2.863

ampl



B1L001S, U20-ch45, adc5

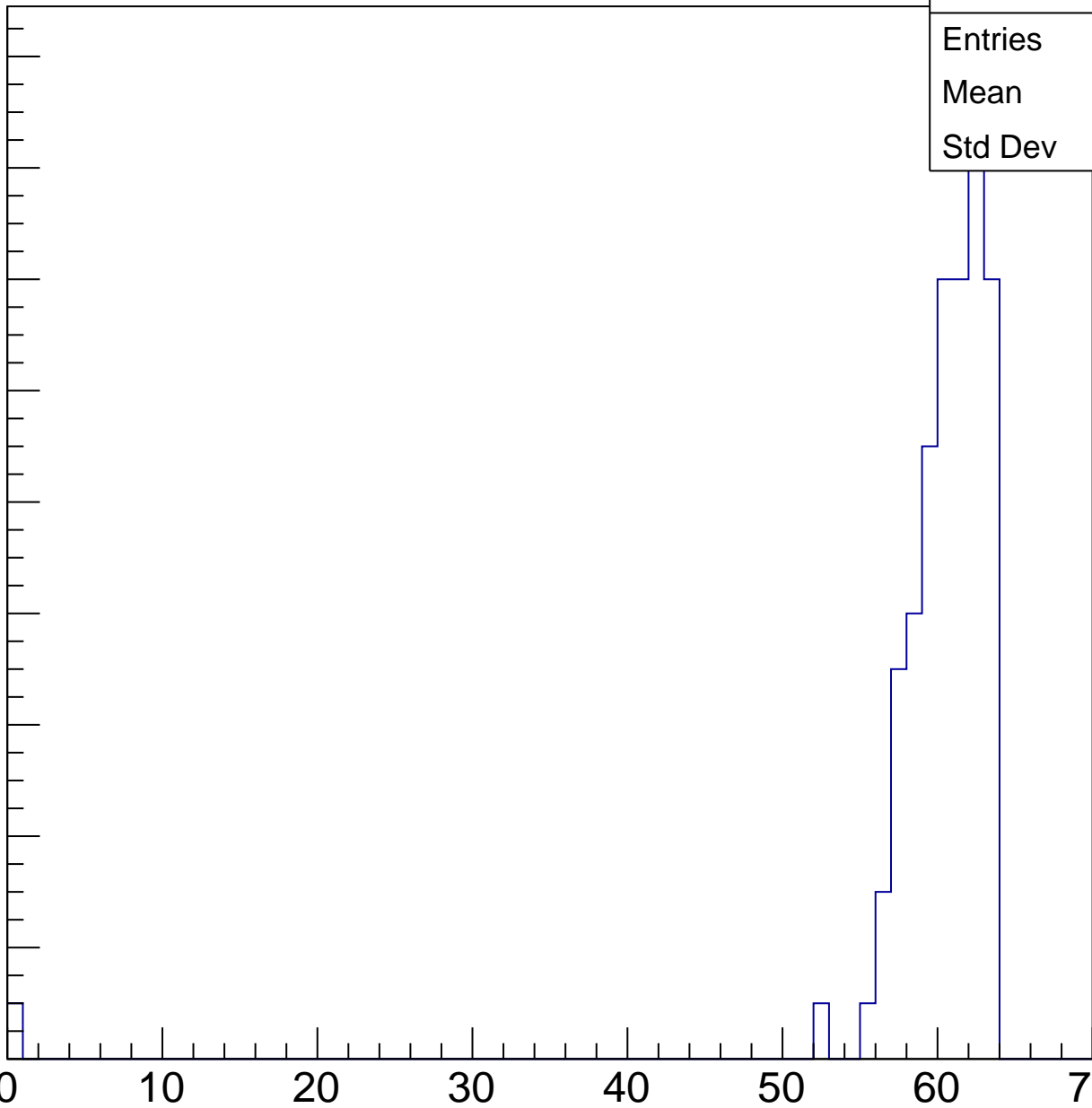
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.55
Std Dev	6.624

ampl

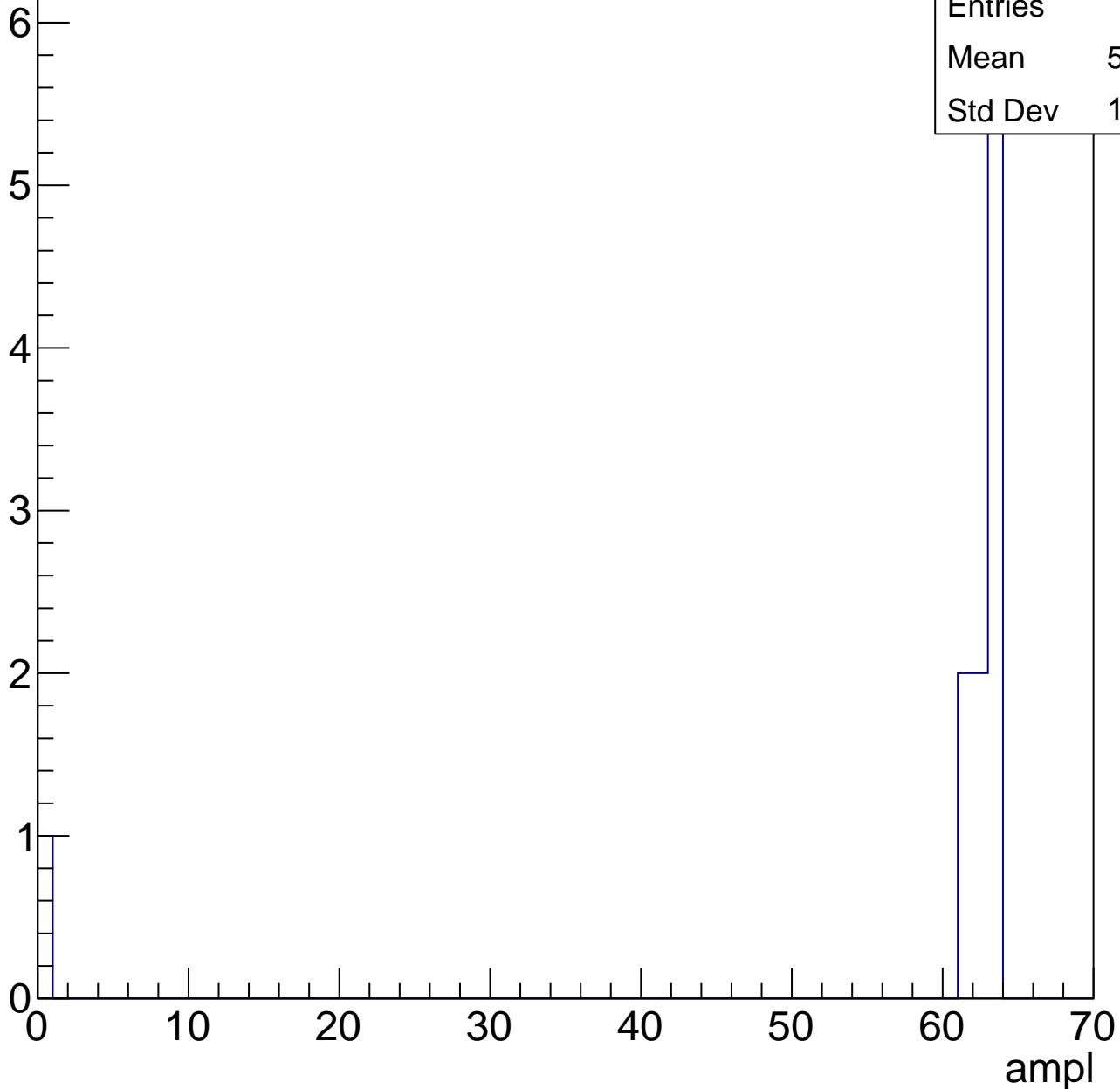


B1L001S, U20-ch45, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

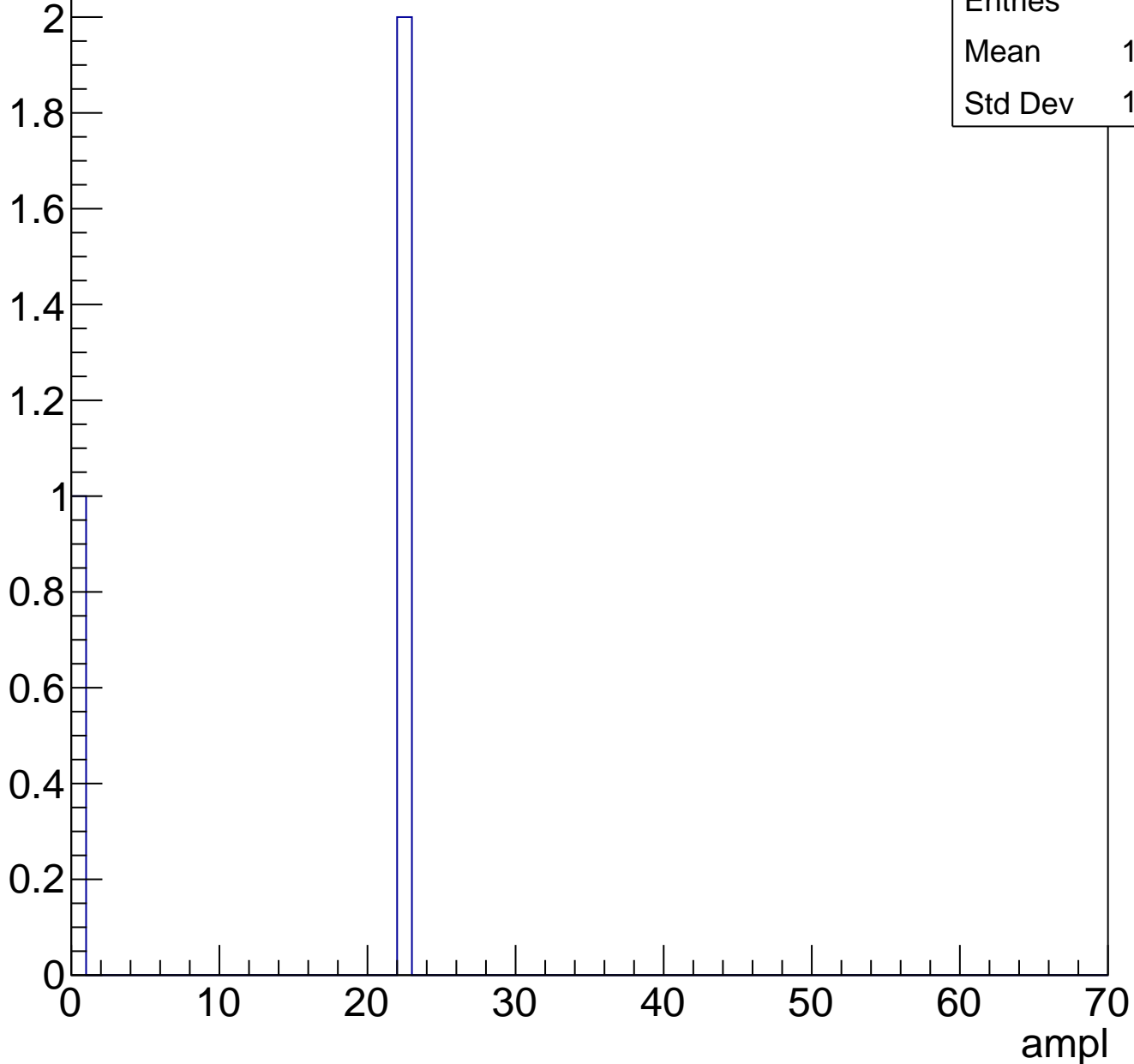
Entries	11
Mean	56.73
Std Dev	17.95



B1L001S, U20-ch45, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	14.67
Std Dev	10.37

B1L001S, U20-ch46, adc0

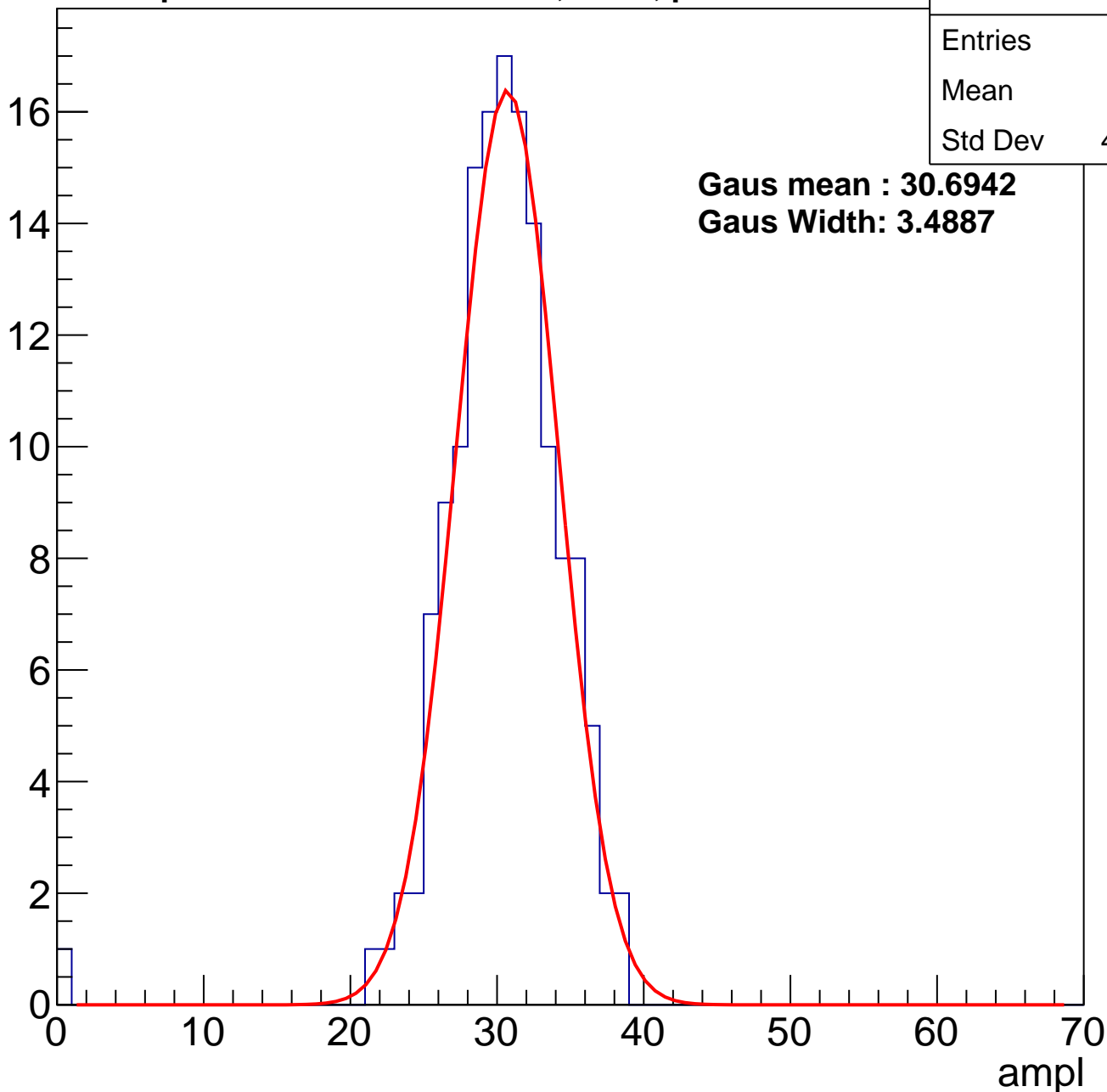
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.9
Std Dev	4.216

Gaus mean : 30.6942

Gaus Width: 3.4887

Entry



B1L001S, U20-ch46, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	37.04
Std Dev	3.373

Gaus mean : 37.2075

Gaus Width: 3.2070

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

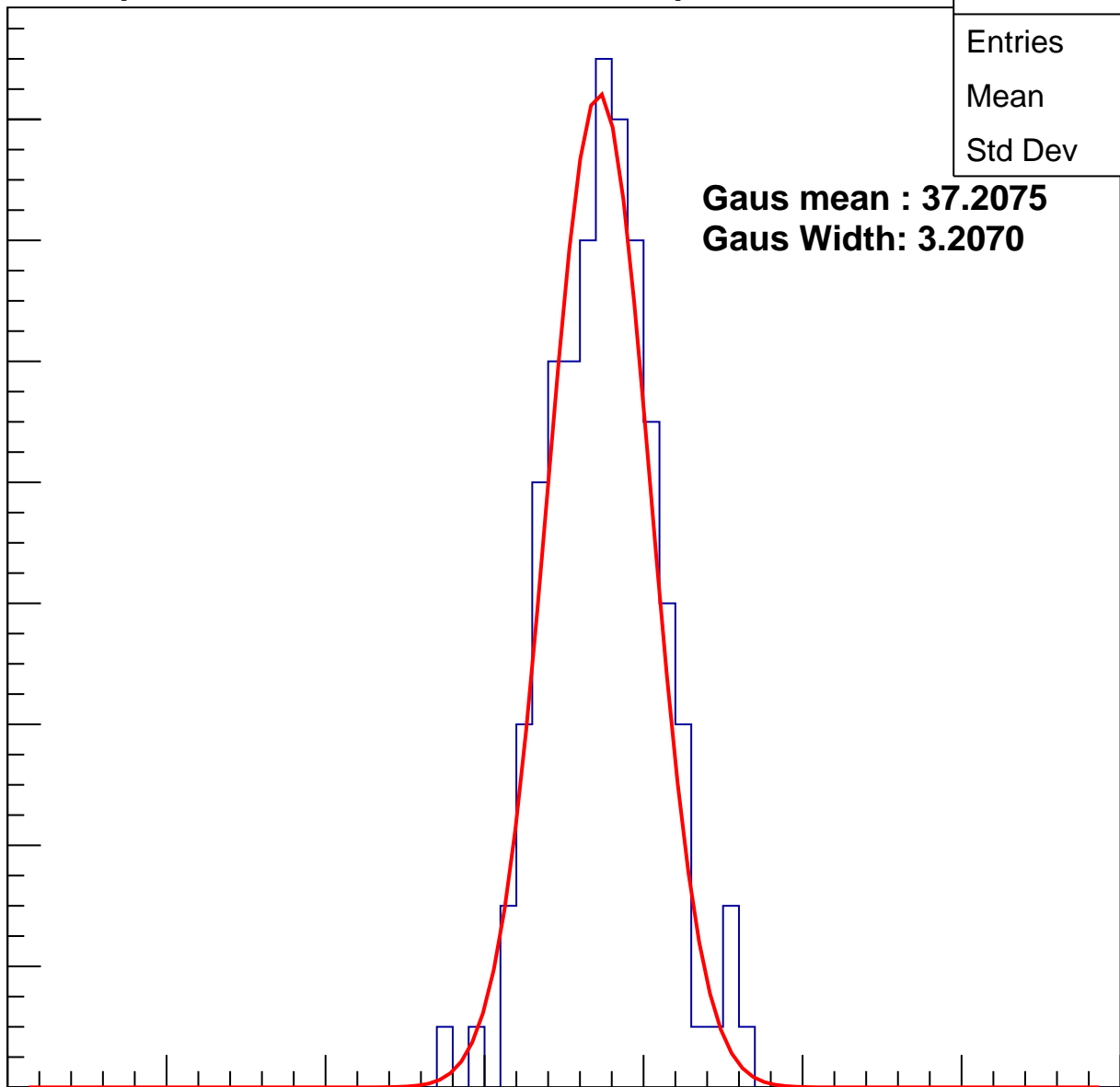
40

50

60

70

ampl



B1L001S, U20-ch46, adc2

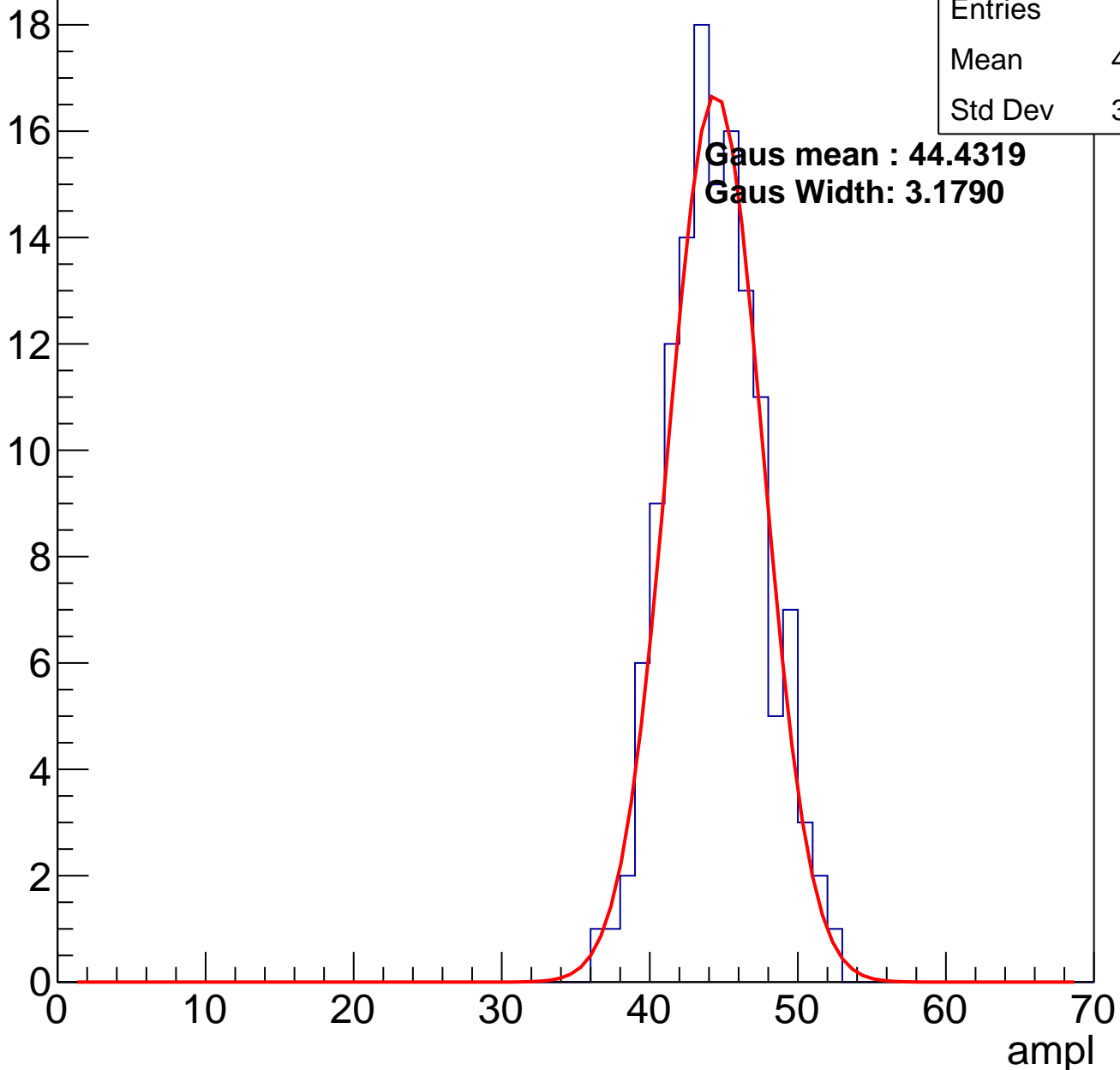
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	43.96
Std Dev	3.154

Gaus mean : 44.4319

Gaus Width: 3.1790

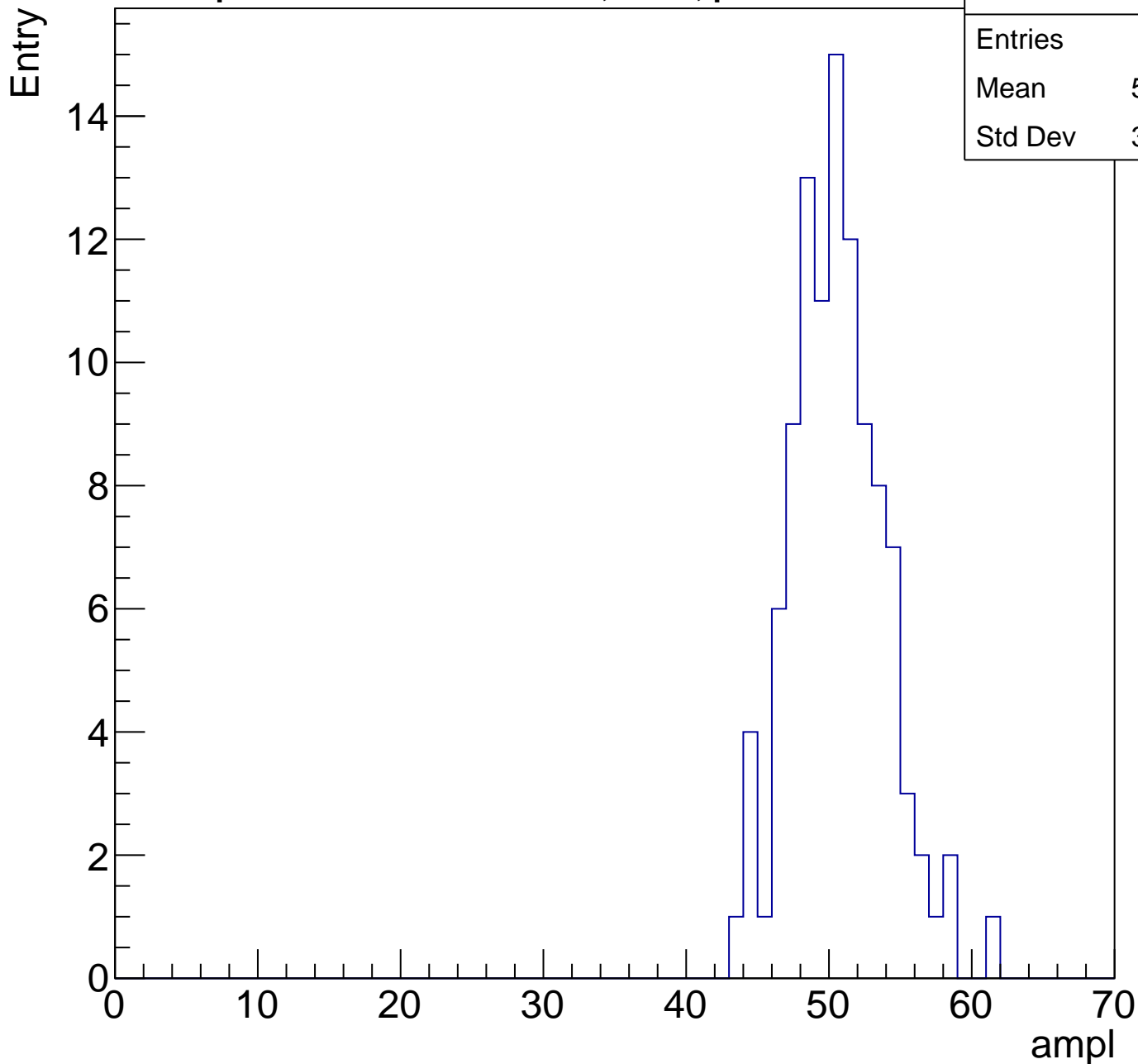
Entry



B1L001S, U20-ch46, adc3

calib_packv5_042523_0143.root, FC#2, port C2

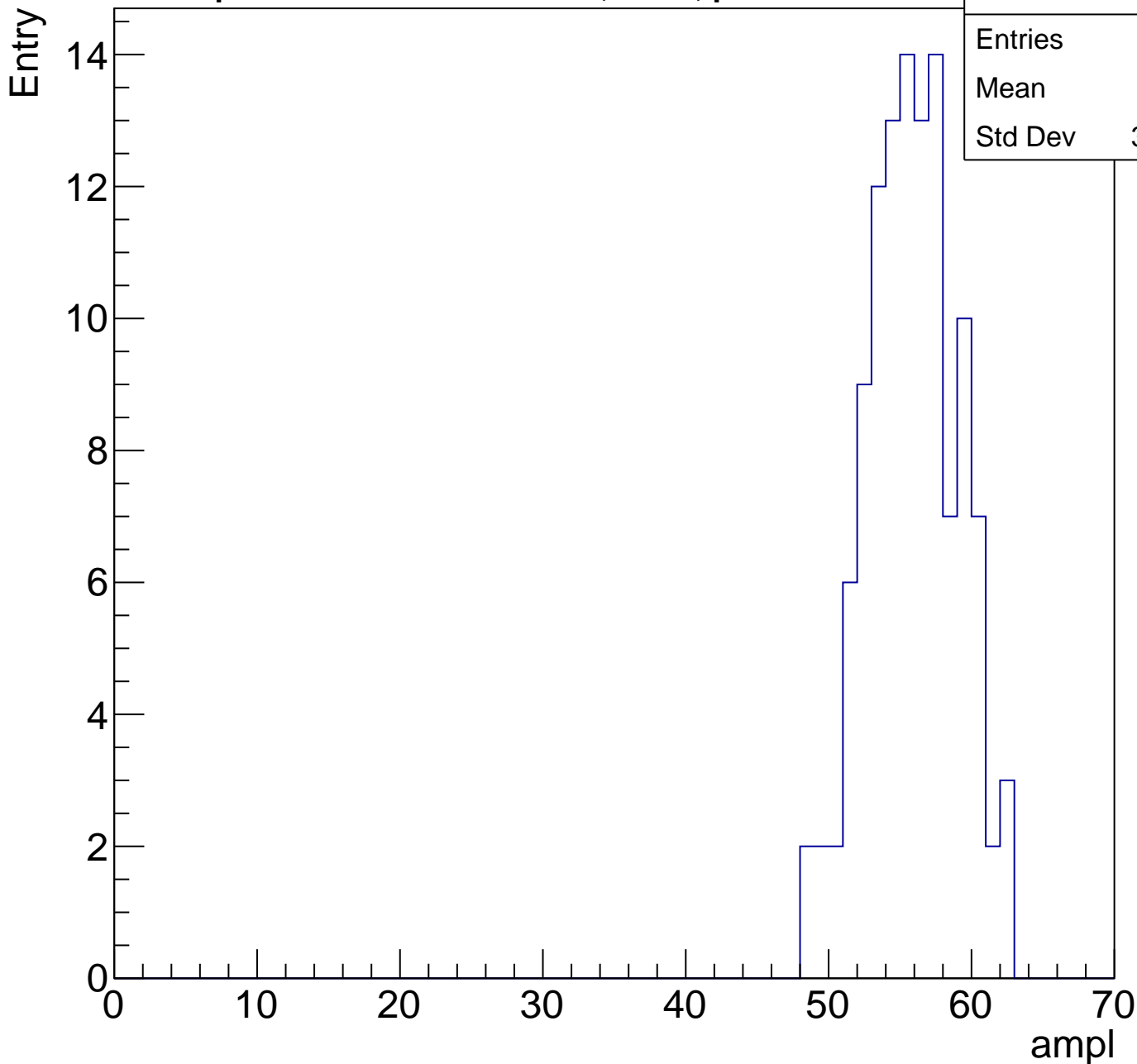
Entries	105
Mean	50.18
Std Dev	3.283



B1L001S, U20-ch46, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	55.4
Std Dev	3.115



B1L001S, U20-ch46, adc5

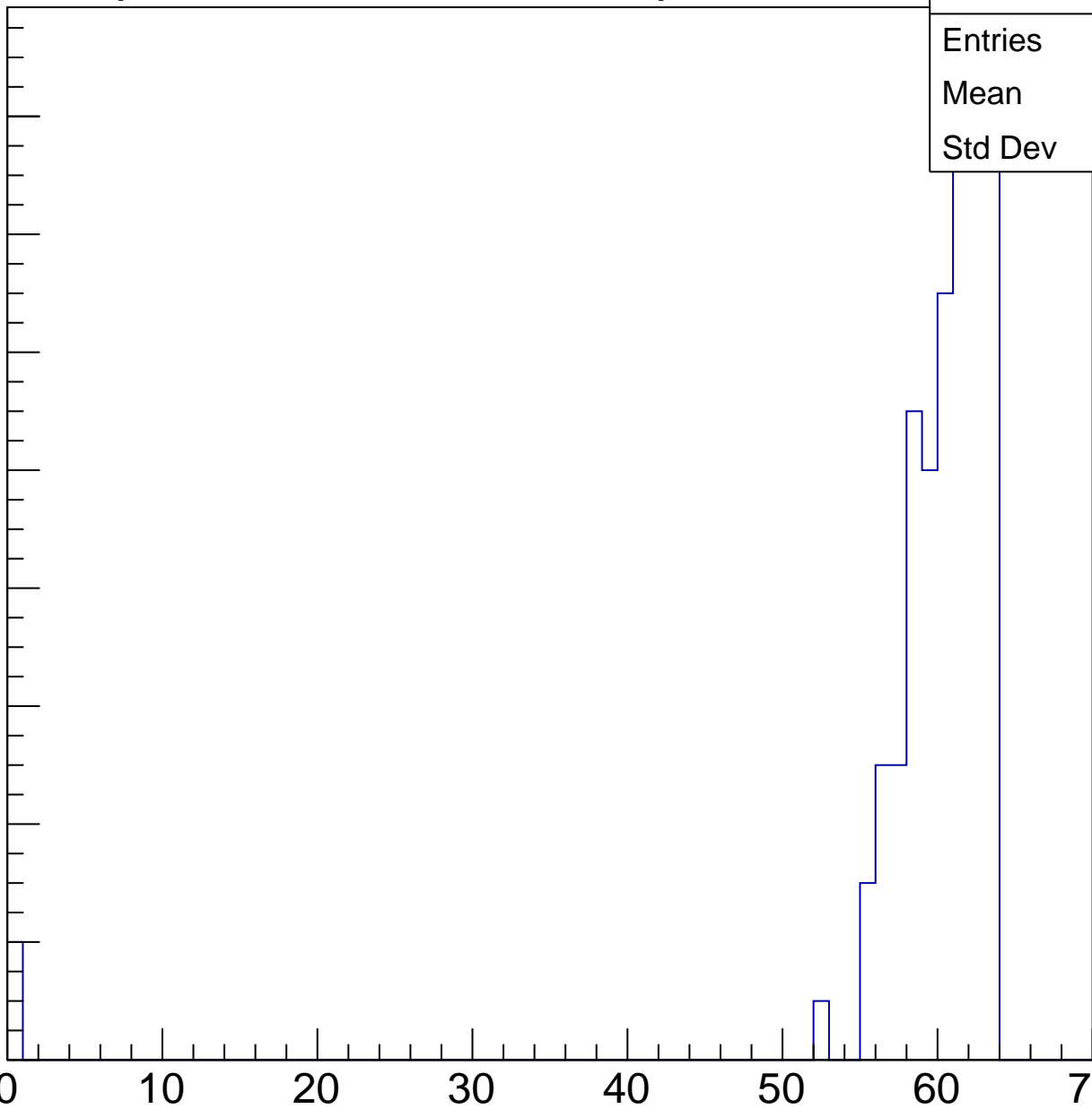
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	58.87
Std Dev	8.773

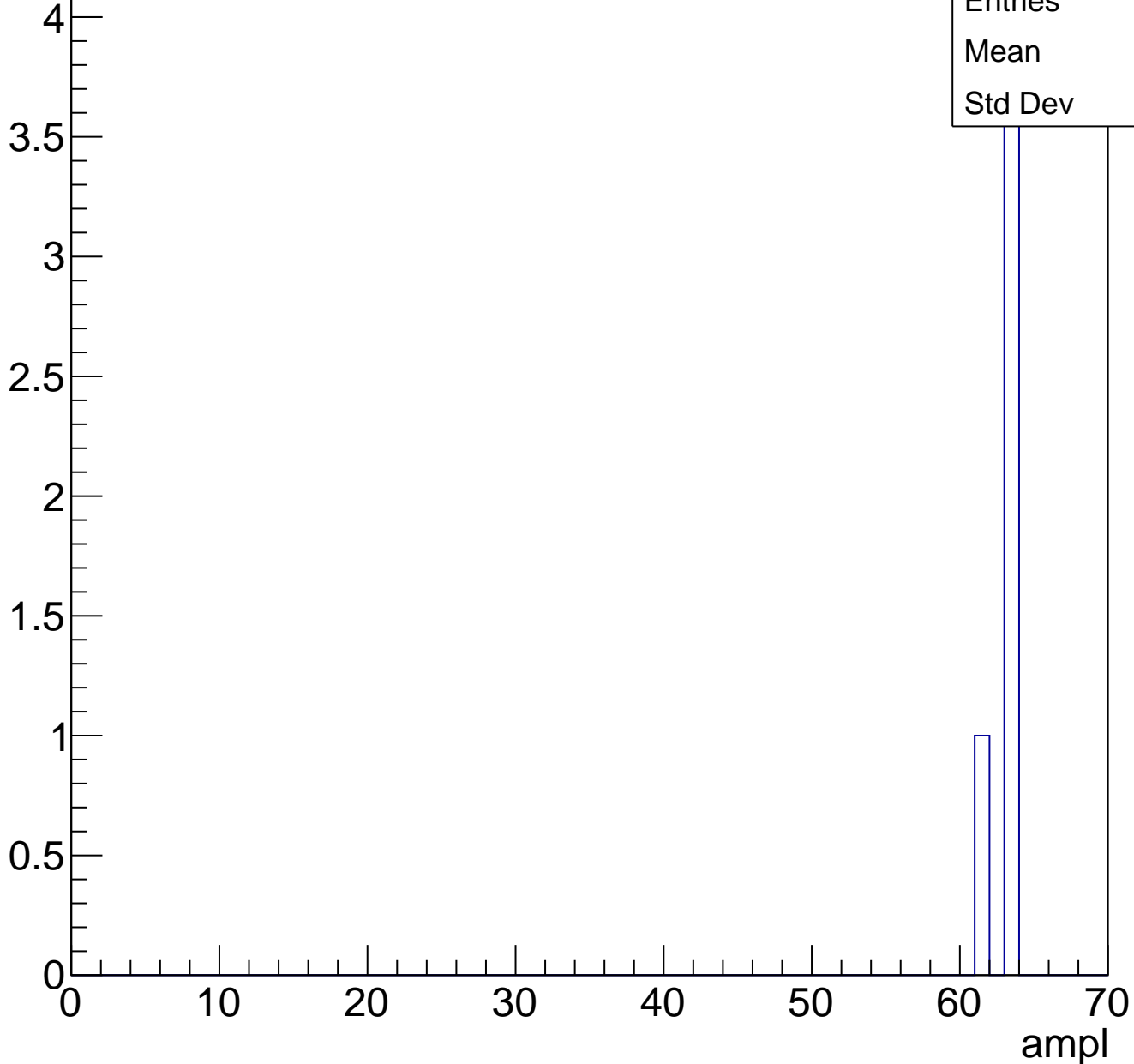
ampl



B1L001S, U20-ch46, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	62.6
Std Dev	0.8

B1L001S, U20-ch46, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



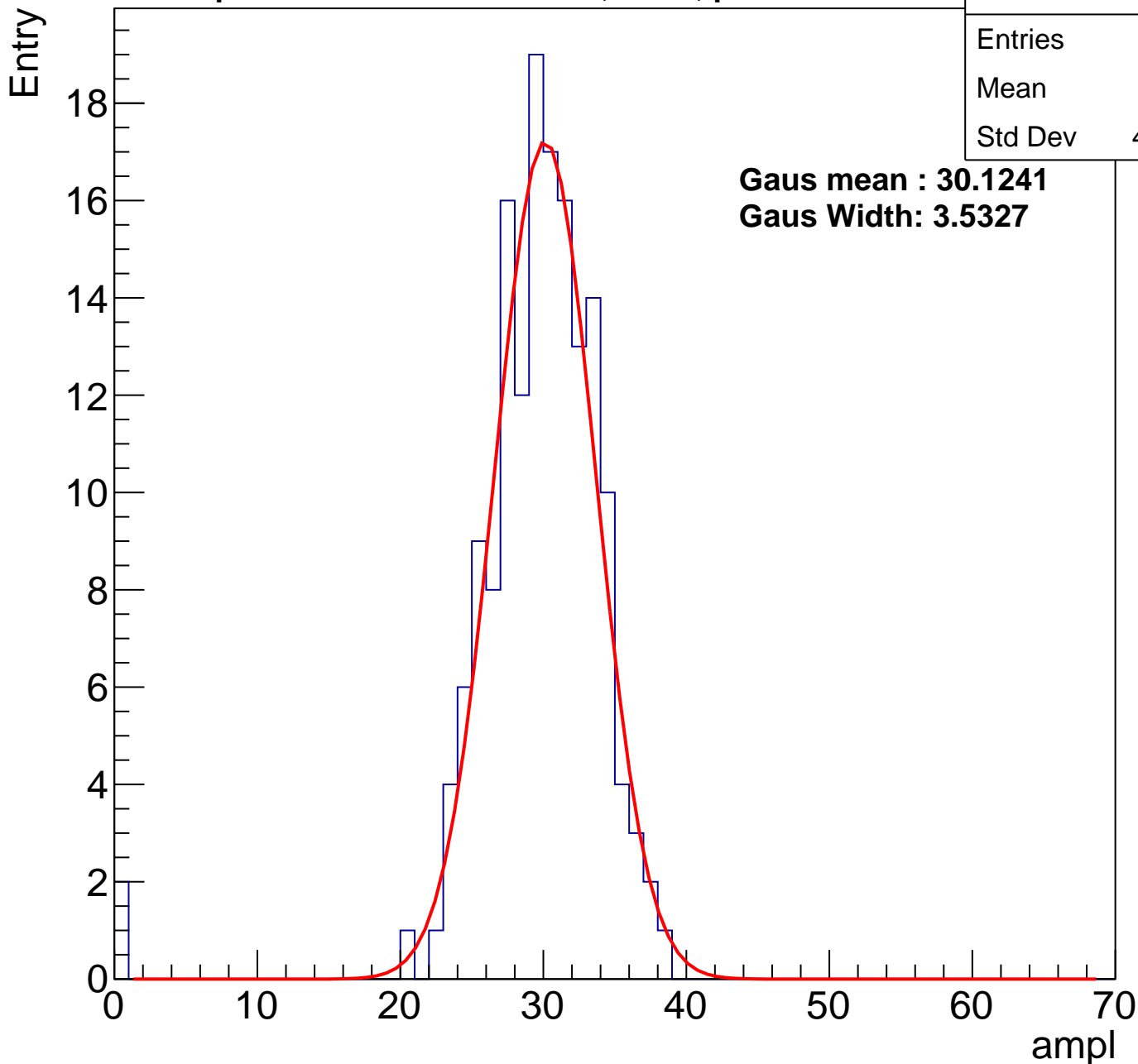
Entries	0
Mean	0
Std Dev	0

B1L001S, U20-ch47, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	29.2
Std Dev	4.749

Gaus mean : 30.1241
Gaus Width: 3.5327

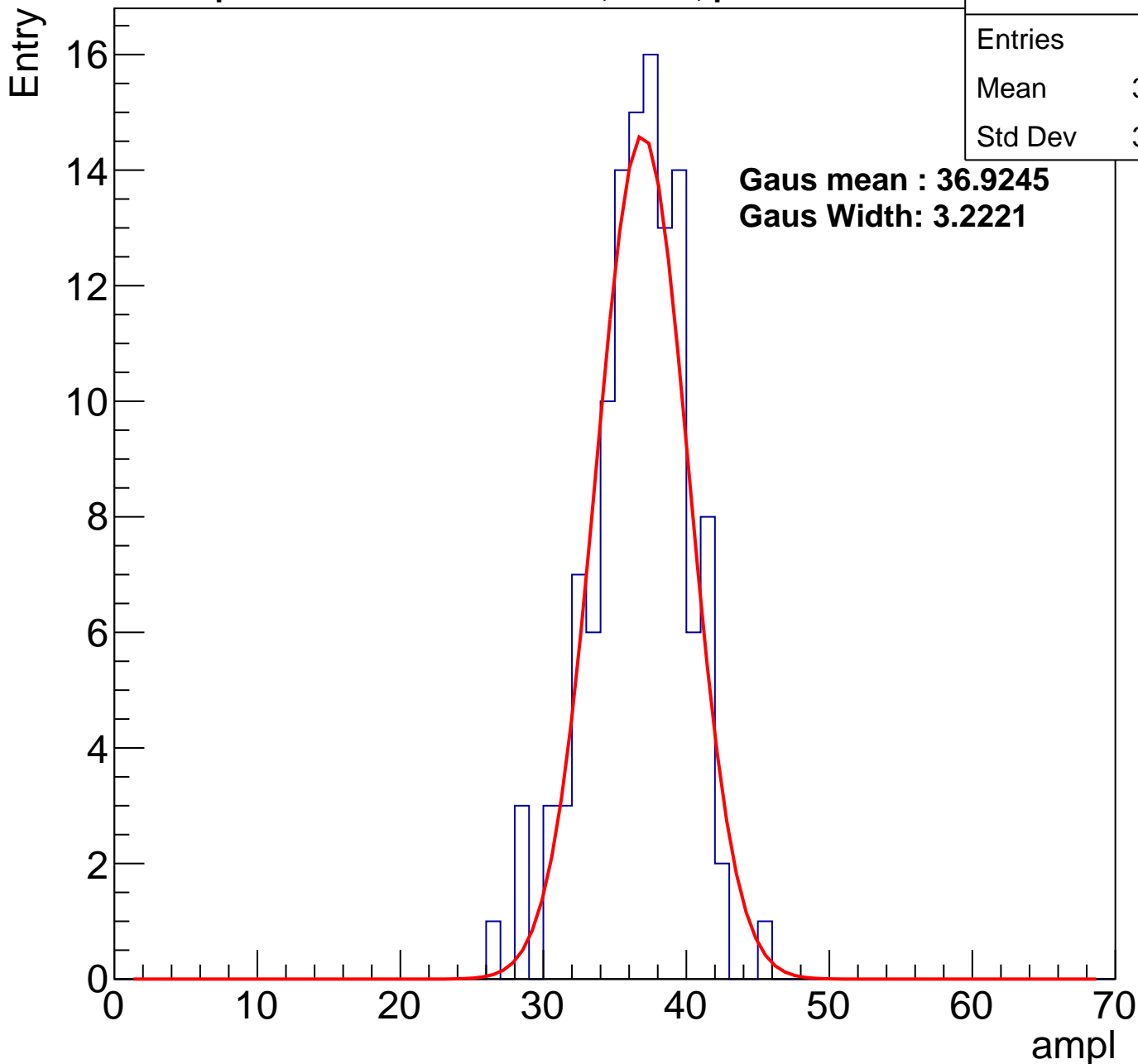


B1L001S, U20-ch47, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	36.18
Std Dev	3.307

Gaus mean : 36.9245
Gaus Width: 3.2221



B1L001S, U20-ch47, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	42.7
Std Dev	3.074

Gaus mean : 43.2401

Gaus Width: 3.0331

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

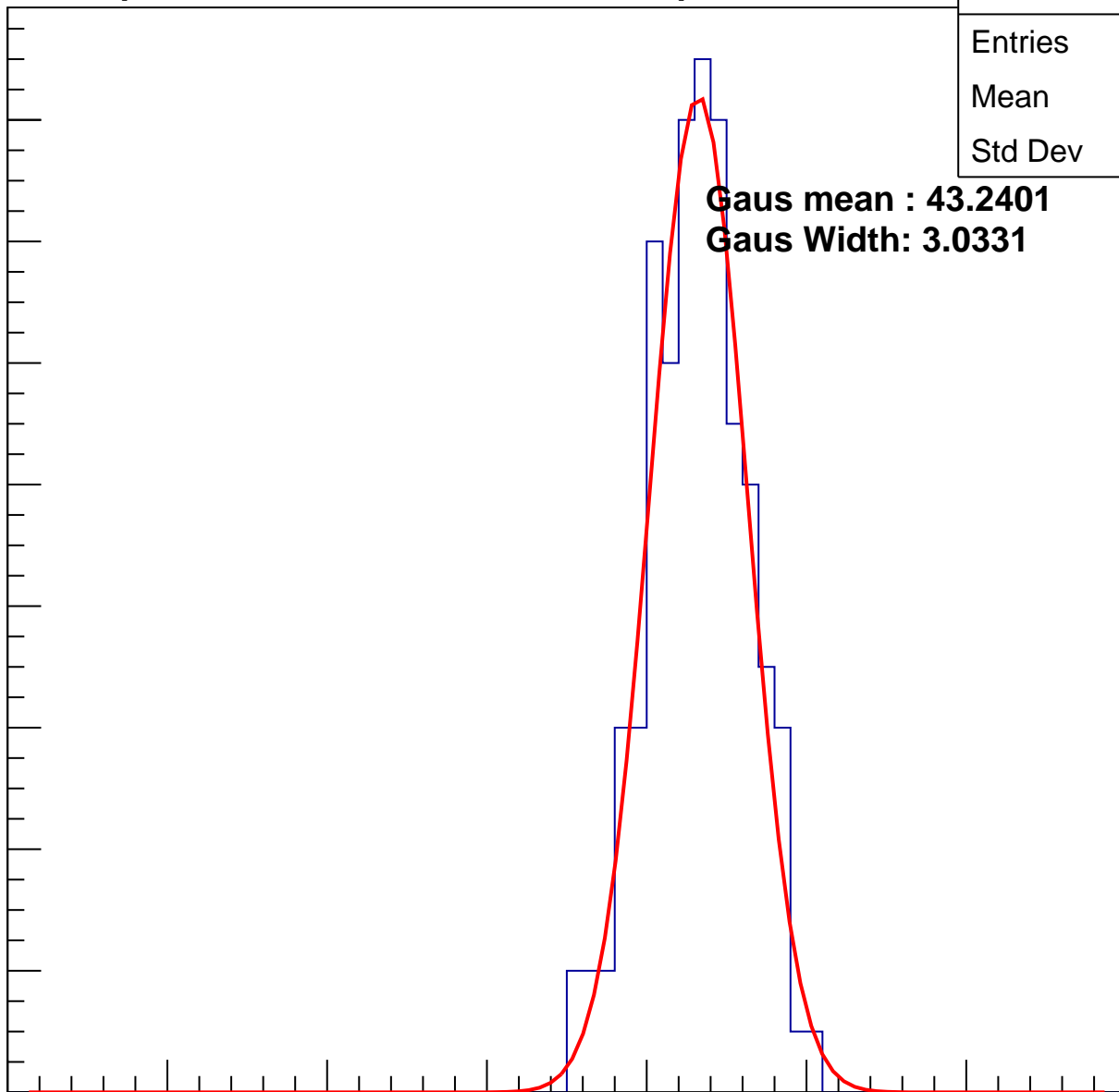
40

50

60

70

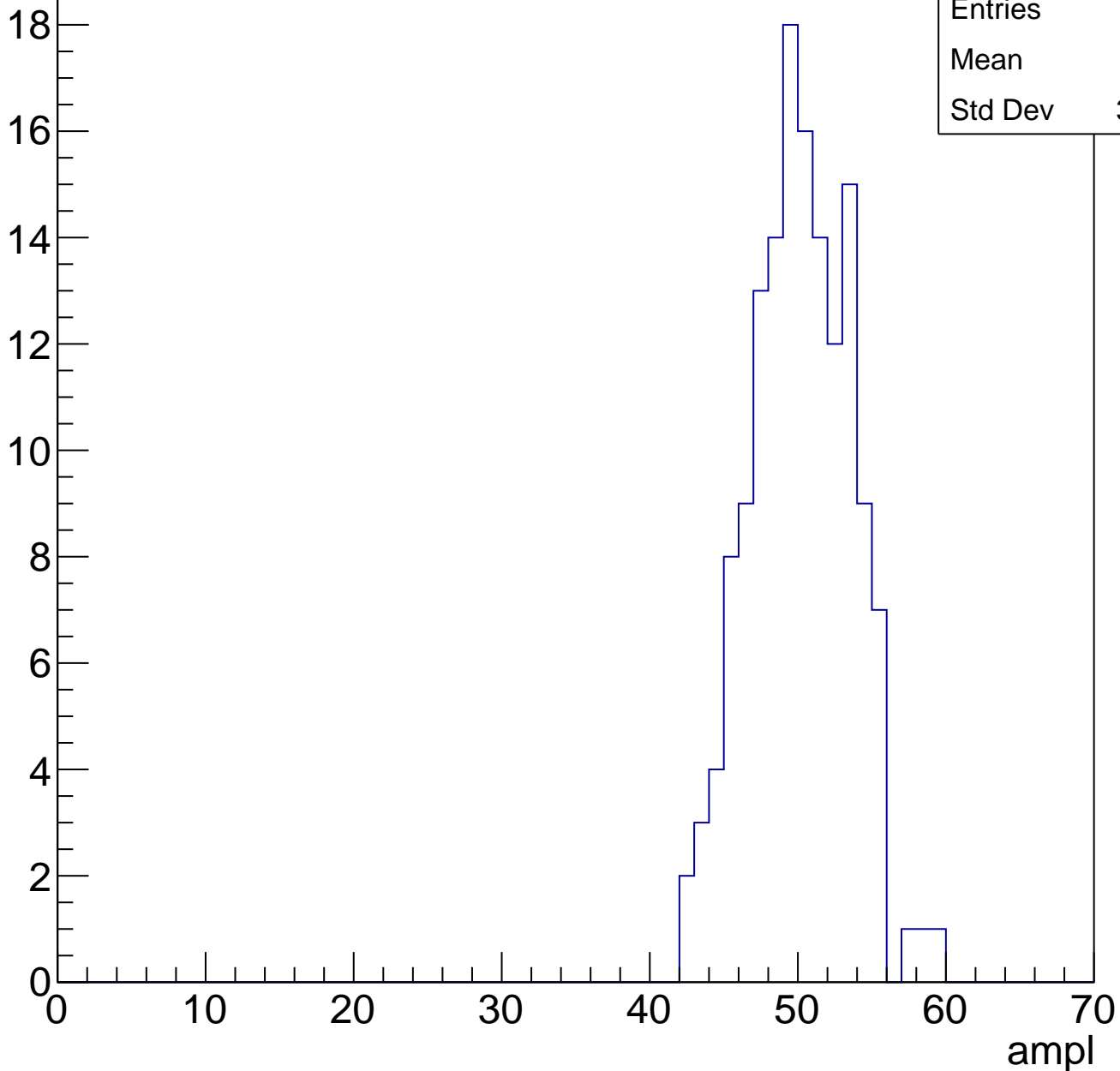
ampl



B1L001S, U20-ch47, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

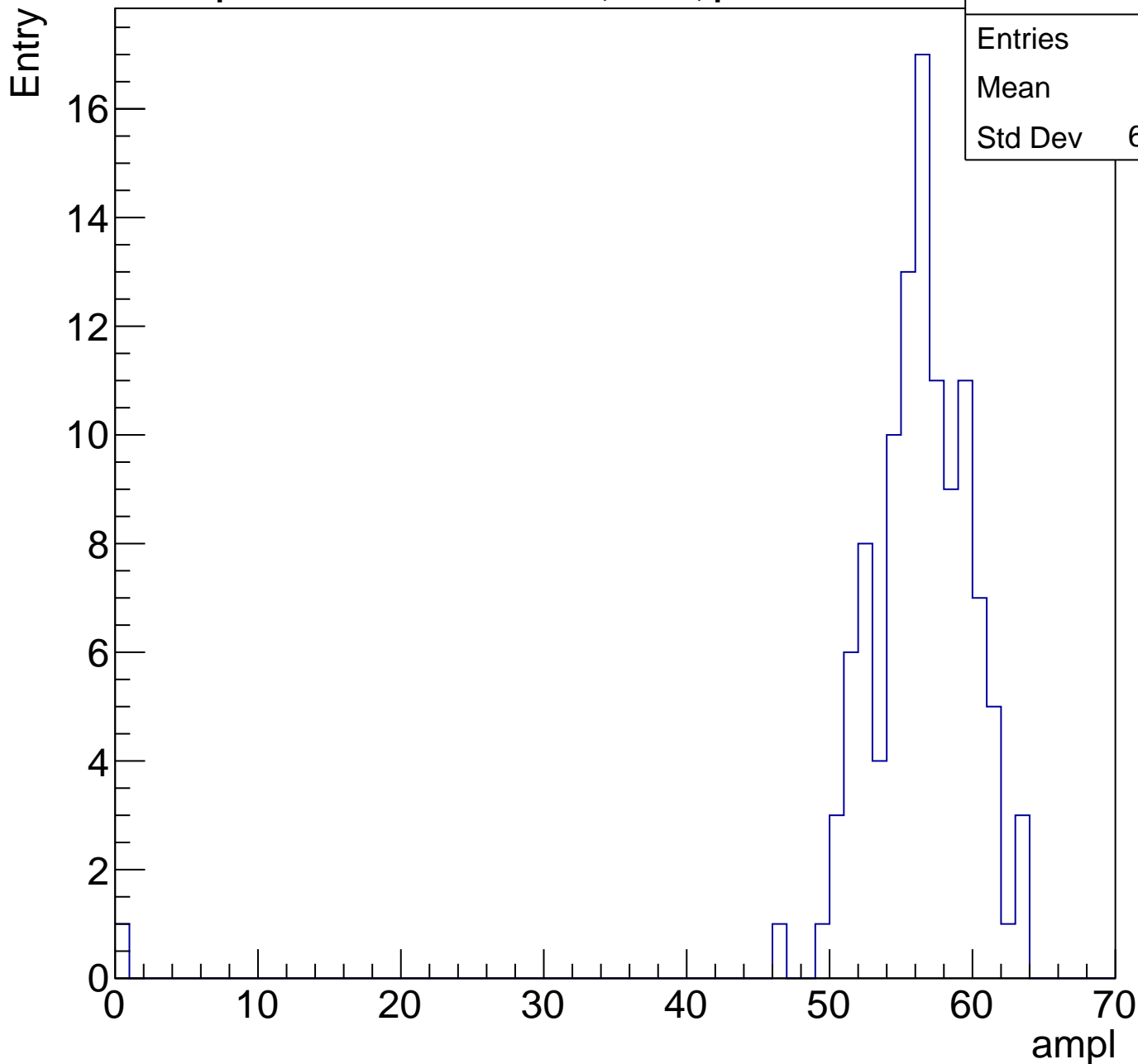


Entries	147
Mean	49.7
Std Dev	3.351

B1L001S, U20-ch47, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	55.5
Std Dev	6.222



B1L001S, U20-ch47, adc5

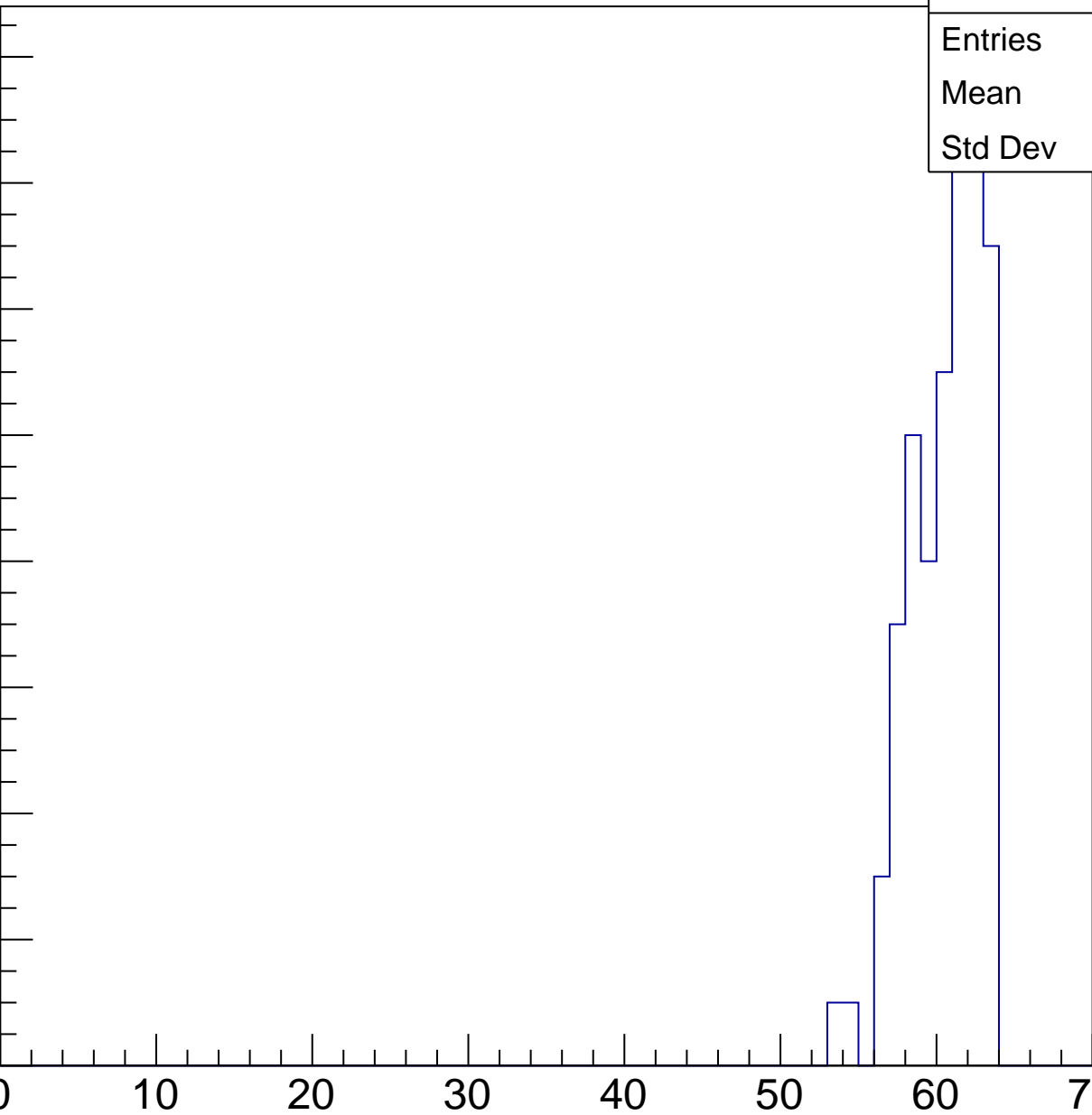
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	60.14
Std Dev	2.271

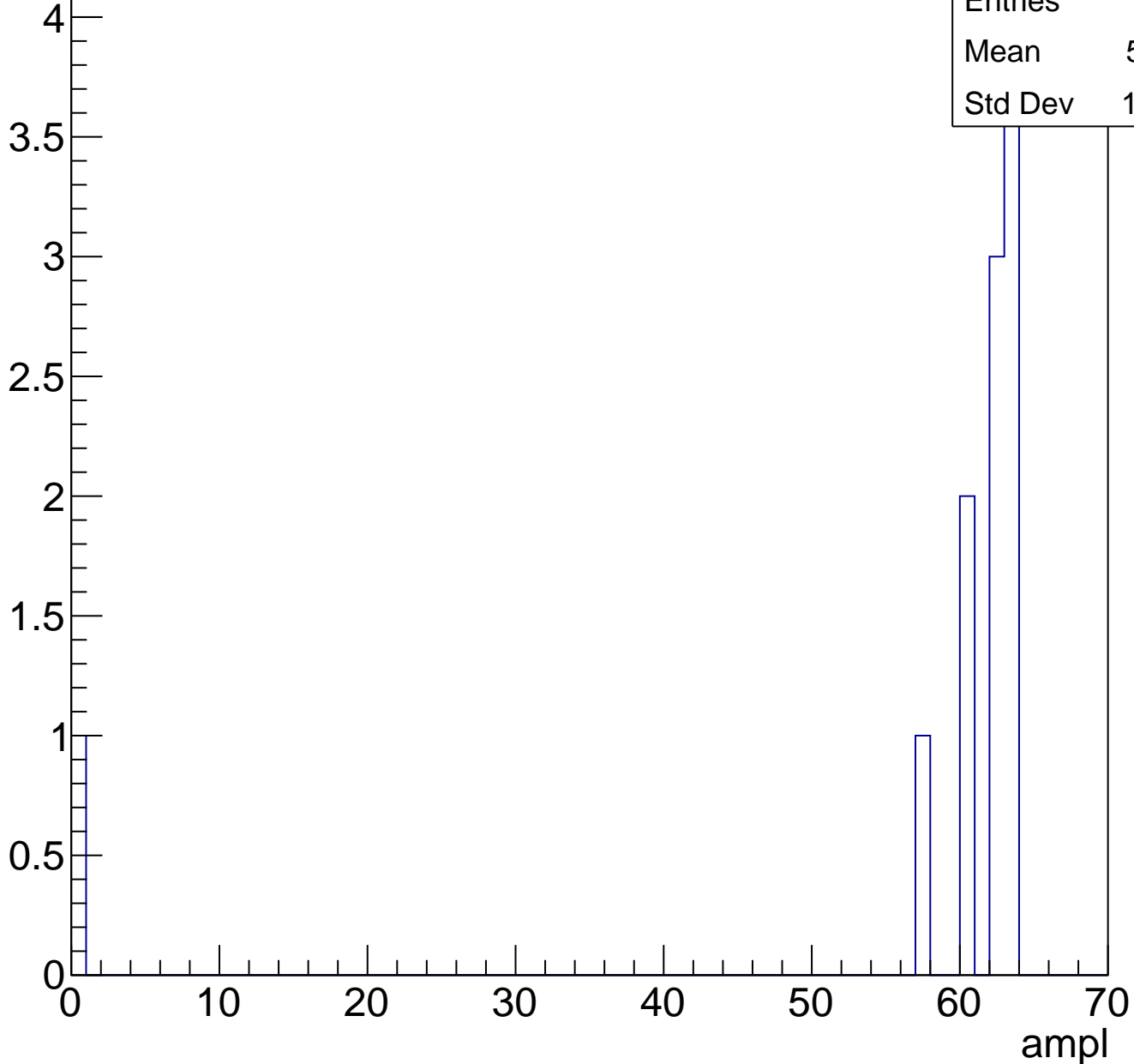
ampl



B1L001S, U20-ch47, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch47, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U20-ch48, adc0

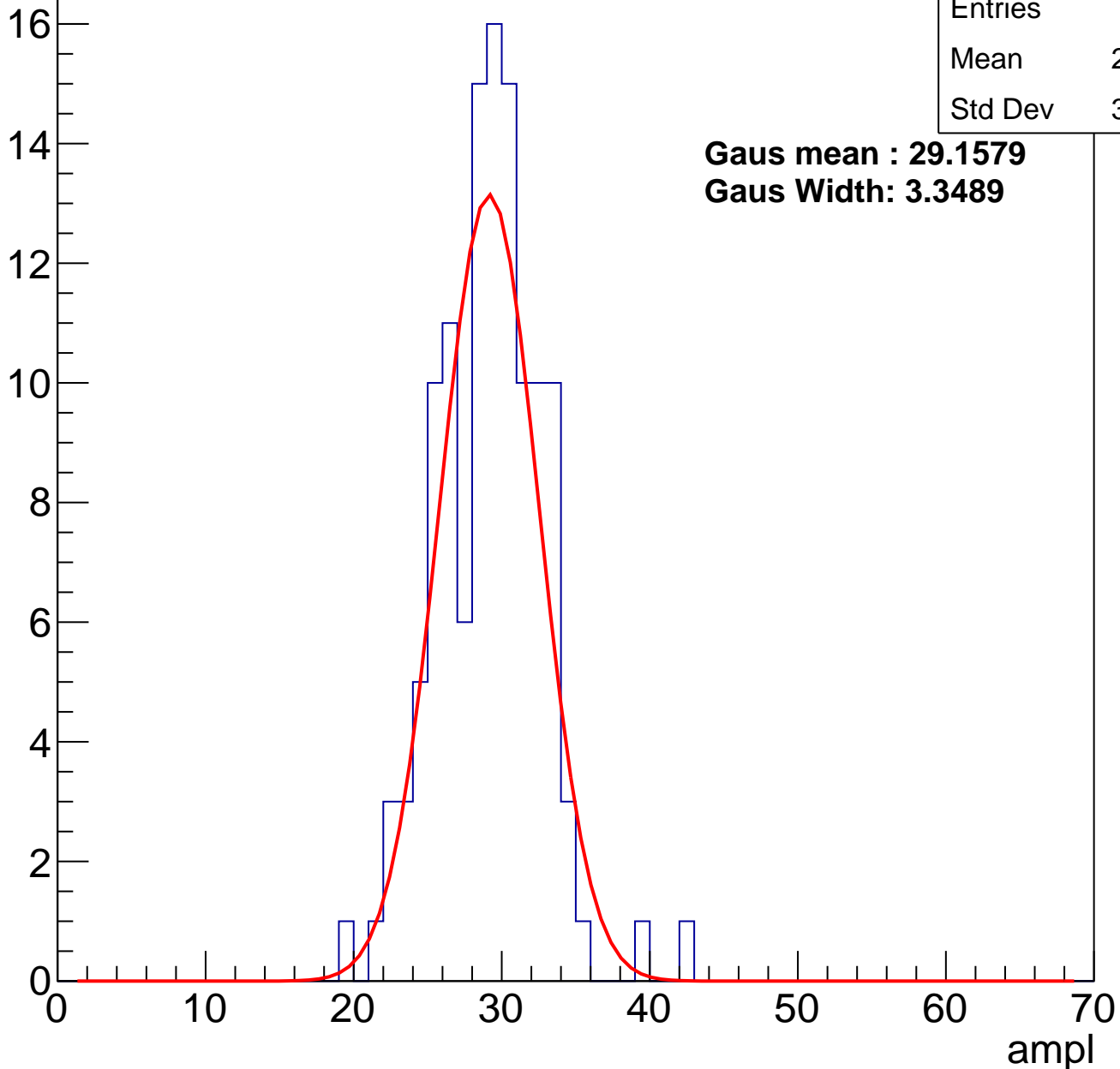
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	28.73
Std Dev	3.527

Gaus mean : 29.1579

Gaus Width: 3.3489

Entry



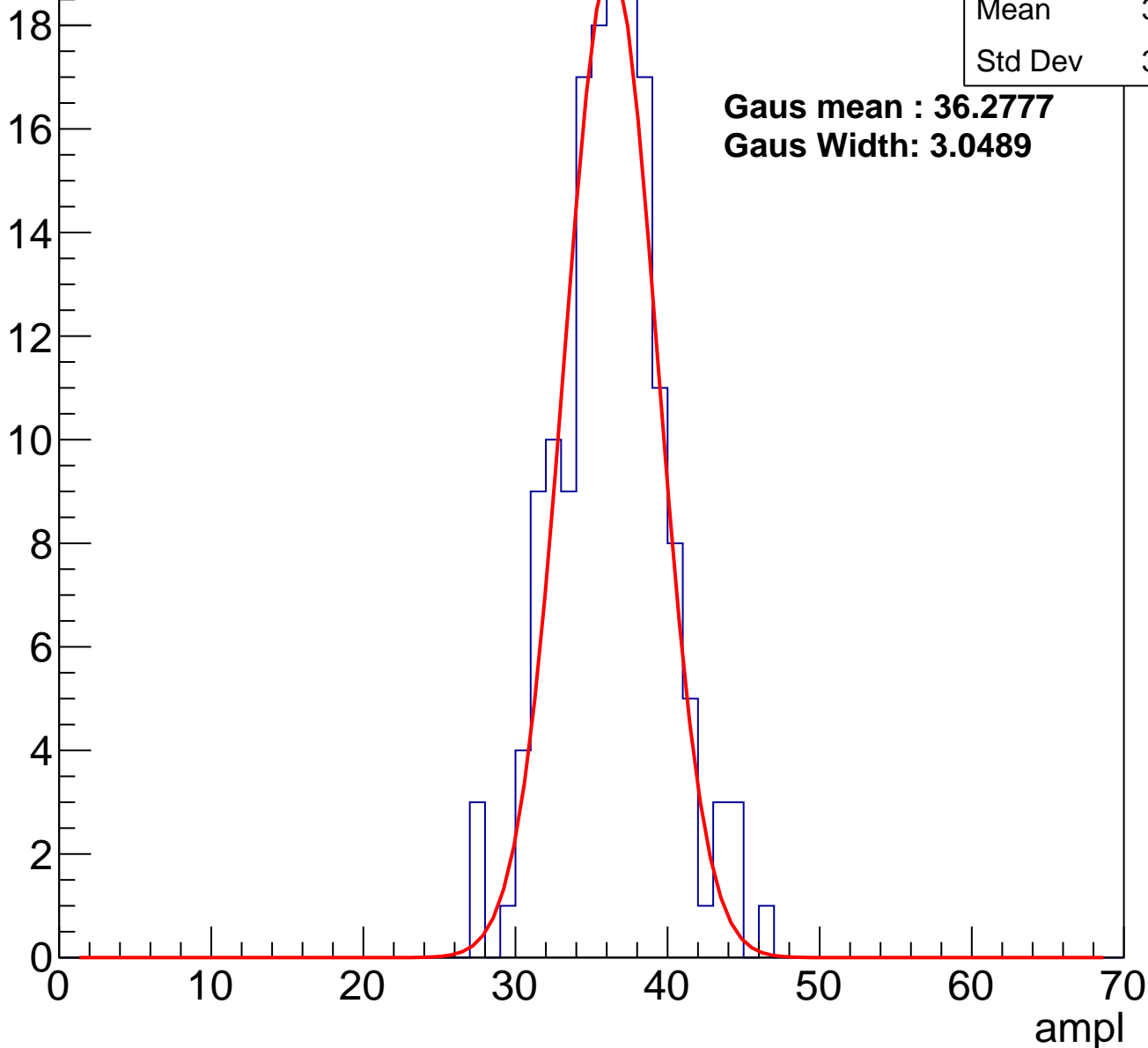
B1L001S, U20-ch48, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	35.89
Std Dev	3.468

Gaus mean : 36.2777
Gaus Width: 3.0489

Entry



B1L001S, U20-ch48, adc2

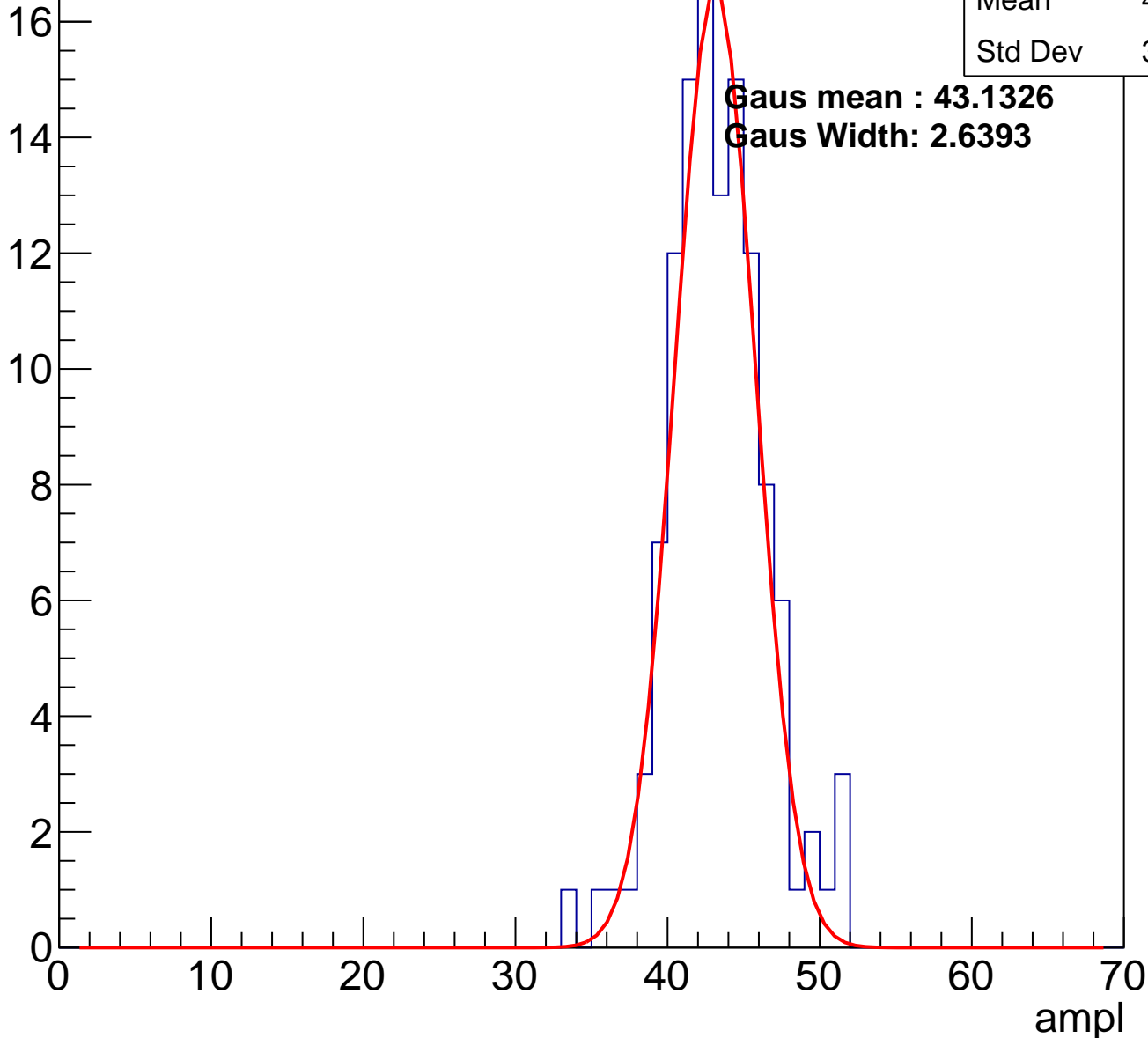
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	119
Mean	42.82
Std Dev	3.157

Gaus mean : 43.1326

Gaus Width: 2.6393



B1L001S, U20-ch48, adc3

calib_packv5_042523_0143.root, FC#2, port C2

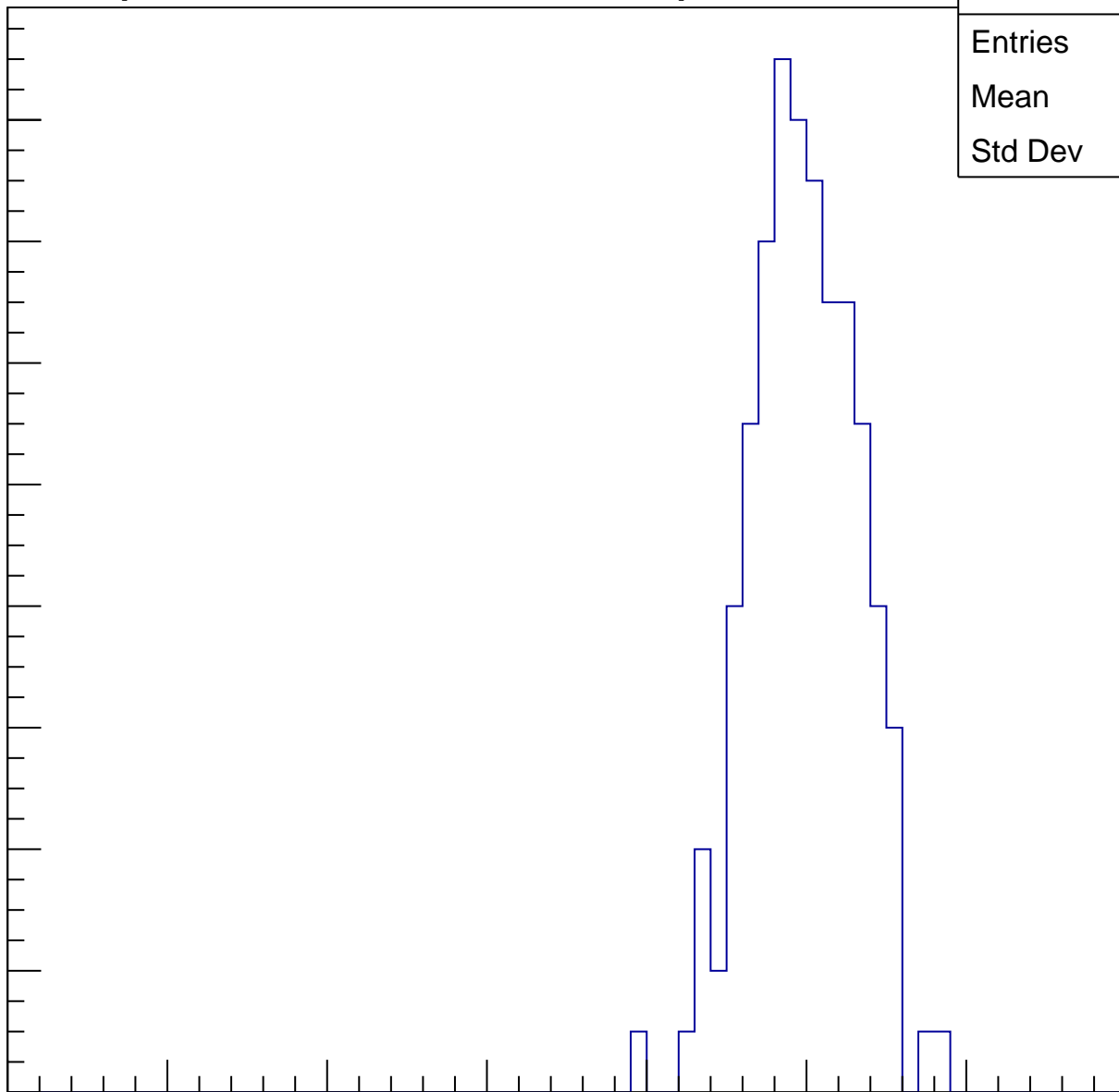
Entries	142
Mean	49.39
Std Dev	3.295

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

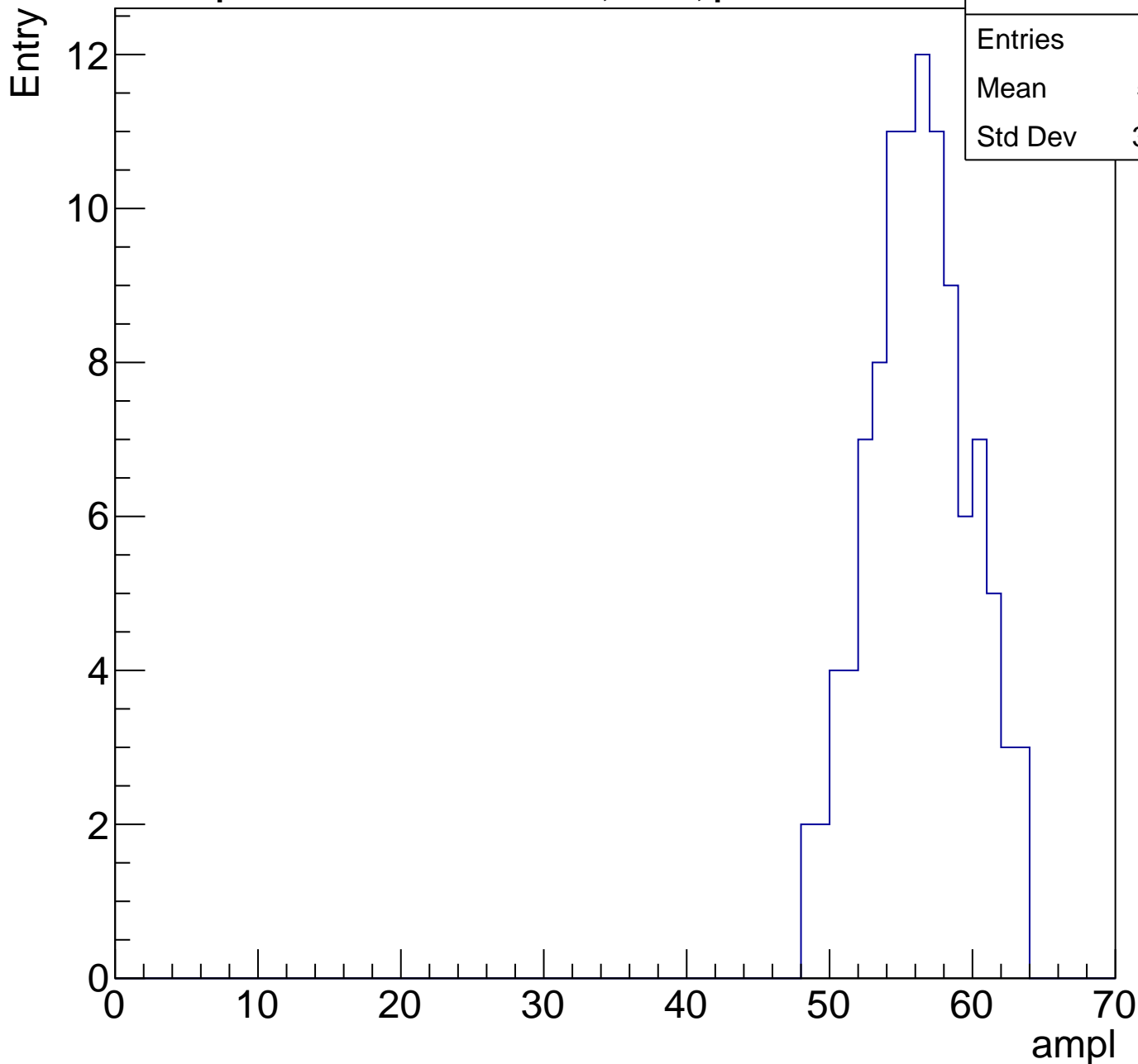
ampl



B1L001S, U20-ch48, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	55.81
Std Dev	3.516



B1L001S, U20-ch48, adc5

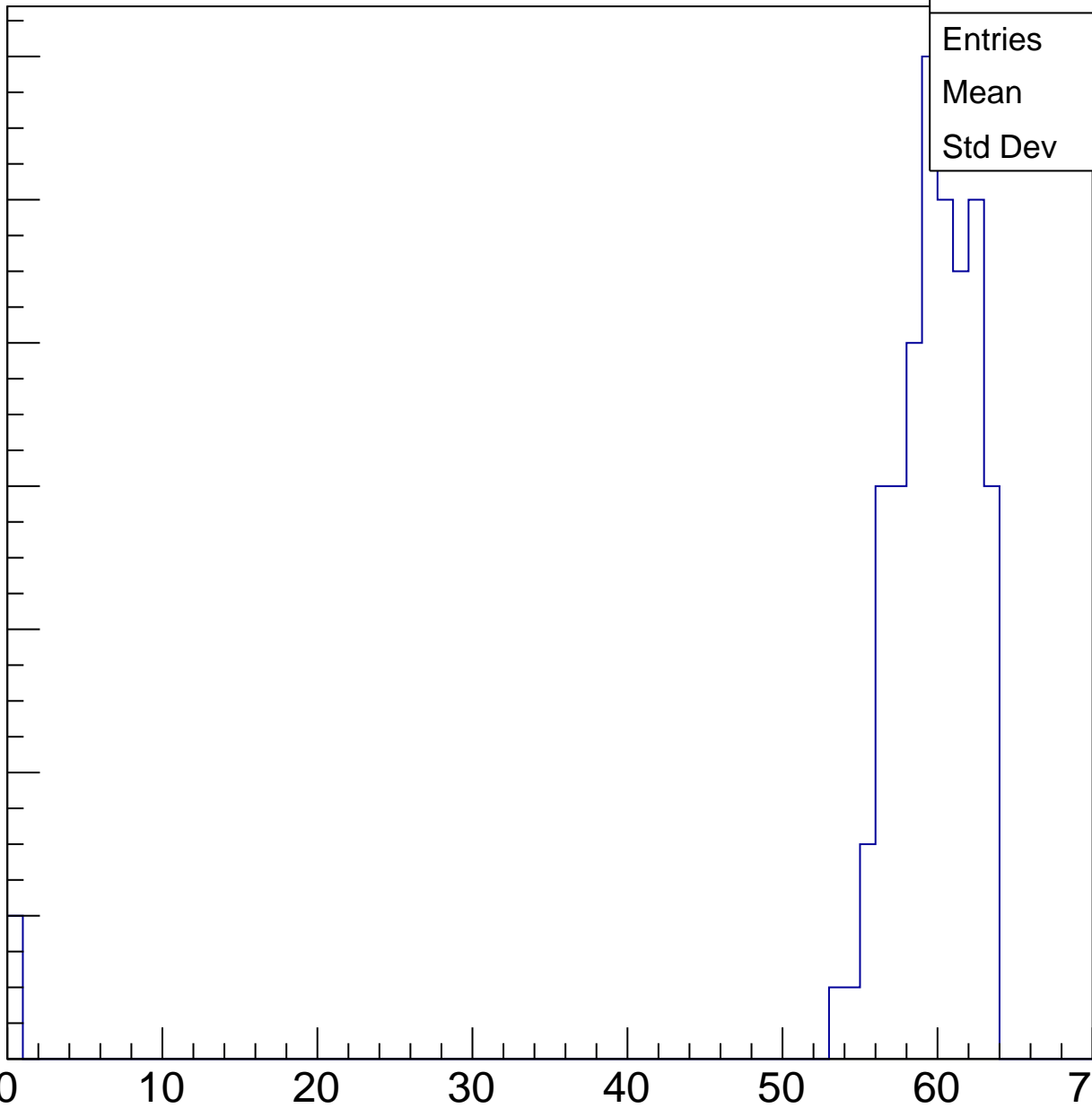
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	58.01
Std Dev	9.061

ampl

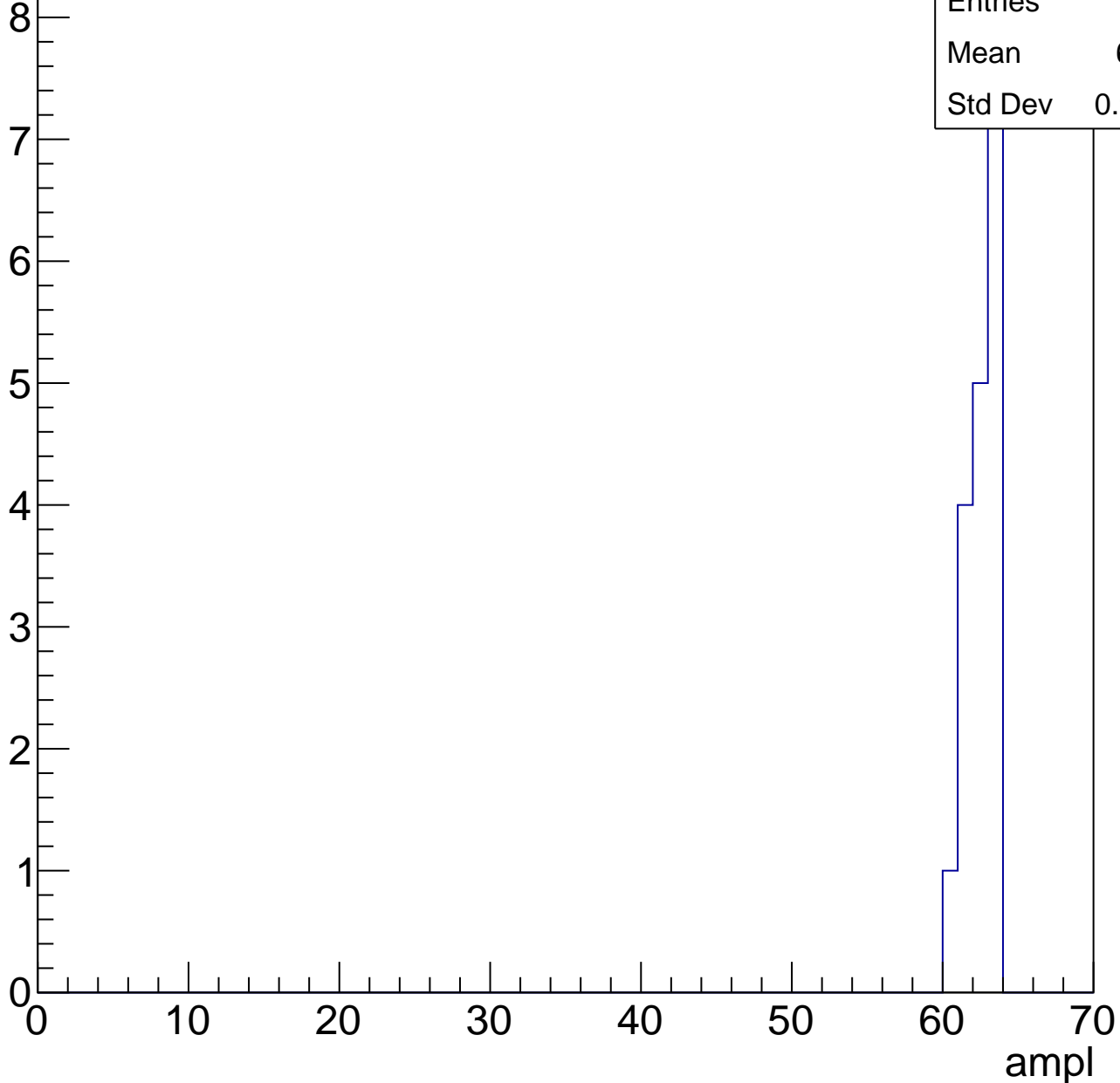


B1L001S, U20-ch48, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	62.11
Std Dev	0.9362



B1L001S, U20-ch48, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch49, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	29.13
Std Dev	3.246

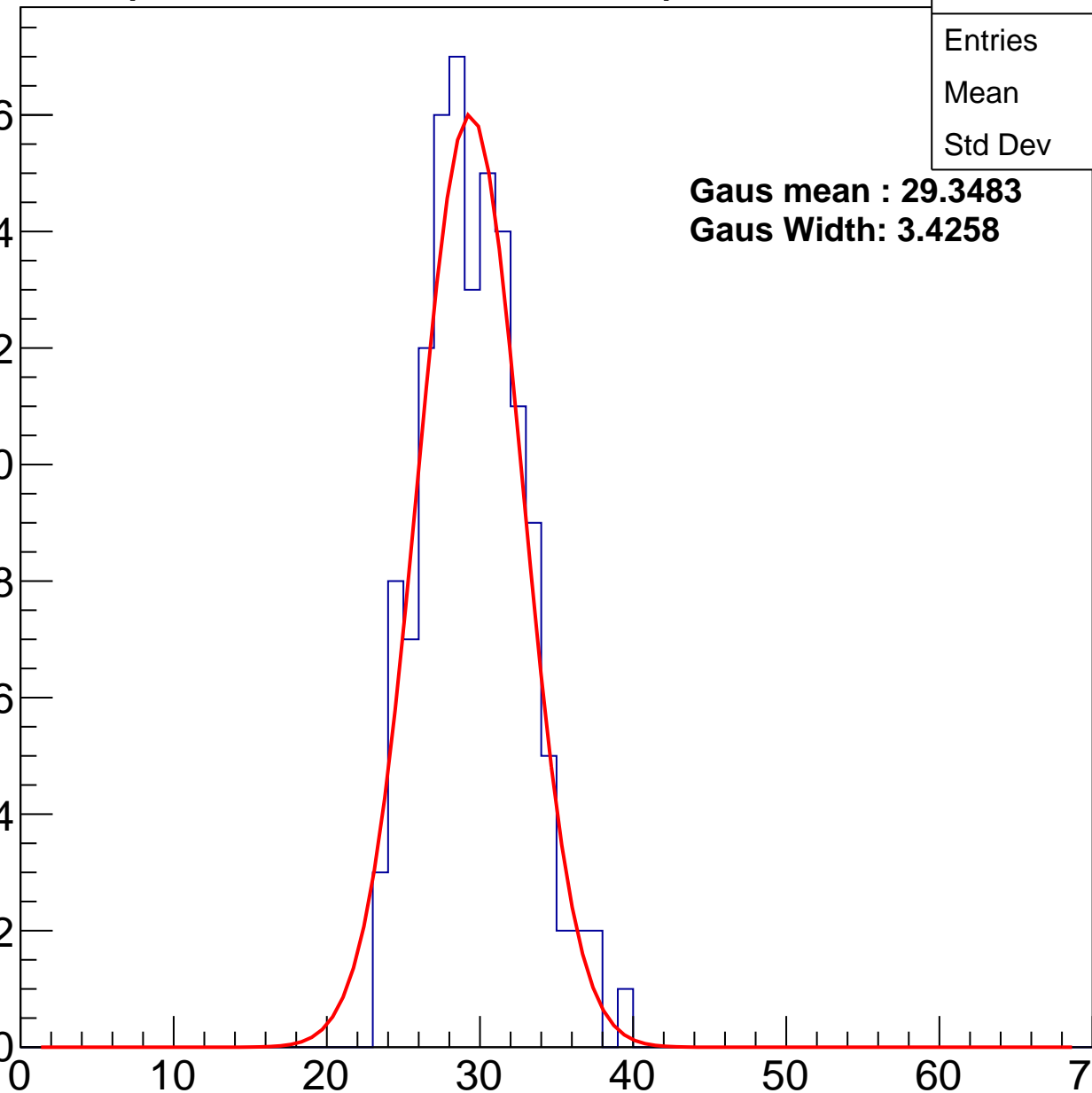
Gaus mean : 29.3483

Gaus Width: 3.4258

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch49, adc1

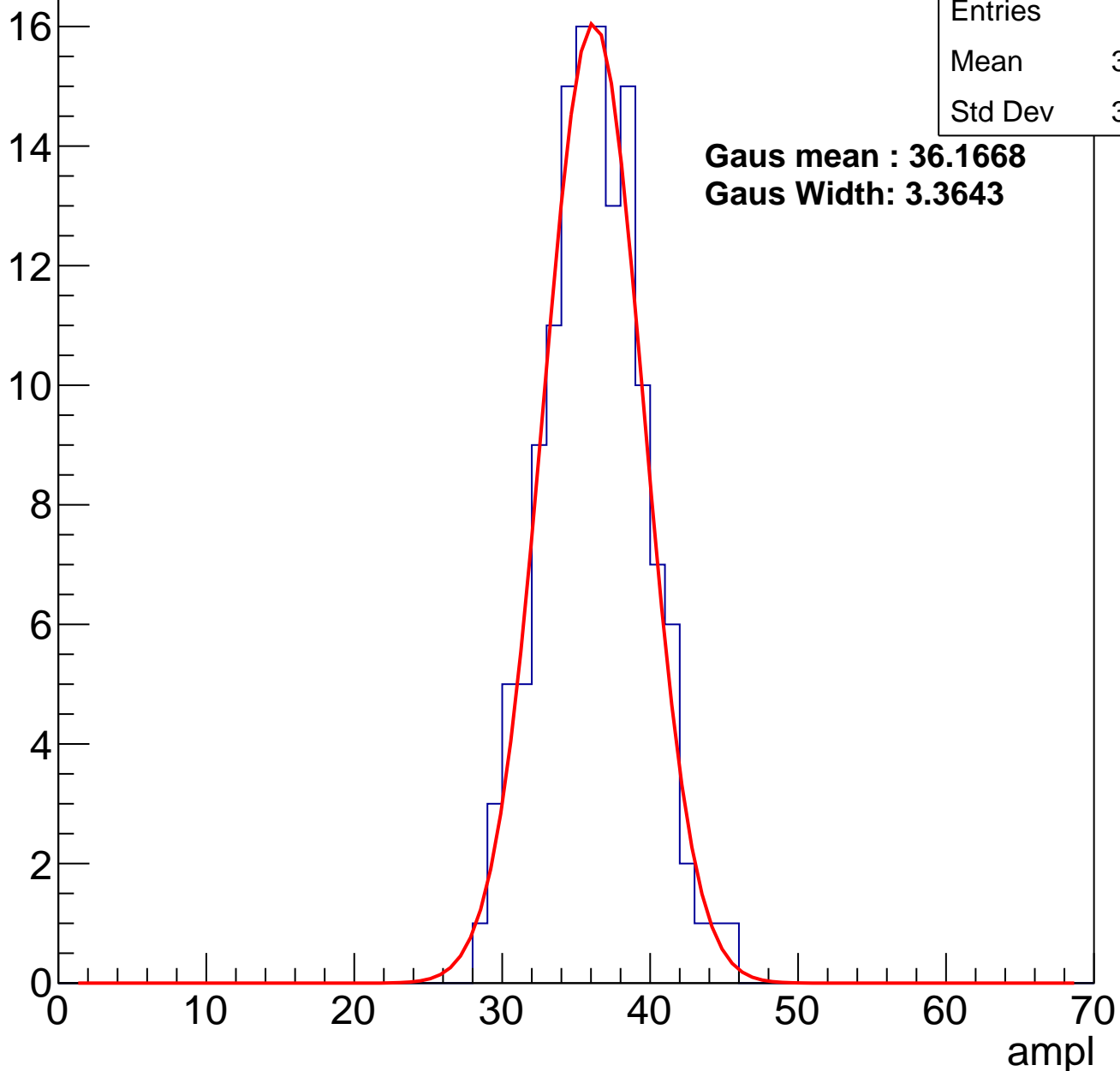
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.77
Std Dev	3.329

Gaus mean : 36.1668

Gaus Width: 3.3643

Entry



B1L001S, U20-ch49, adc2

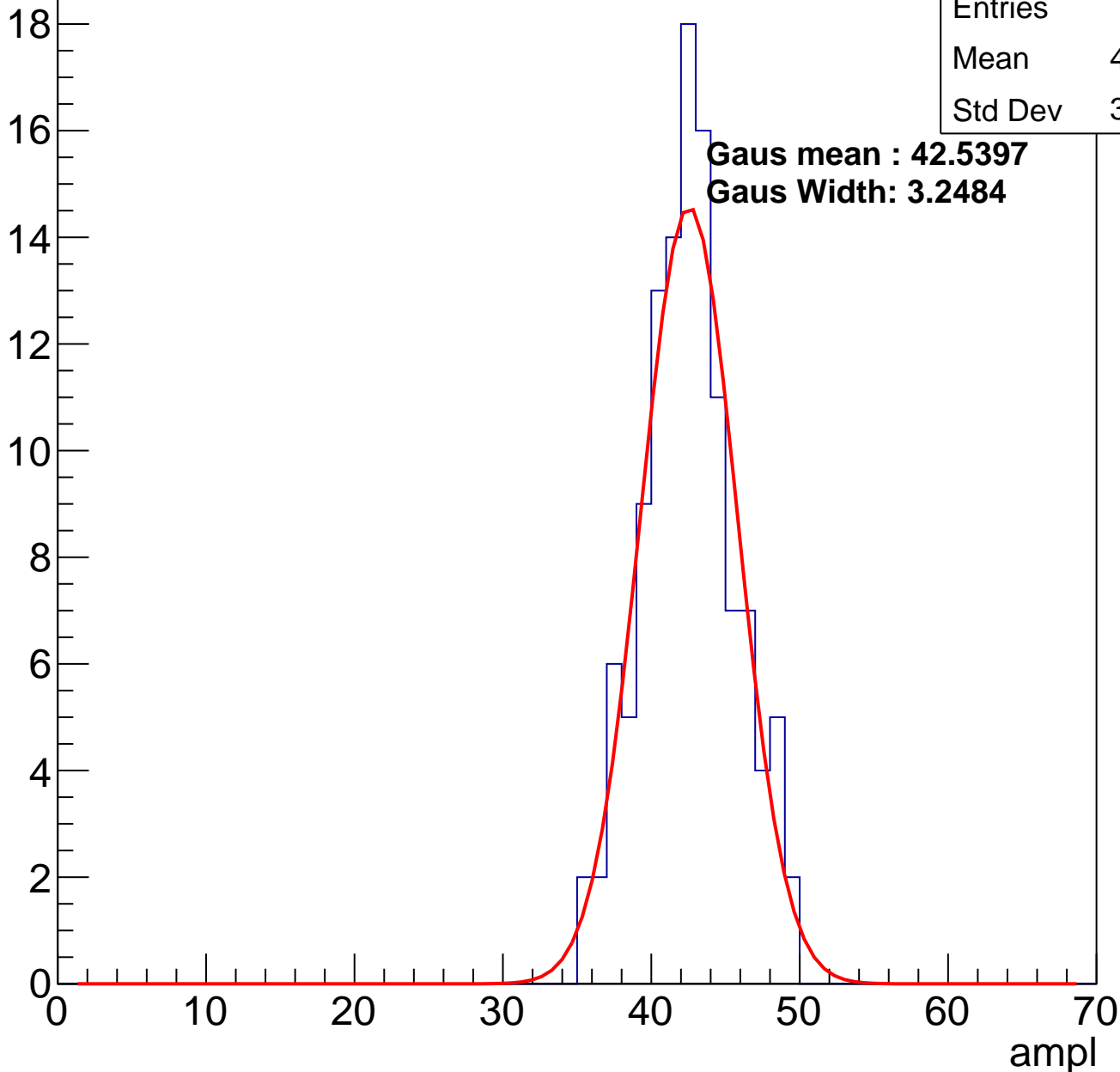
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	42.07
Std Dev	3.096

Gaus mean : 42.5397

Gaus Width: 3.2484

Entry



B1L001S, U20-ch49, adc3

calib_packv5_042523_0143.root, FC#2, port C2

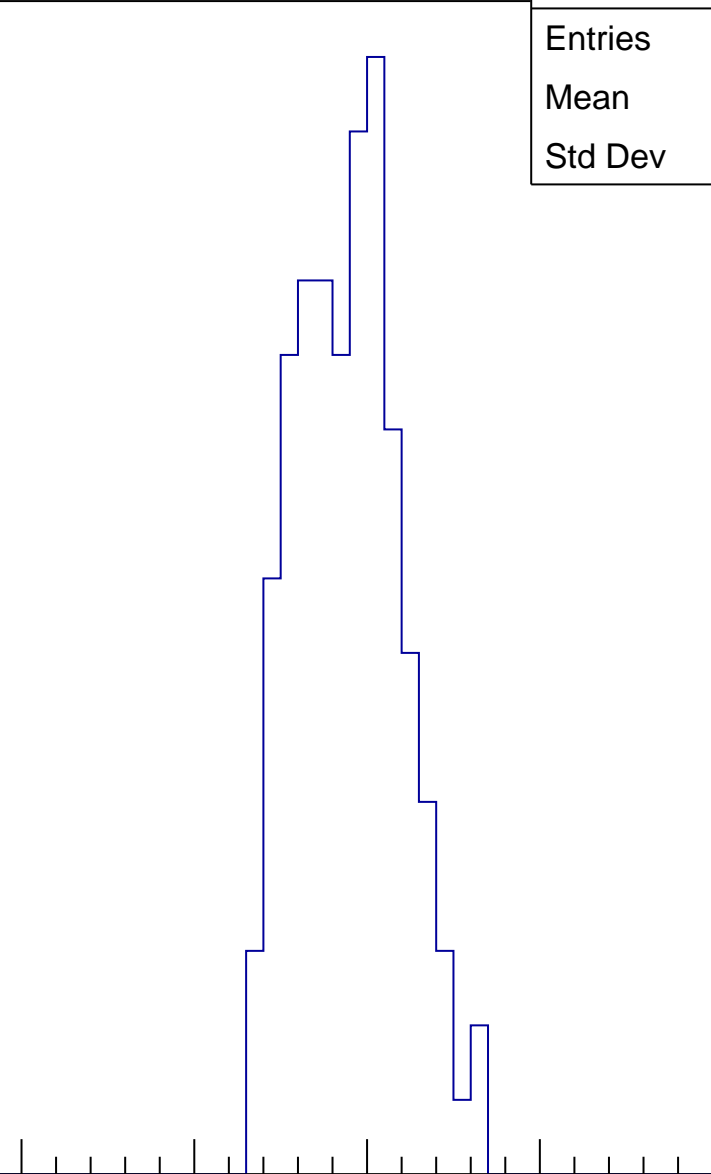
Entries	114
Mean	48.46
Std Dev	2.997

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch49, adc4

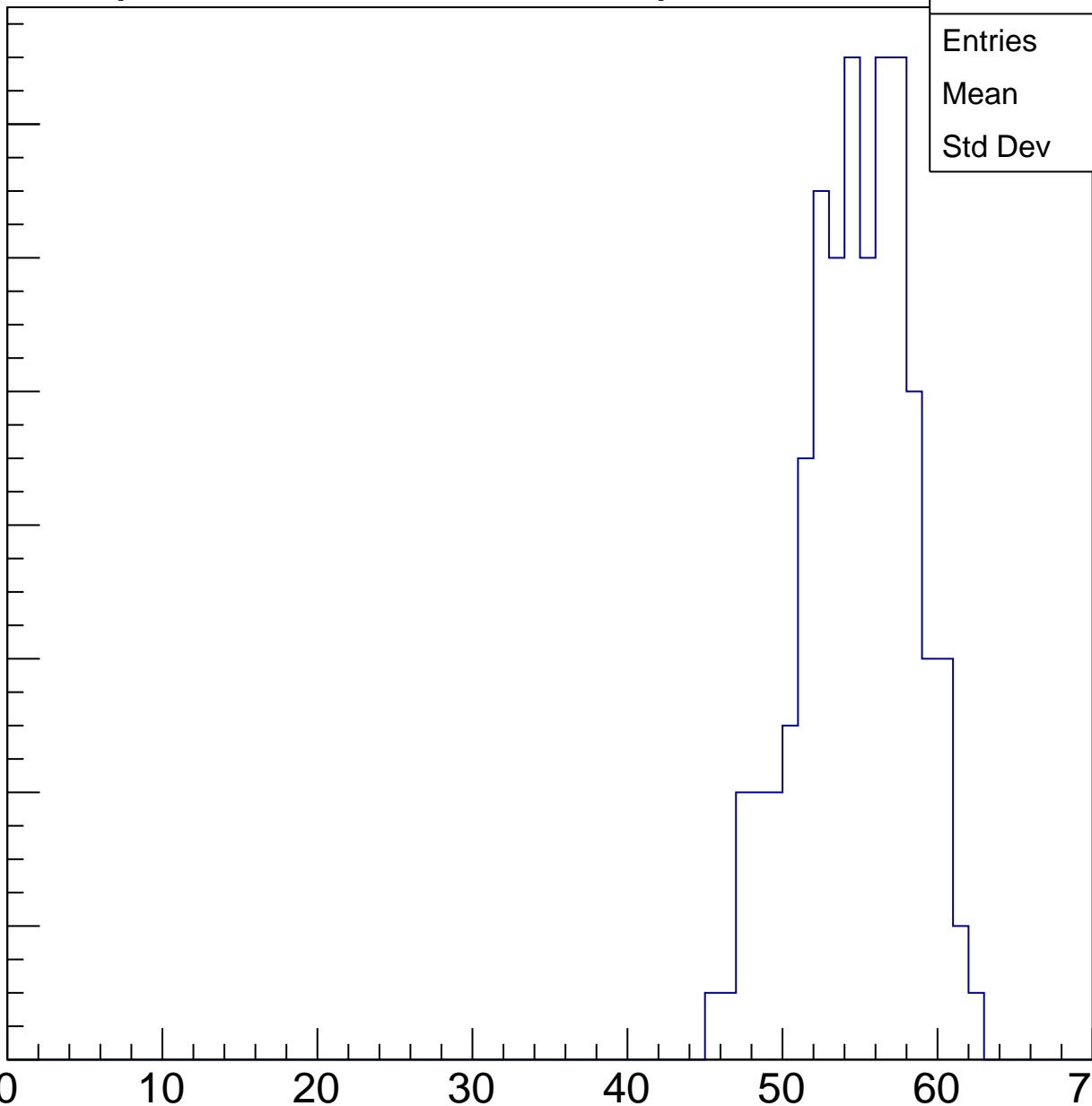
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	135
Mean	54.3
Std Dev	3.541

ampl



B1L001S, U20-ch49, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

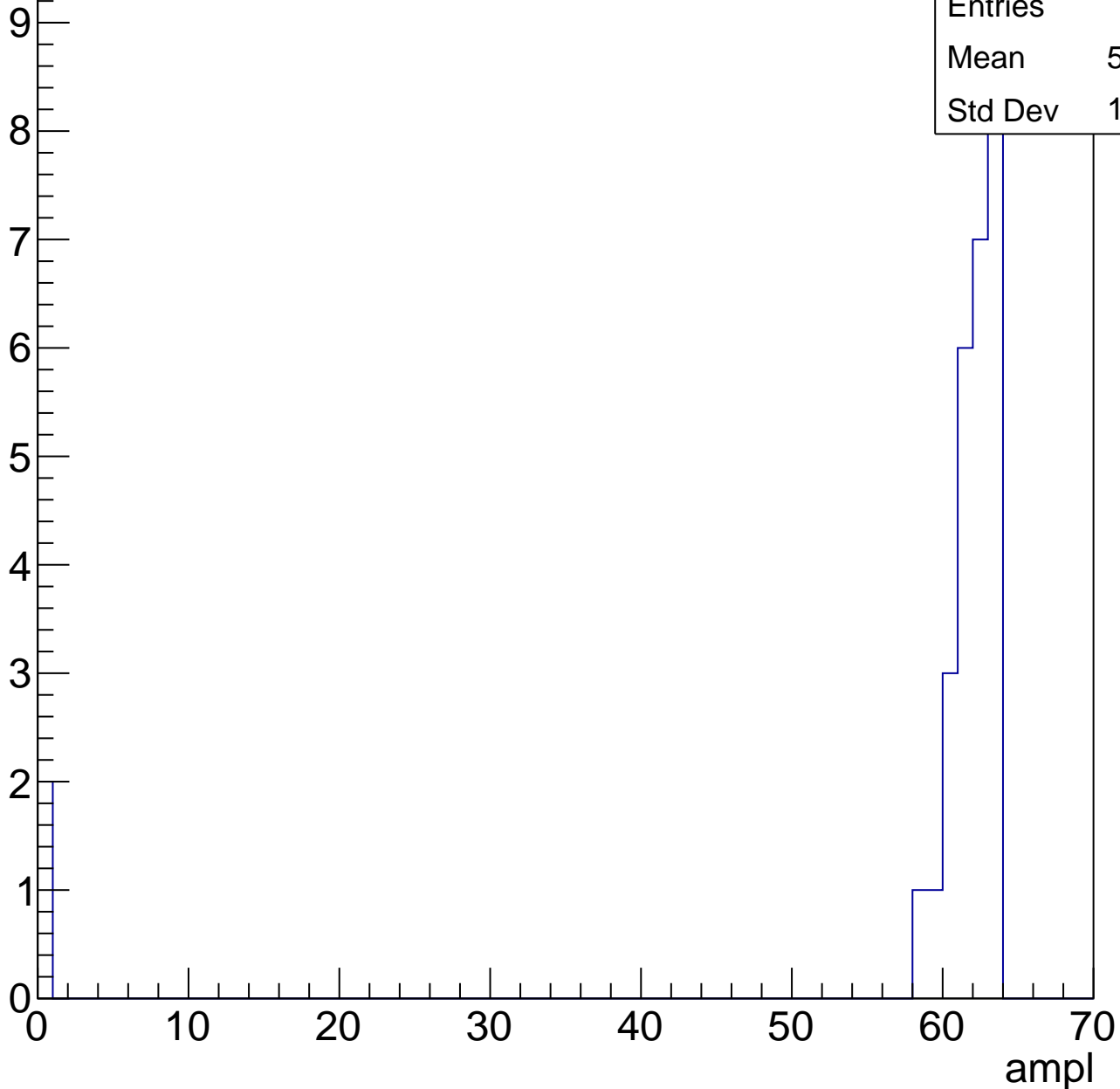
Entries	89
Mean	59.26
Std Dev	2.858

B1L001S, U20-ch49, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

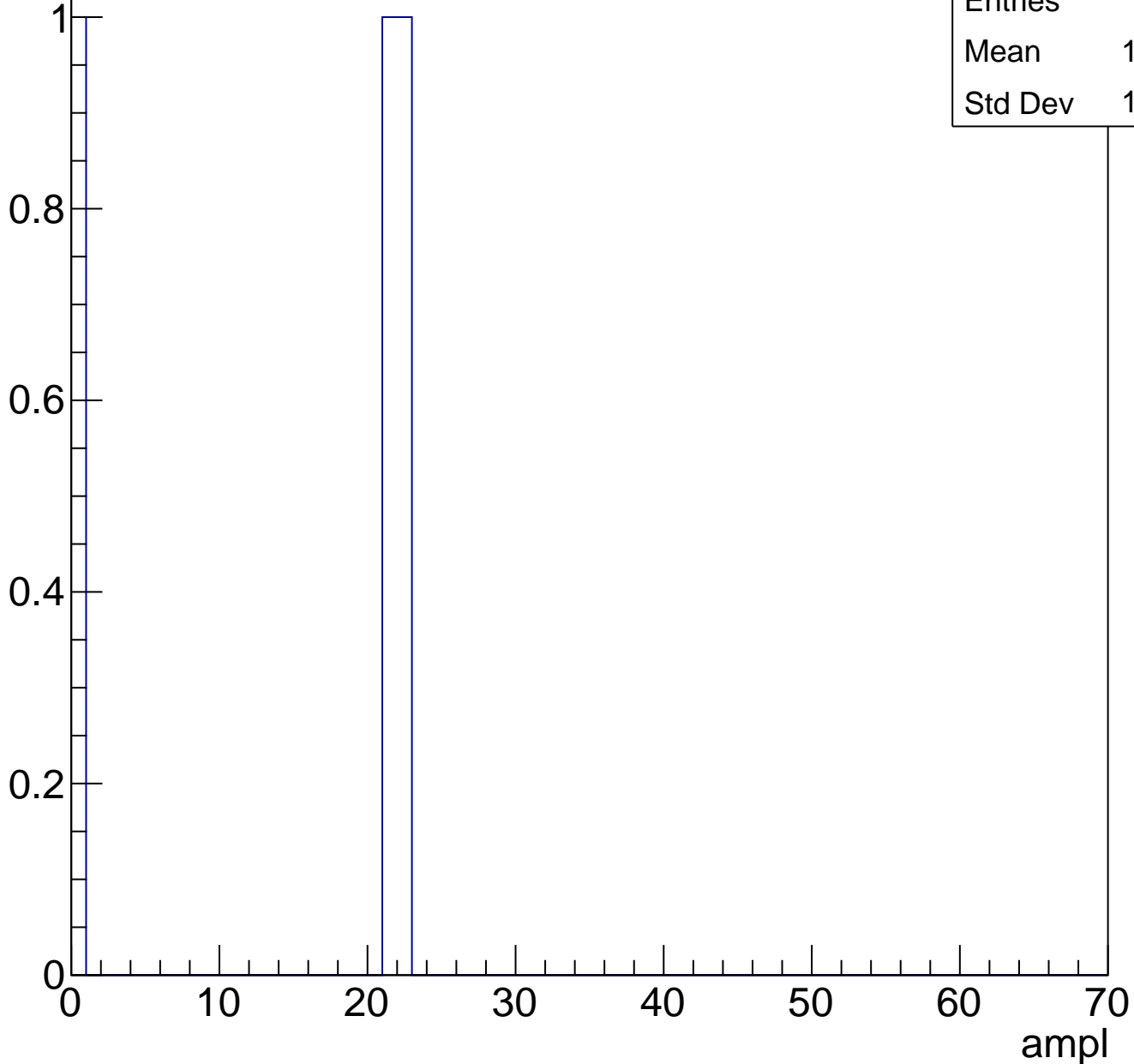
Entries	29
Mean	57.38
Std Dev	15.67



B1L001S, U20-ch49, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	14.33
Std Dev	10.14

B1L001S, U20-ch50, adc0

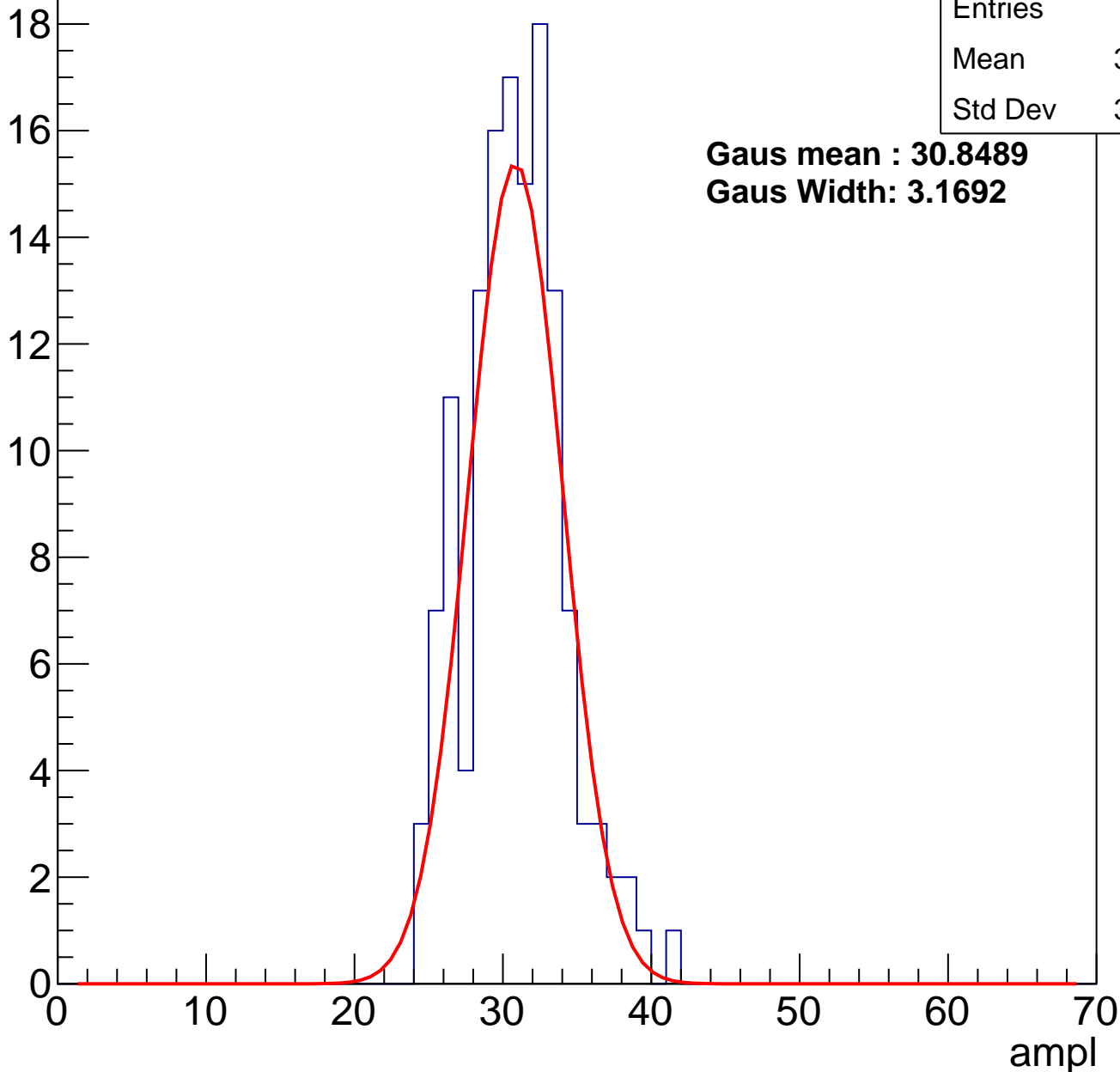
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	30.37
Std Dev	3.289

Gaus mean : 30.8489

Gaus Width: 3.1692

Entry

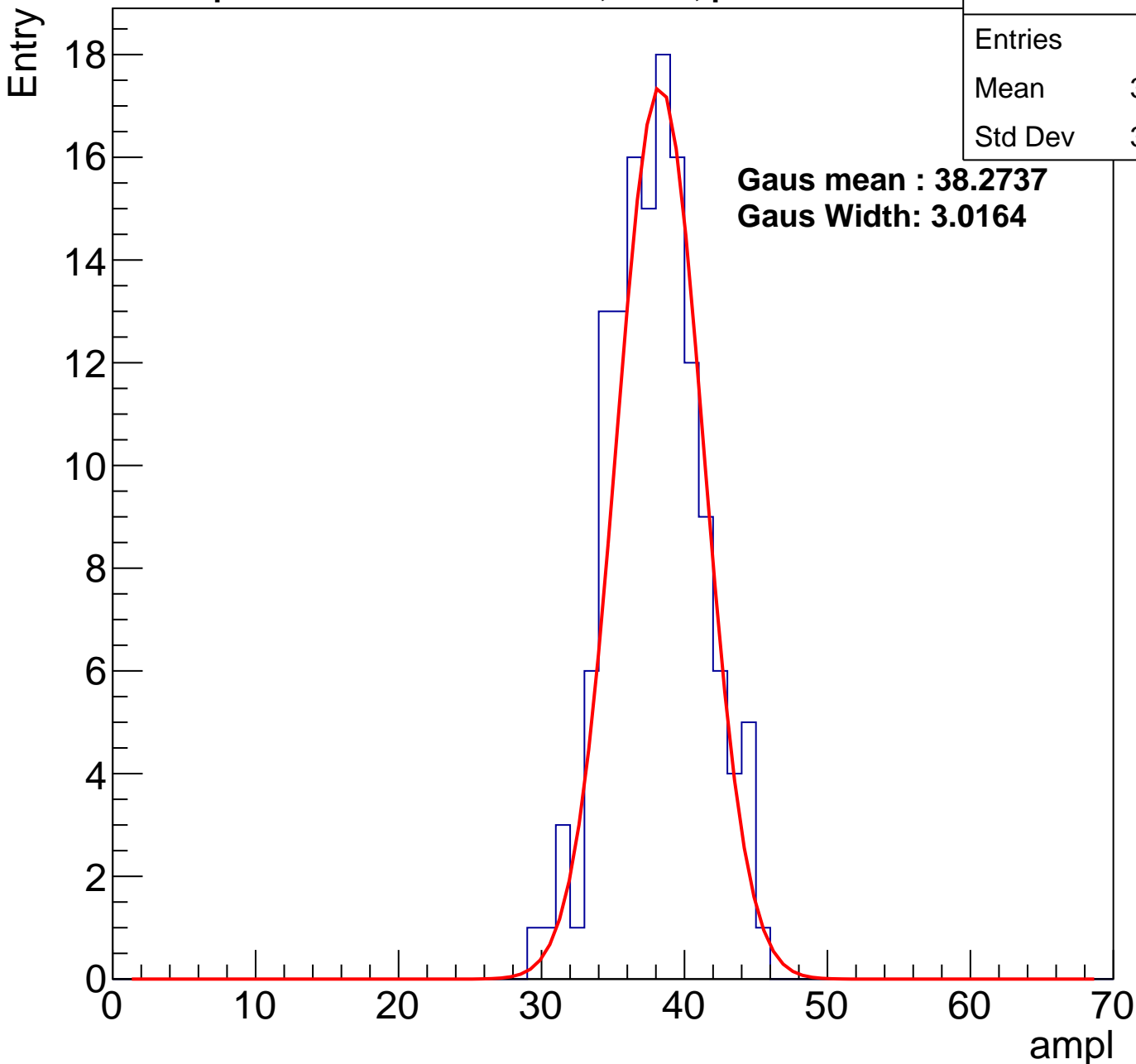


B1L001S, U20-ch50, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	37.54
Std Dev	3.188

Gaus mean : 38.2737
Gaus Width: 3.0164



B1L001S, U20-ch50, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	43.64
Std Dev	3.488

Gaus mean : 44.4506

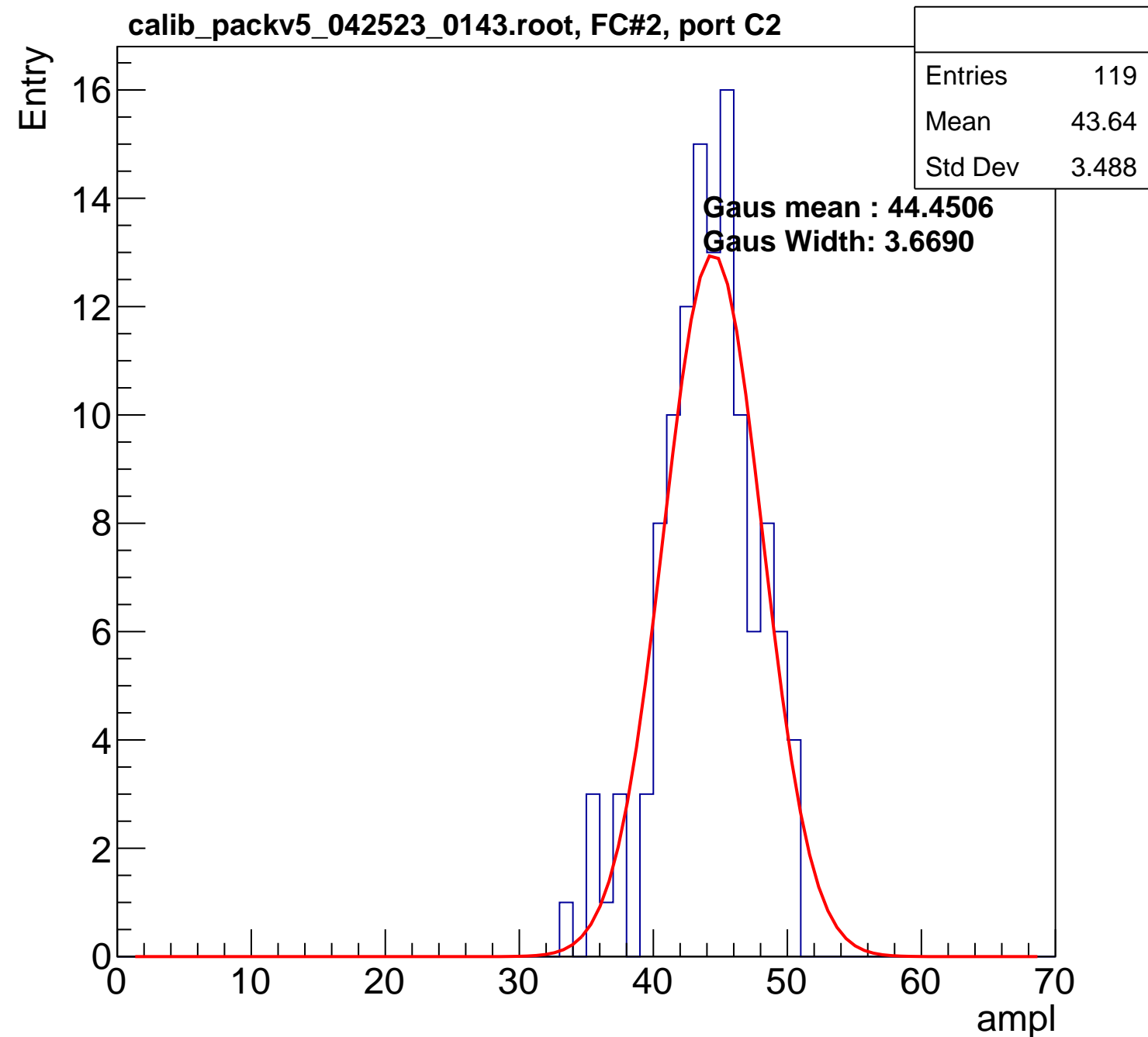
Gaus Width: 3.6690

Entry

16
14
12
10
8
6
4
2
0

ampl

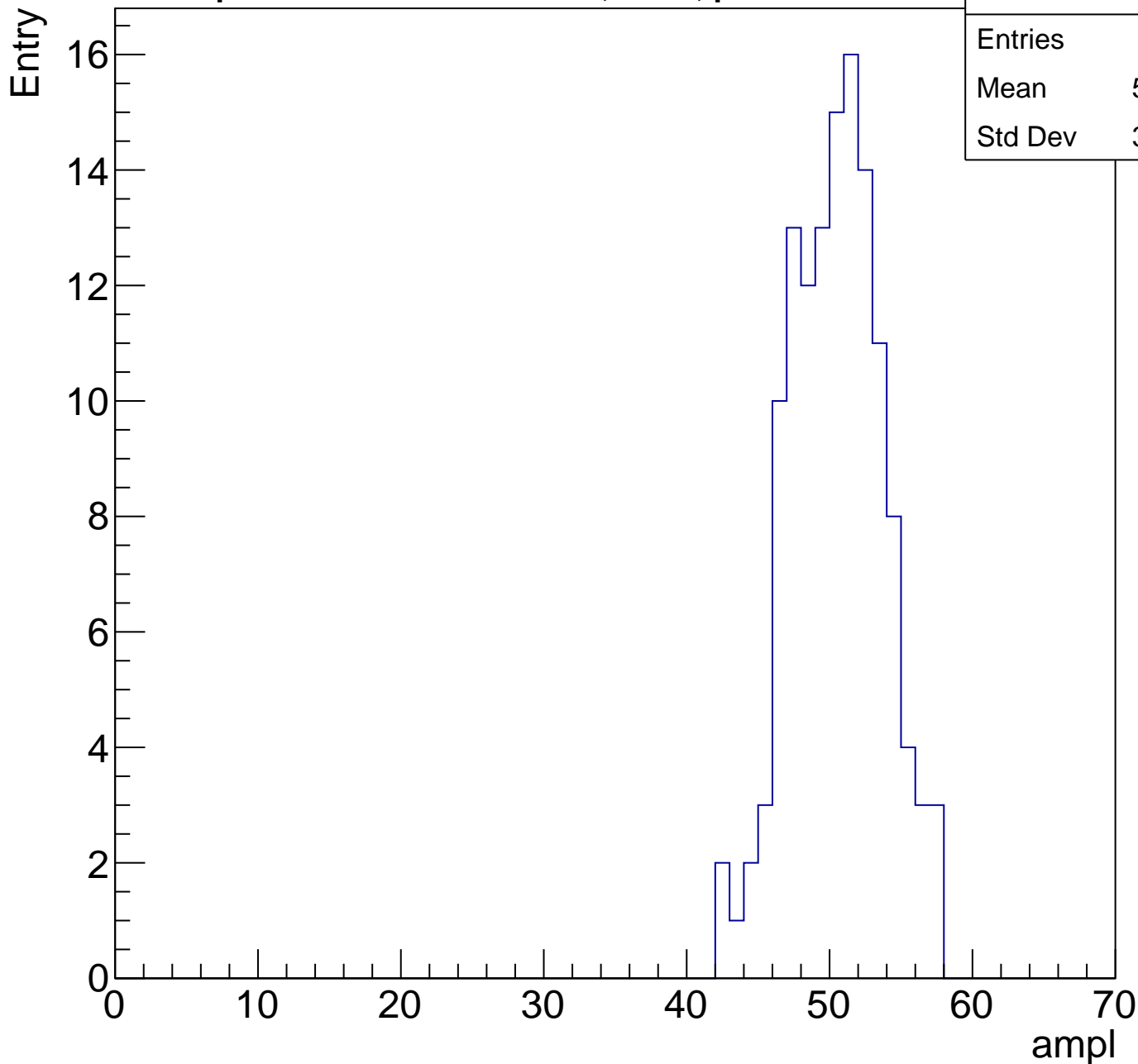
0 10 20 30 40 50 60 70



B1L001S, U20-ch50, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	50.02
Std Dev	3.182



B1L001S, U20-ch50, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

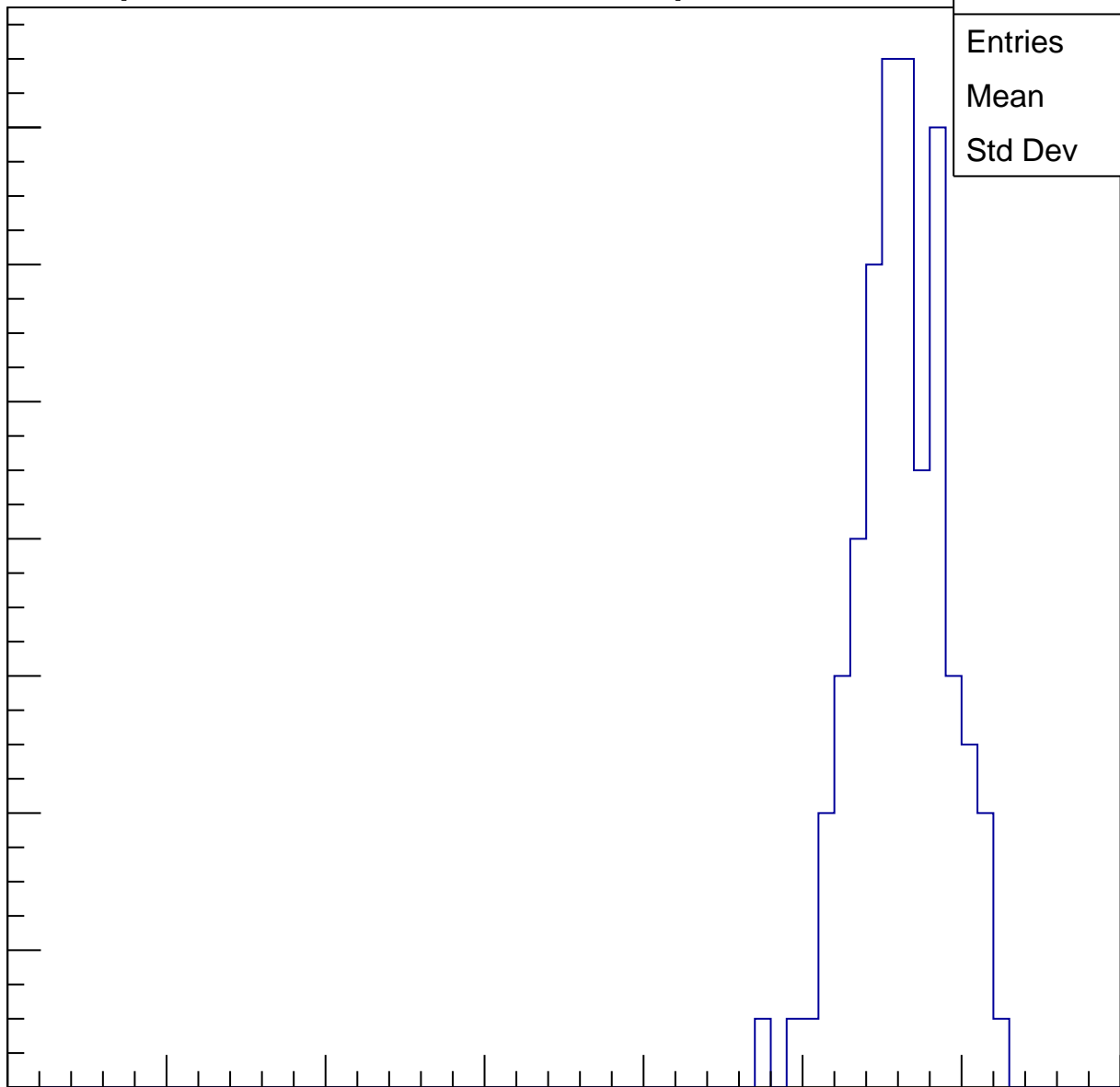
50

60

70

ampl

Entries	102
Mean	55.73
Std Dev	2.843



B1L001S, U20-ch50, adc5

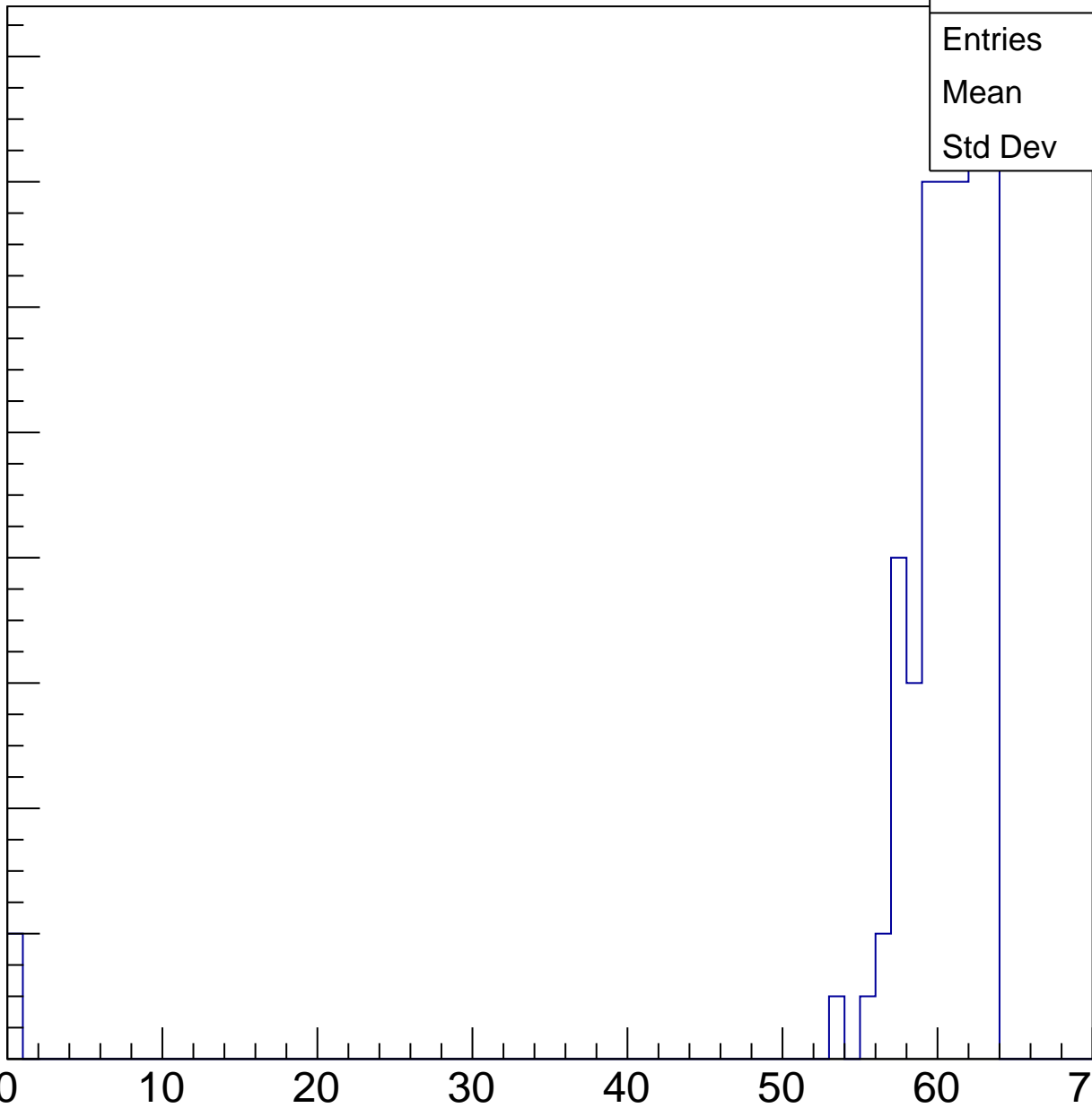
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.95
Std Dev	8.999

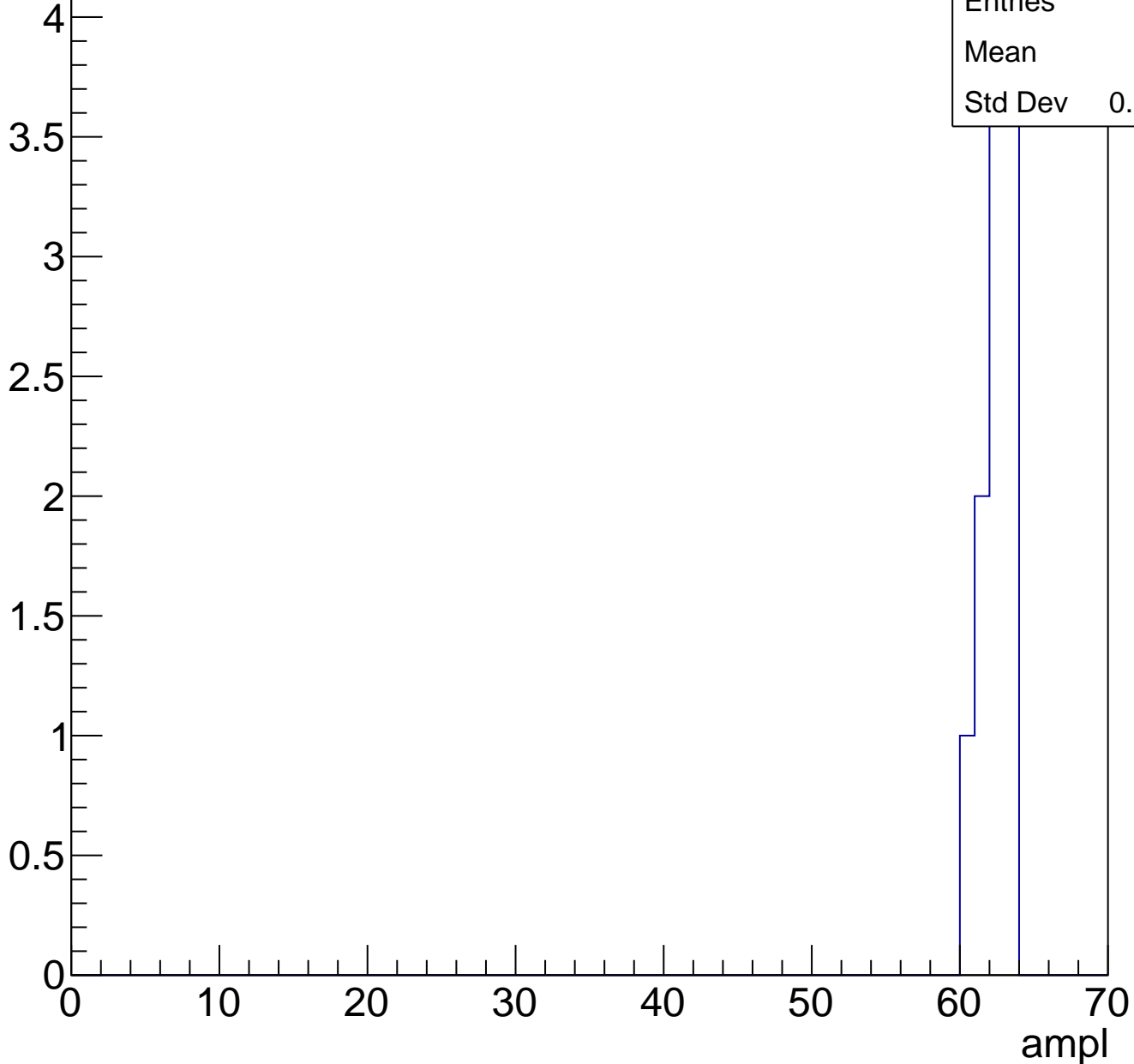
ampl



B1L001S, U20-ch50, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch50, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

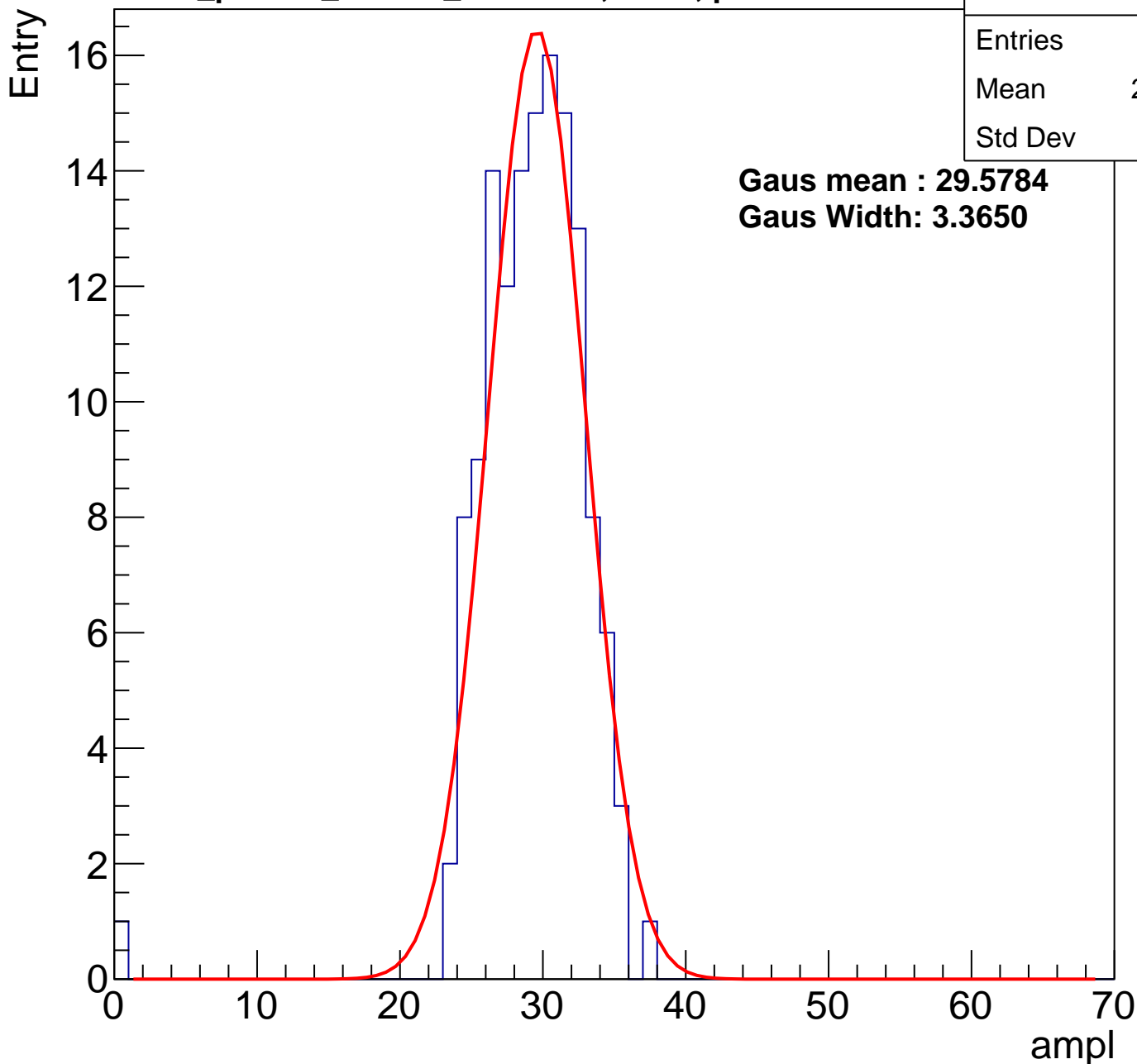
B1L001S, U20-ch51, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	28.82
Std Dev	3.9

Gaus mean : 29.5784

Gaus Width: 3.3650



B1L001S, U20-ch51, adc1

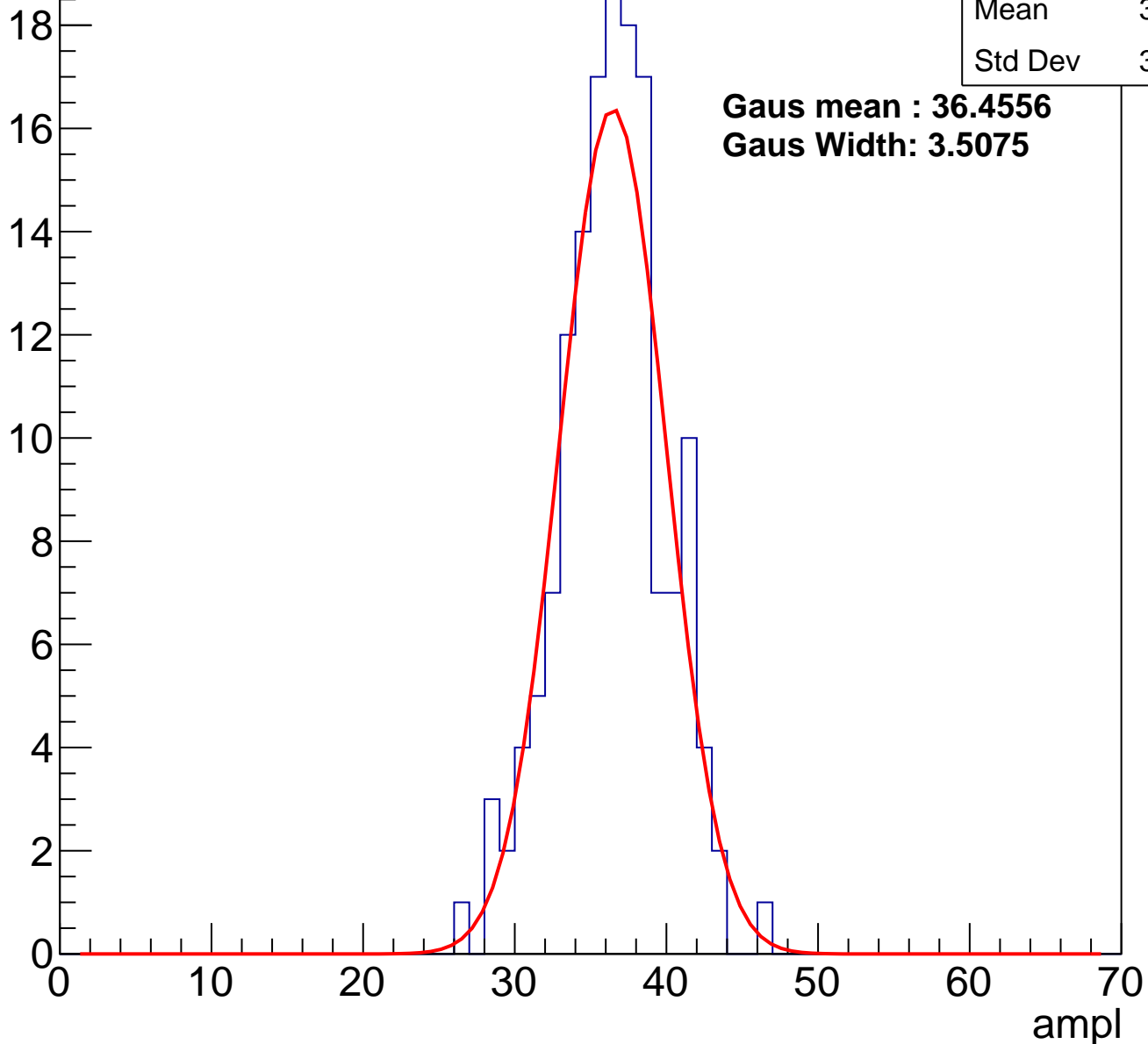
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	35.95
Std Dev	3.469

Gaus mean : 36.4556

Gaus Width: 3.5075

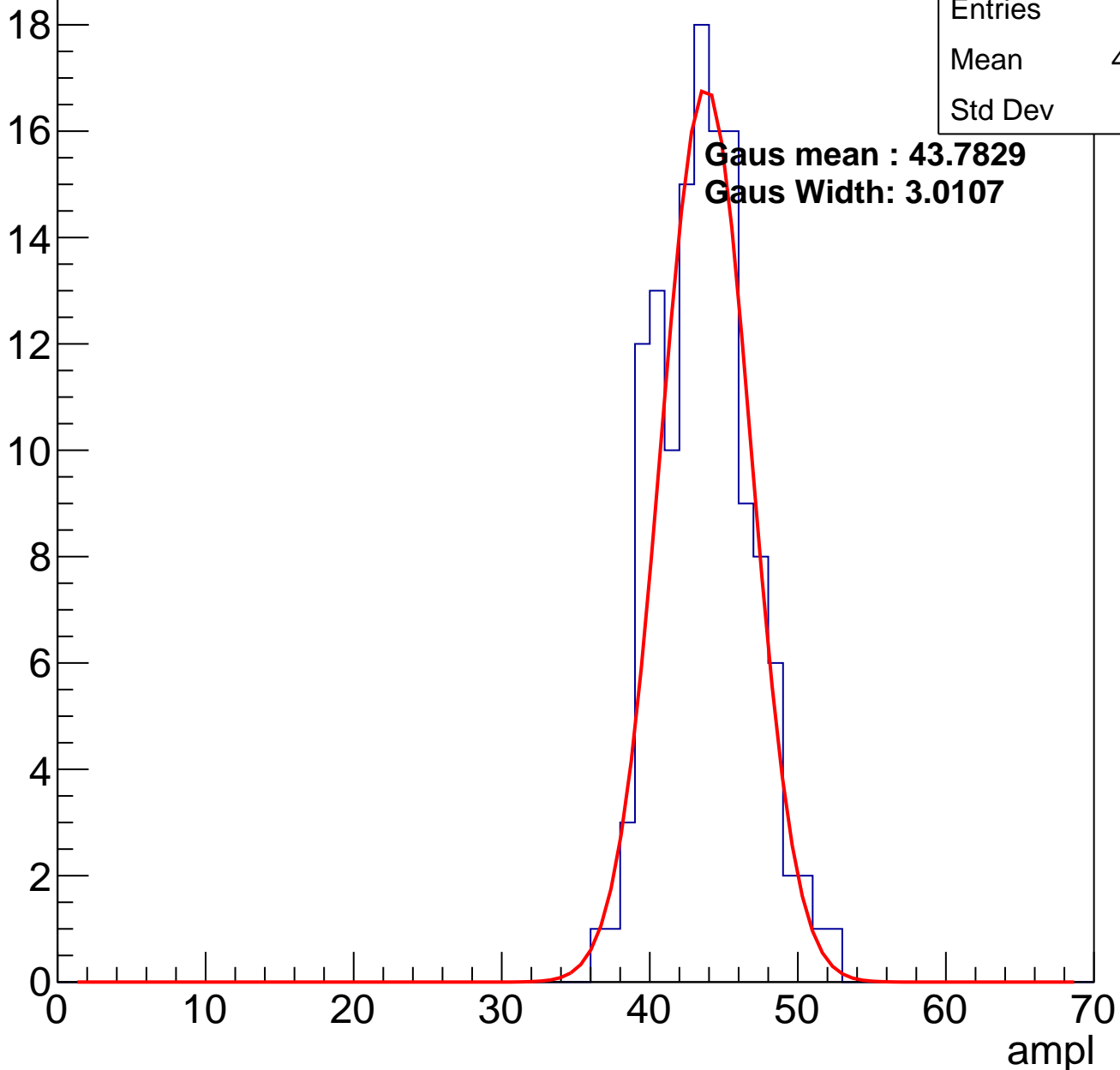
Entry



B1L001S, U20-ch51, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch51, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	49.61
Std Dev	3.09

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

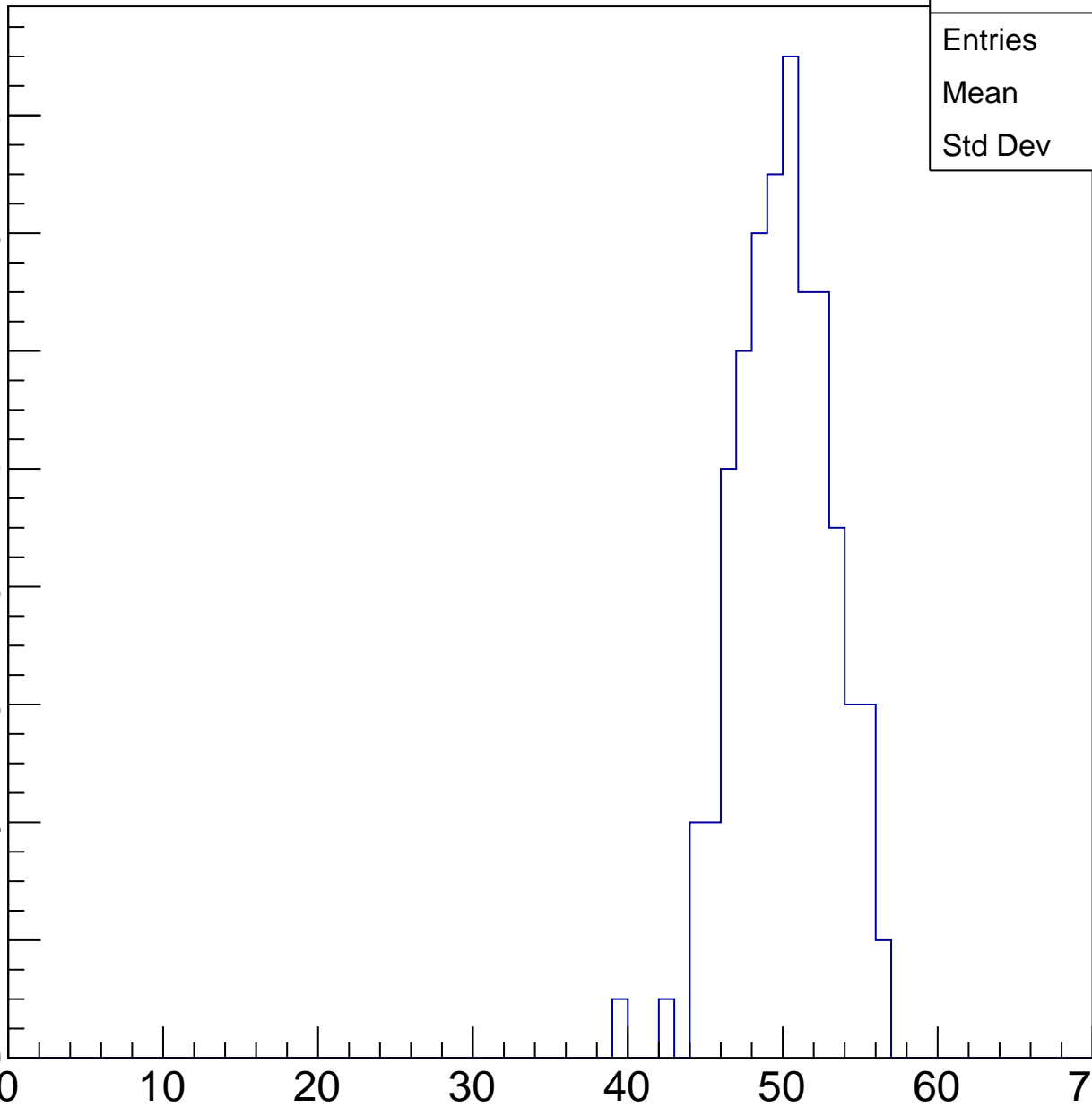
40

50

60

70

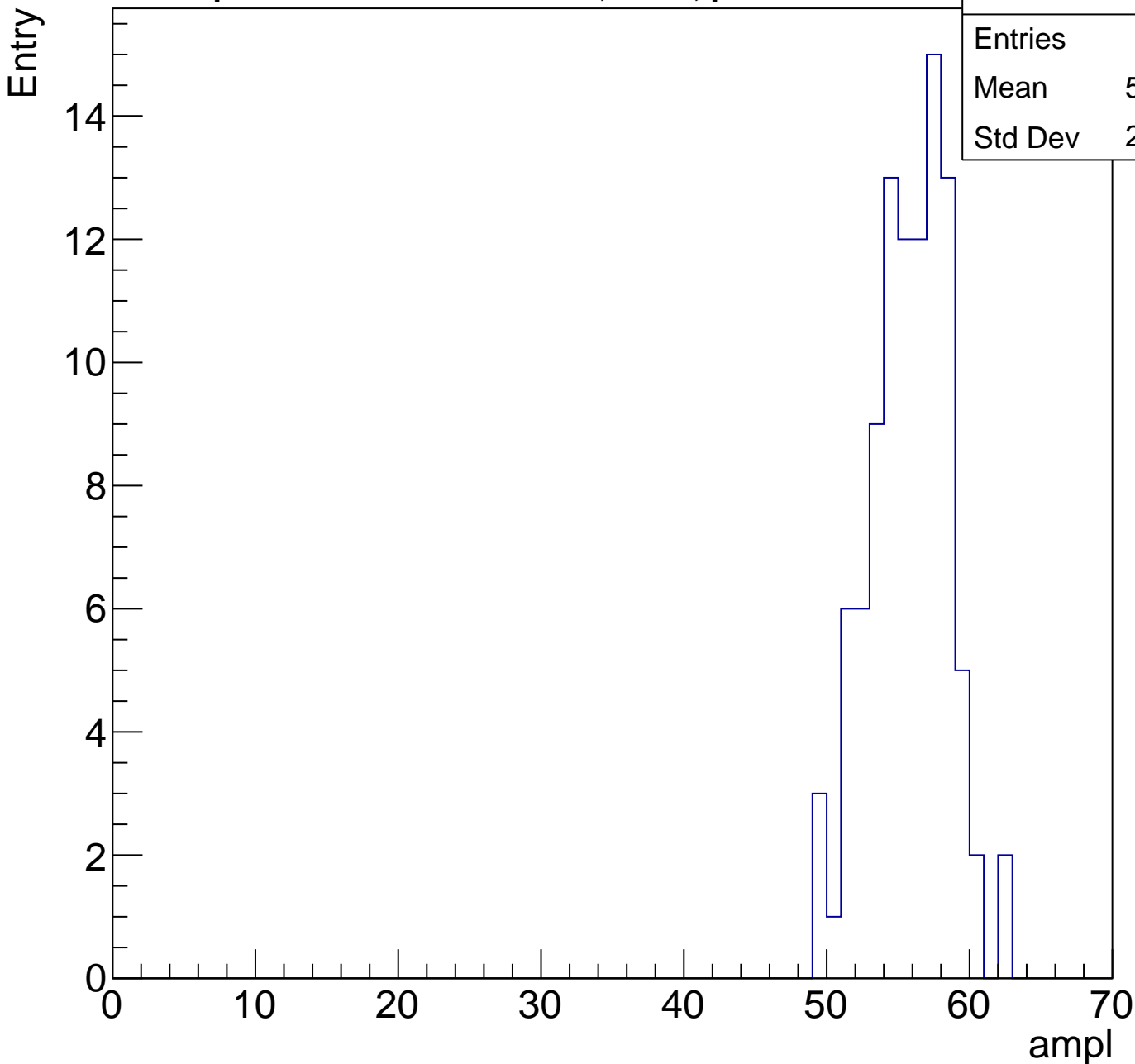
ampl



B1L001S, U20-ch51, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	99
Mean	55.29
Std Dev	2.735



B1L001S, U20-ch51, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	58.77
Std Dev	8.597

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

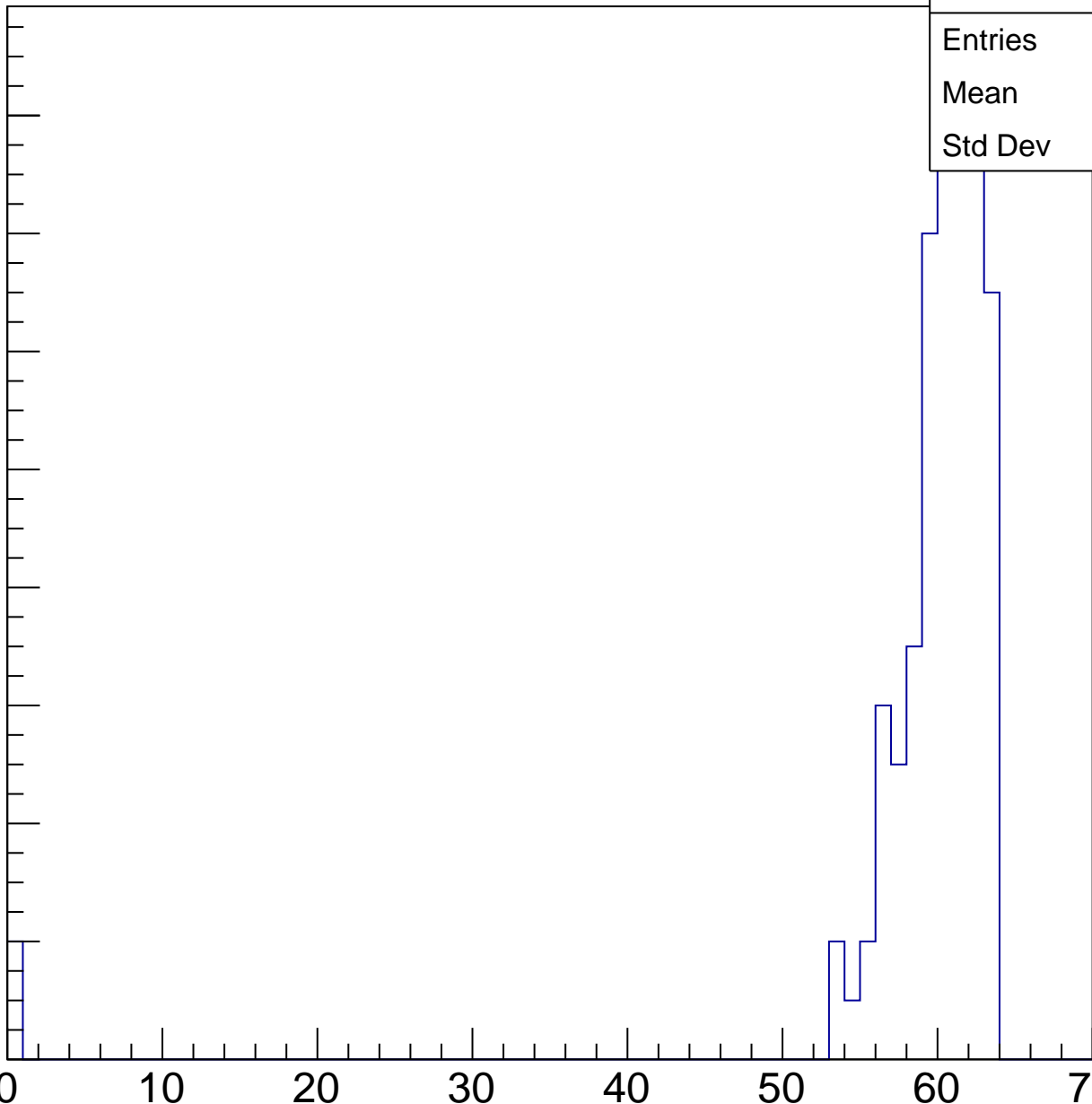
40

50

60

70

ampl

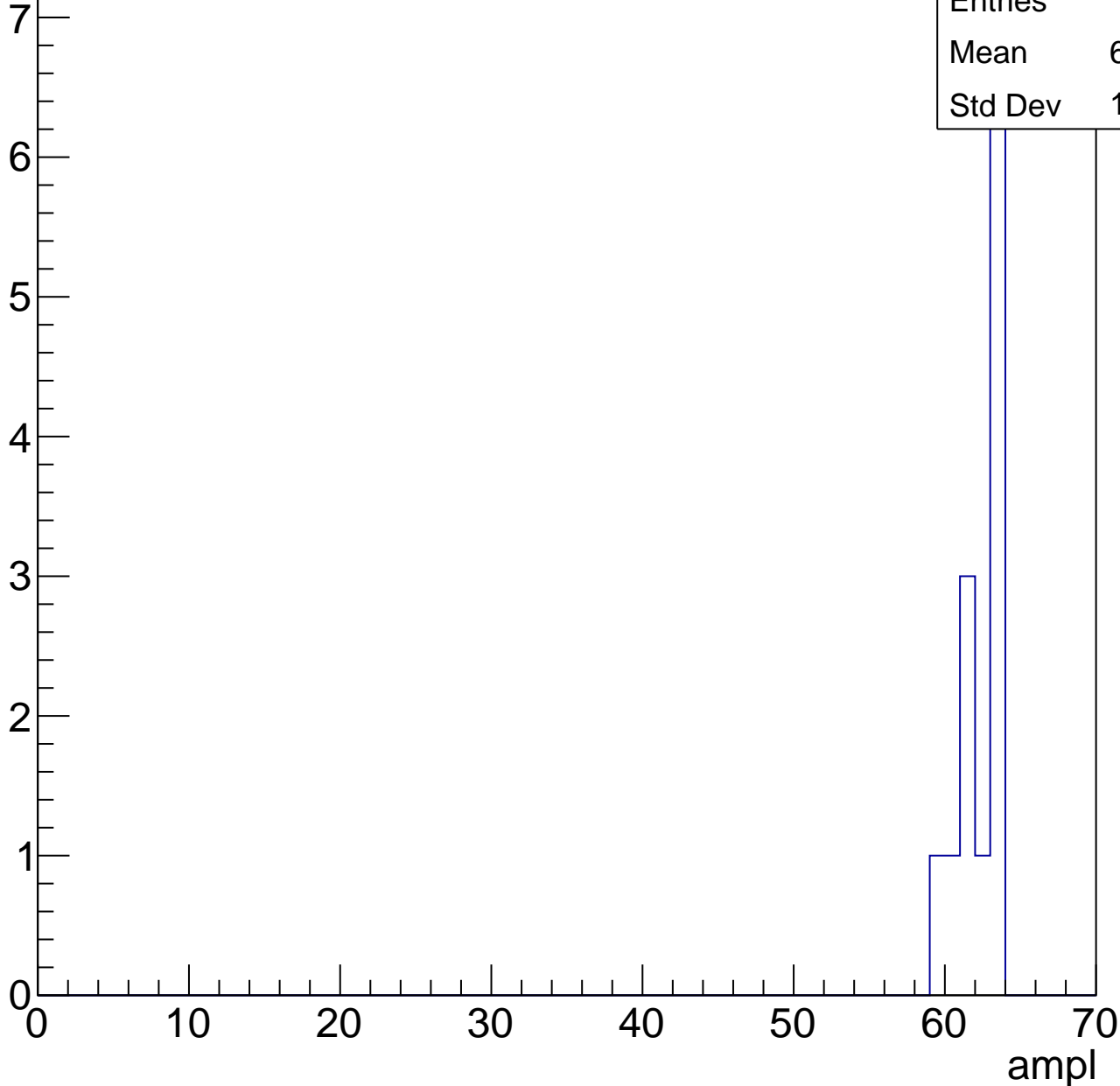


B1L001S, U20-ch51, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

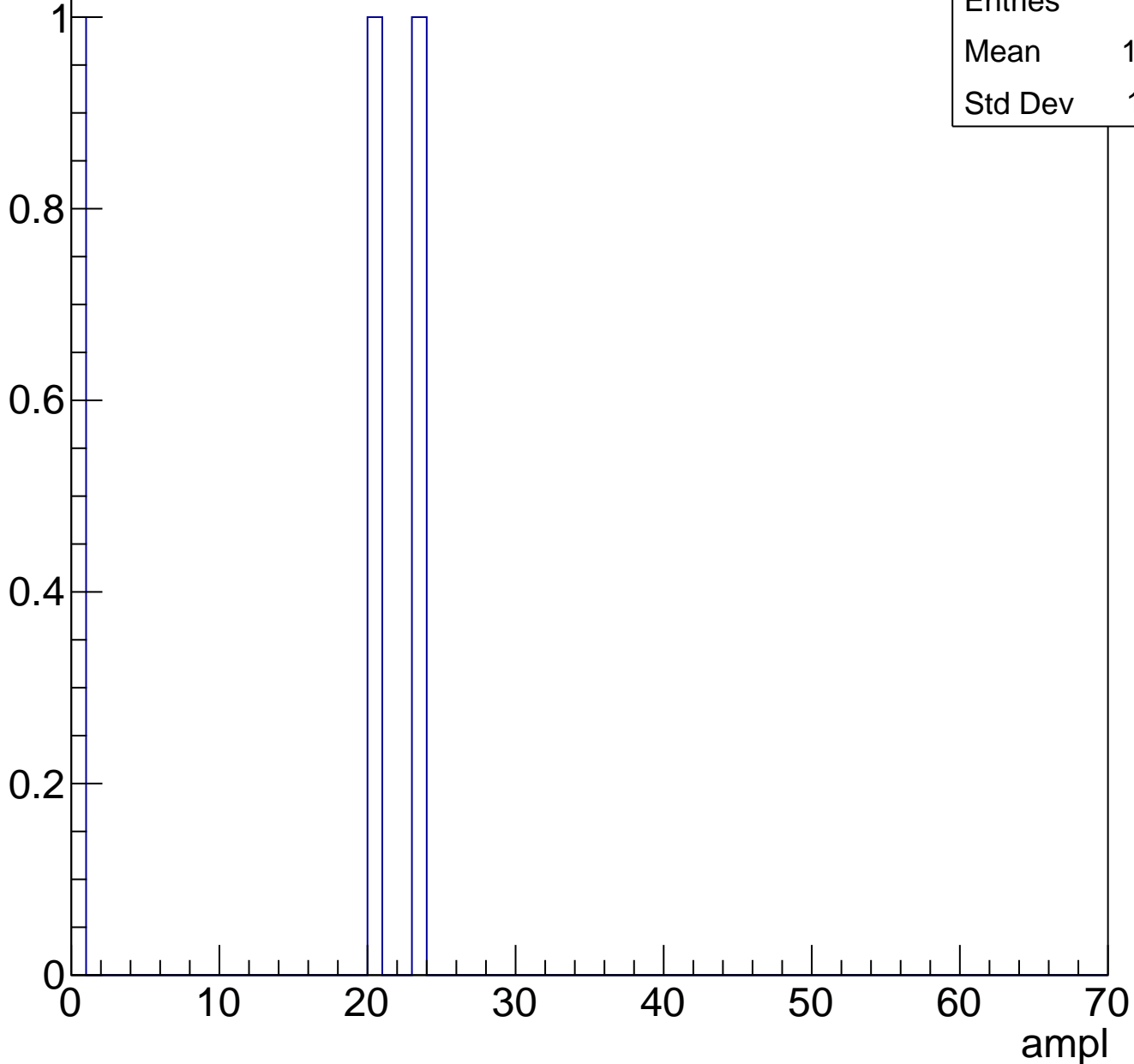
Entries	13
Mean	61.92
Std Dev	1.328



B1L001S, U20-ch51, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch52, adc0

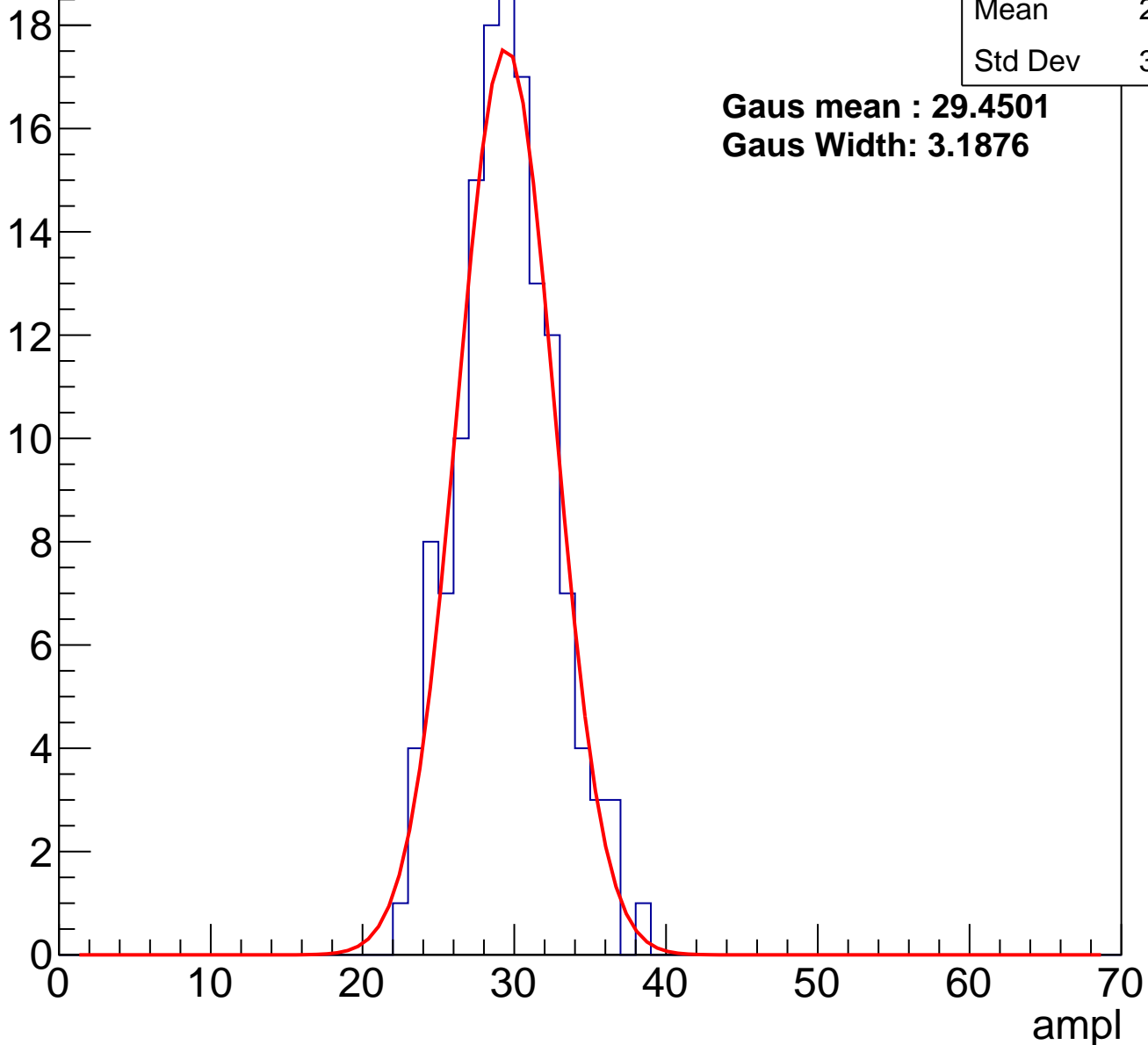
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	28.99
Std Dev	3.138

Gaus mean : 29.4501

Gaus Width: 3.1876

Entry



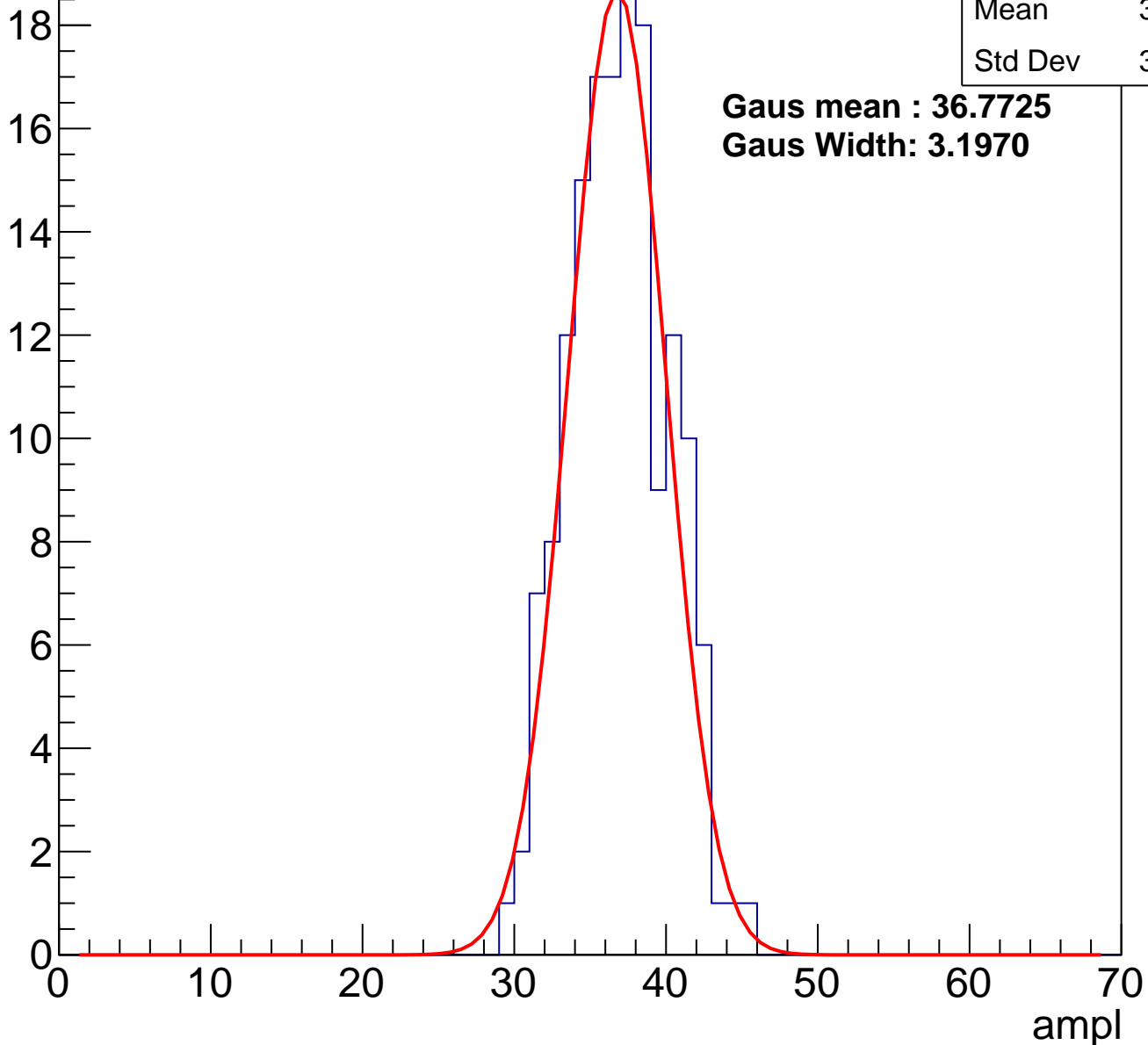
B1L001S, U20-ch52, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	36.46
Std Dev	3.203

Gaus mean : 36.7725
Gaus Width: 3.1970

Entry



B1L001S, U20-ch52, adc2

calib_packv5_042523_0143.root, FC#2, port C2

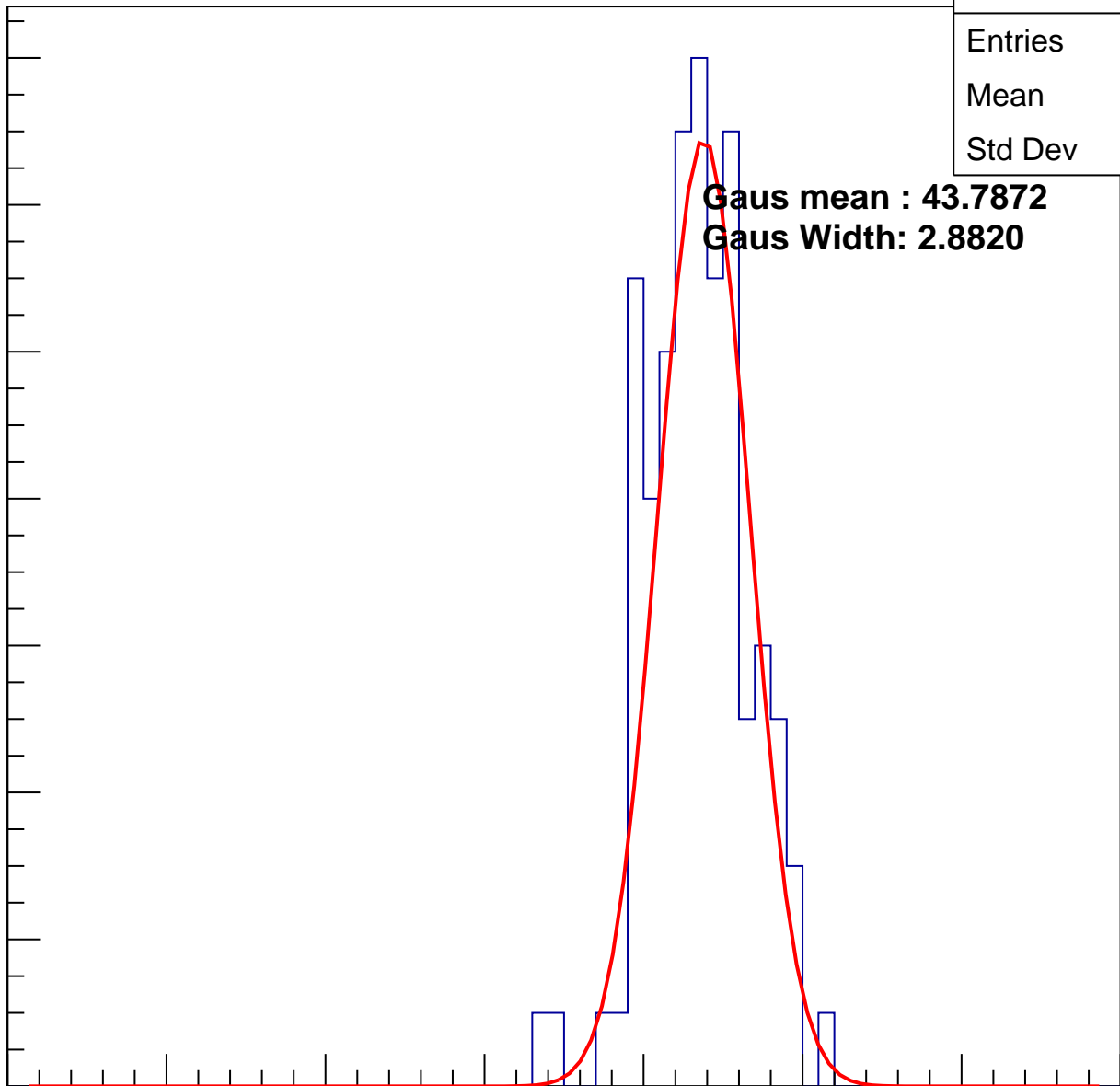
Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	42.96
Std Dev	3.168

Gaus mean : 43.7872
Gaus Width: 2.8820

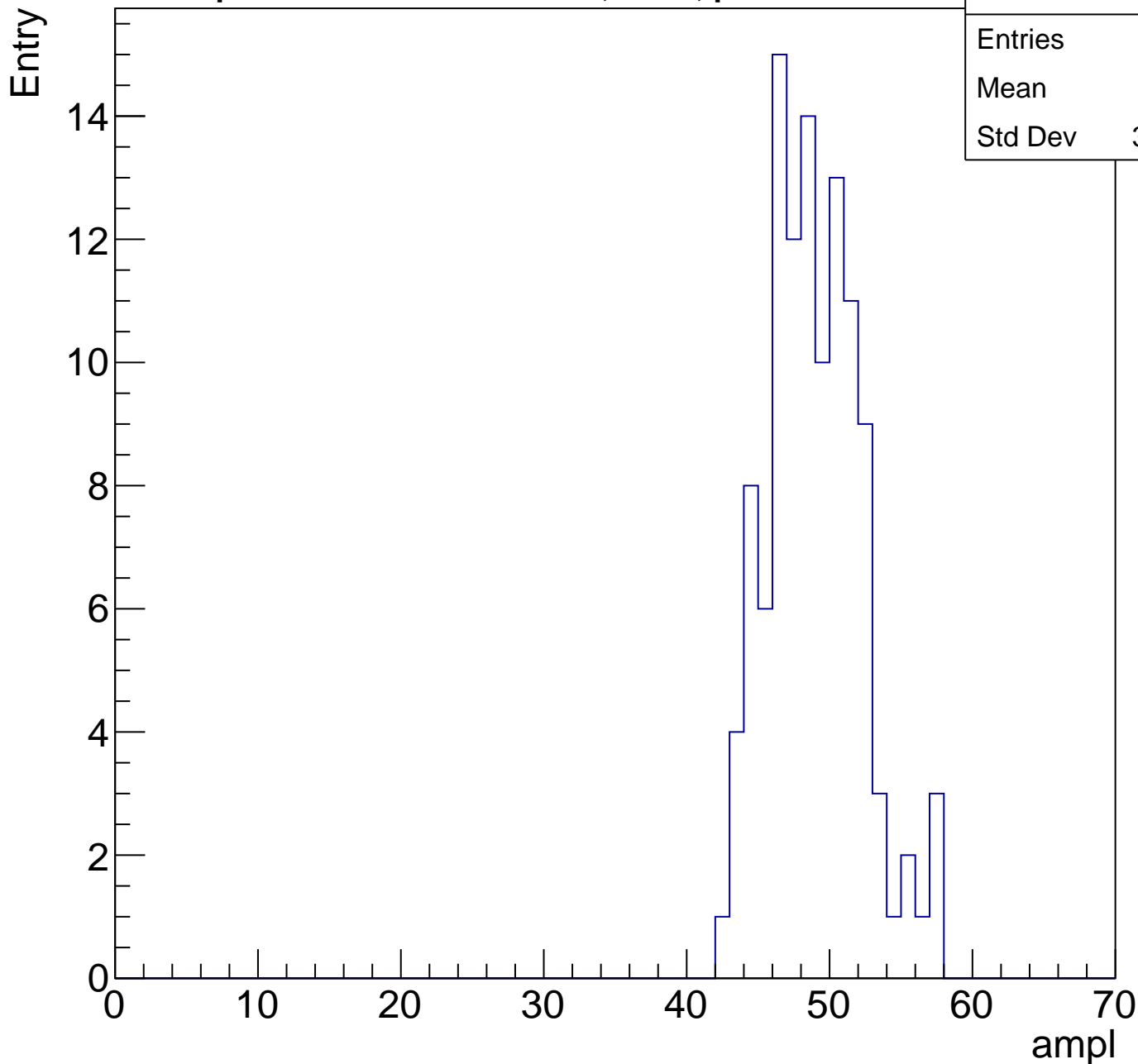
0 10 20 30 40 50 60 70
ampl



B1L001S, U20-ch52, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	48.5
Std Dev	3.235

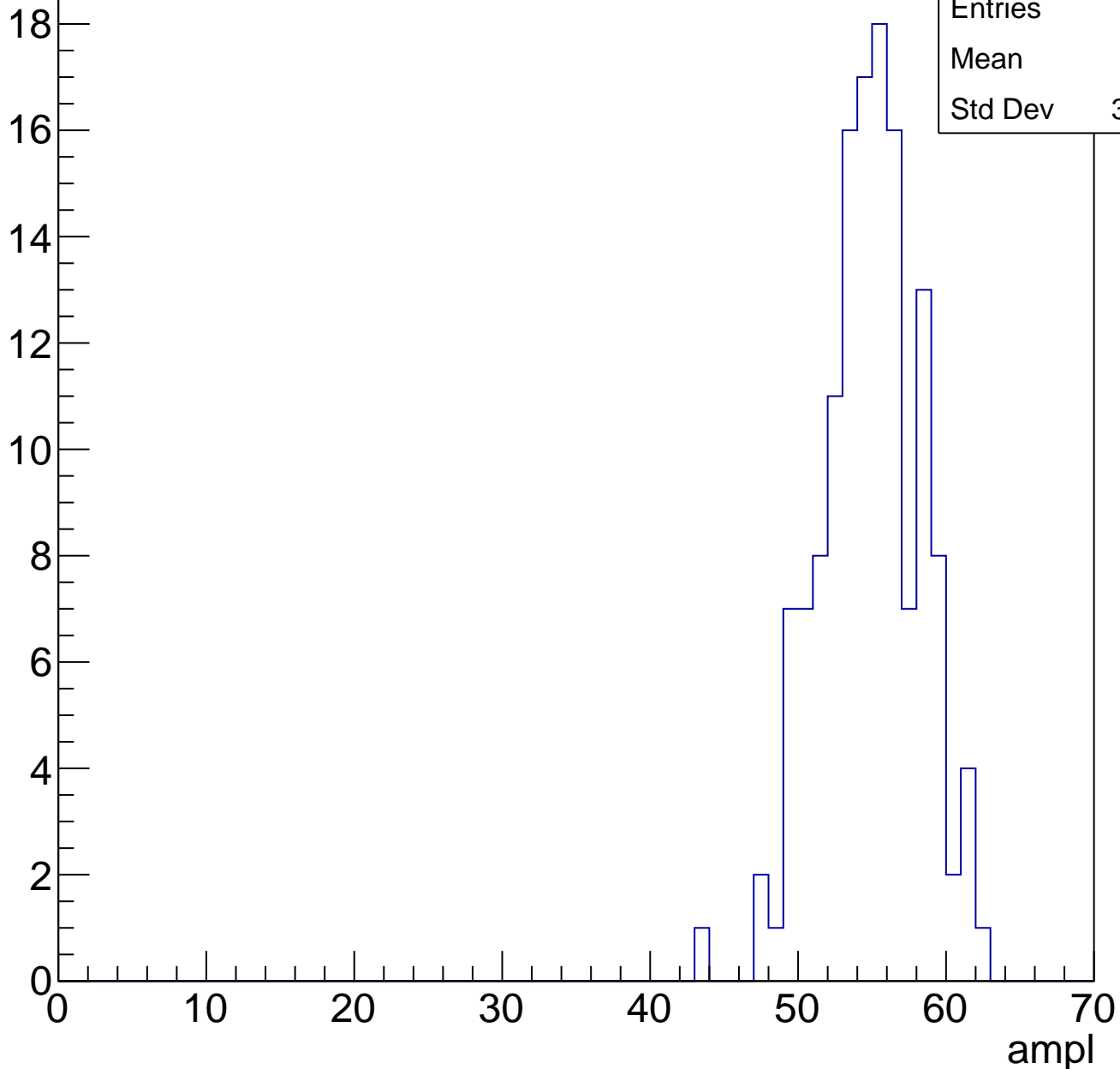


B1L001S, U20-ch52, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	54.4
Std Dev	3.326

Entry



B1L001S, U20-ch52, adc5

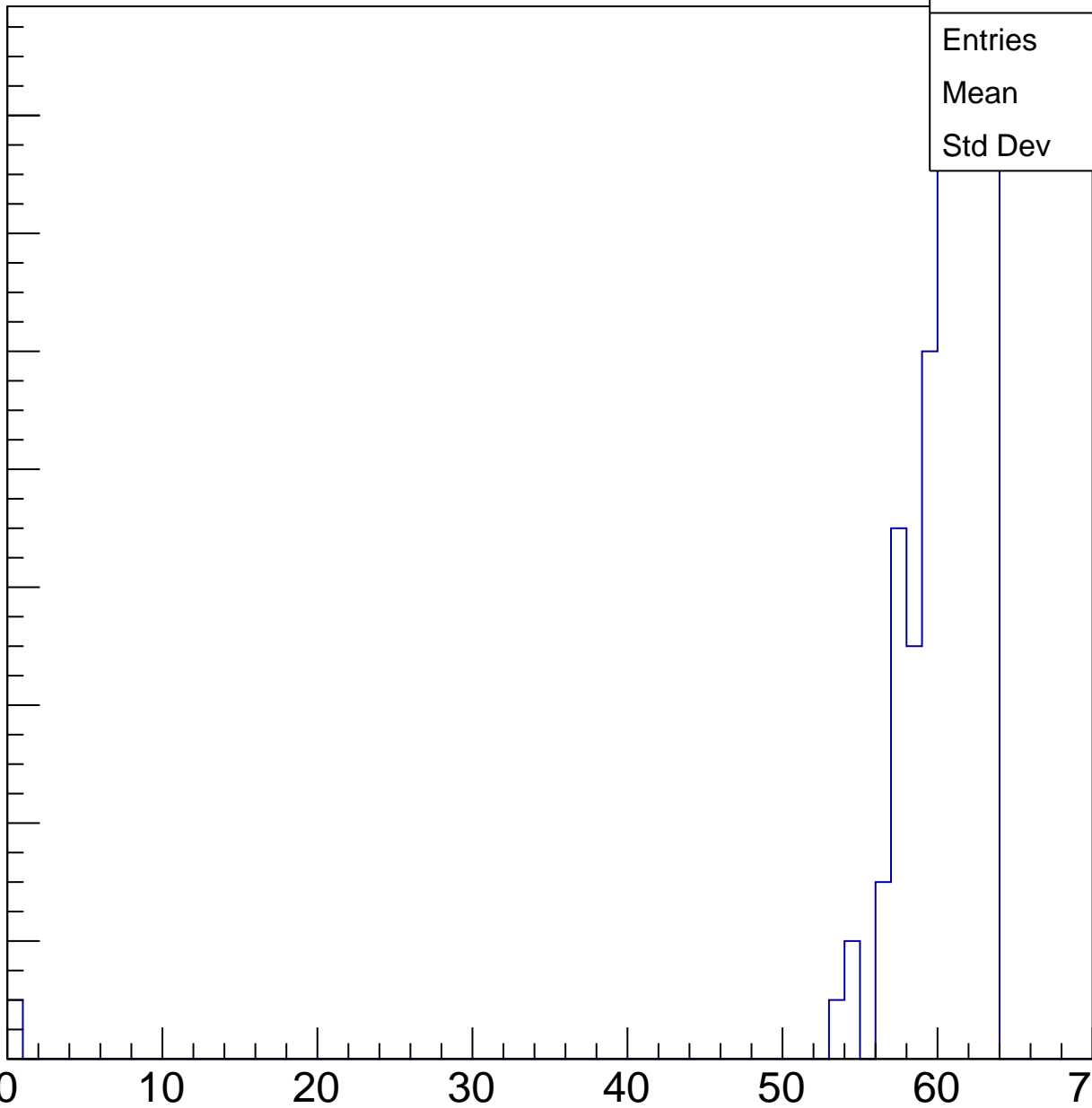
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	59.58
Std Dev	6.347

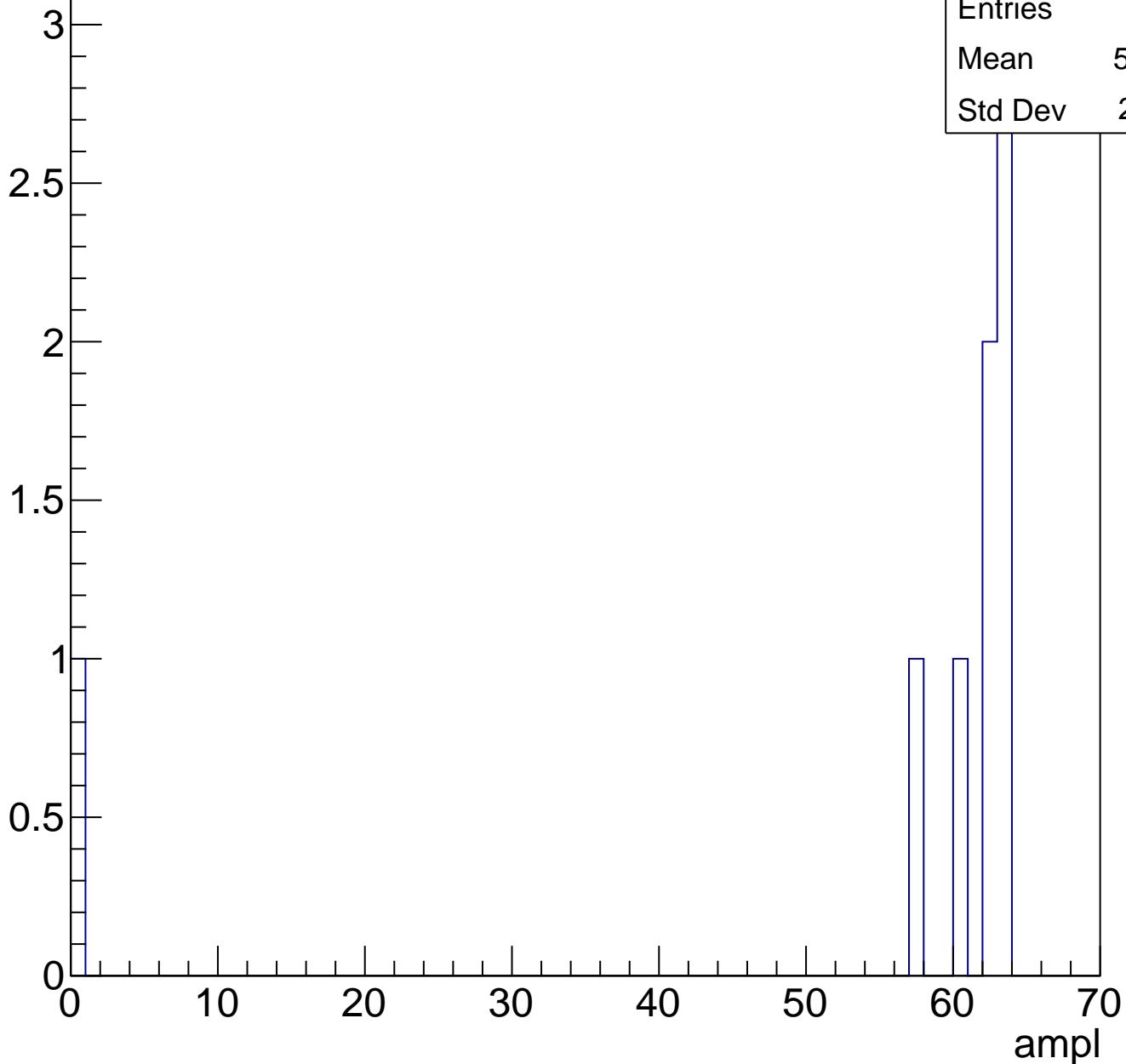
ampl



B1L001S, U20-ch52, adc6

calib_packv5_042523_0143.root, FC#2, port C2

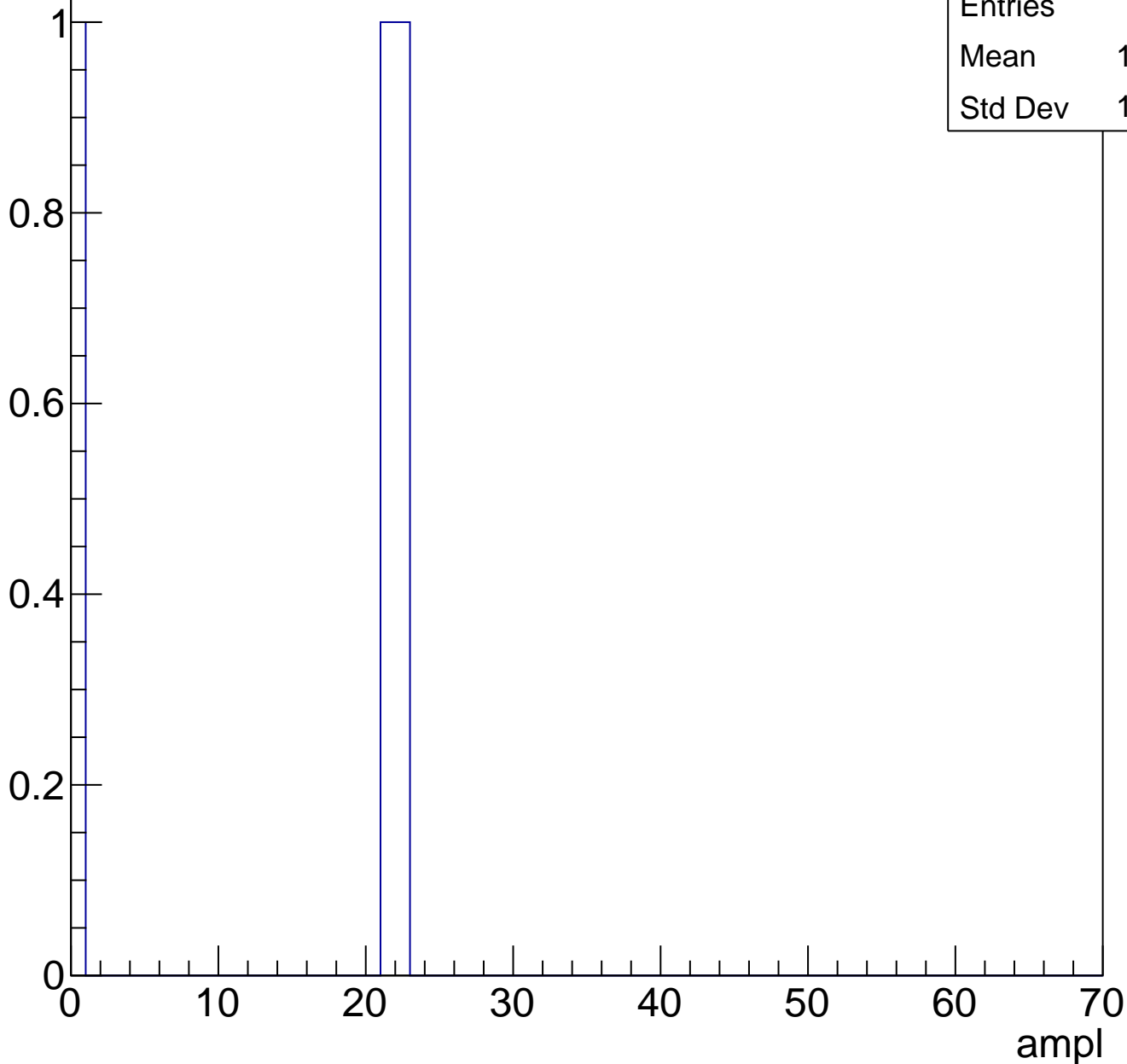
Entry



B1L001S, U20-ch52, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch53, adc0

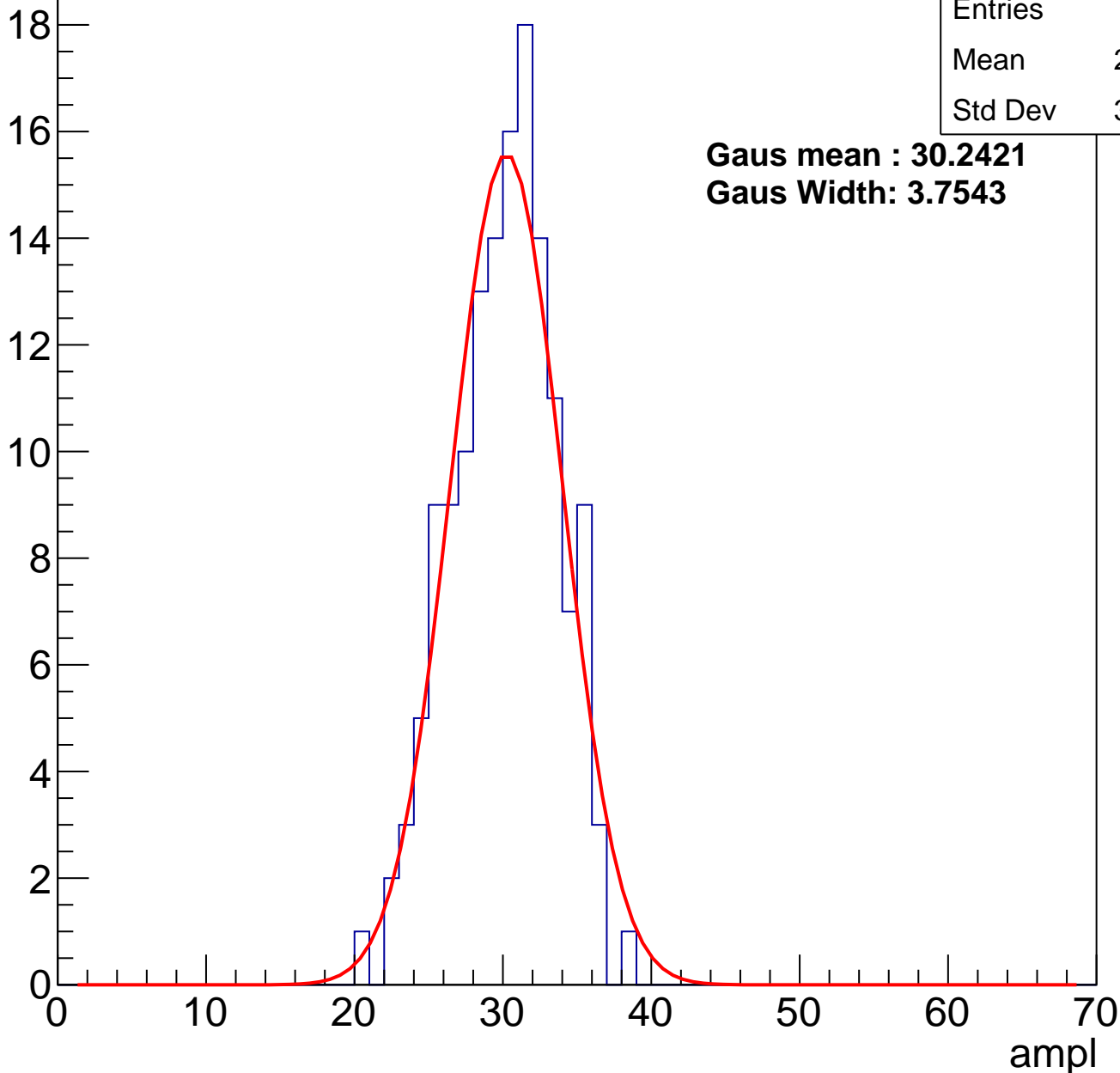
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	29.66
Std Dev	3.469

Gaus mean : 30.2421

Gaus Width: 3.7543

Entry



B1L001S, U20-ch53, adc1

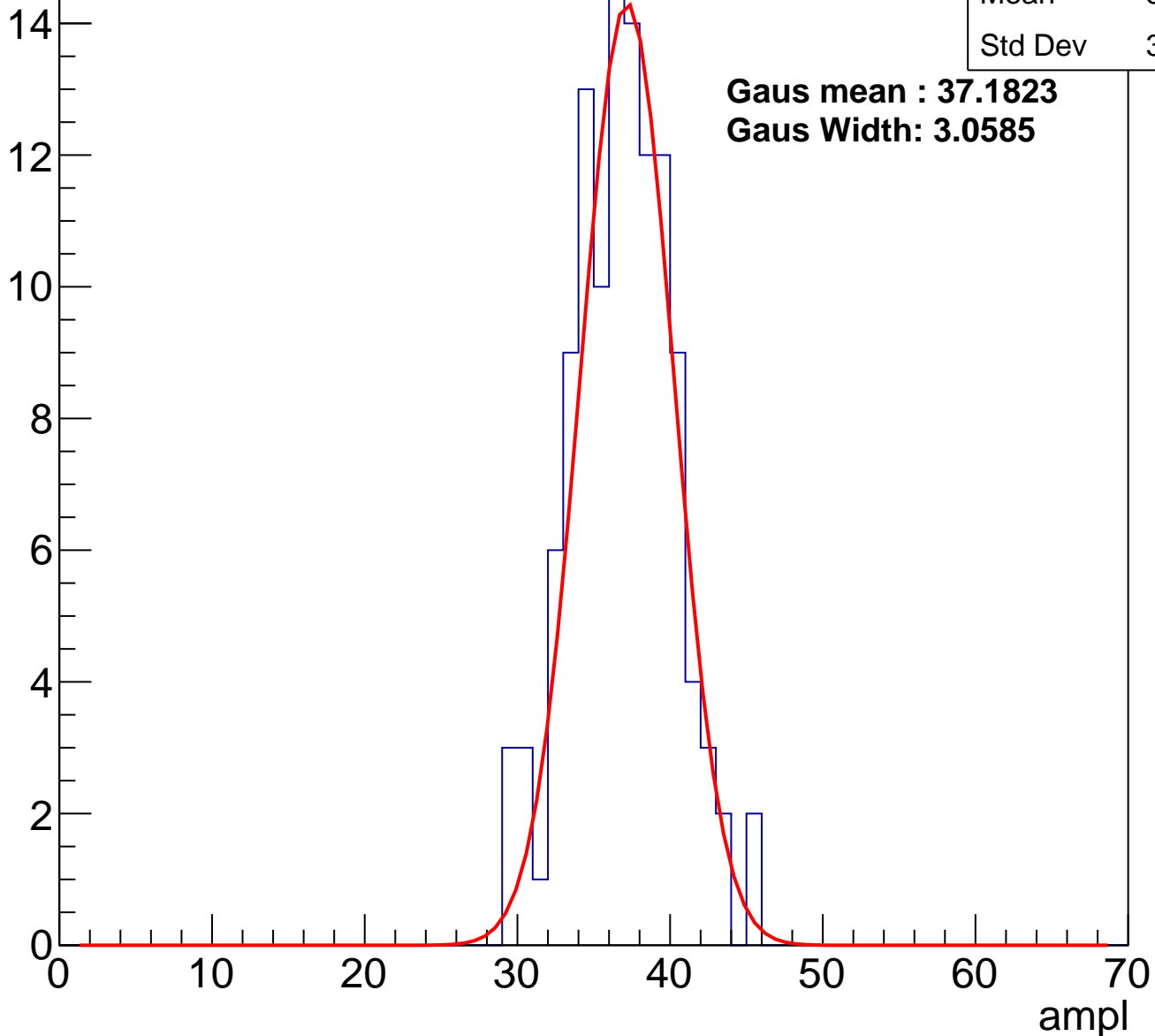
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	36.42
Std Dev	3.294

Gaus mean : 37.1823

Gaus Width: 3.0585

Entry



B1L001S, U20-ch53, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	42.82
Std Dev	3.297

Gaus mean : 43.2806

Gaus Width: 3.4139

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

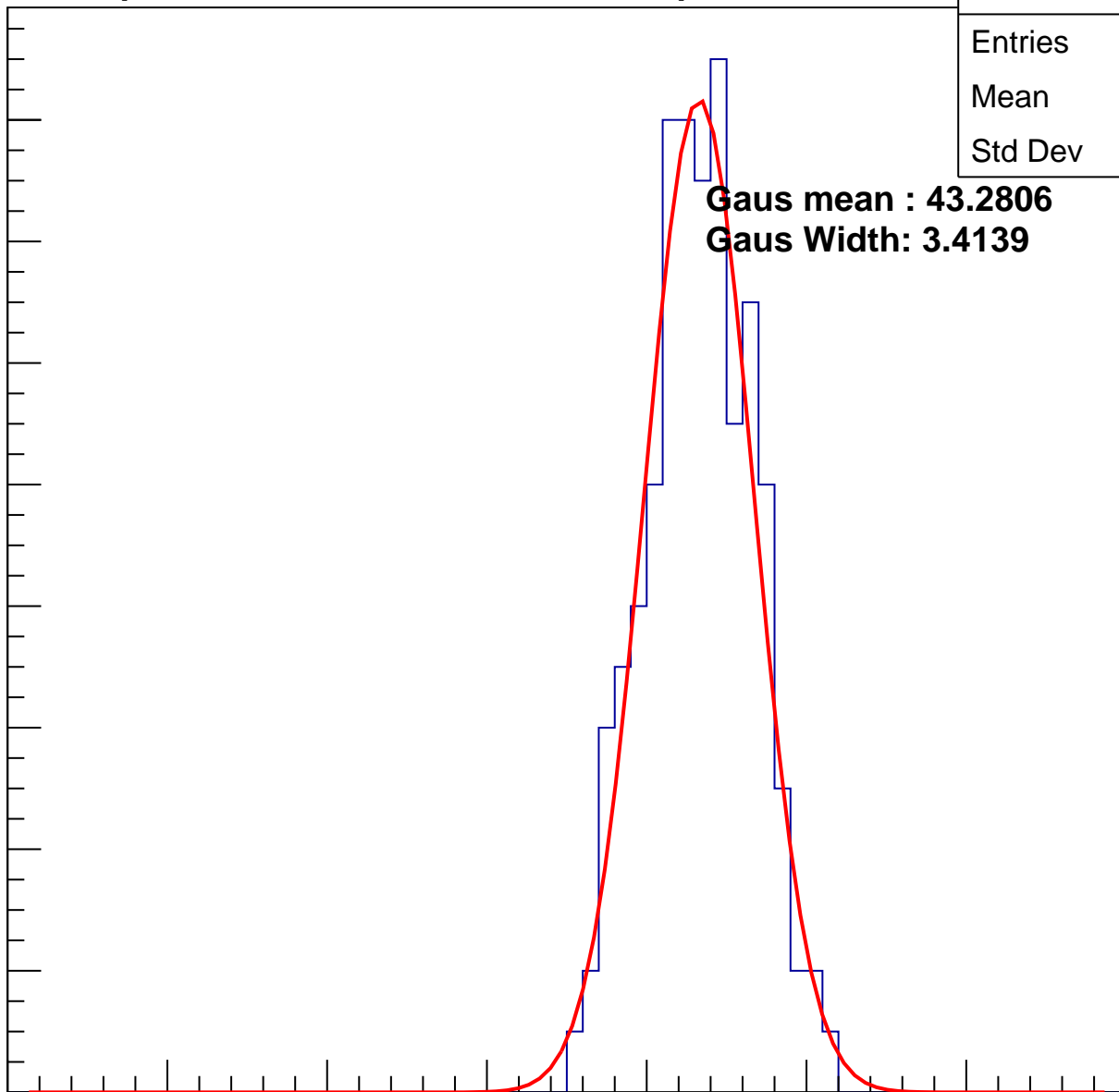
40

50

60

70

ampl



B1L001S, U20-ch53, adc3

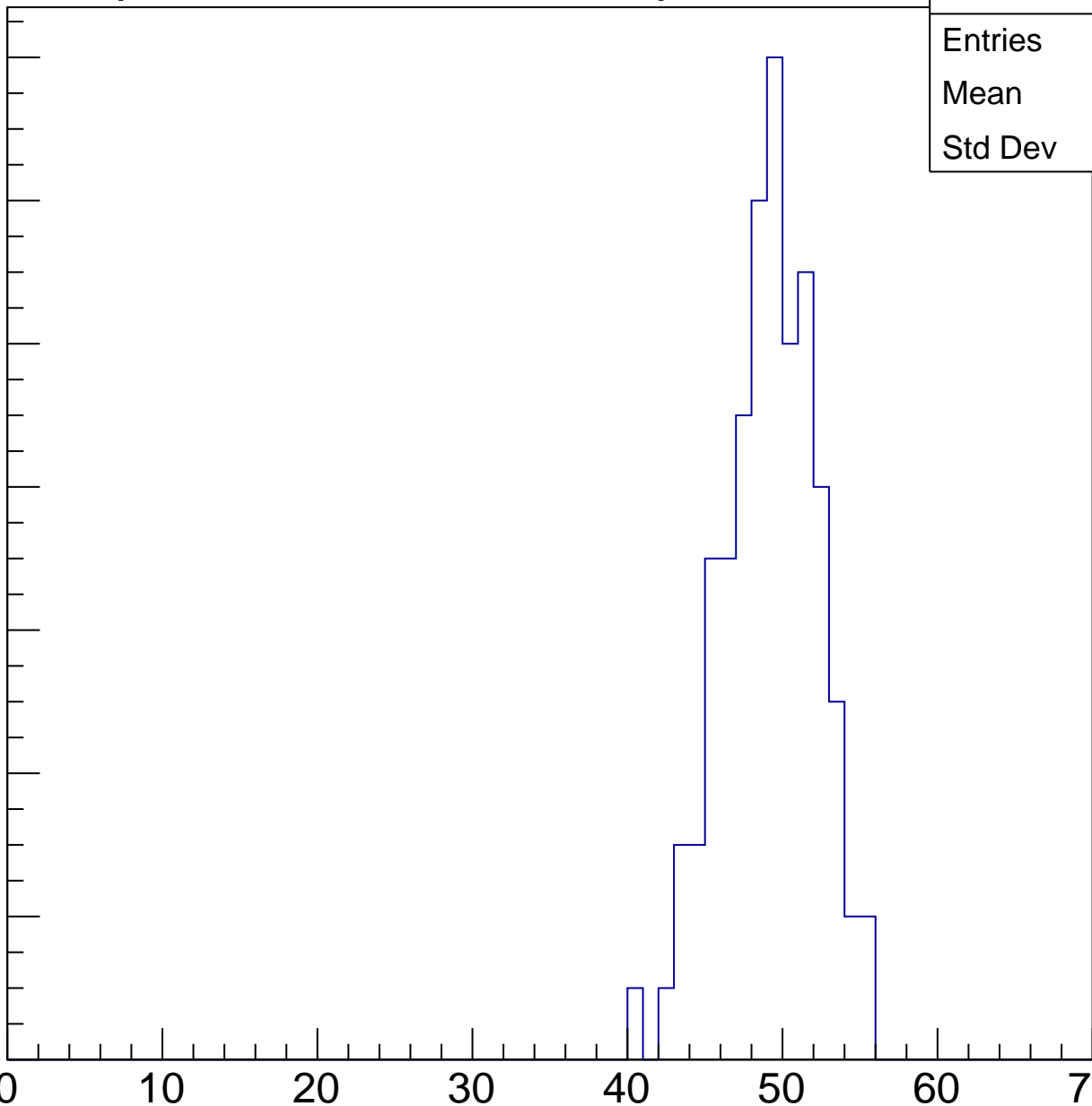
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	48.68
Std Dev	2.999

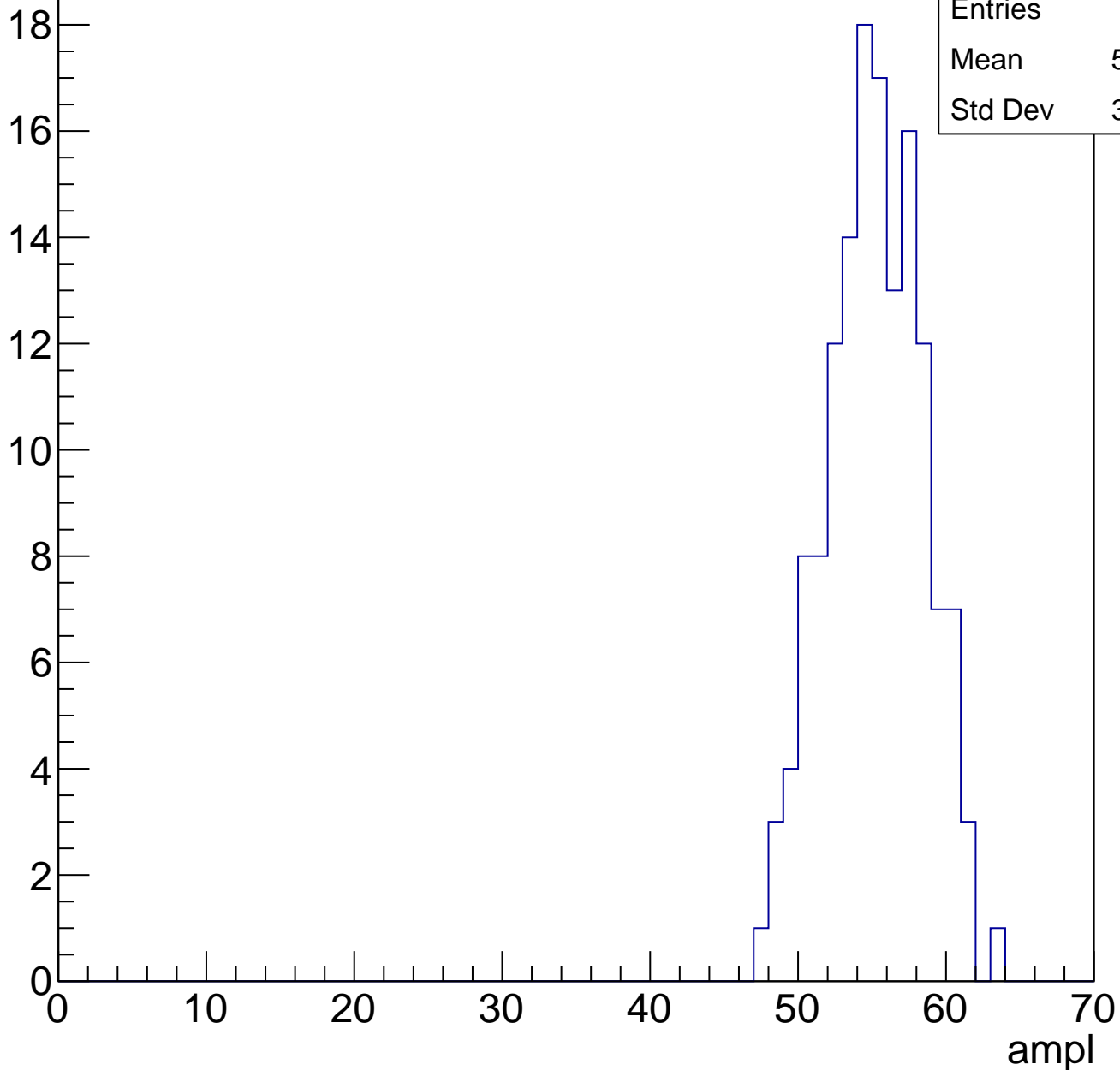
ampl



B1L001S, U20-ch53, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch53, adc5

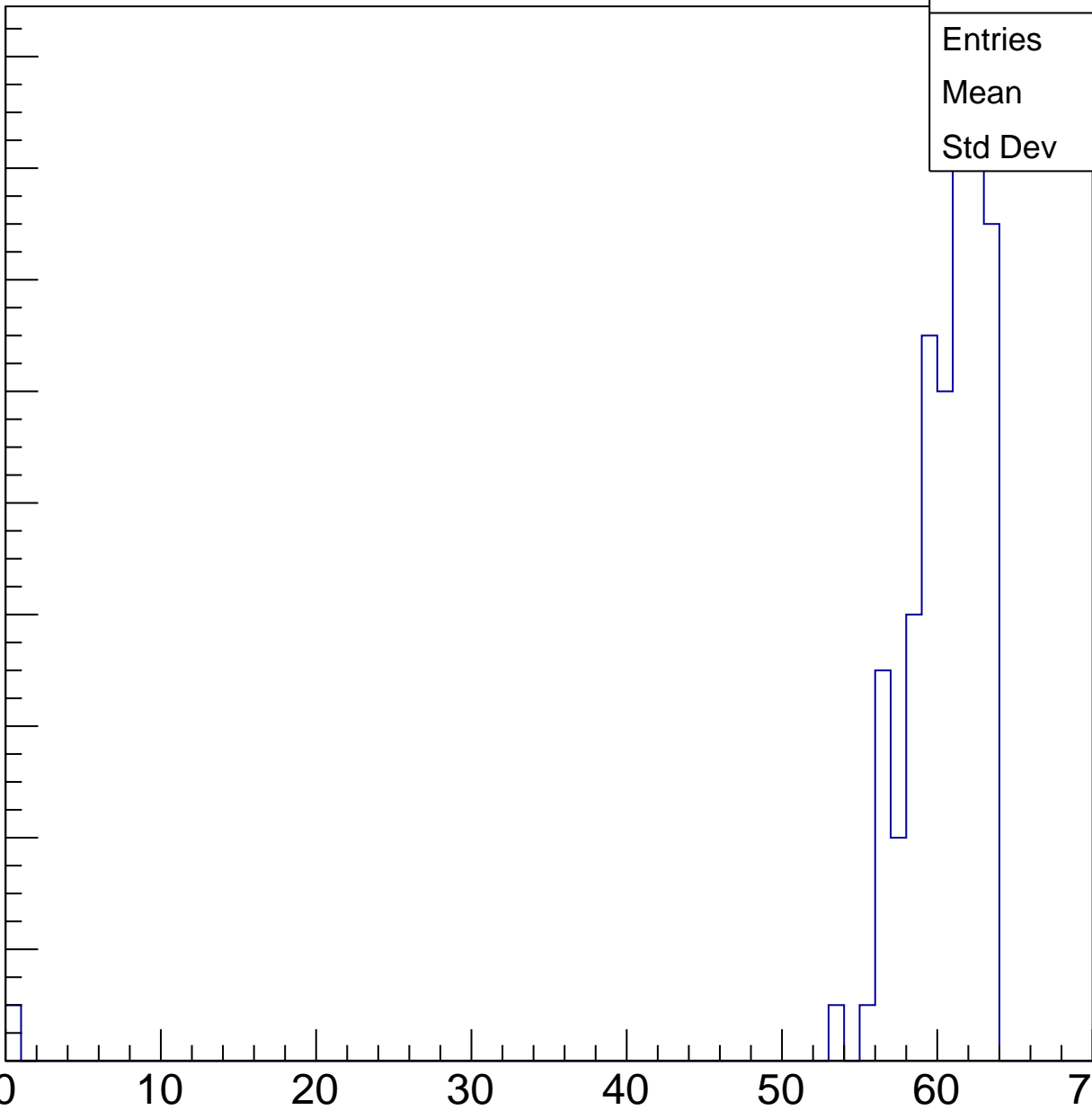
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.54
Std Dev	6.511

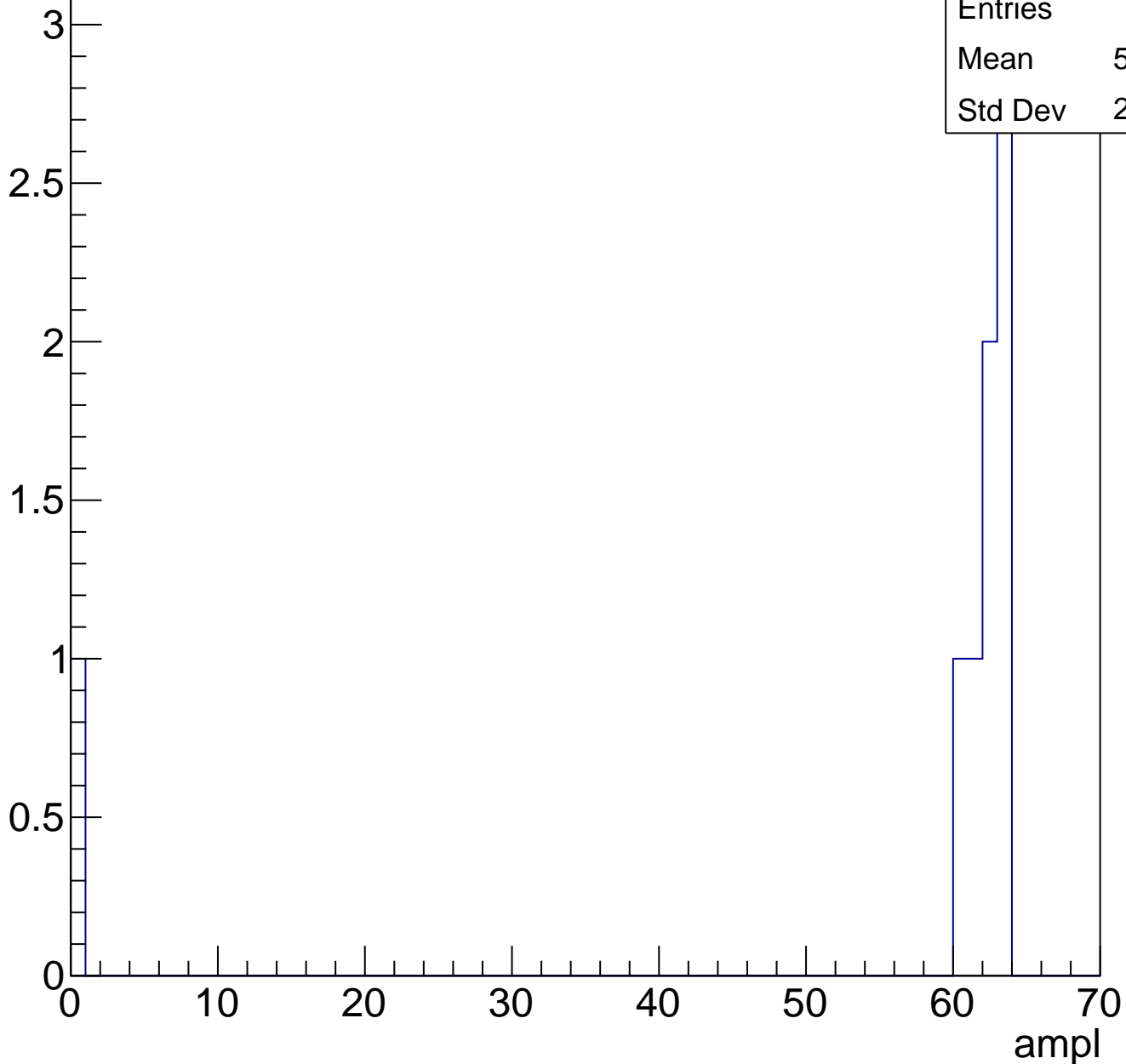
ampl



B1L001S, U20-ch53, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch53, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch54, adc0

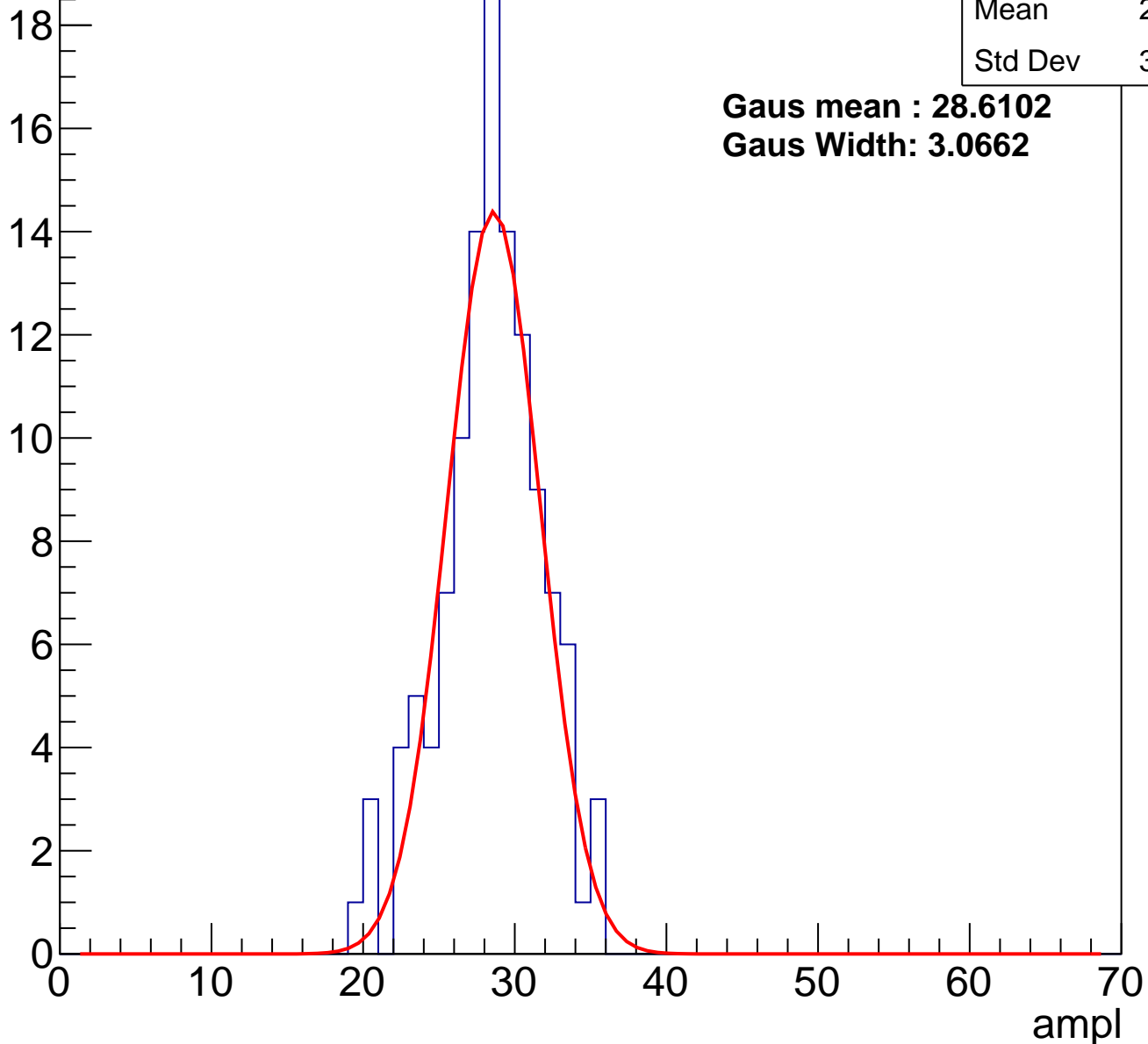
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	27.97
Std Dev	3.319

Gaus mean : 28.6102

Gaus Width: 3.0662

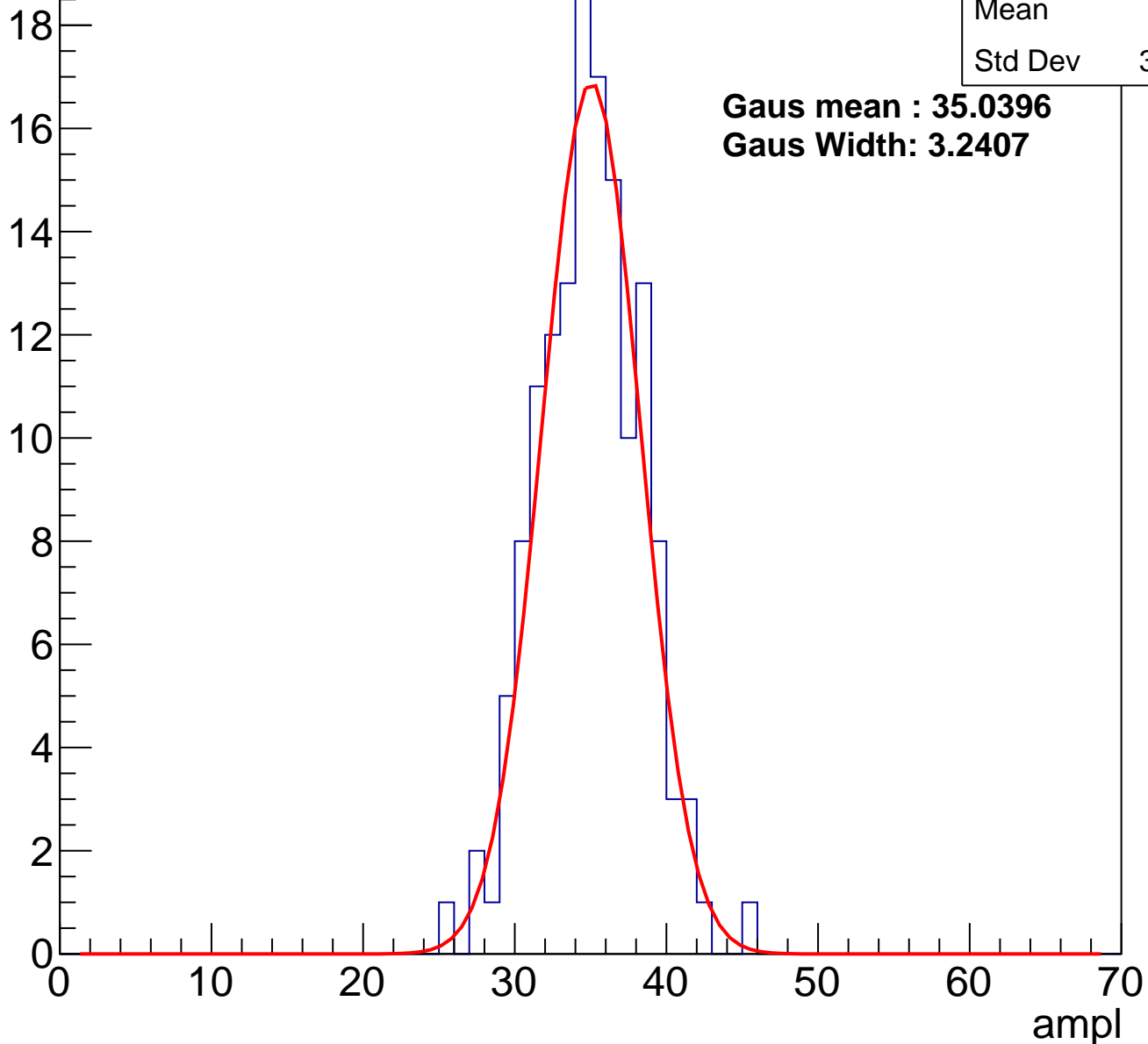
Entry



B1L001S, U20-ch54, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	143
Mean	34.5
Std Dev	3.353

Gaus mean : 35.0396

Gaus Width: 3.2407

B1L001S, U20-ch54, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

107

Mean

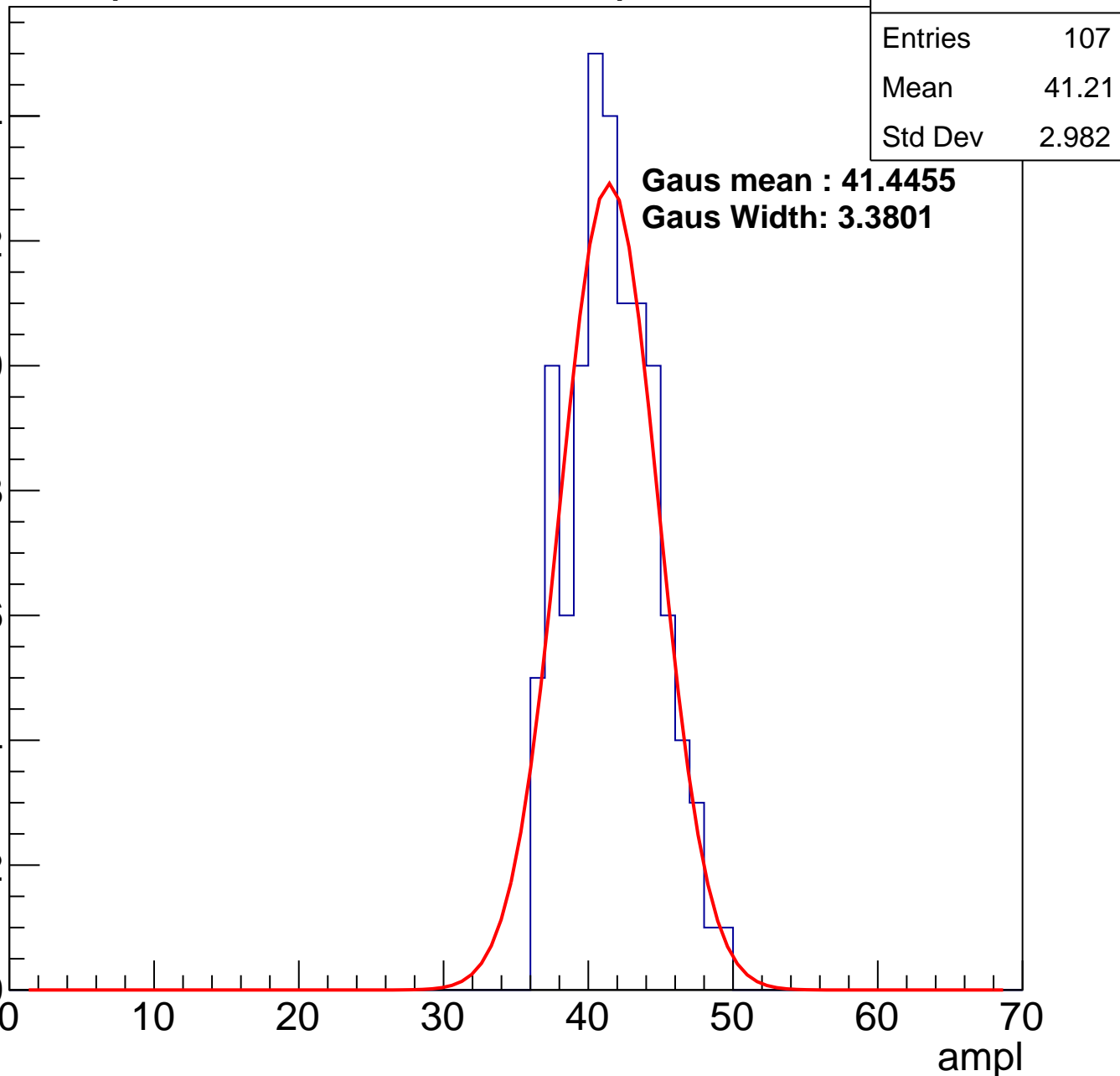
41.21

Std Dev

2.982

Gaus mean : 41.4455

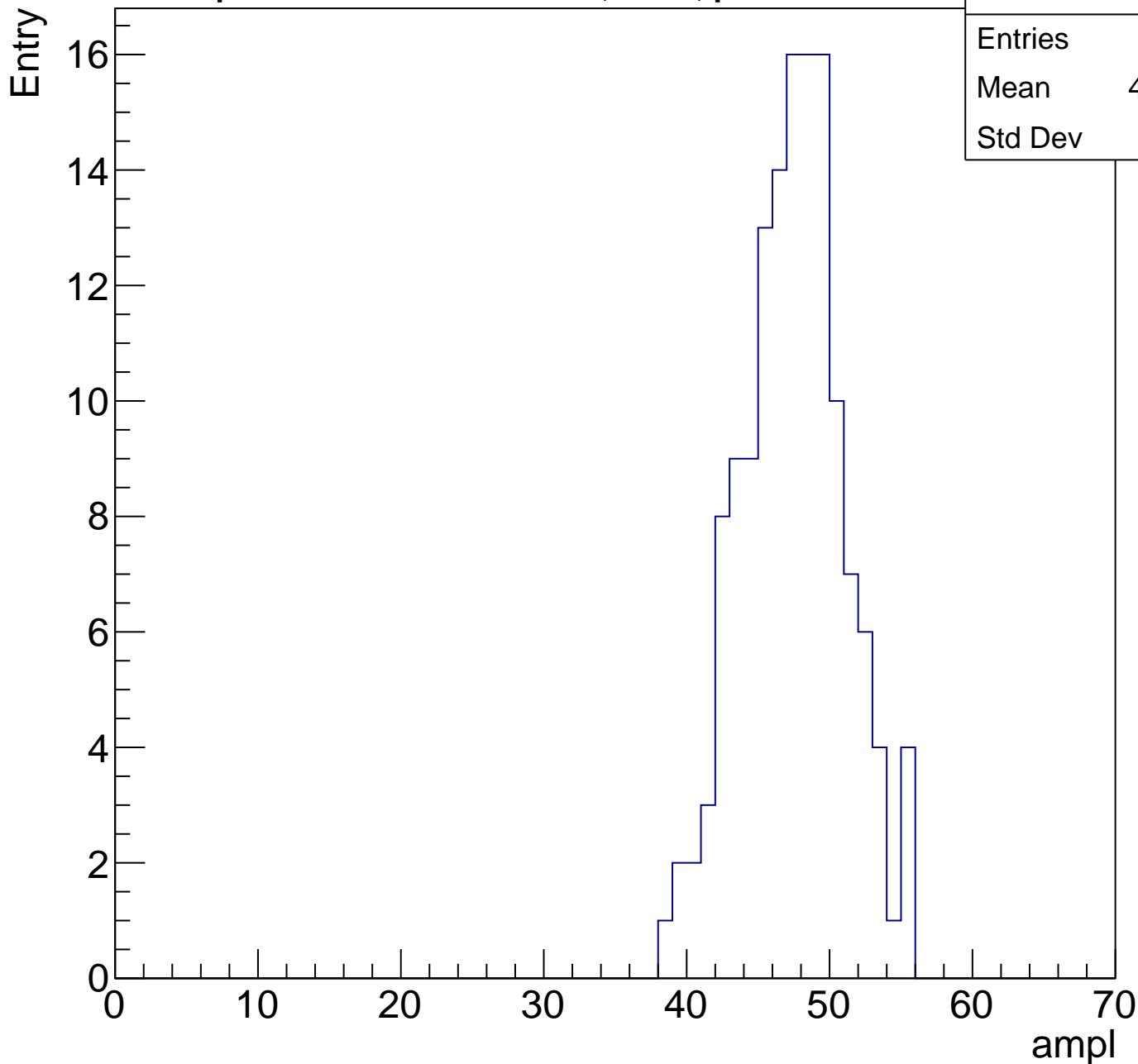
Gaus Width: 3.3801



B1L001S, U20-ch54, adc3

calib_packv5_042523_0143.root, FC#2, port C2

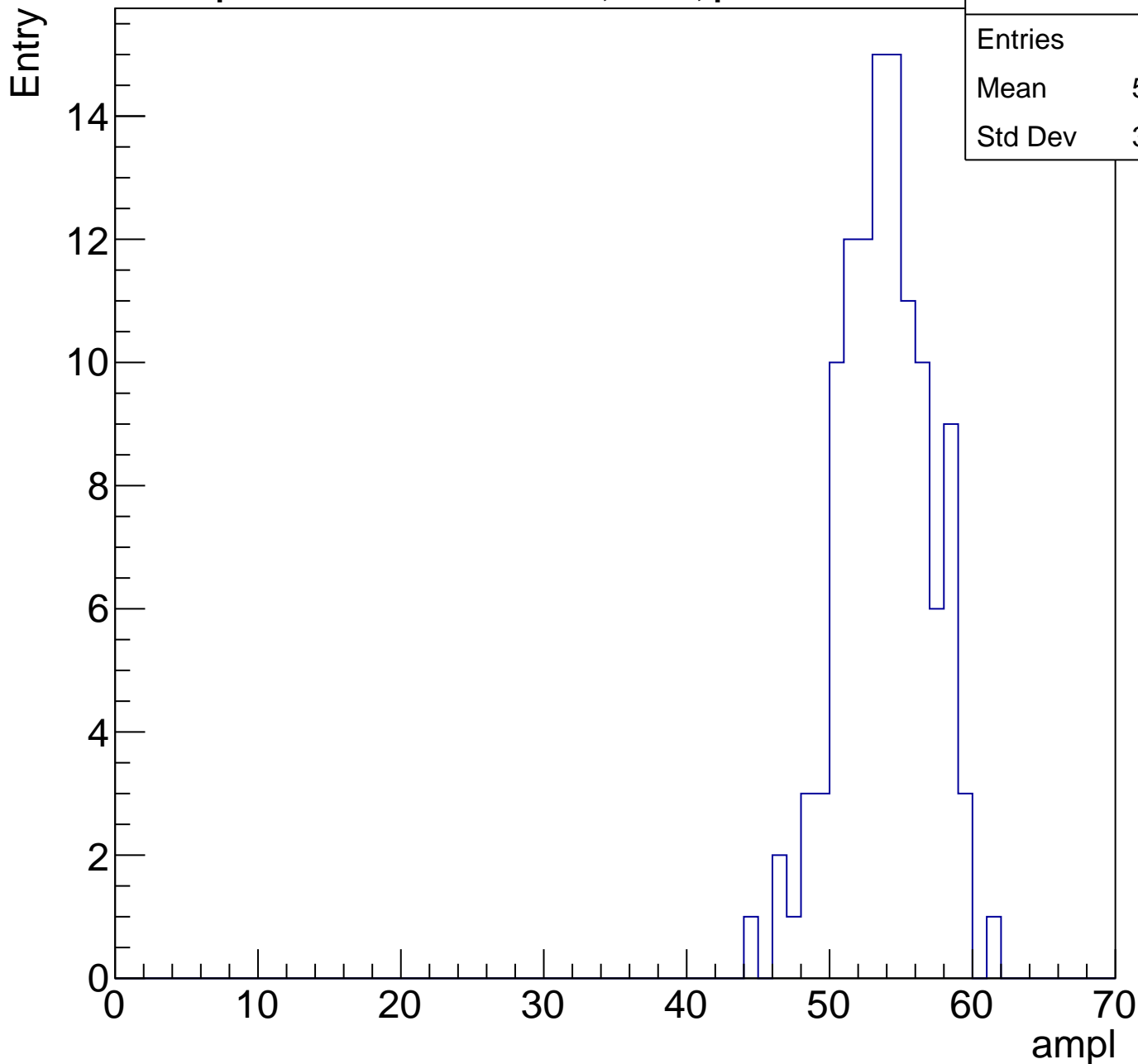
Entries	141
Mean	46.99
Std Dev	3.56



B1L001S, U20-ch54, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	53.35
Std Dev	3.129



B1L001S, U20-ch54, adc5

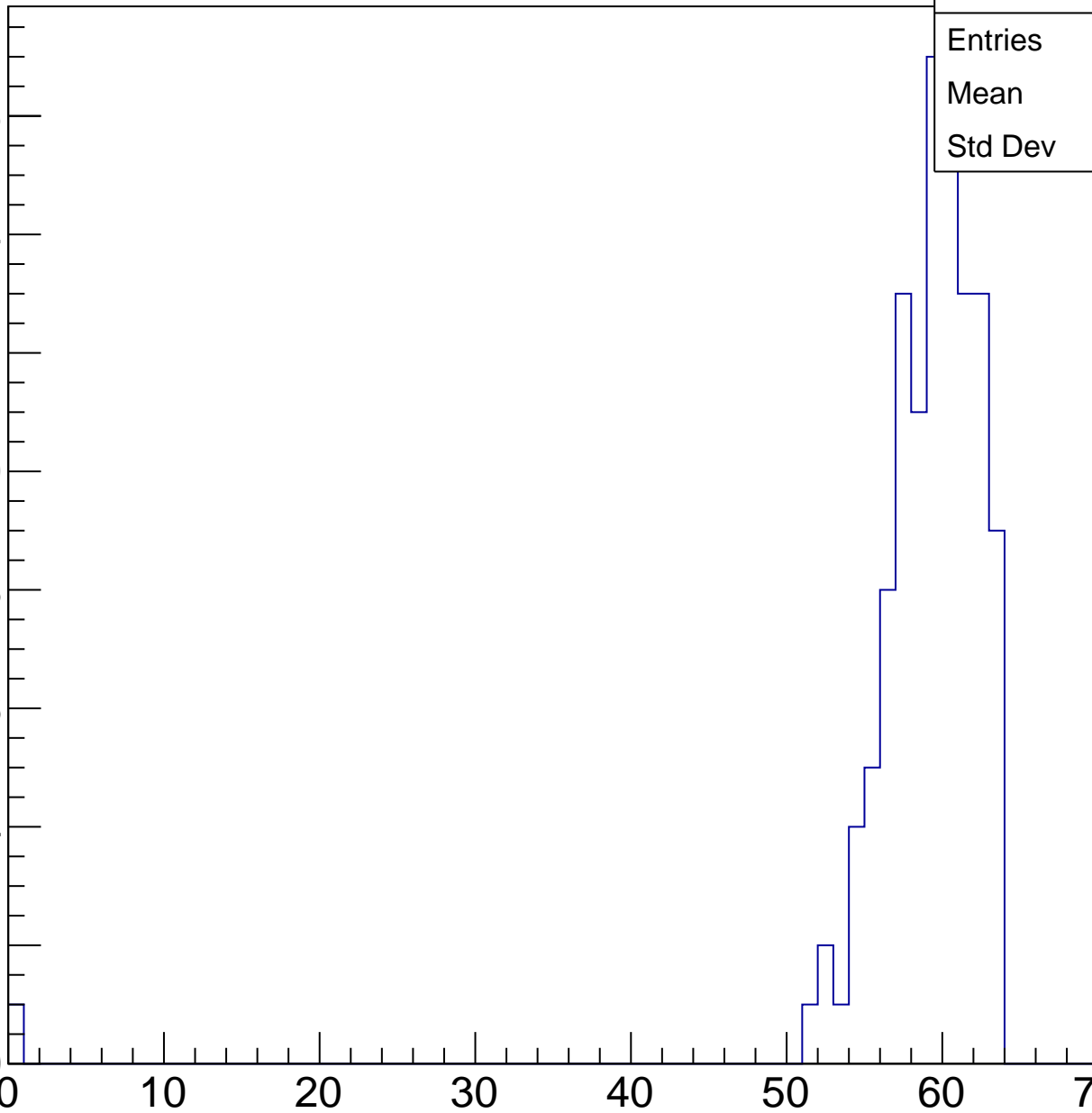
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	114
Mean	58.38
Std Dev	6.126

ampl



B1L001S, U20-ch54, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	32
Mean	59.44
Std Dev	10.85



B1L001S, U20-ch54, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U20-ch55, adc0

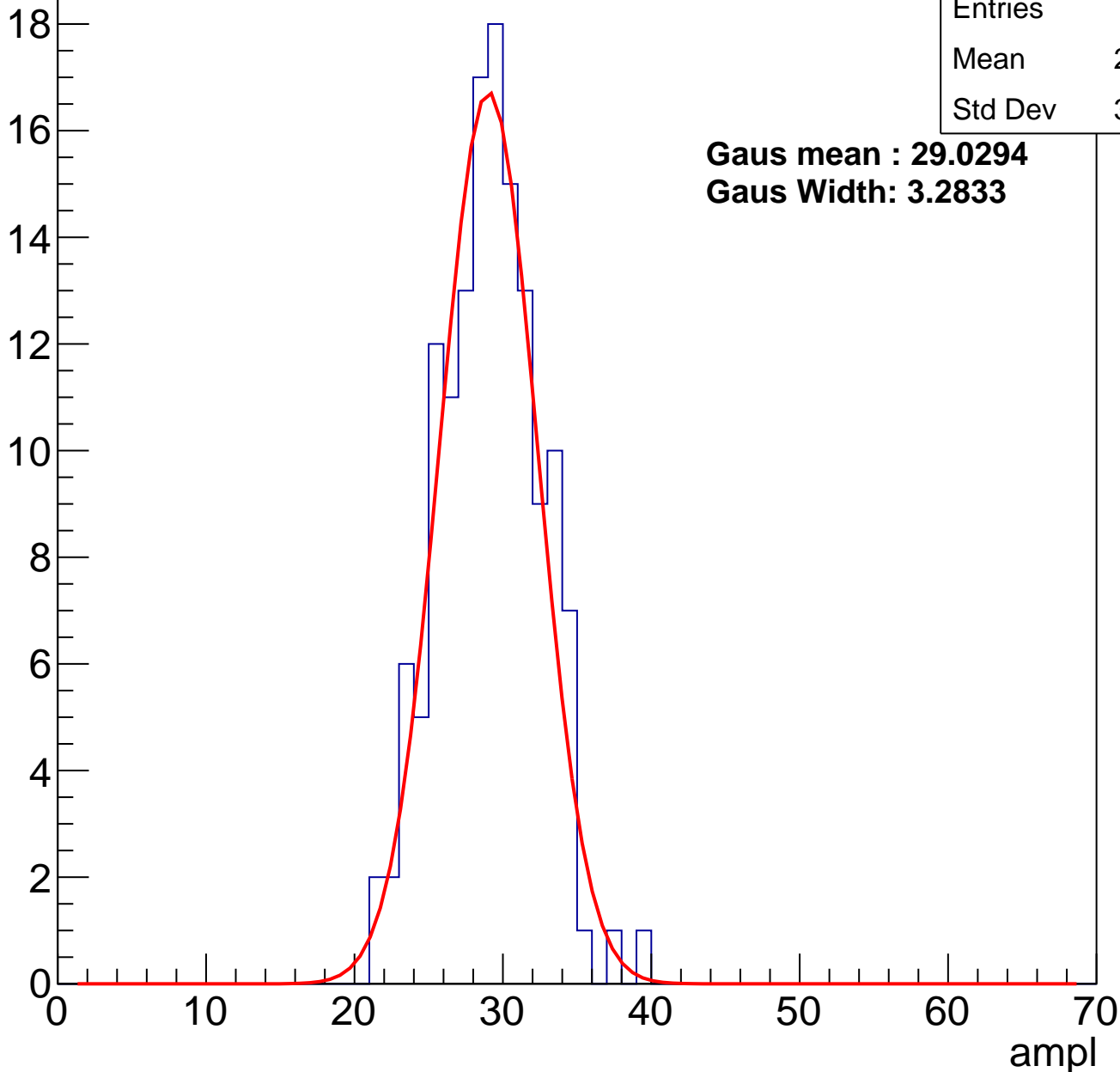
calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	28.66
Std Dev	3.339

Gaus mean : 29.0294

Gaus Width: 3.2833

Entry



B1L001S, U20-ch55, adc1

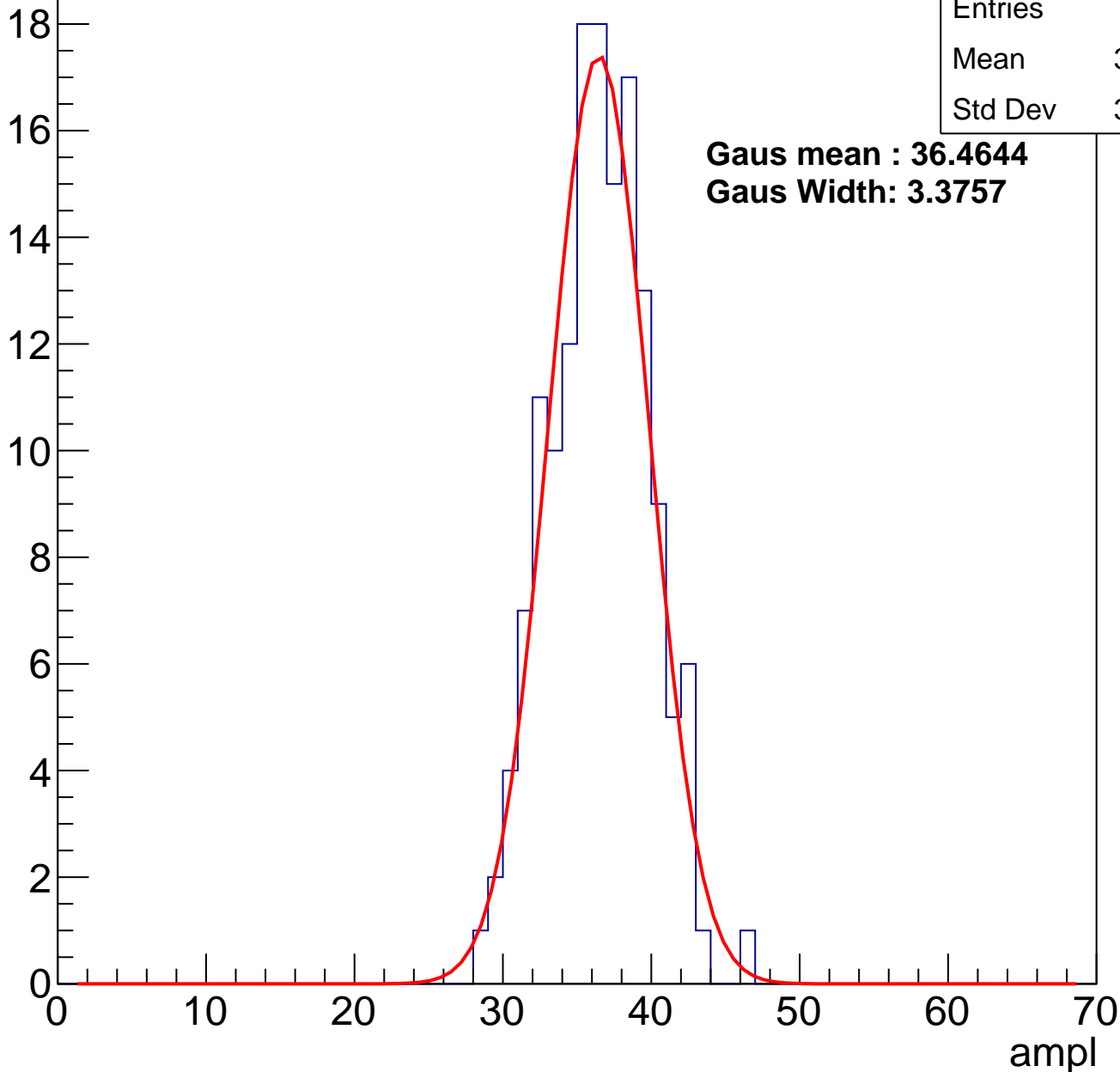
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	36.03
Std Dev	3.313

Gaus mean : 36.4644

Gaus Width: 3.3757

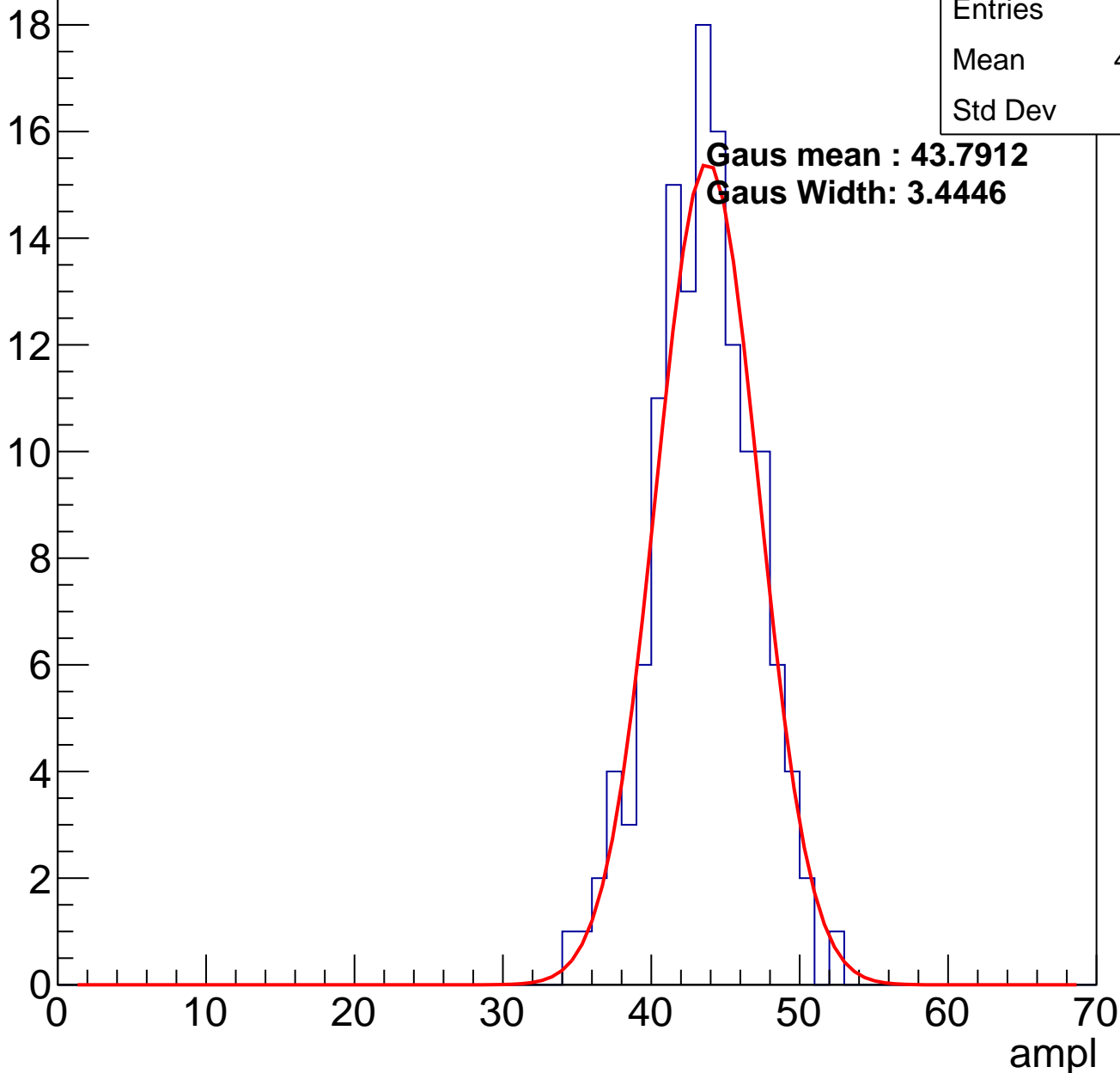
Entry



B1L001S, U20-ch55, adc2

calib_packv5_042523_0143.root, FC#2, port C2

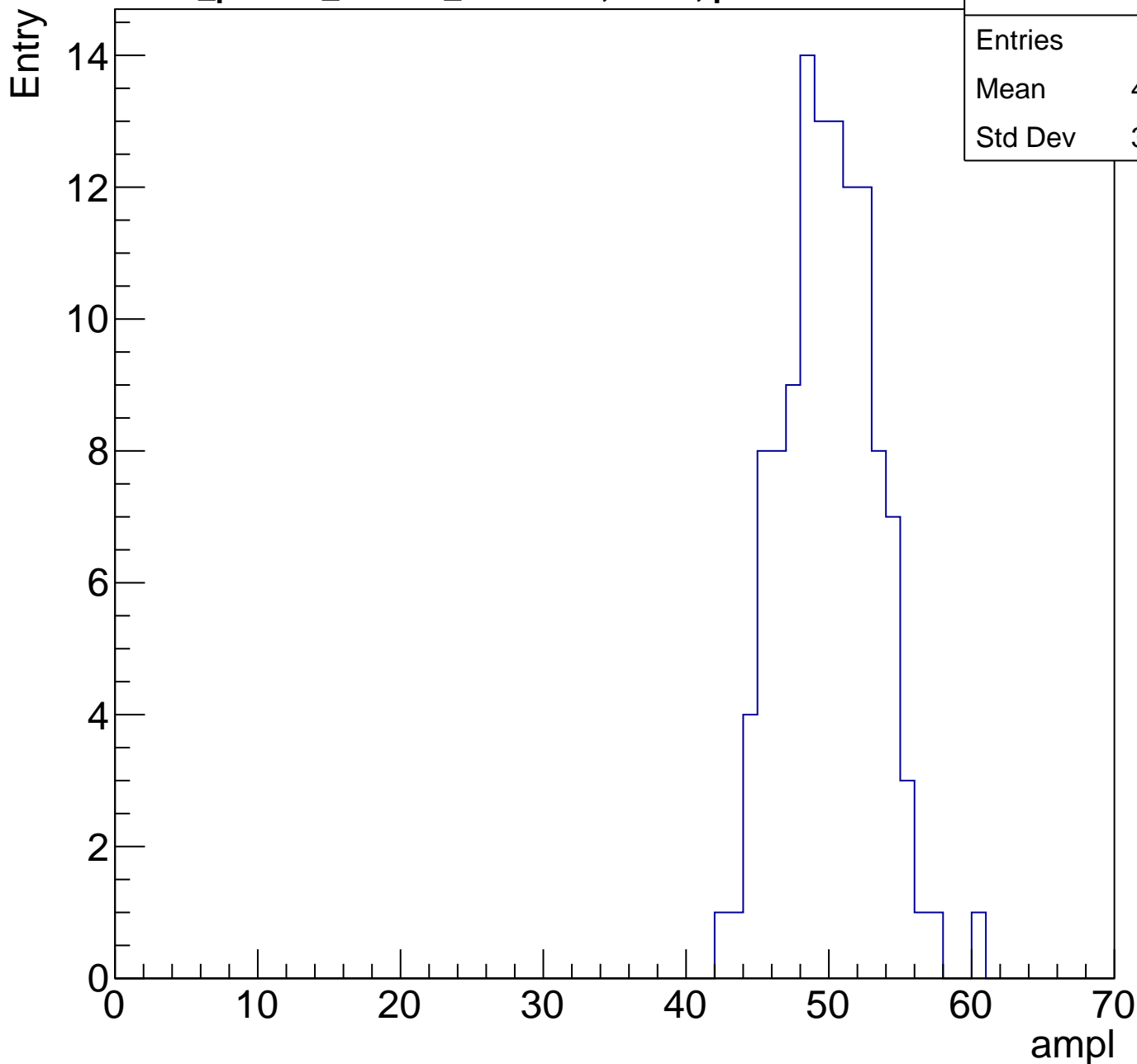
Entry



B1L001S, U20-ch55, adc3

calib_packv5_042523_0143.root, FC#2, port C2

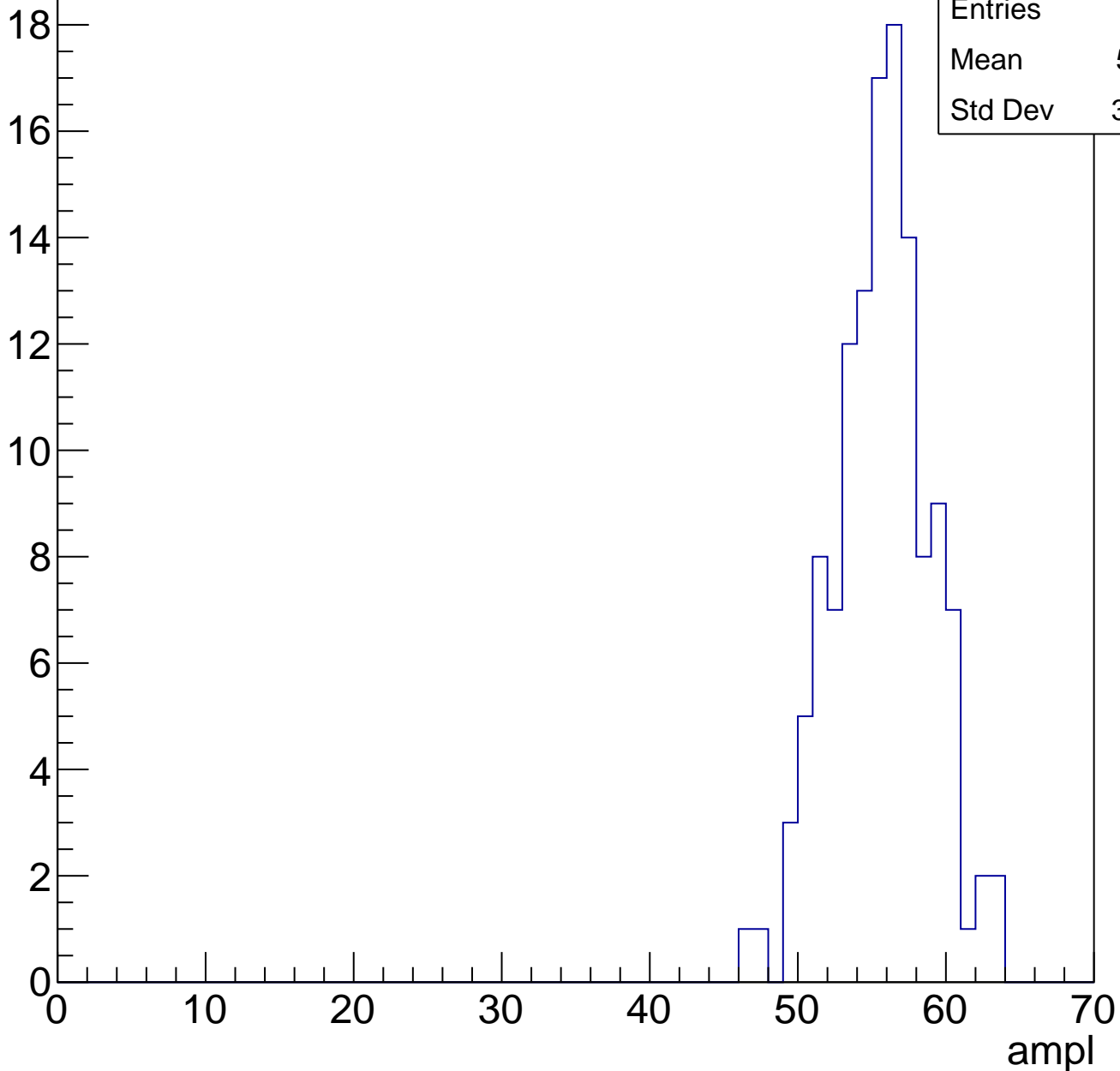
Entries	116
Mean	49.54
Std Dev	3.236



B1L001S, U20-ch55, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch55, adc5

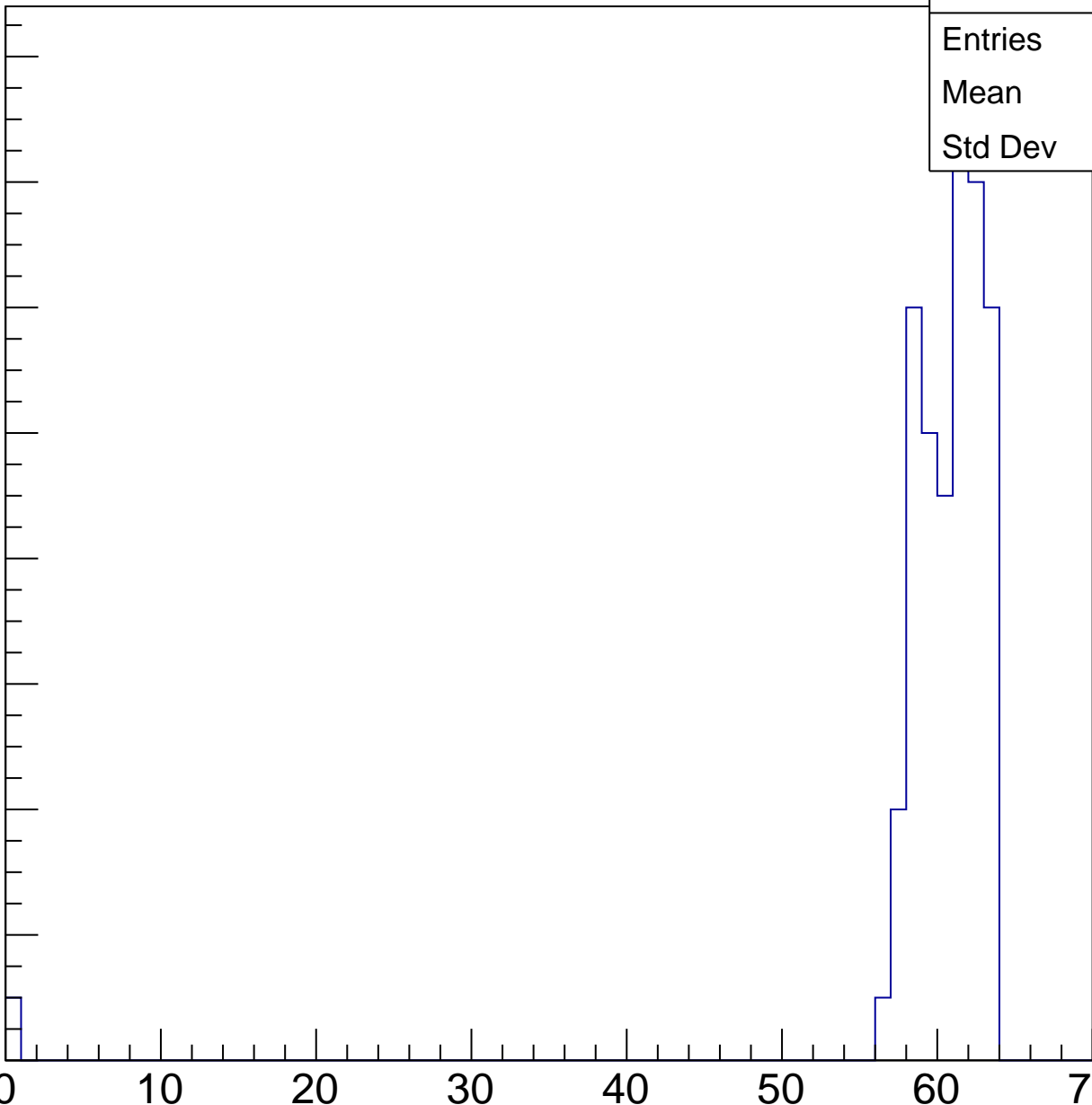
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	59.62
Std Dev	7.007

ampl

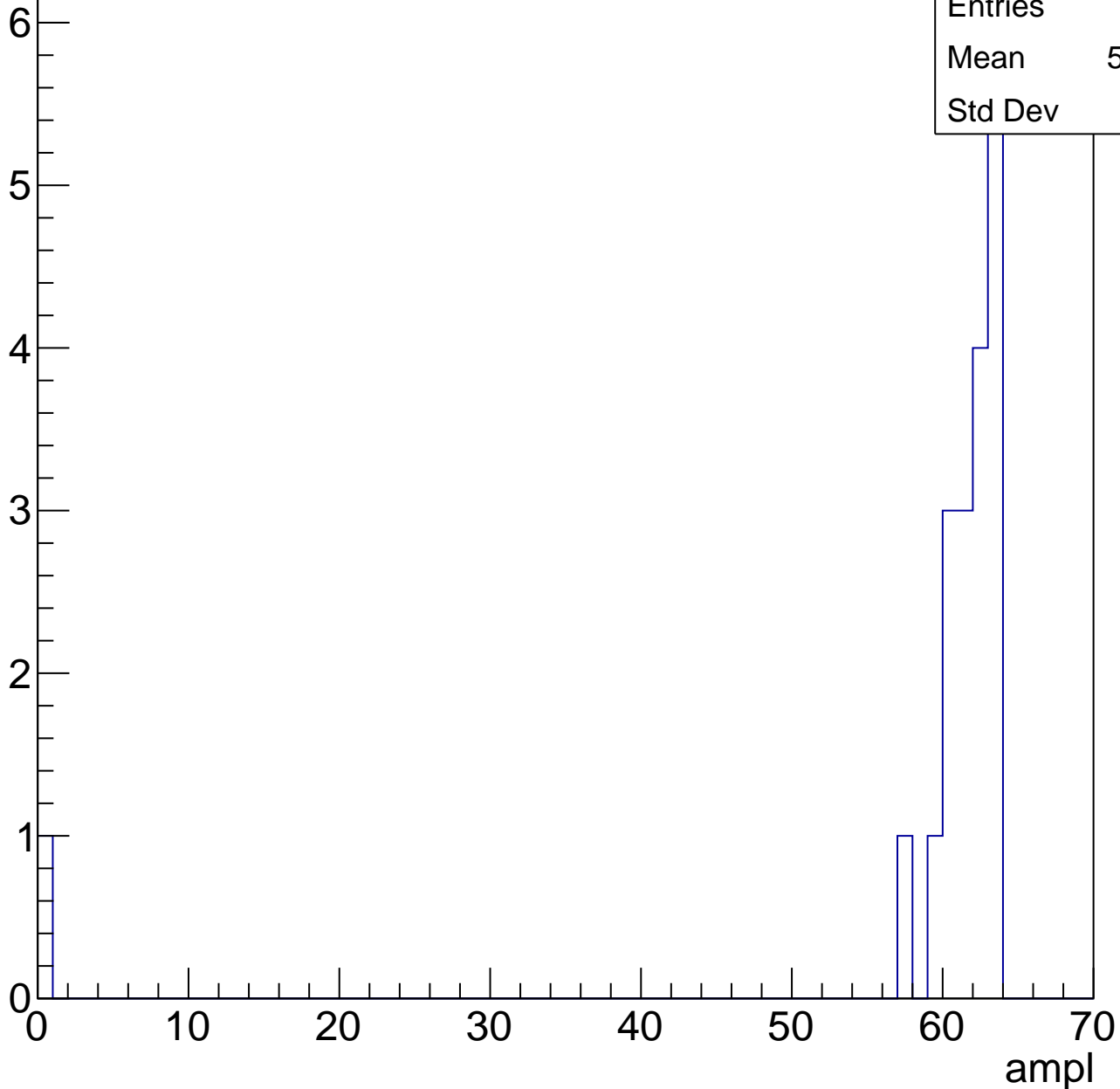


B1L001S, U20-ch55, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	58.16
Std Dev	13.8



B1L001S, U20-ch55, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L001S, U20-ch56, adc0

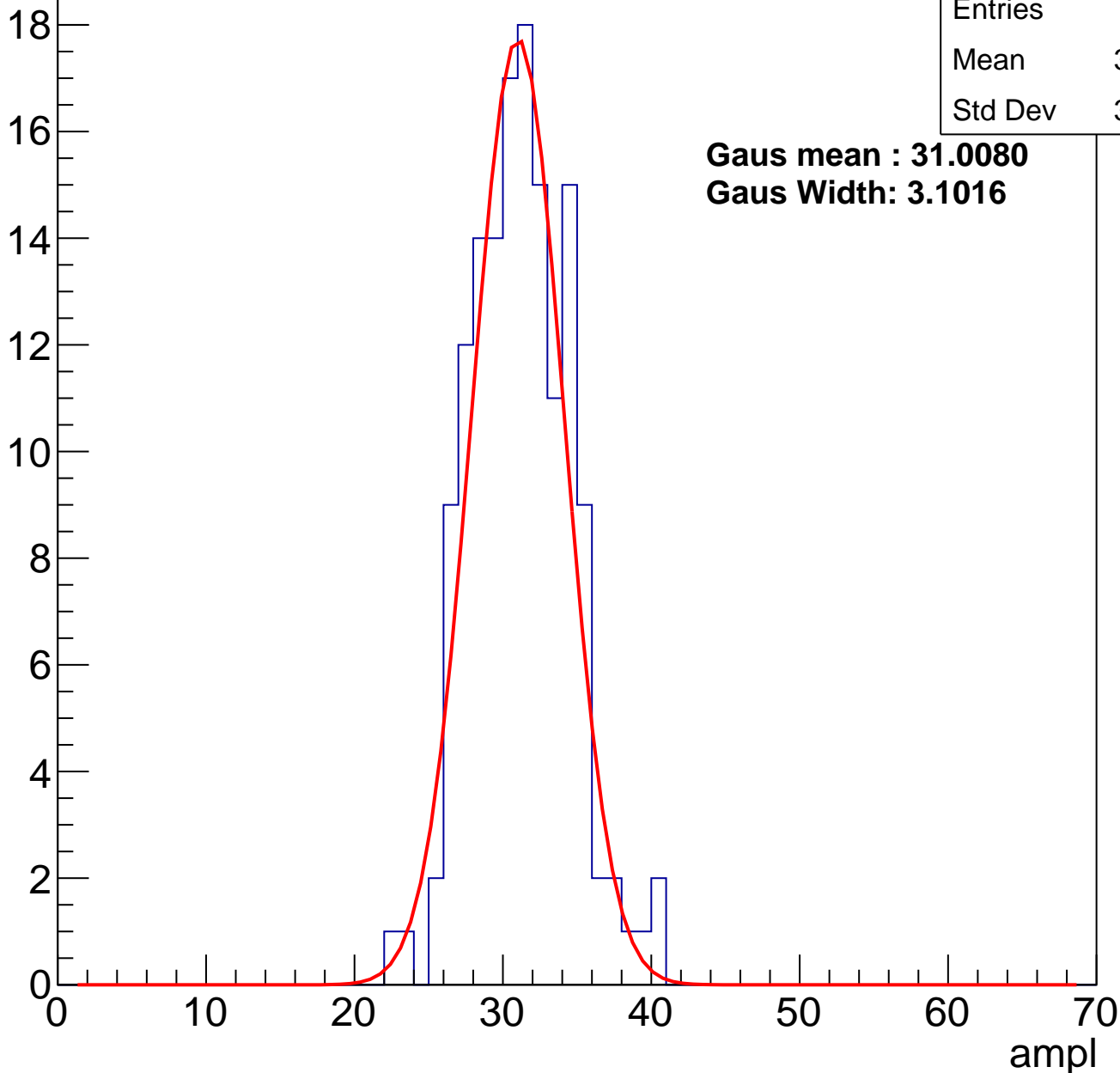
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	30.75
Std Dev	3.268

Gaus mean : 31.0080

Gaus Width: 3.1016

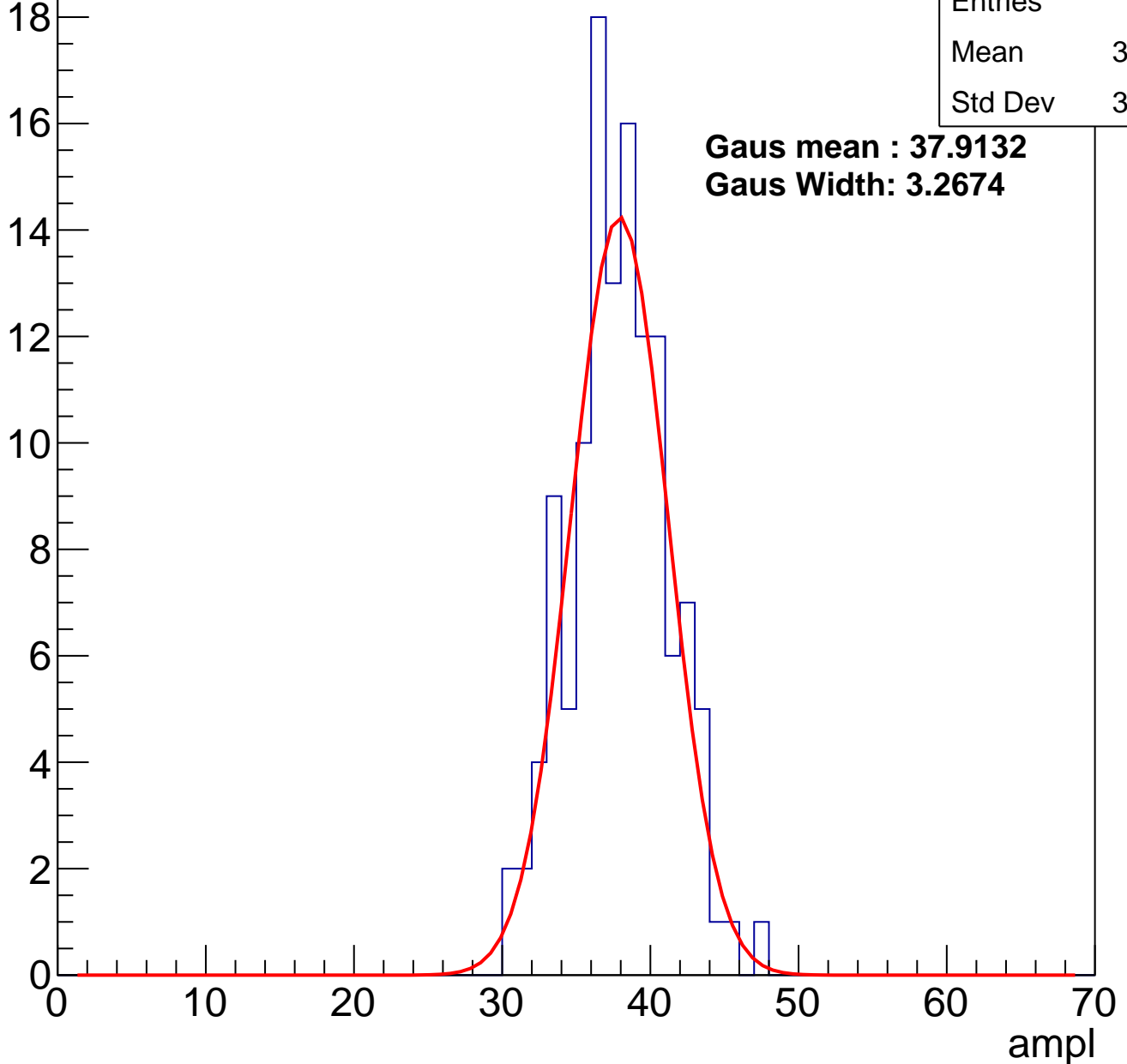
Entry



B1L001S, U20-ch56, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch56, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	42.98
Std Dev	3.033

Gaus mean : 43.3330

Gaus Width: 2.7886

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

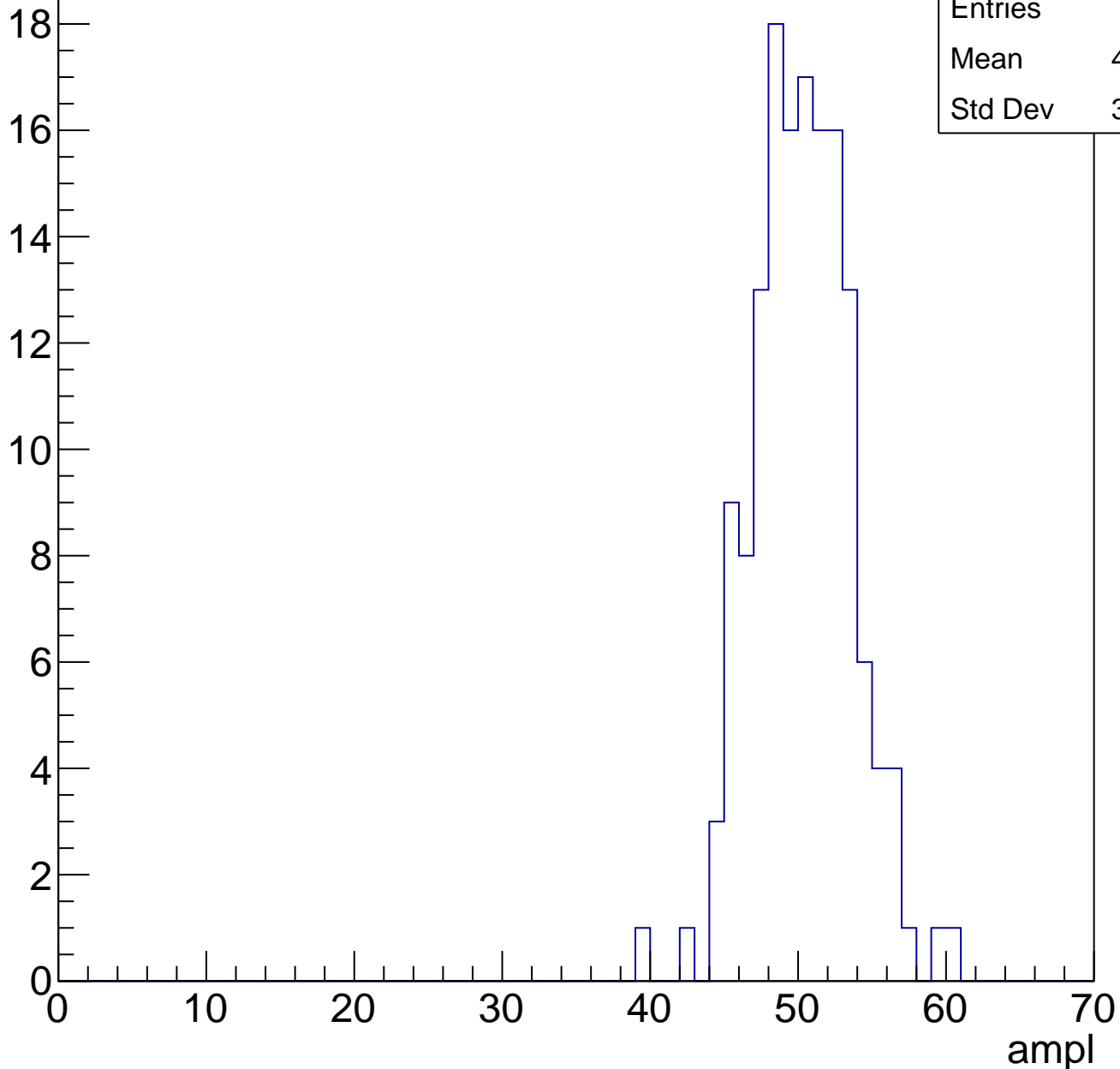
70

B1L001S, U20-ch56, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	49.84
Std Dev	3.302

Entry



B1L001S, U20-ch56, adc4

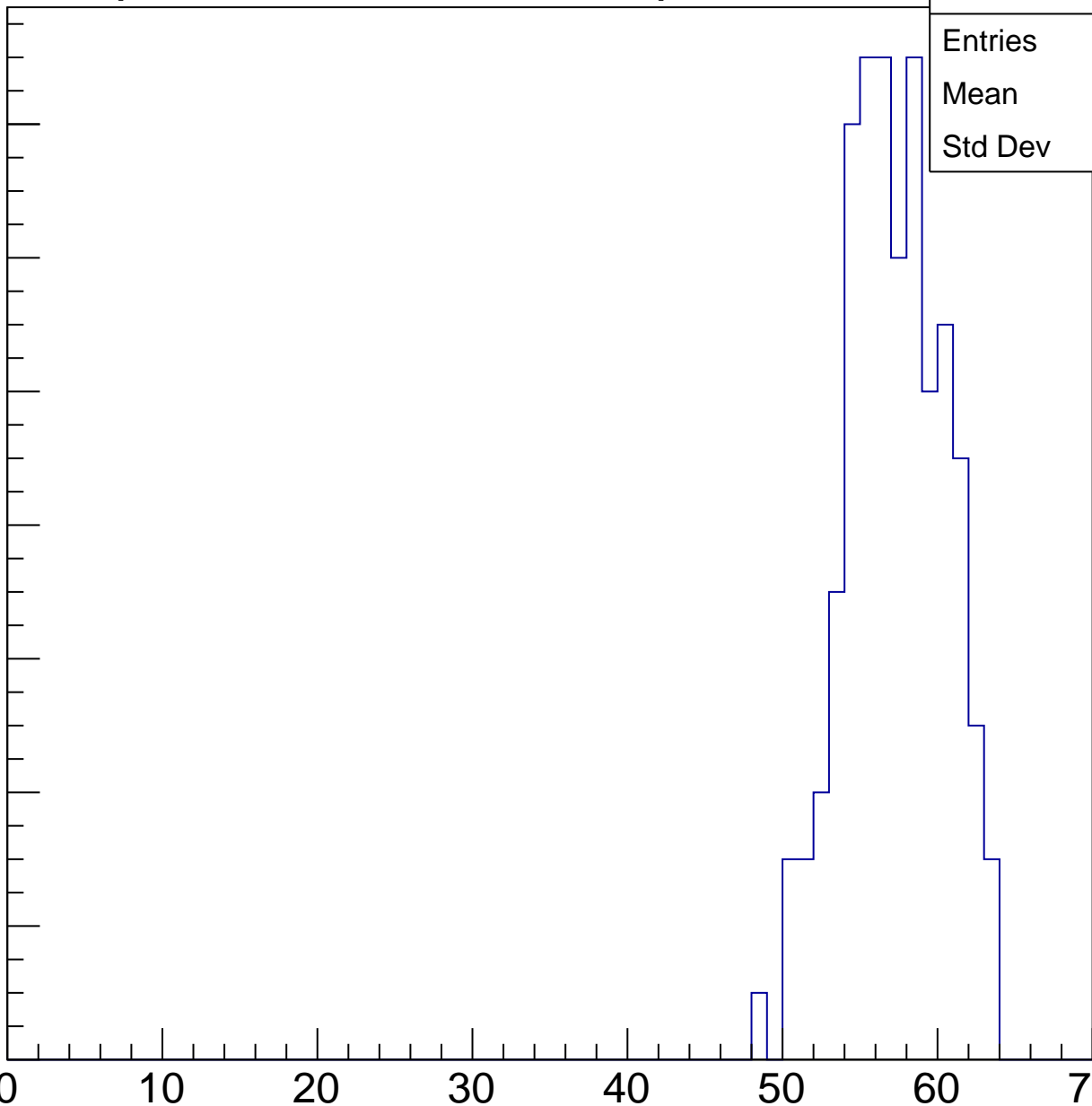
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	56.72
Std Dev	3.162

ampl



B1L001S, U20-ch56, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

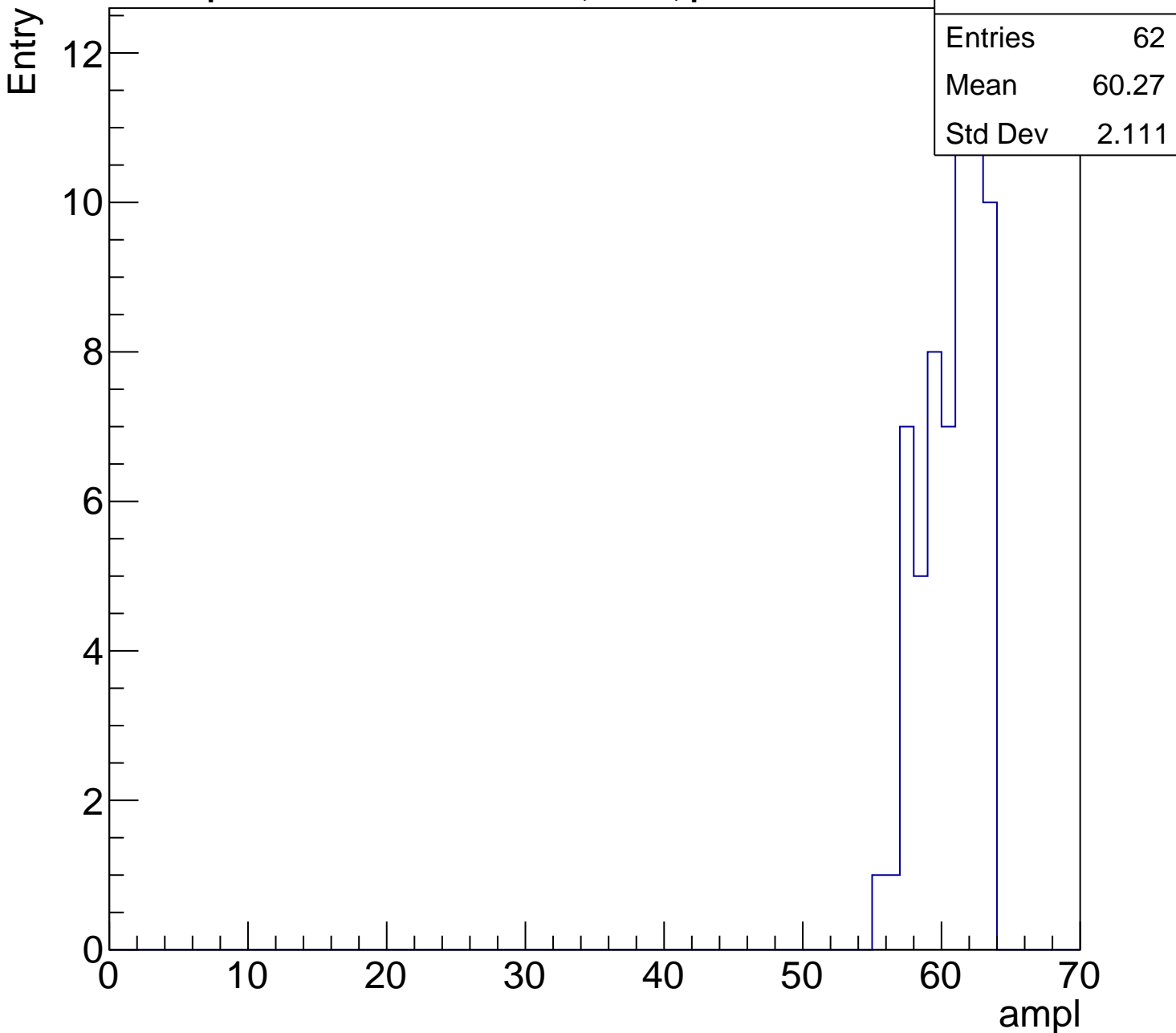
50

60

70

ampl

Entries	62
Mean	60.27
Std Dev	2.111

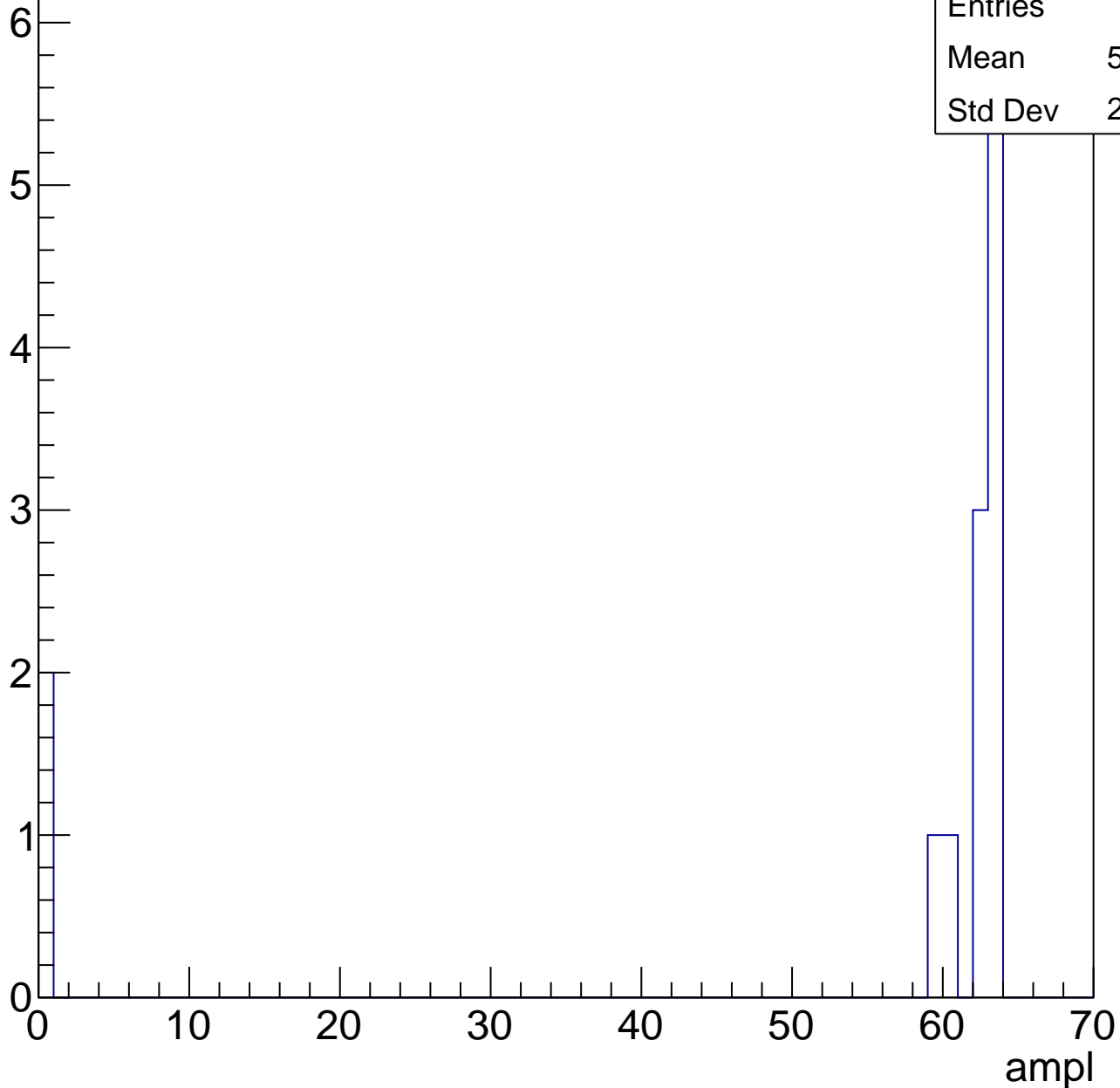


B1L001S, U20-ch56, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	52.54
Std Dev	22.43



B1L001S, U20-ch56, adc7

calib_packv5_042523_0143.root, FC#2, port C2

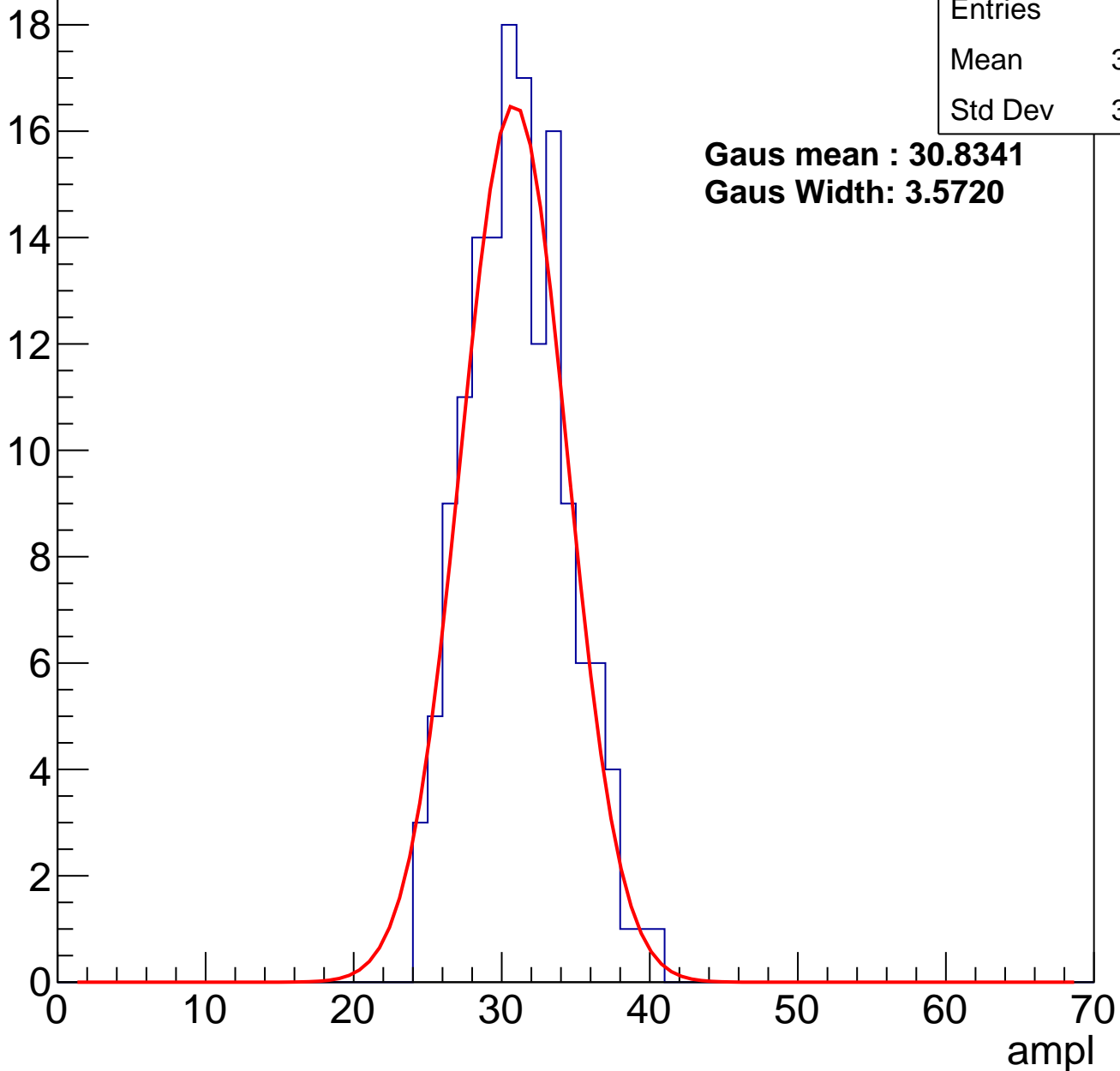
Entry



B1L001S, U20-ch57, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

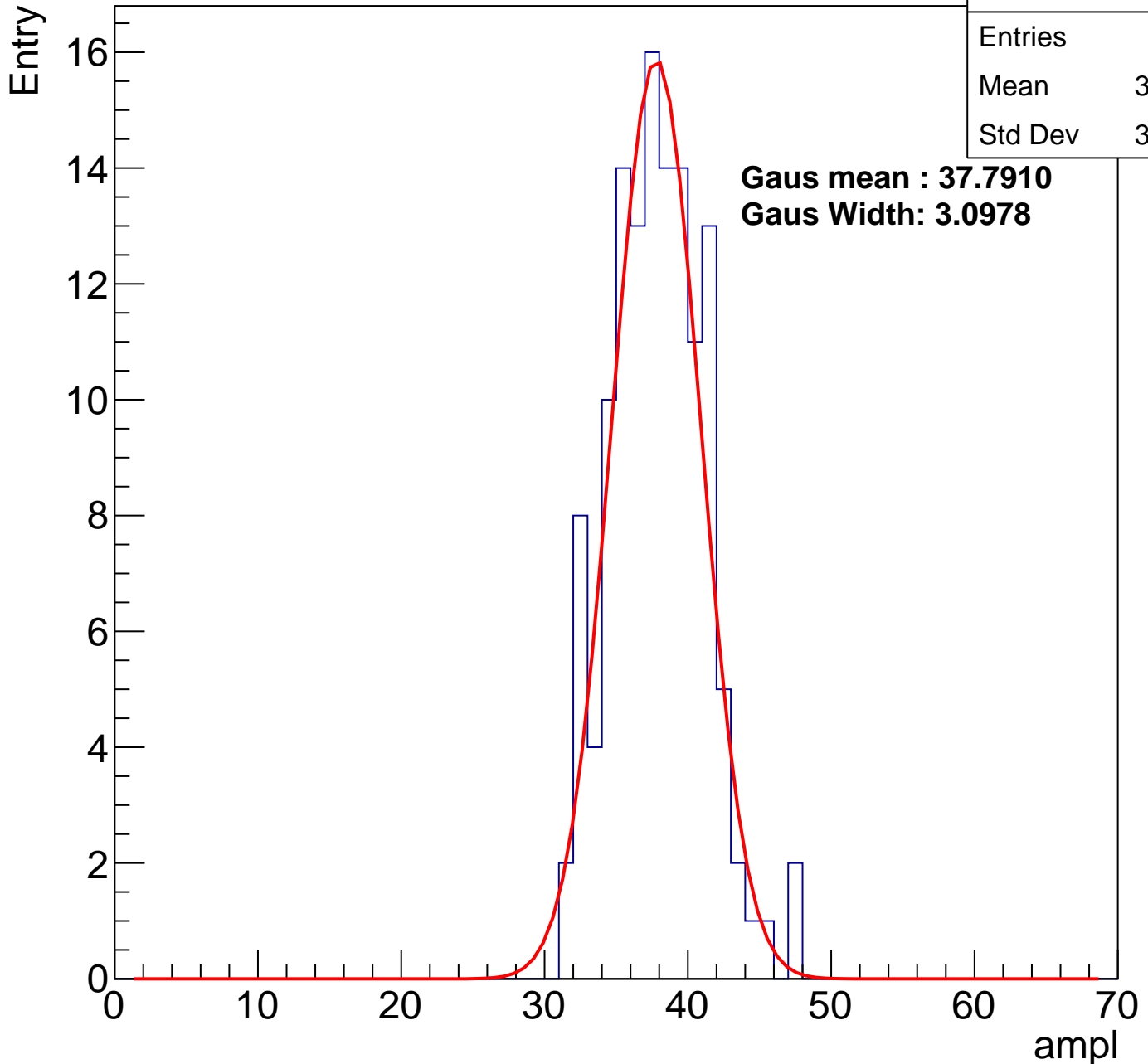


B1L001S, U20-ch57, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	37.46
Std Dev	3.228

Gaus mean : 37.7910
Gaus Width: 3.0978



B1L001S, U20-ch57, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	43.5
Std Dev	3.263

Gaus mean : 43.9066

Gaus Width: 3.2927

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

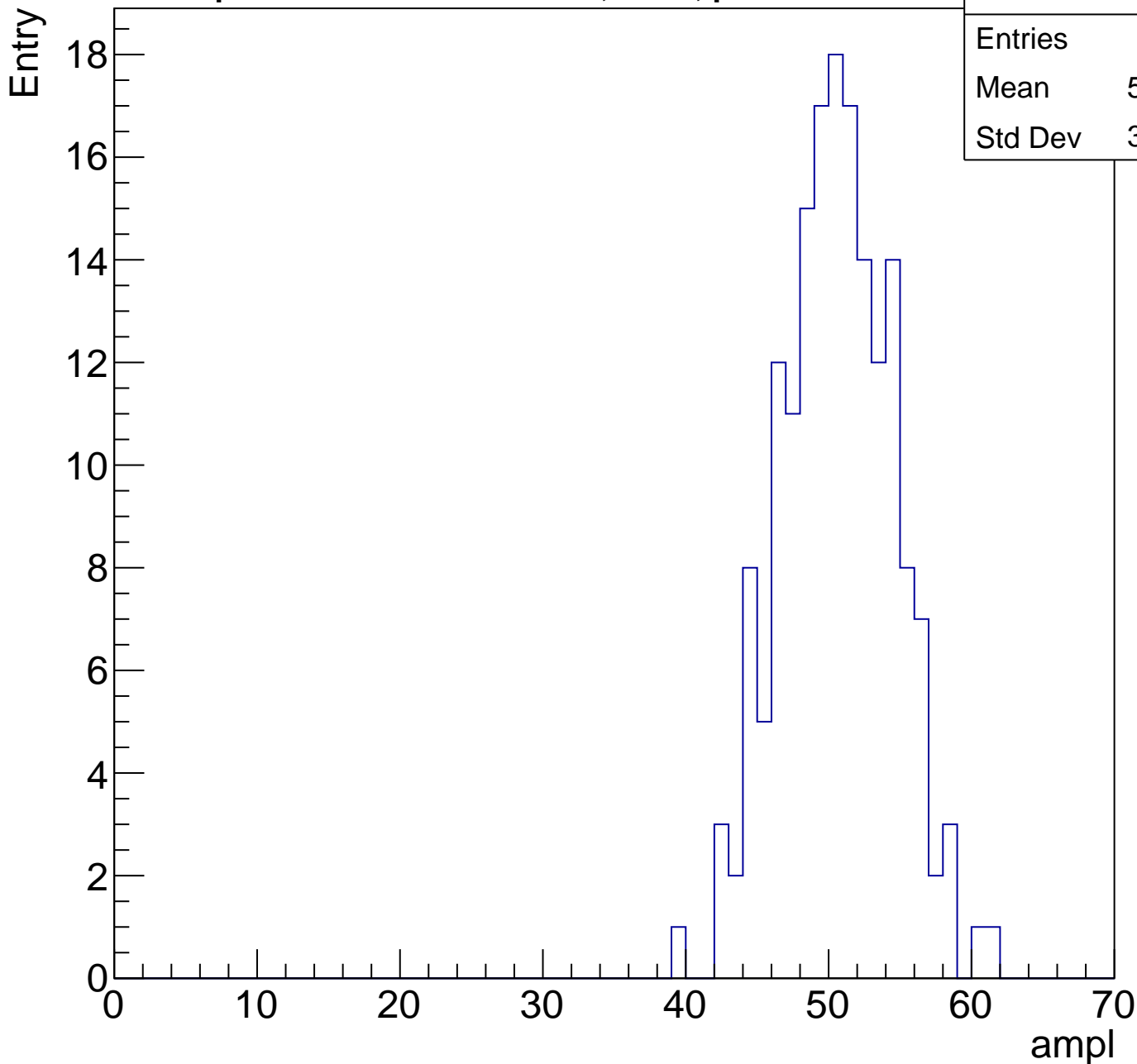
60

70

B1L001S, U20-ch57, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	171
Mean	50.16
Std Dev	3.866



B1L001S, U20-ch57, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	81
Mean	56.36
Std Dev	2.886

Entry

12

10

8

6

4

2

0

0

10

20

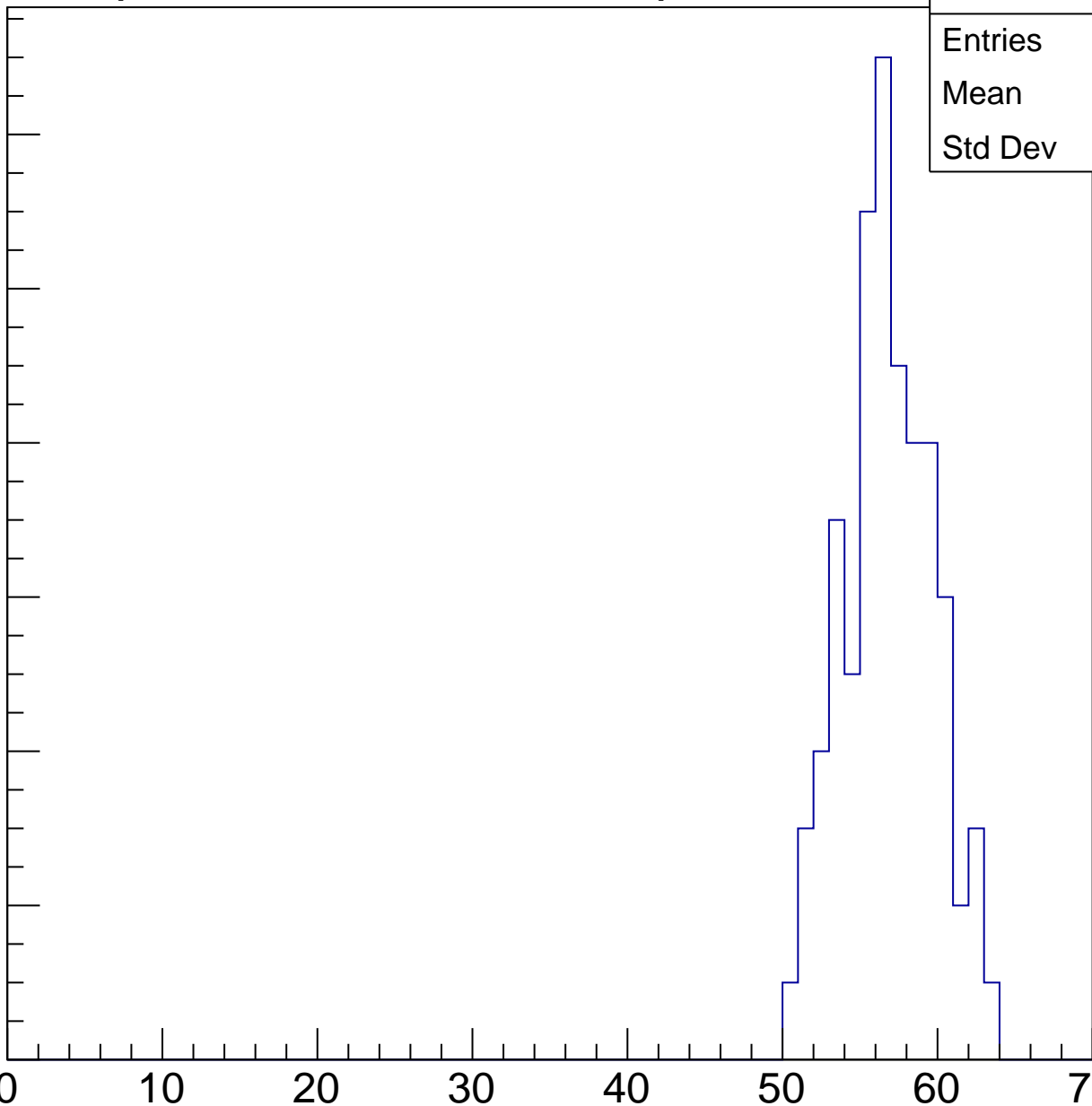
30

40

50

60

ampl



B1L001S, U20-ch57, adc5

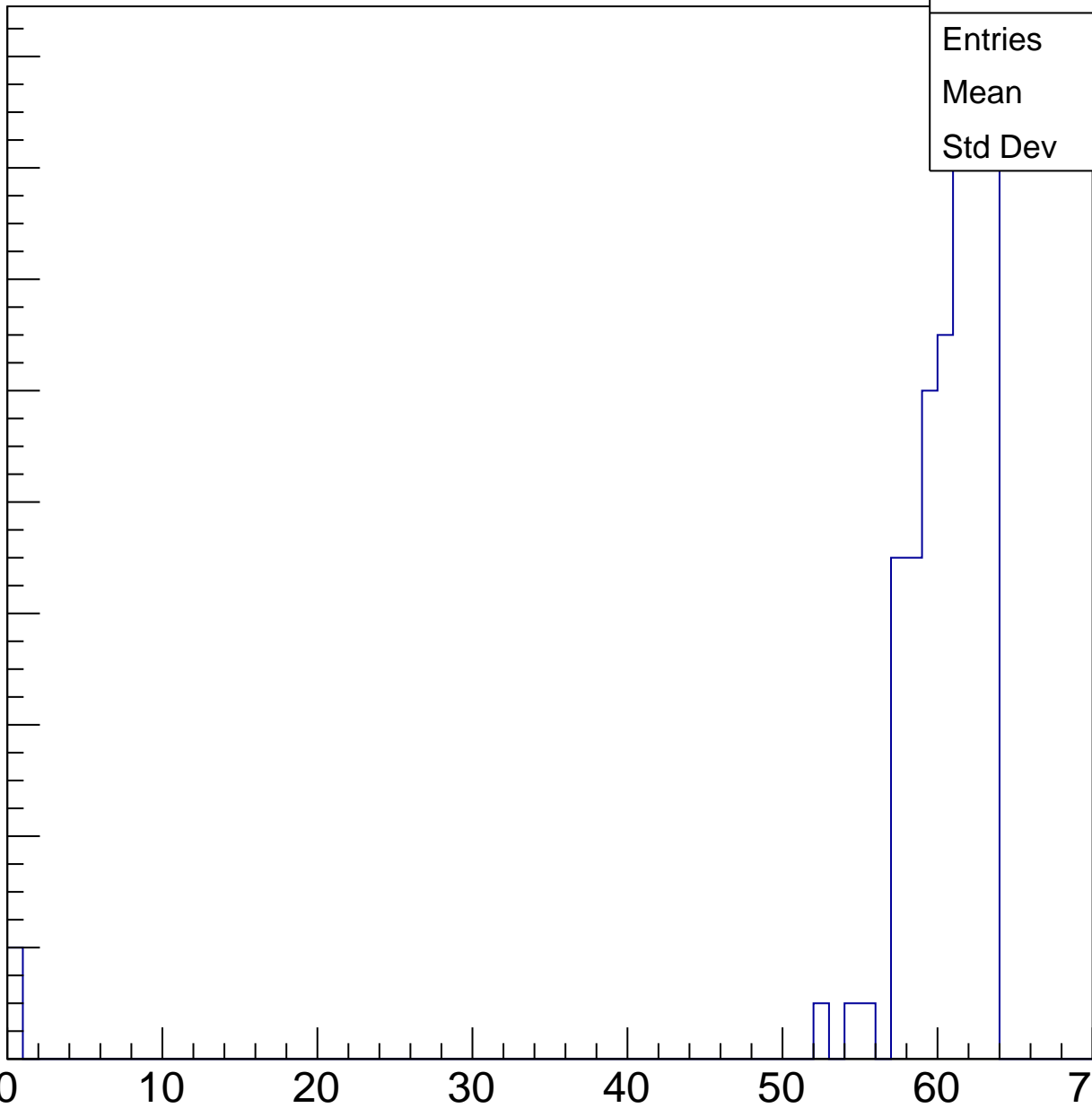
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.06
Std Dev	8.767

ampl



B1L001S, U20-ch57, adc6

calib_packv5_042523_0143.root, FC#2, port C2

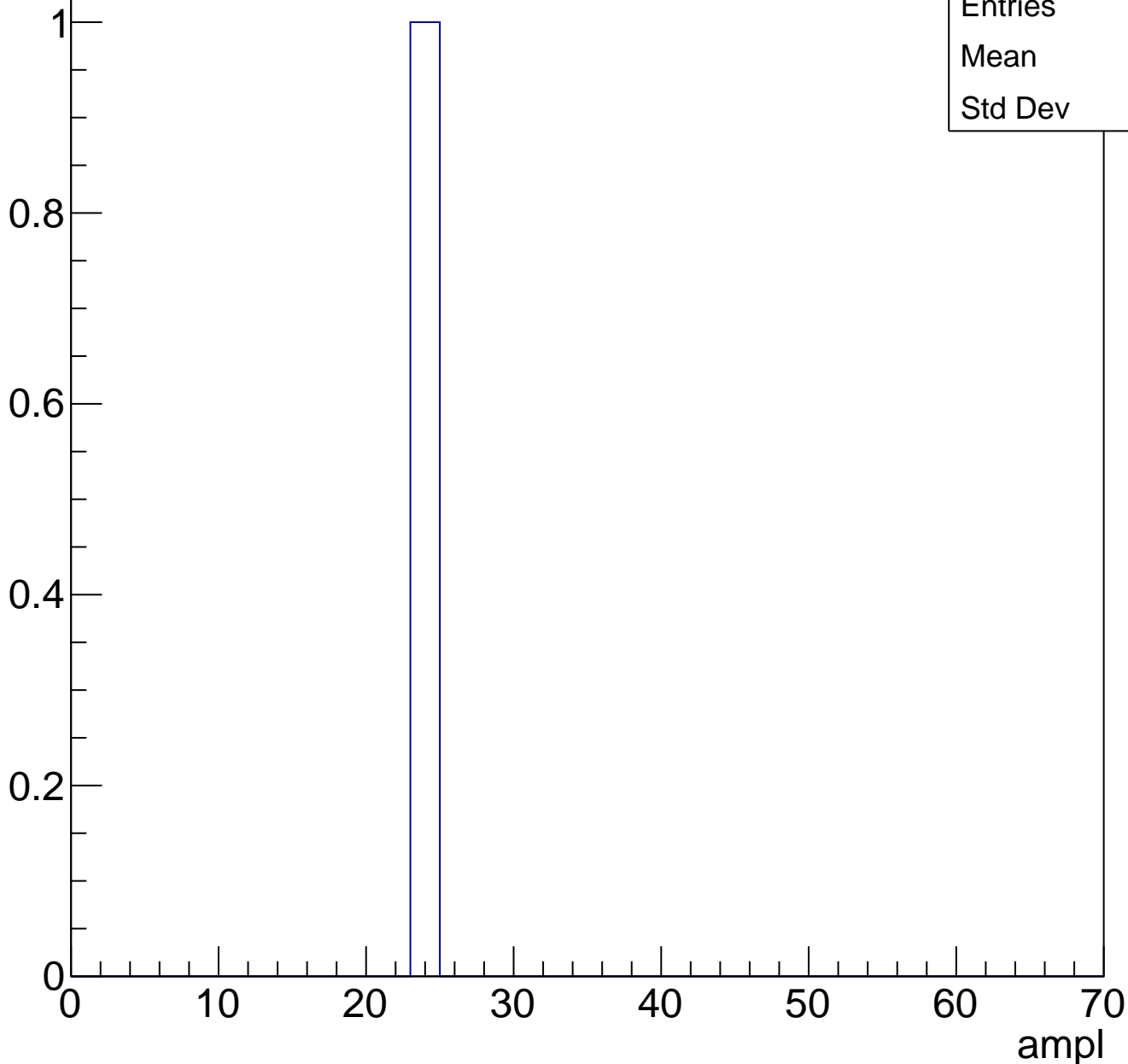
Entry



B1L001S, U20-ch57, adc7

calib_packv5_042523_0143.root, FC#2, port C2

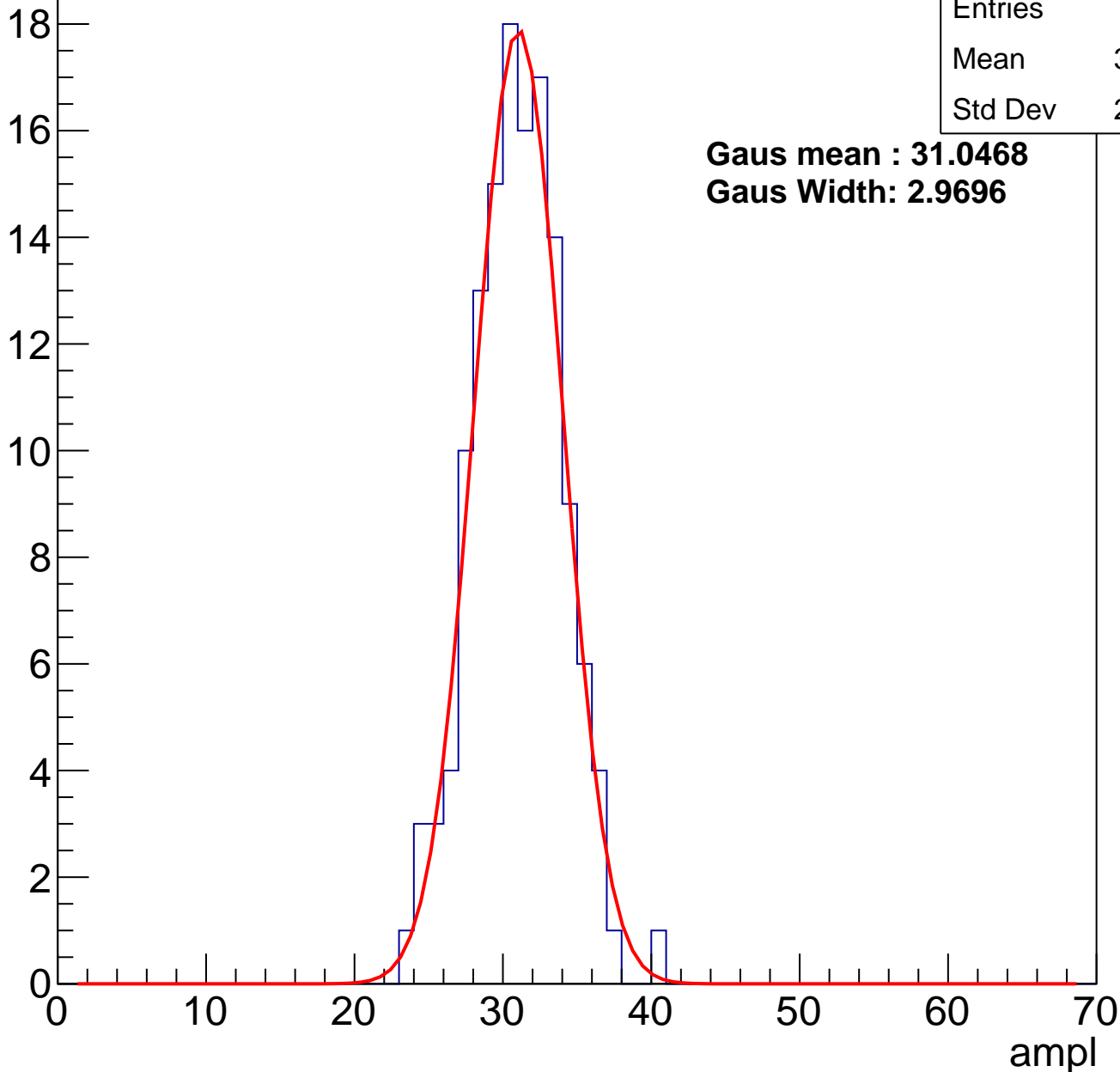
Entry



B1L001S, U20-ch58, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch58, adc1

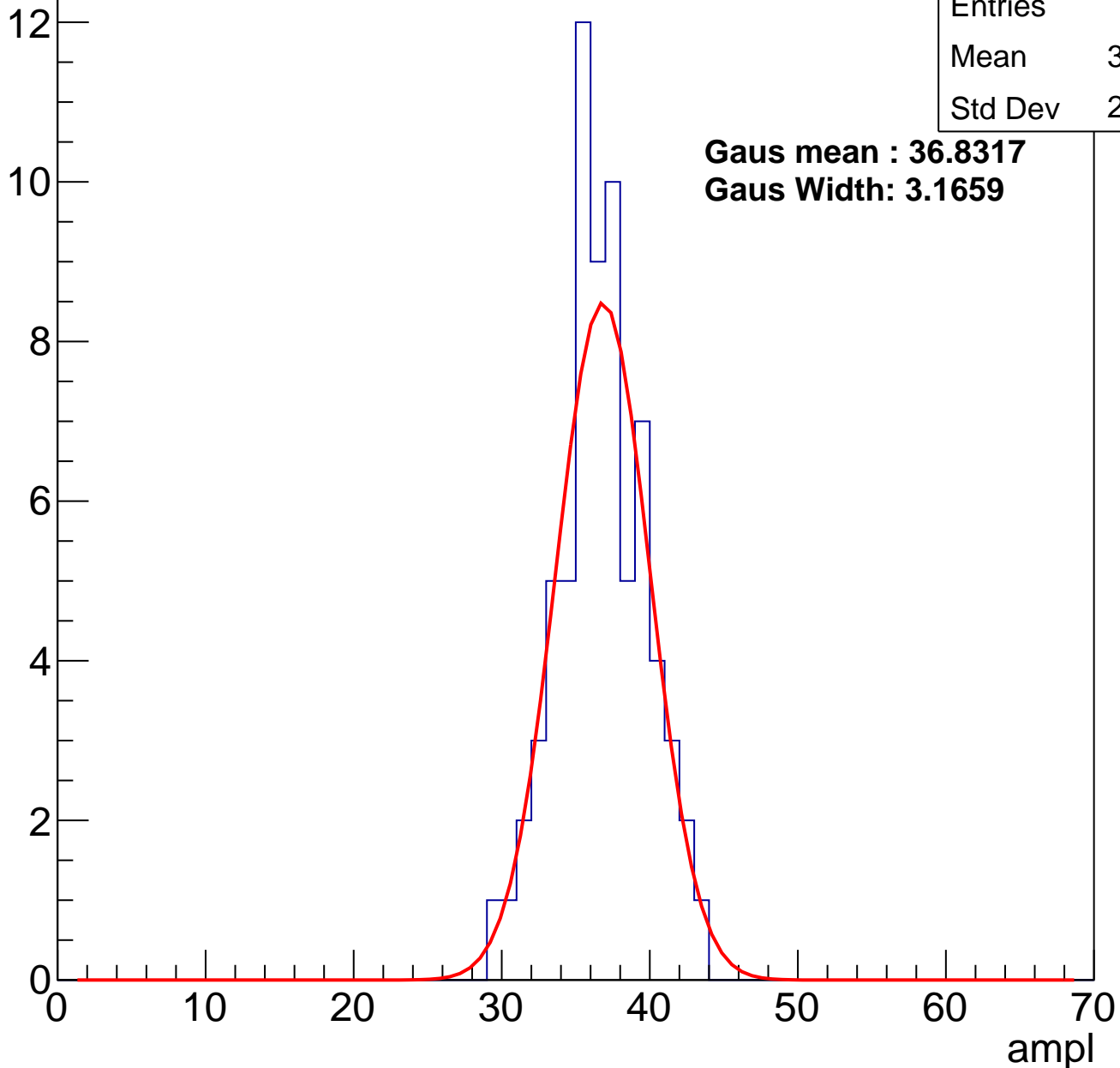
calib_packv5_042523_0143.root, FC#2, port C2

Entries	70
Mean	36.27
Std Dev	2.947

Gaus mean : 36.8317

Gaus Width: 3.1659

Entry



B1L001S, U20-ch58, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	41.33
Std Dev	3.7

Gaus mean : 41.7206

Gaus Width: 3.5525

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

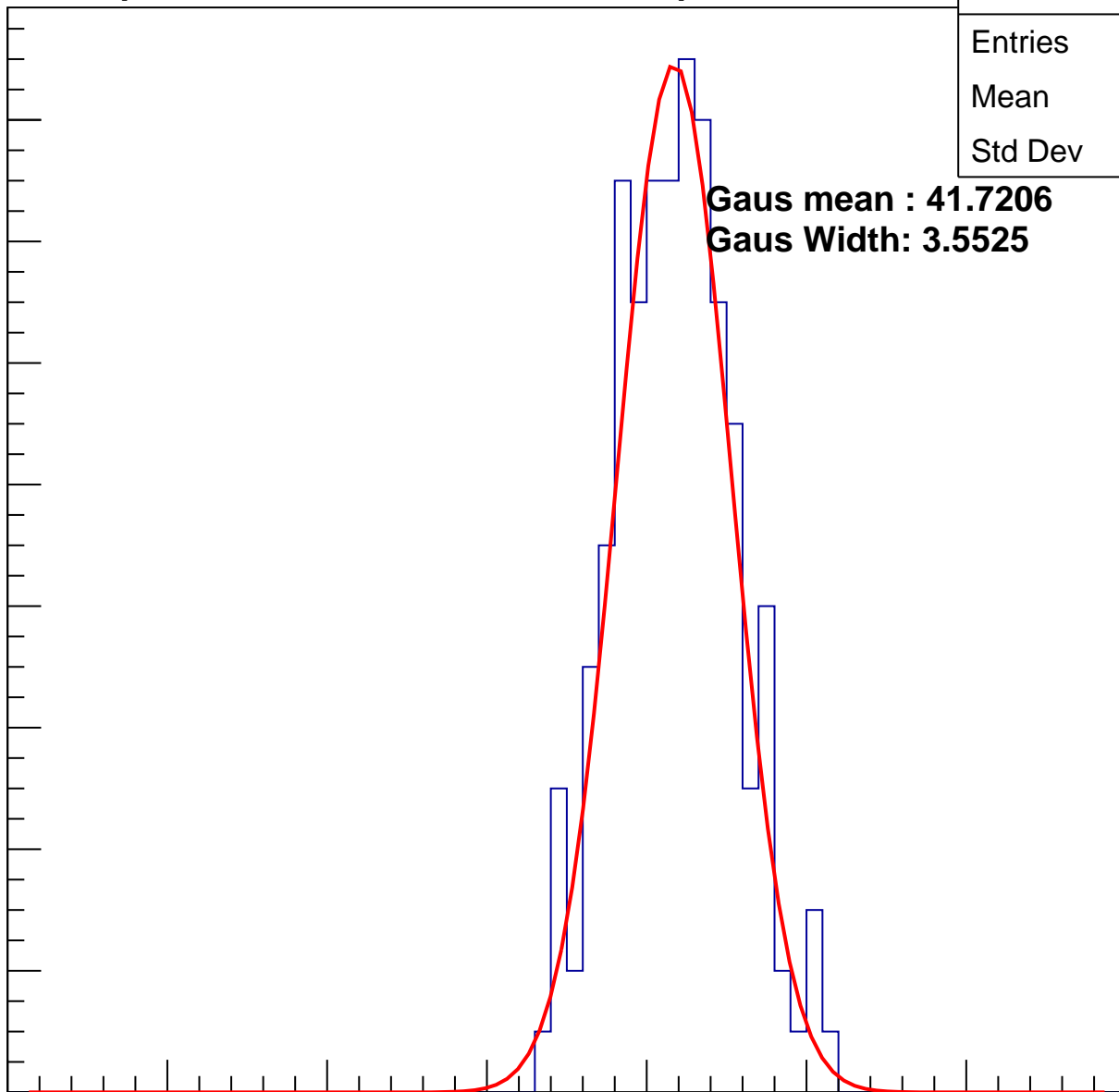
40

50

60

70

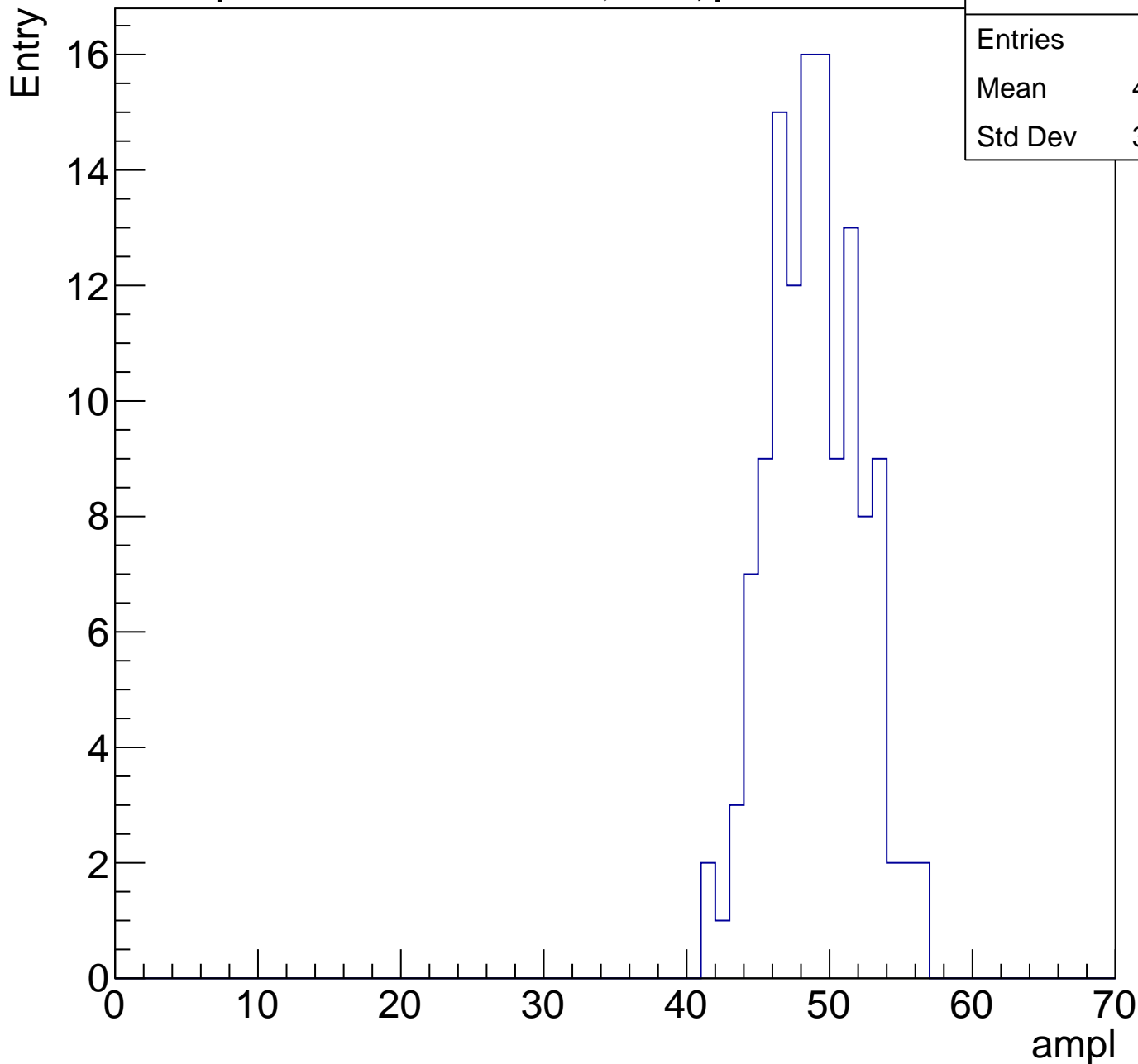
ampl



B1L001S, U20-ch58, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	48.48
Std Dev	3.169



B1L001S, U20-ch58, adc4

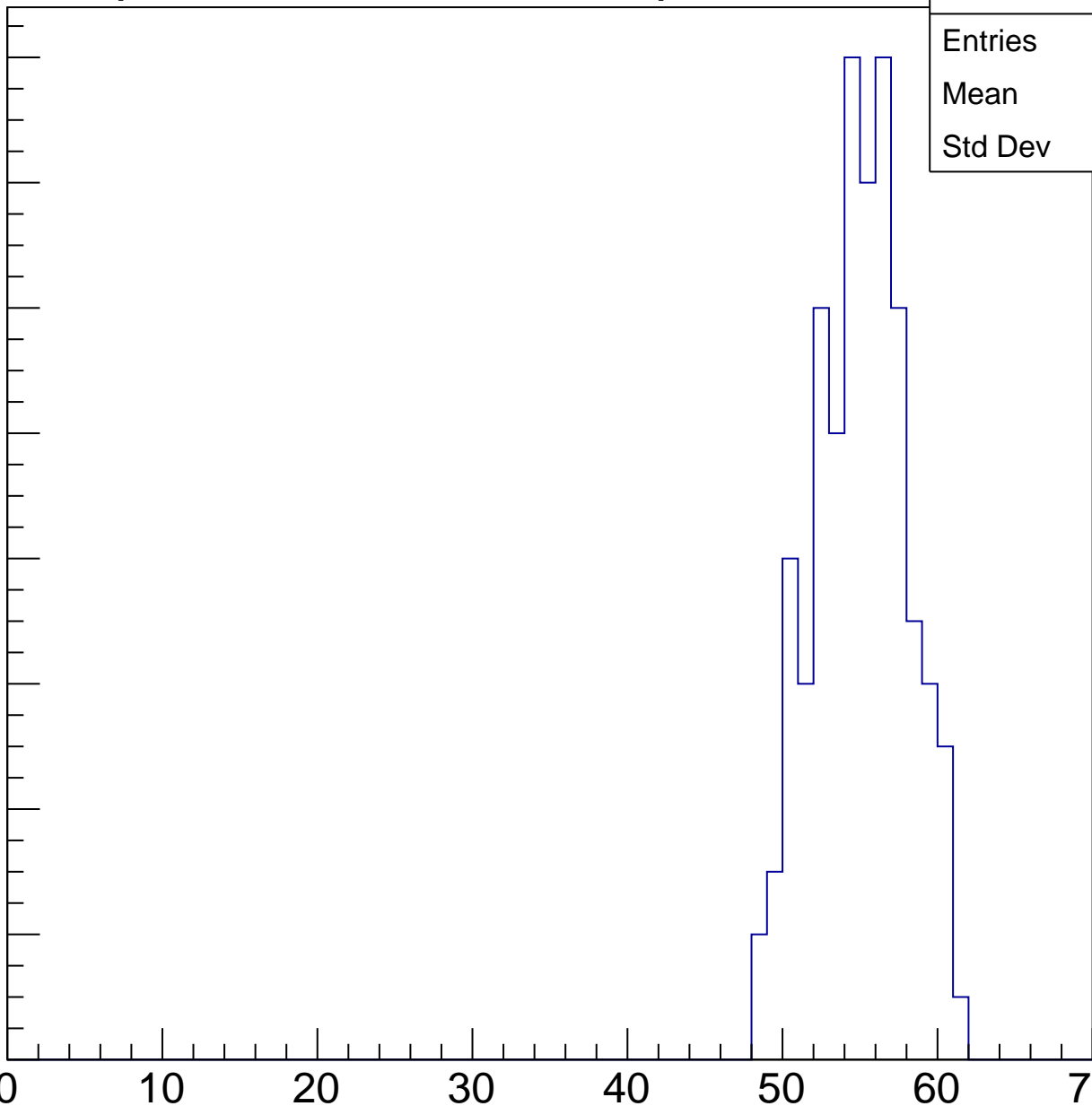
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	54.56
Std Dev	2.95

ampl



B1L001S, U20-ch58, adc5

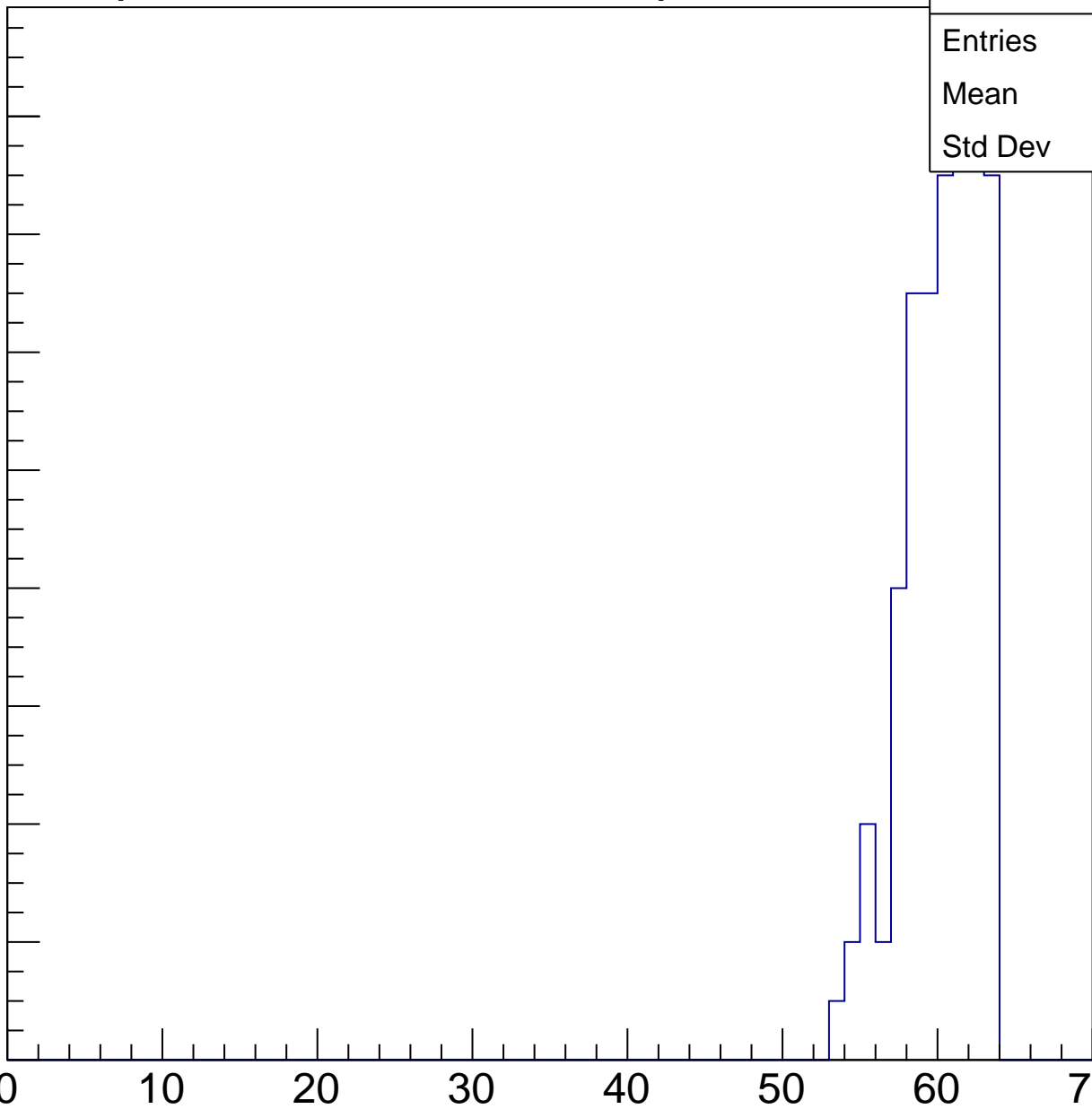
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	59.85
Std Dev	2.386

ampl

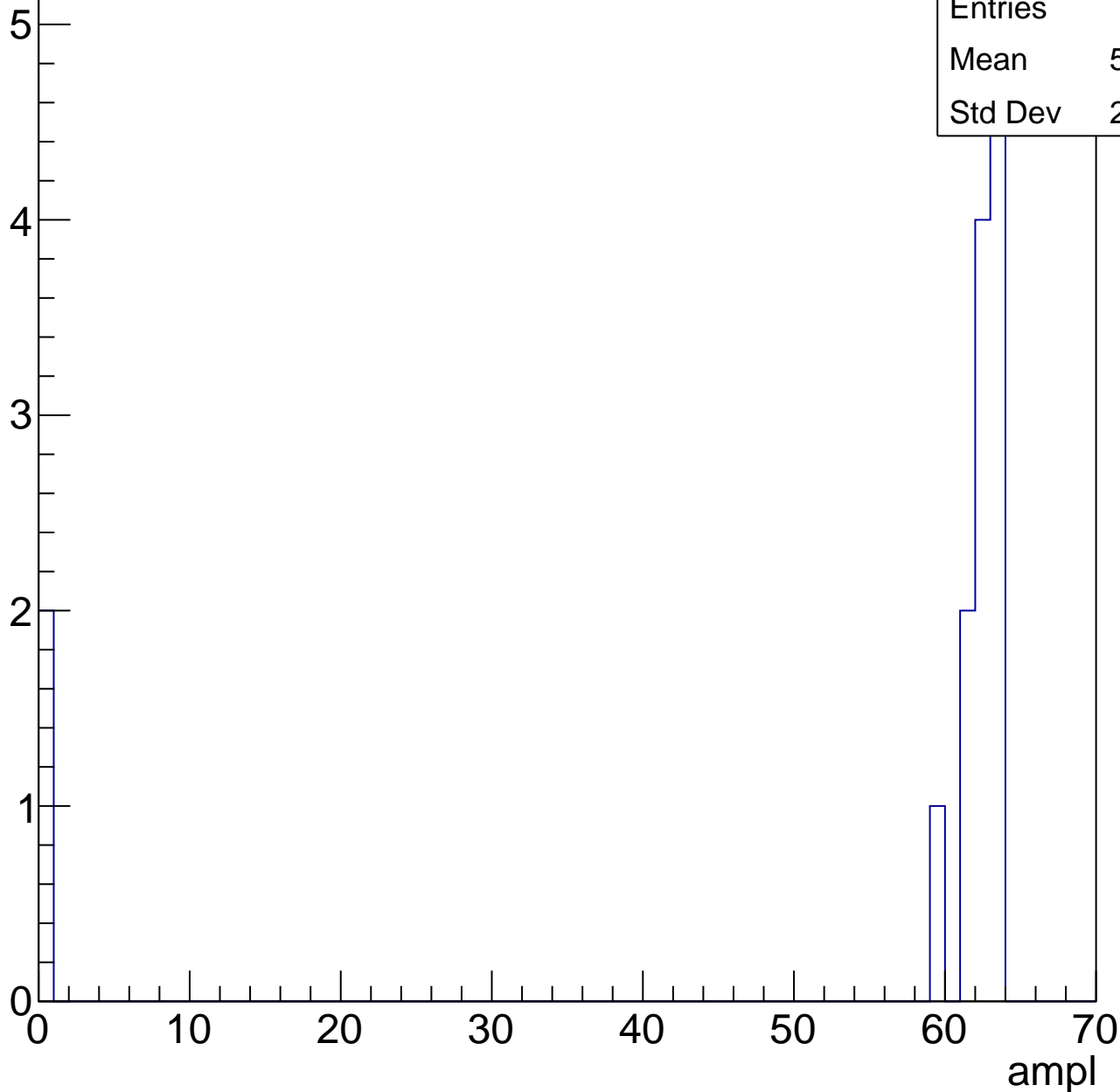


B1L001S, U20-ch58, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	53.14
Std Dev	21.72



B1L001S, U20-ch58, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch59, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	31.58
Std Dev	3.343

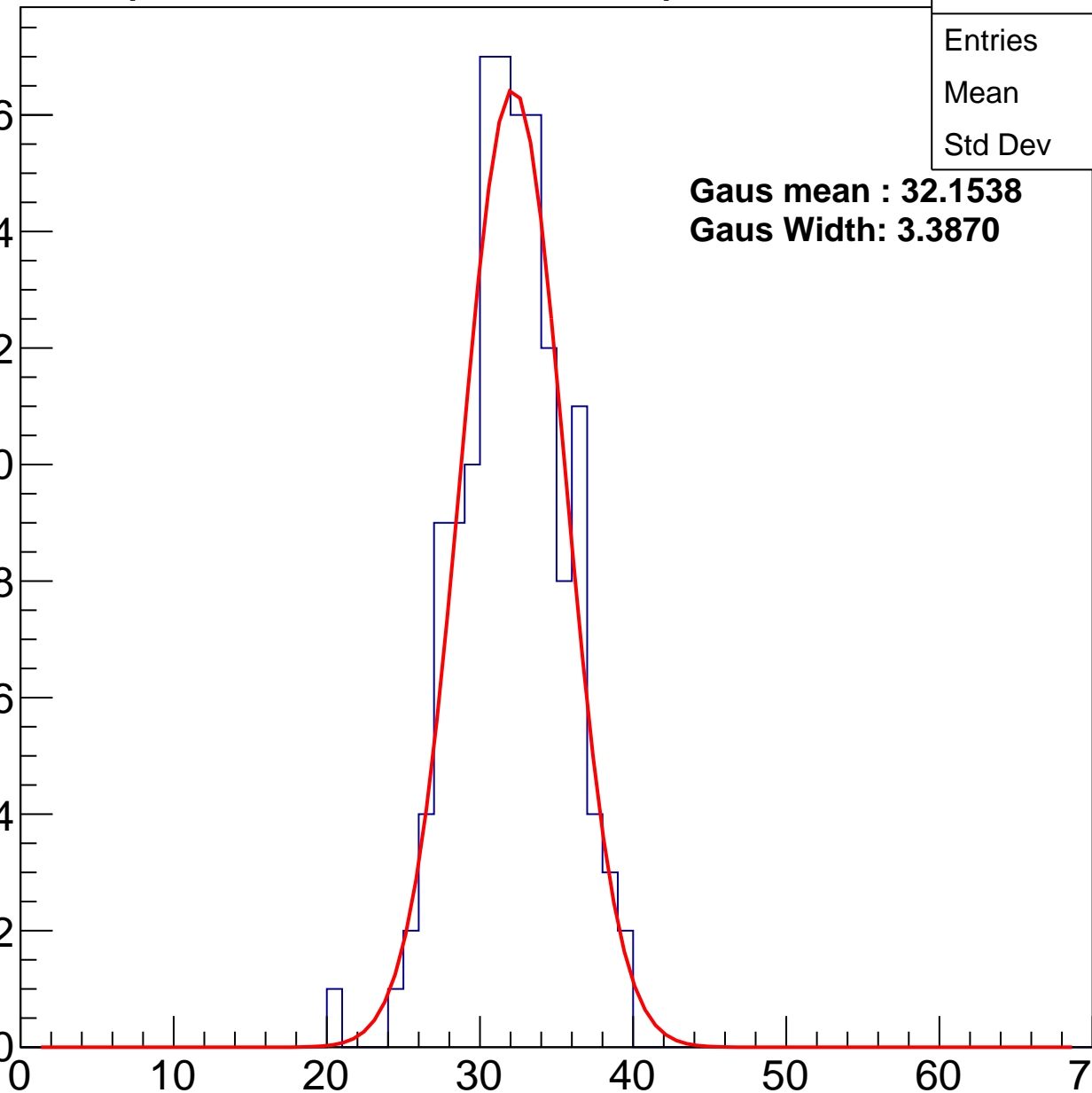
Gaus mean : 32.1538

Gaus Width: 3.3870

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch59, adc1

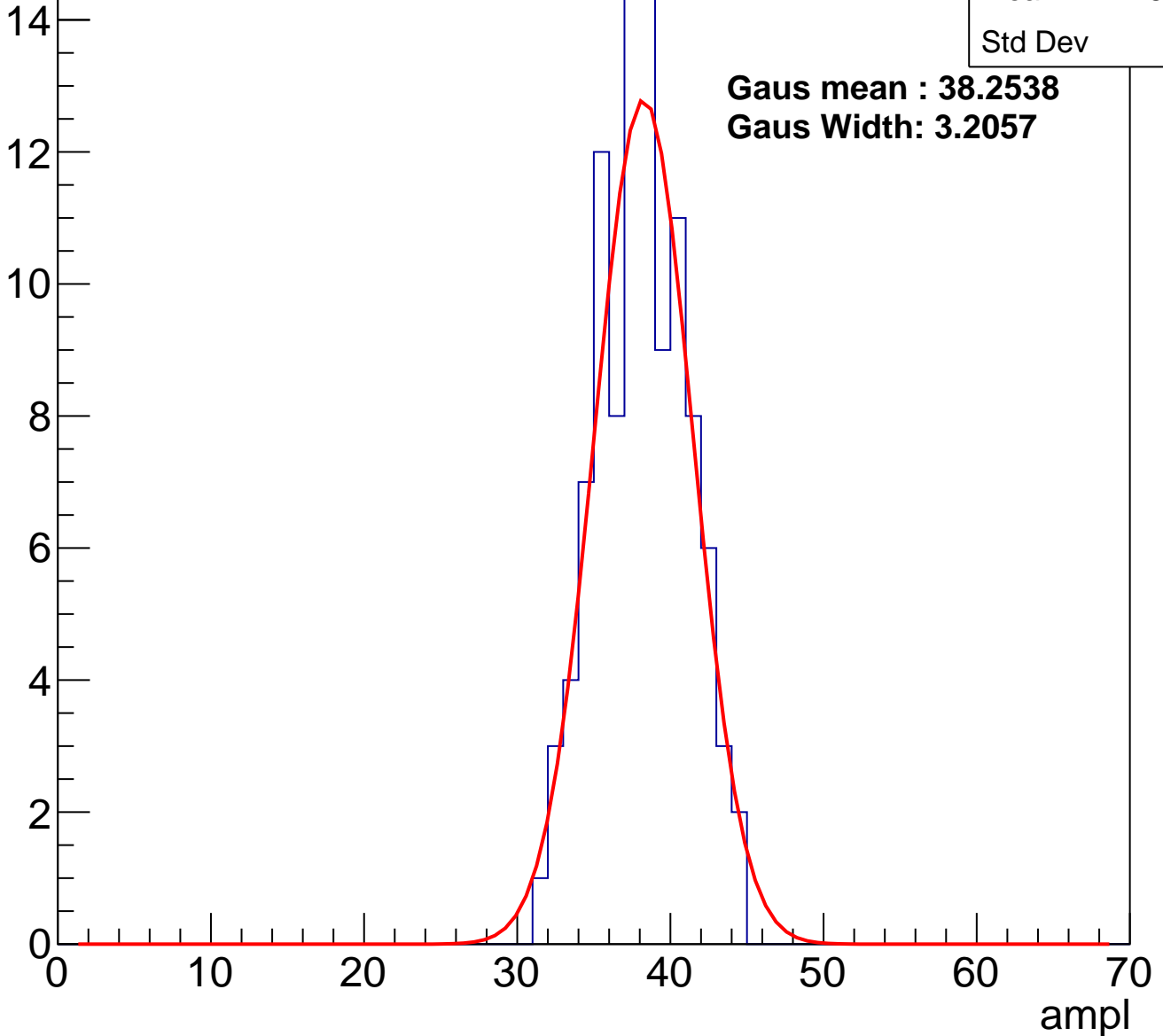
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	104
Mean	37.67
Std Dev	2.91

Gaus mean : 38.2538

Gaus Width: 3.2057



B1L001S, U20-ch59, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

136

Mean

43.72

Std Dev

3.029

Gaus mean : 44.4179

Gaus Width: 3.1341

0

10

20

30

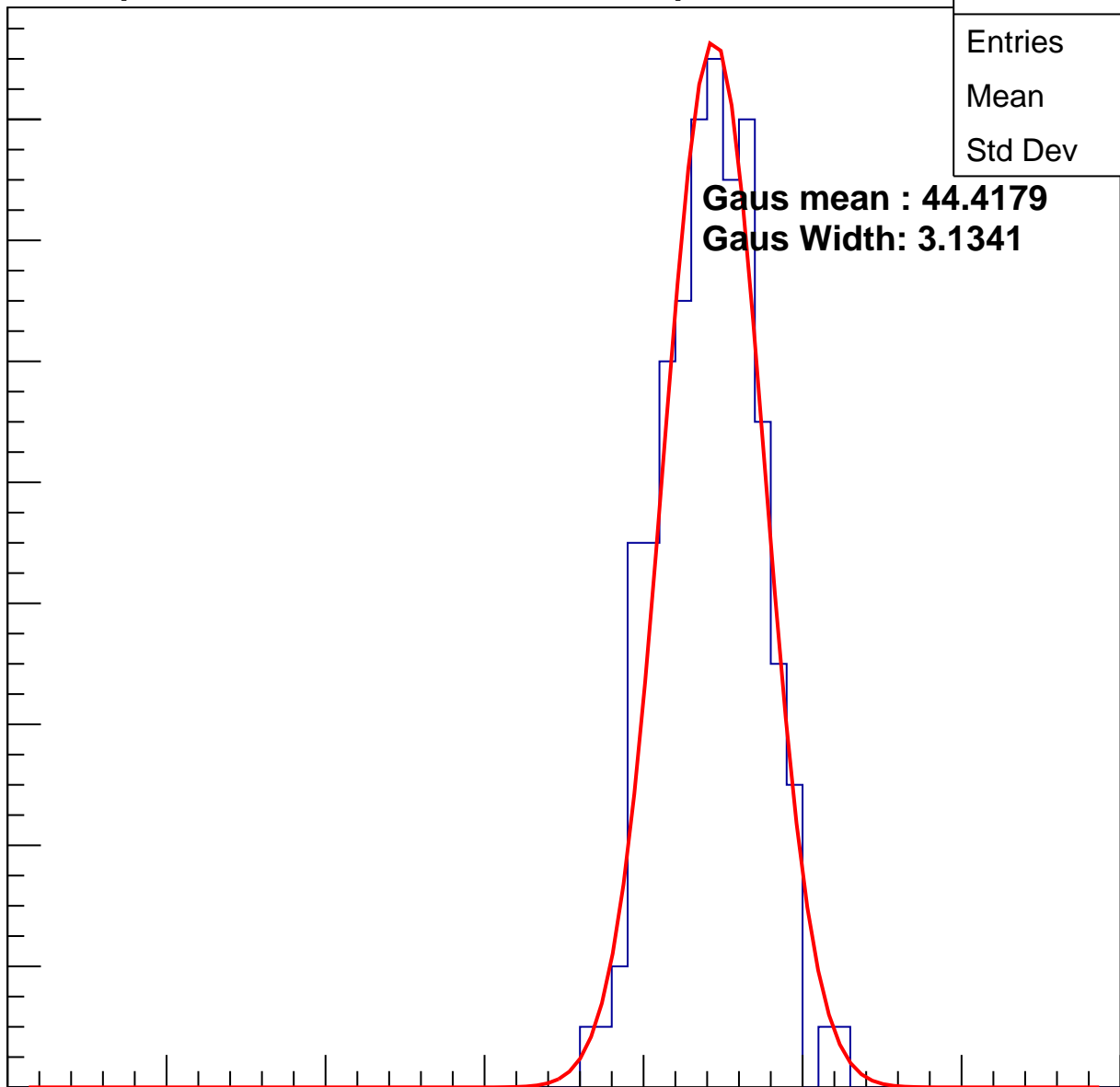
40

50

60

70

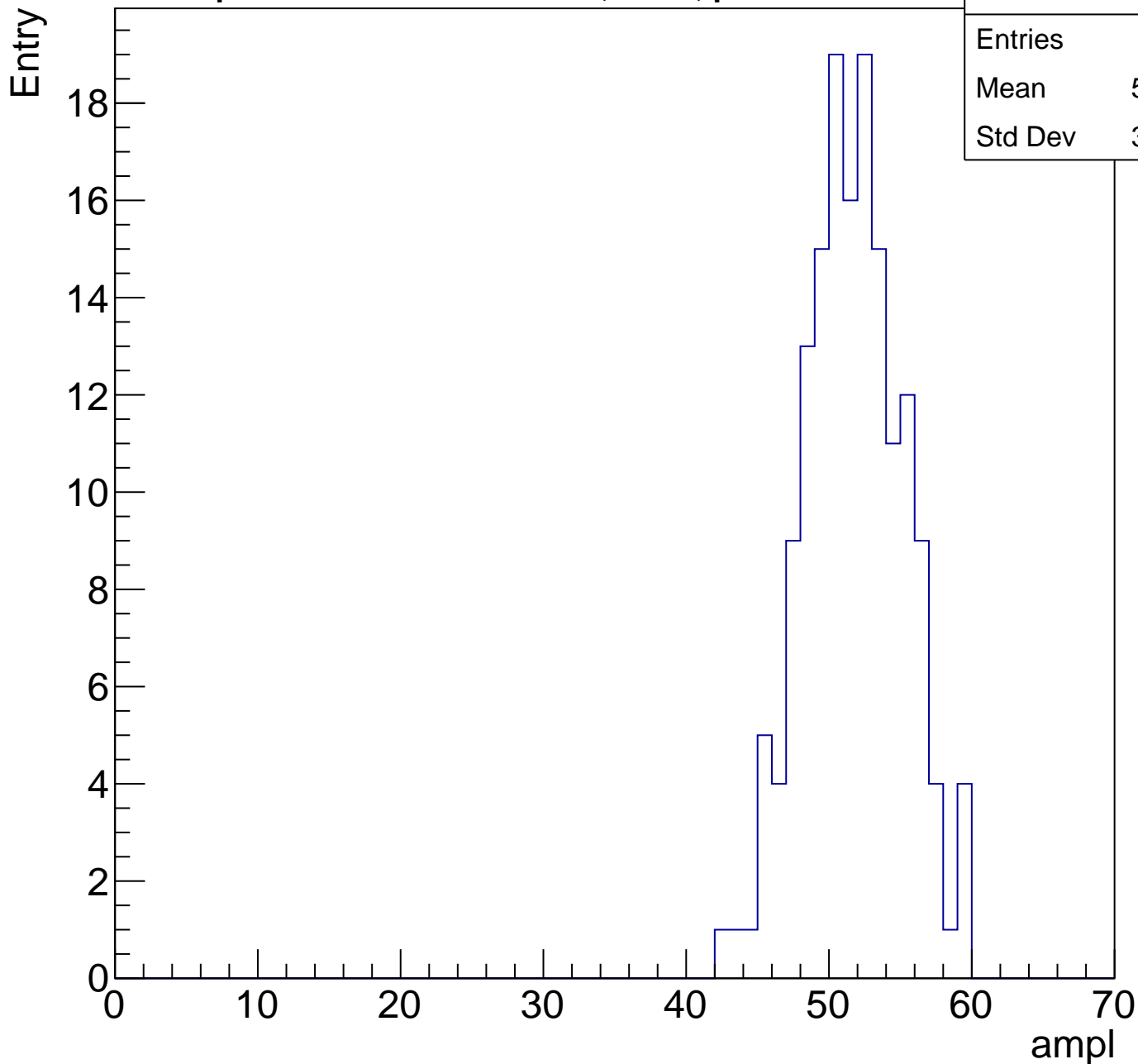
ampl



B1L001S, U20-ch59, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	51.25
Std Dev	3.409



B1L001S, U20-ch59, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	76
Mean	56.95
Std Dev	3.004

Entry

12

10

8

6

4

2

0

0

10

20

30

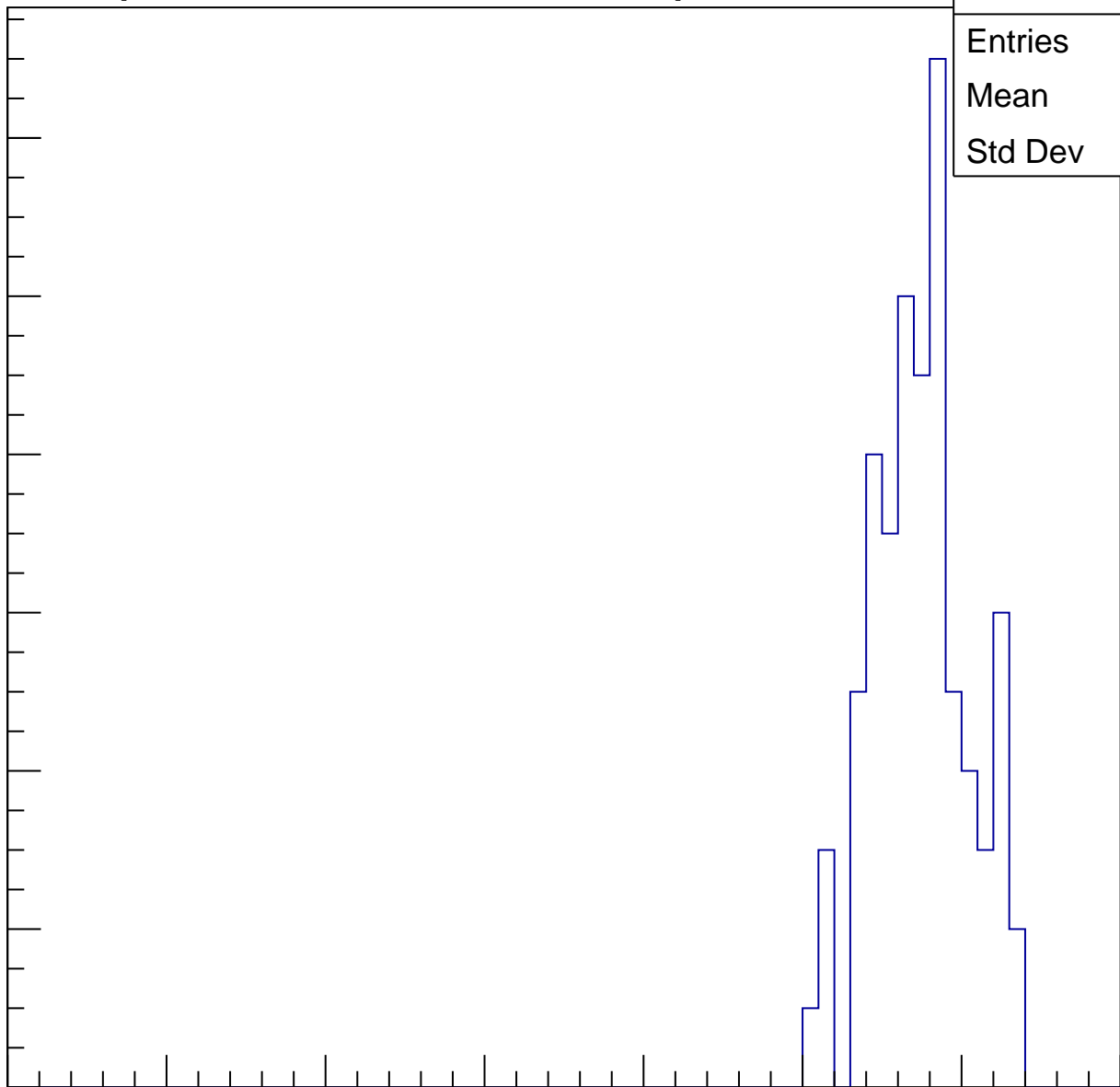
40

50

60

70

ampl



B1L001S, U20-ch59, adc5

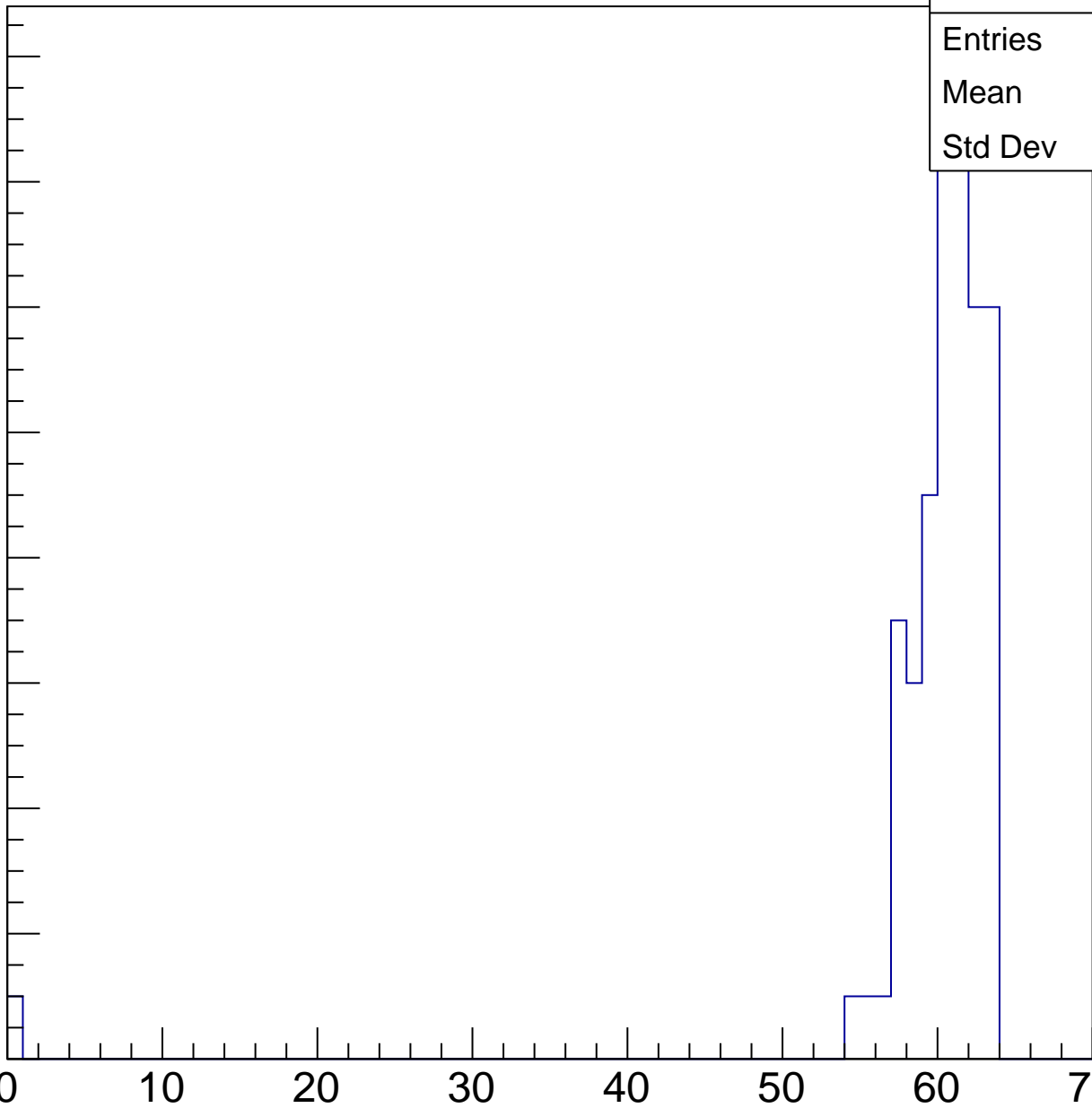
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	81
Mean	59.49
Std Dev	6.96

ampl

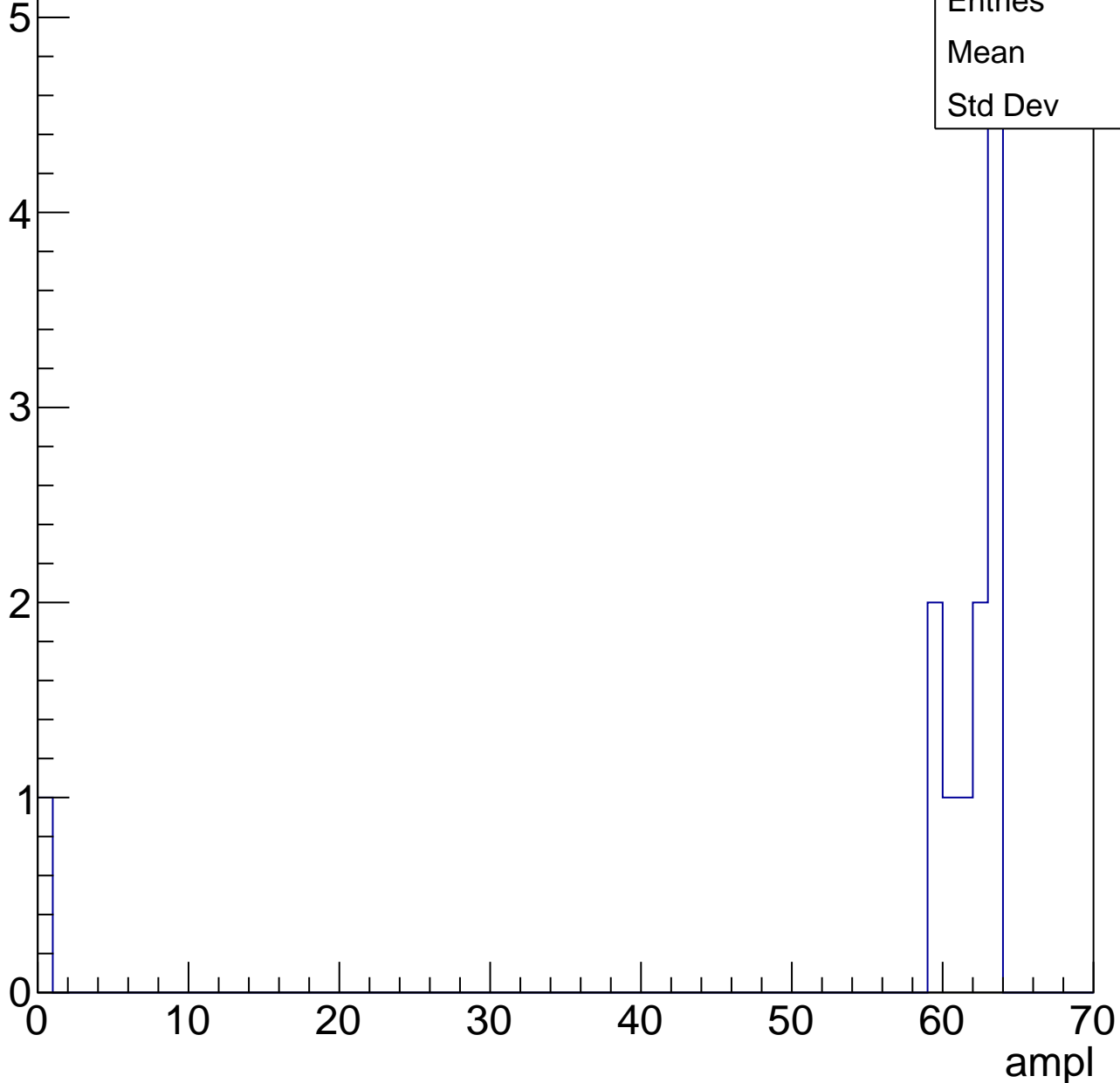


B1L001S, U20-ch59, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

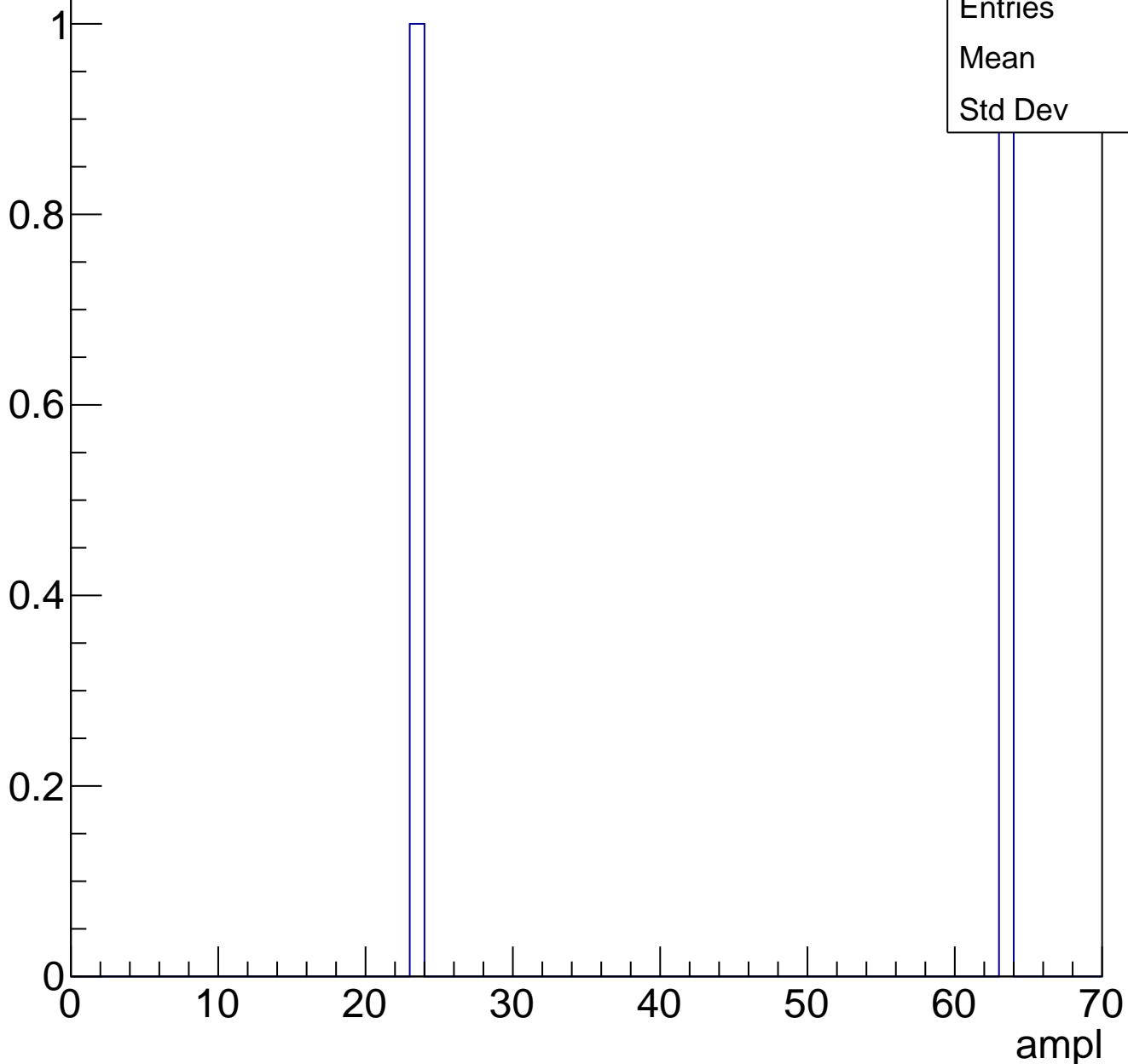
Entries	12
Mean	56.5
Std Dev	17.1



B1L001S, U20-ch59, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch60, adc0

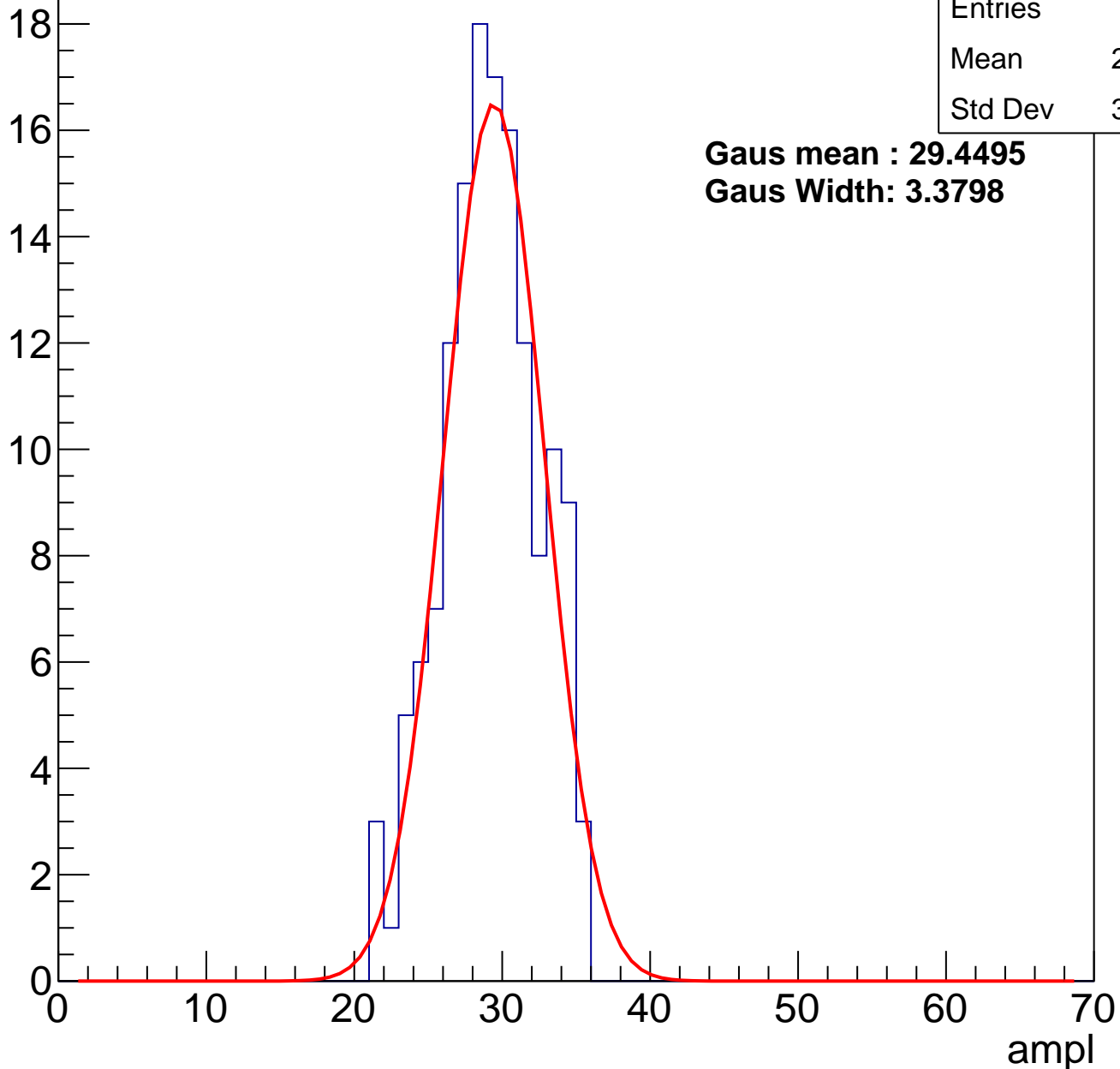
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	28.75
Std Dev	3.238

Gaus mean : 29.4495

Gaus Width: 3.3798

Entry



B1L001S, U20-ch60, adc1

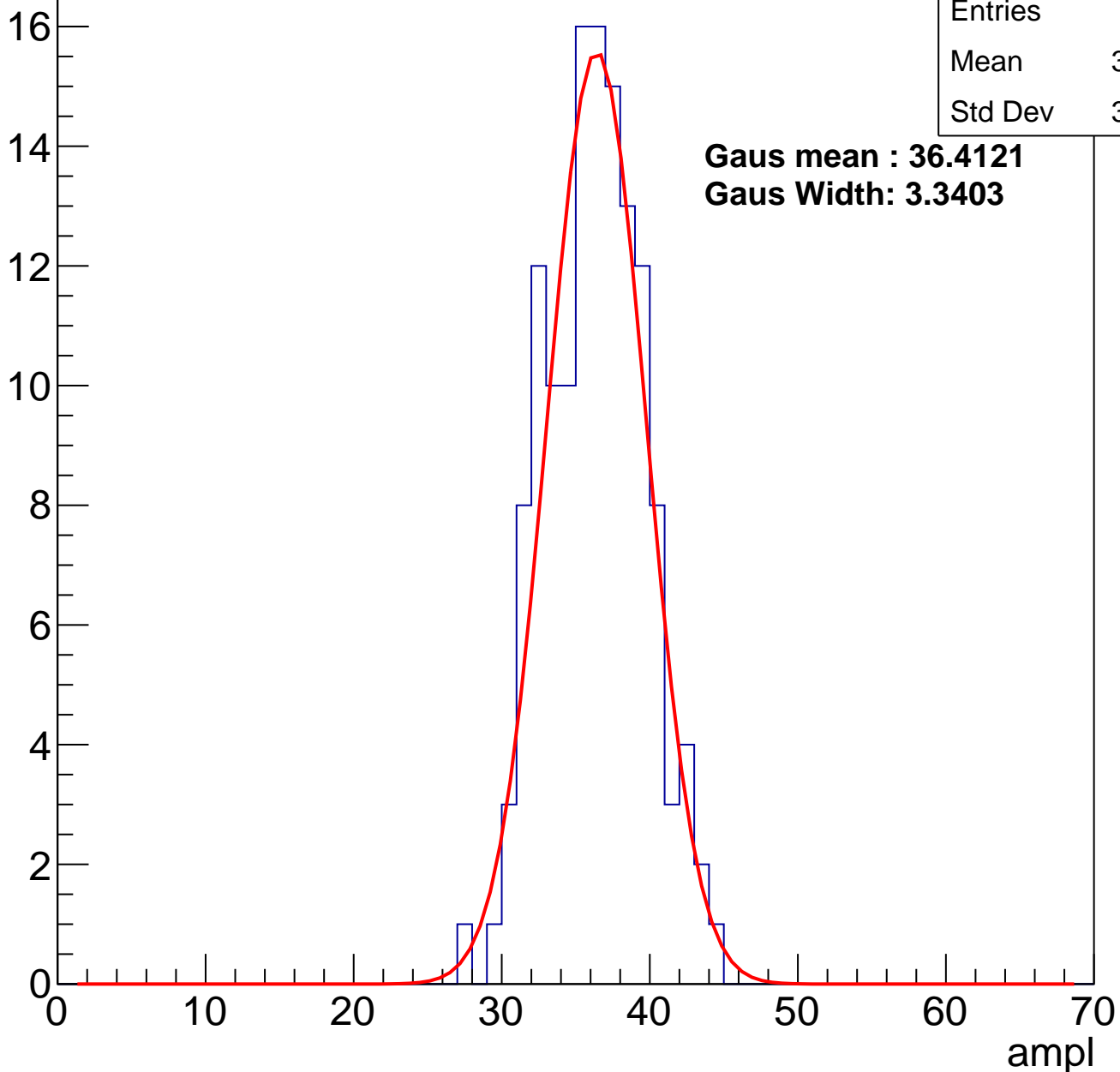
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	35.87
Std Dev	3.286

Gaus mean : 36.4121

Gaus Width: 3.3403

Entry



B1L001S, U20-ch60, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	42.42
Std Dev	3.376

Gaus mean : 42.7952

Gaus Width: 3.3512

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

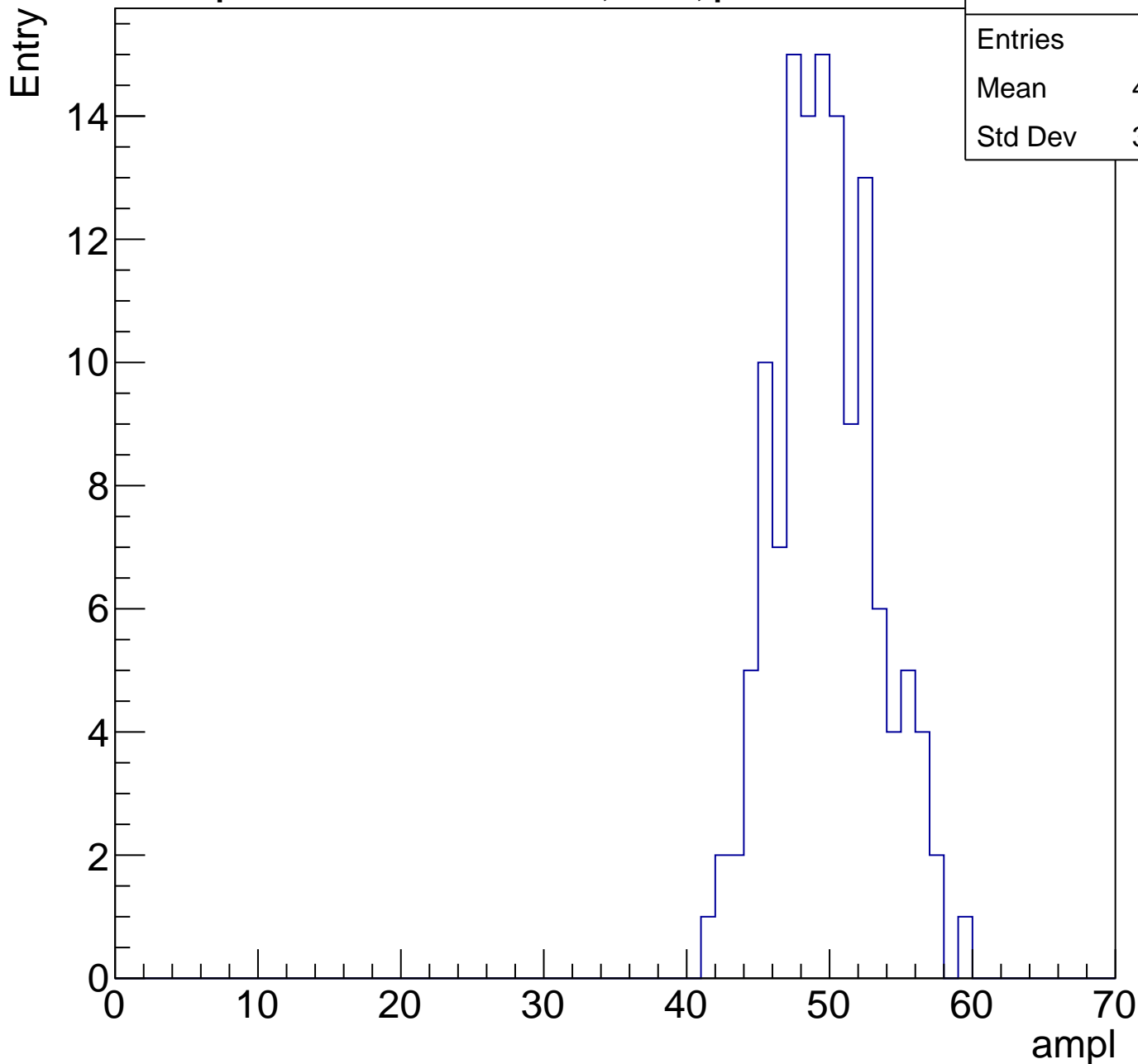
60

70

B1L001S, U20-ch60, adc3

calib_packv5_042523_0143.root, FC#2, port C2

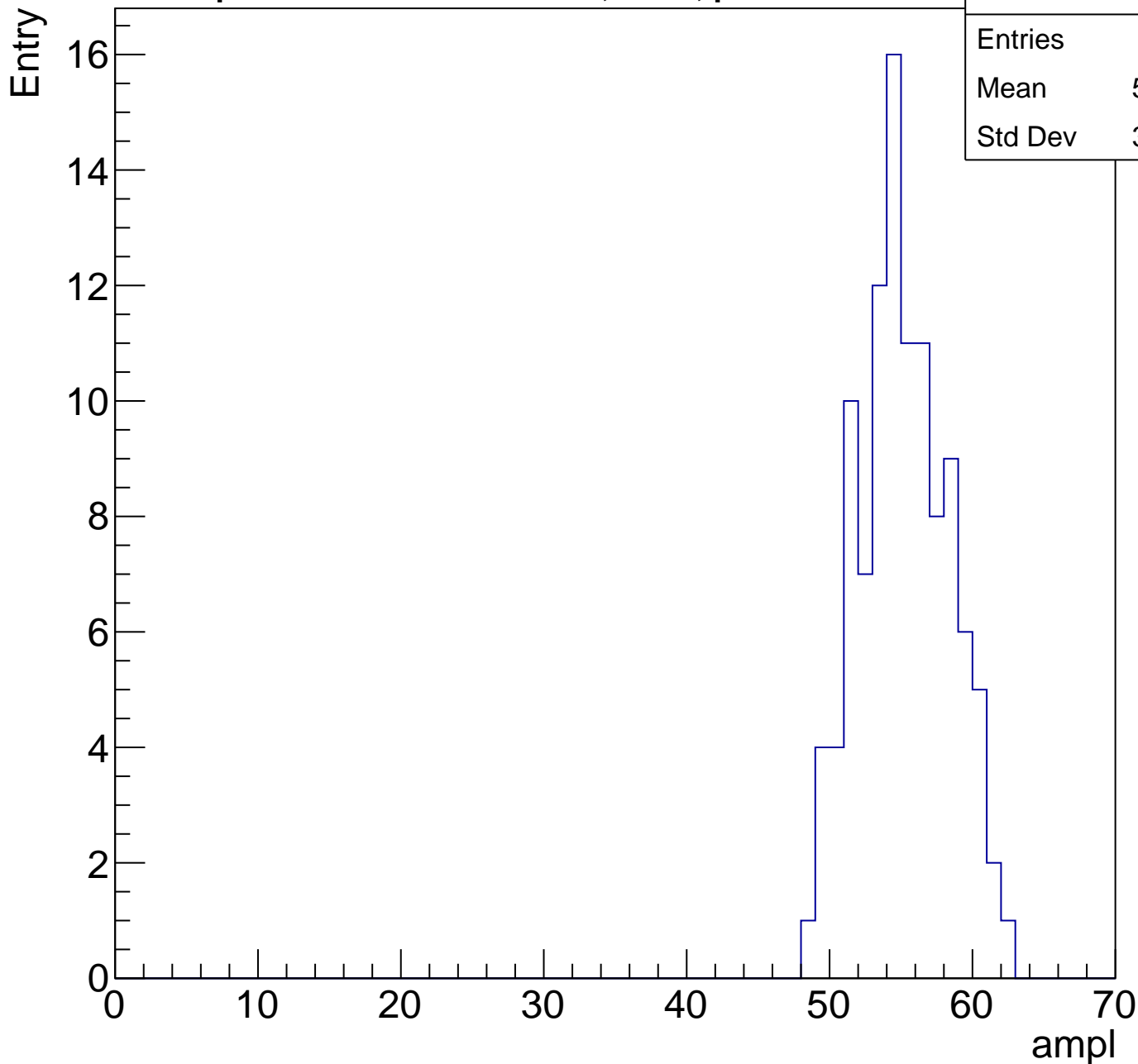
Entries	129
Mean	49.27
Std Dev	3.537



B1L001S, U20-ch60, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	54.72
Std Dev	3.114



B1L001S, U20-ch60, adc5

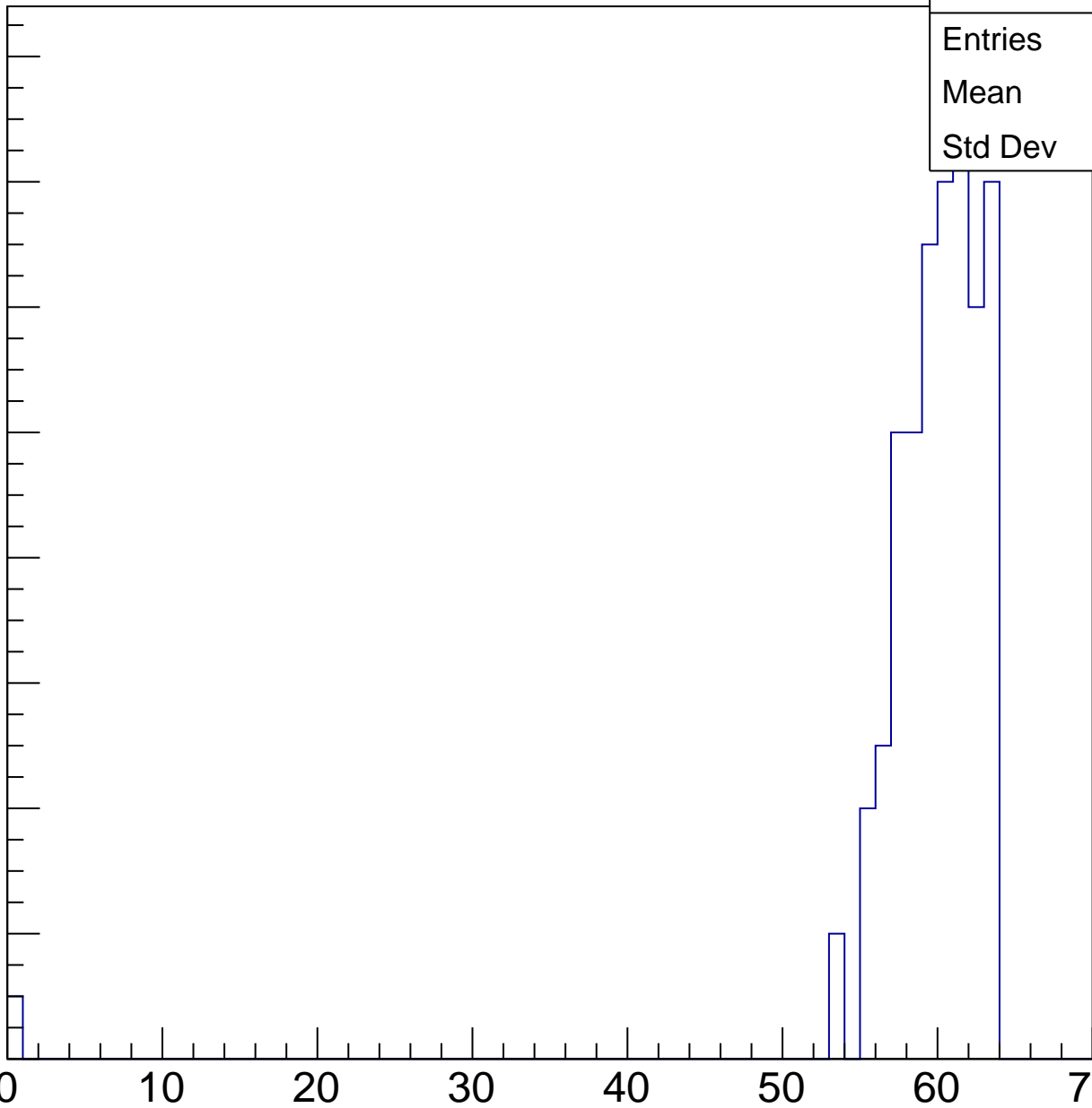
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	59.06
Std Dev	6.388

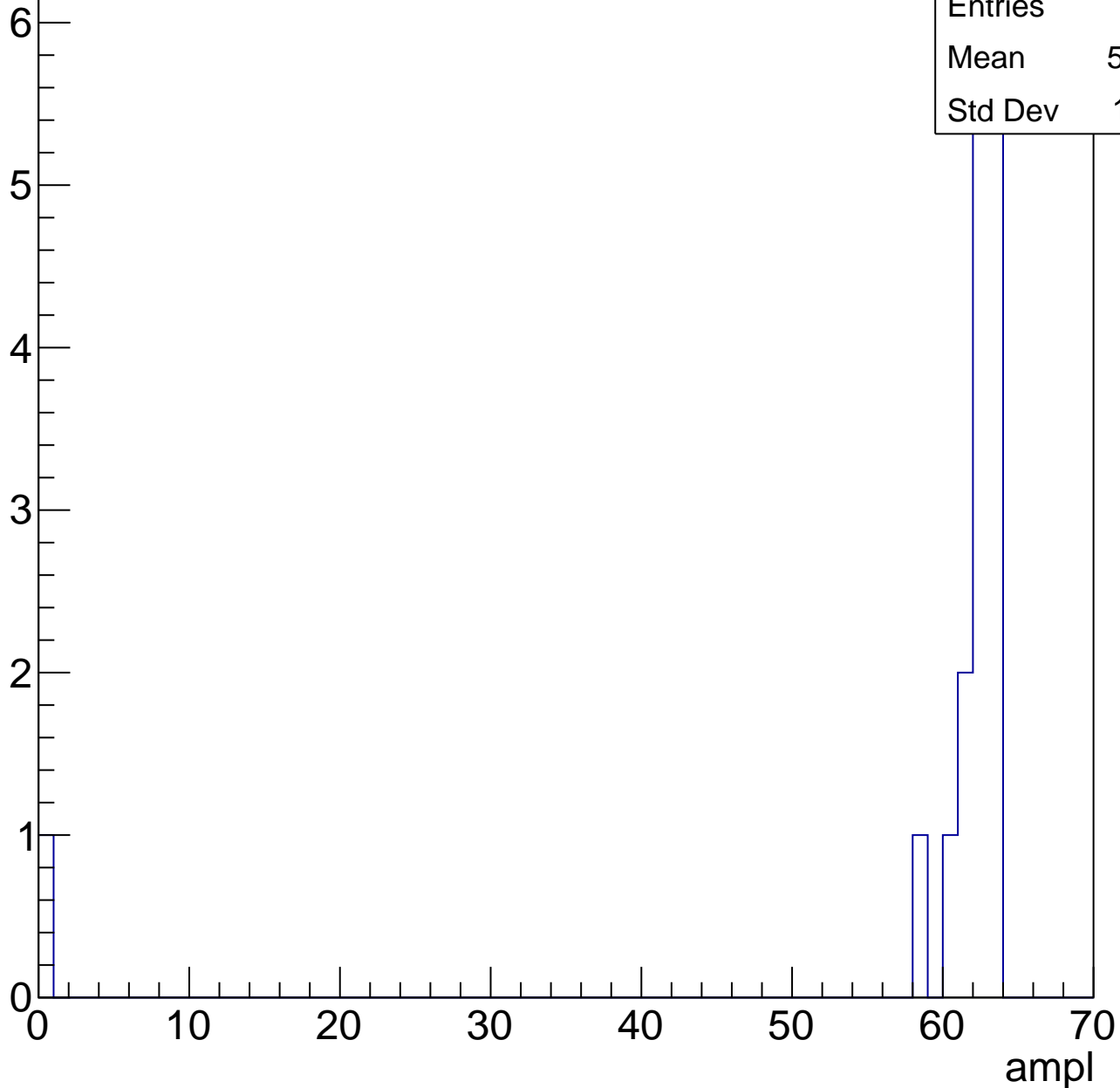
ampl



B1L001S, U20-ch60, adc6

calib_packv5_042523_0143.root, FC#2, port C2

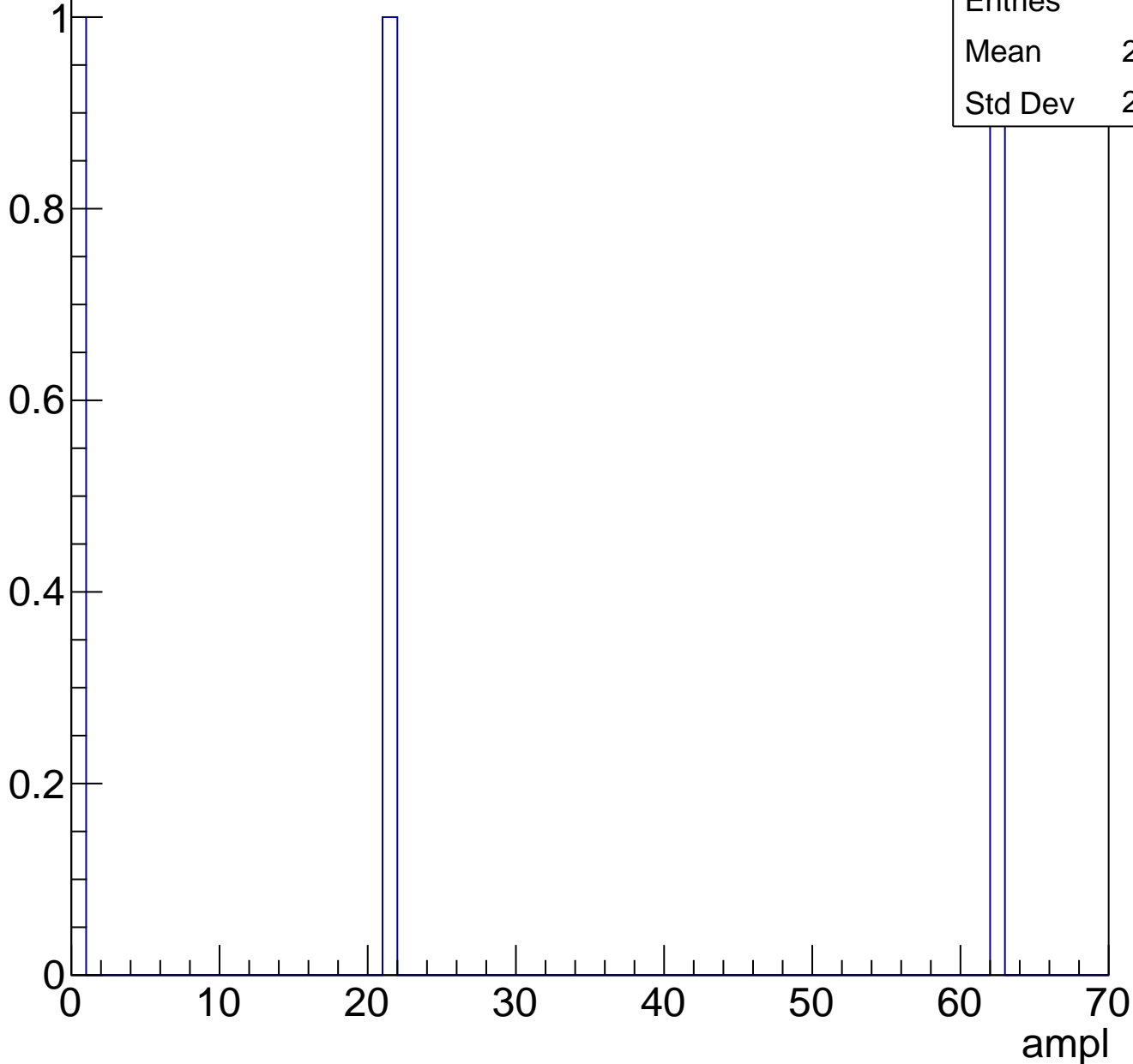
Entry



B1L001S, U20-ch60, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	27.67
Std Dev	25.75

B1L001S, U20-ch61, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	30.2
Std Dev	3.565

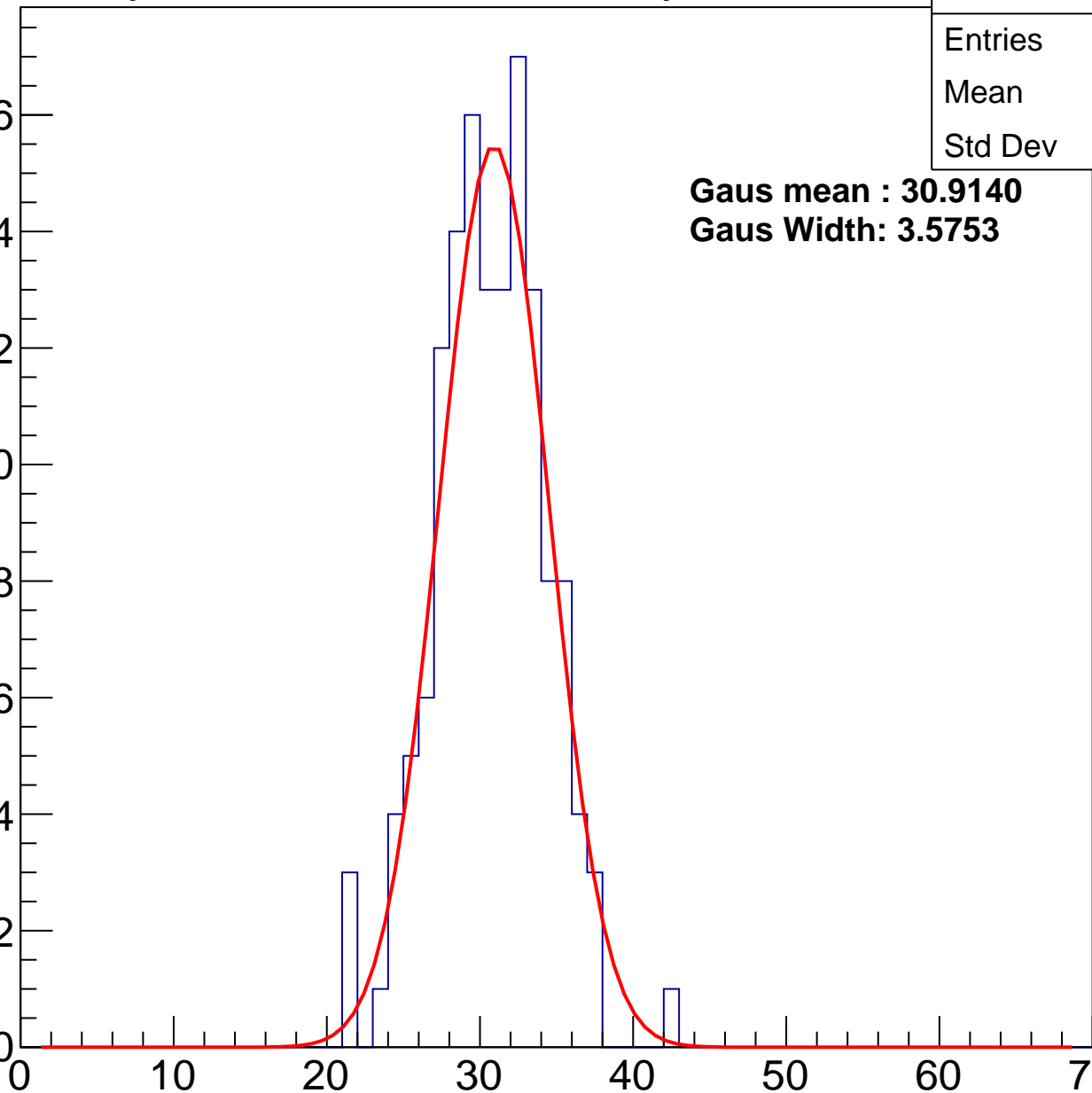
Gaus mean : 30.9140

Gaus Width: 3.5753

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch61, adc1

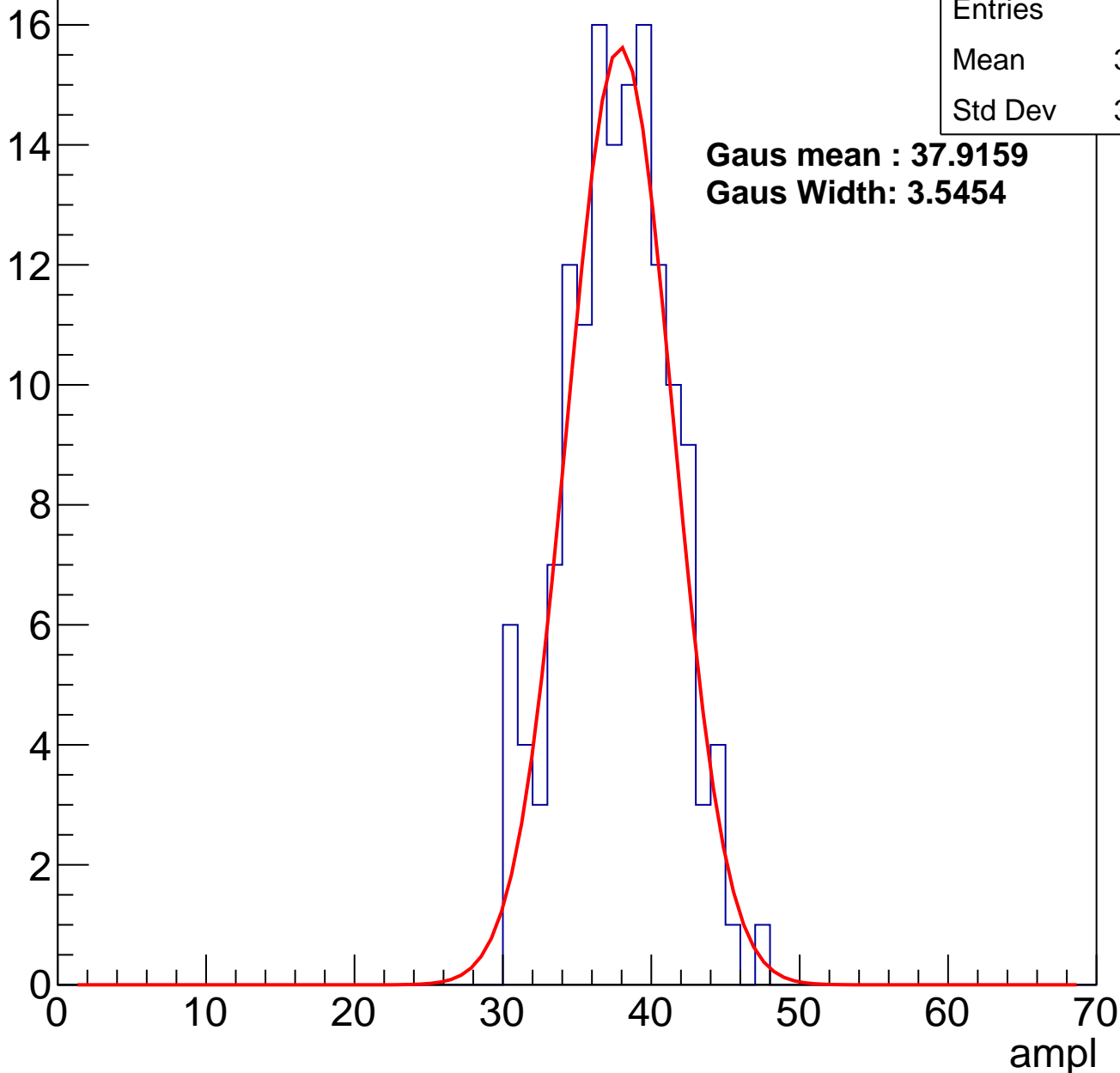
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	37.34
Std Dev	3.552

Gaus mean : 37.9159

Gaus Width: 3.5454

Entry



B1L001S, U20-ch61, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

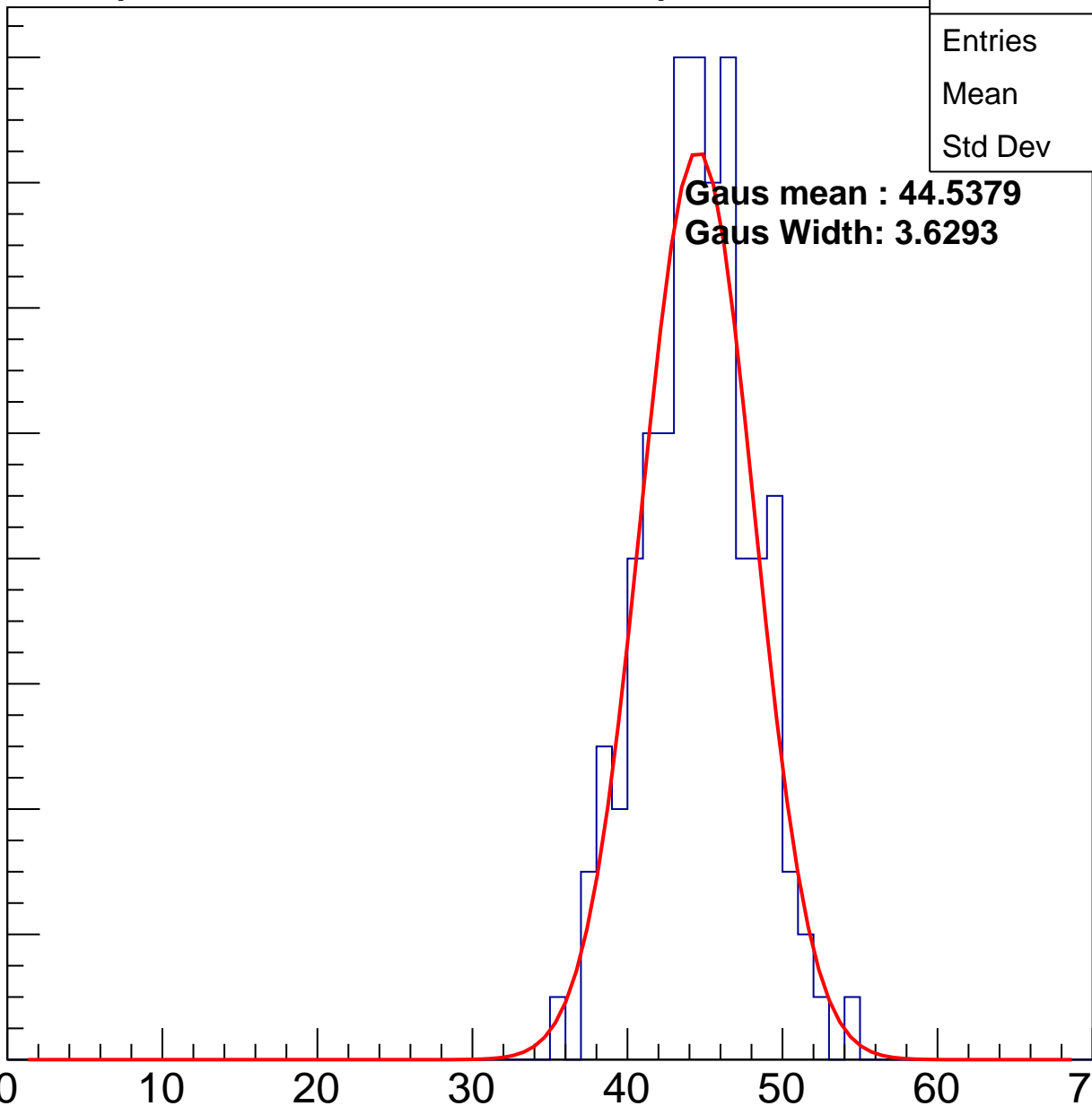
16
14
12
10
8
6
4
2
0

Entries	135
Mean	44.14
Std Dev	3.498

Gaus mean : 44.5379

Gaus Width: 3.6293

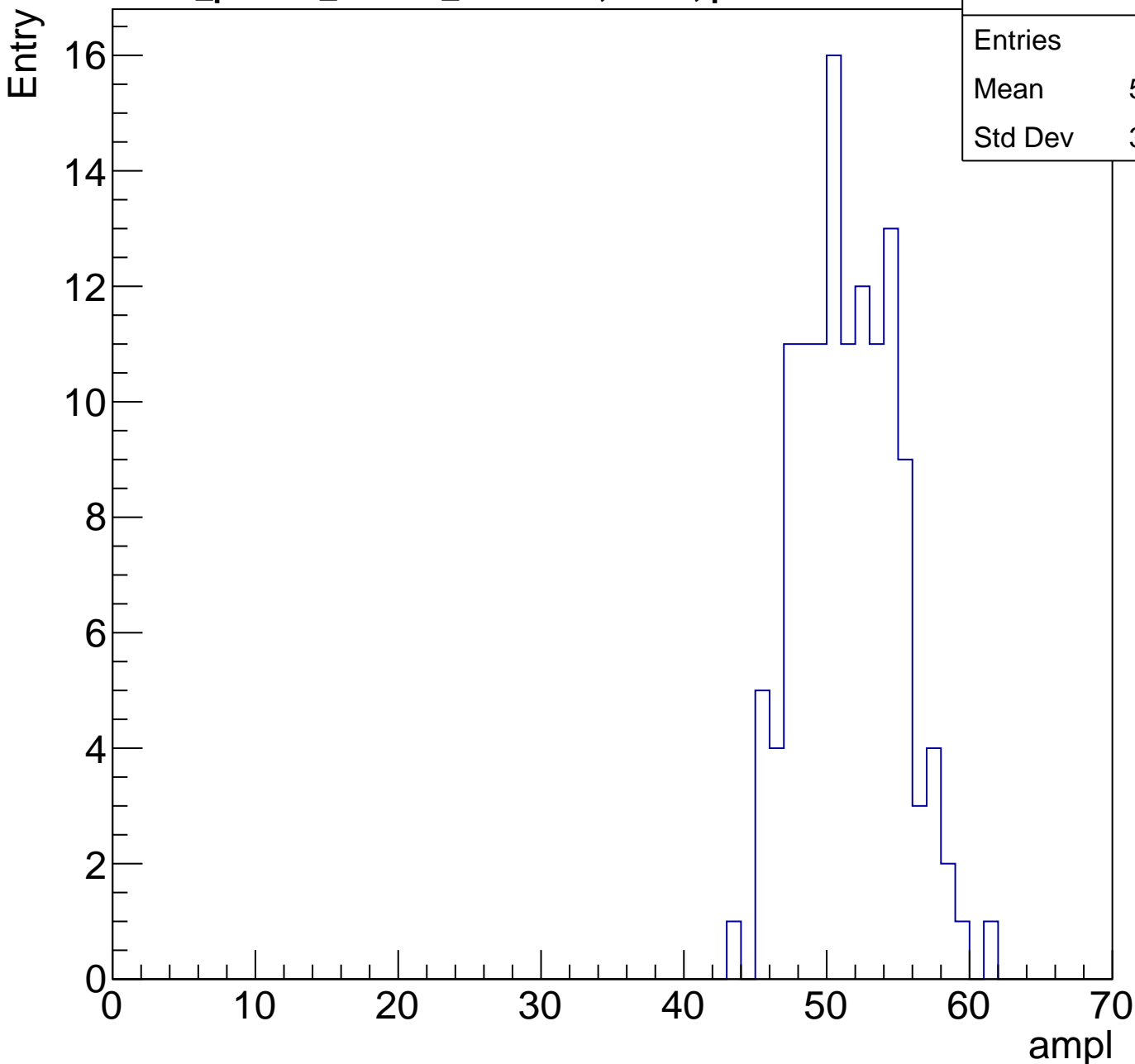
ampl



B1L001S, U20-ch61, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	51.06
Std Dev	3.423



B1L001S, U20-ch61, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

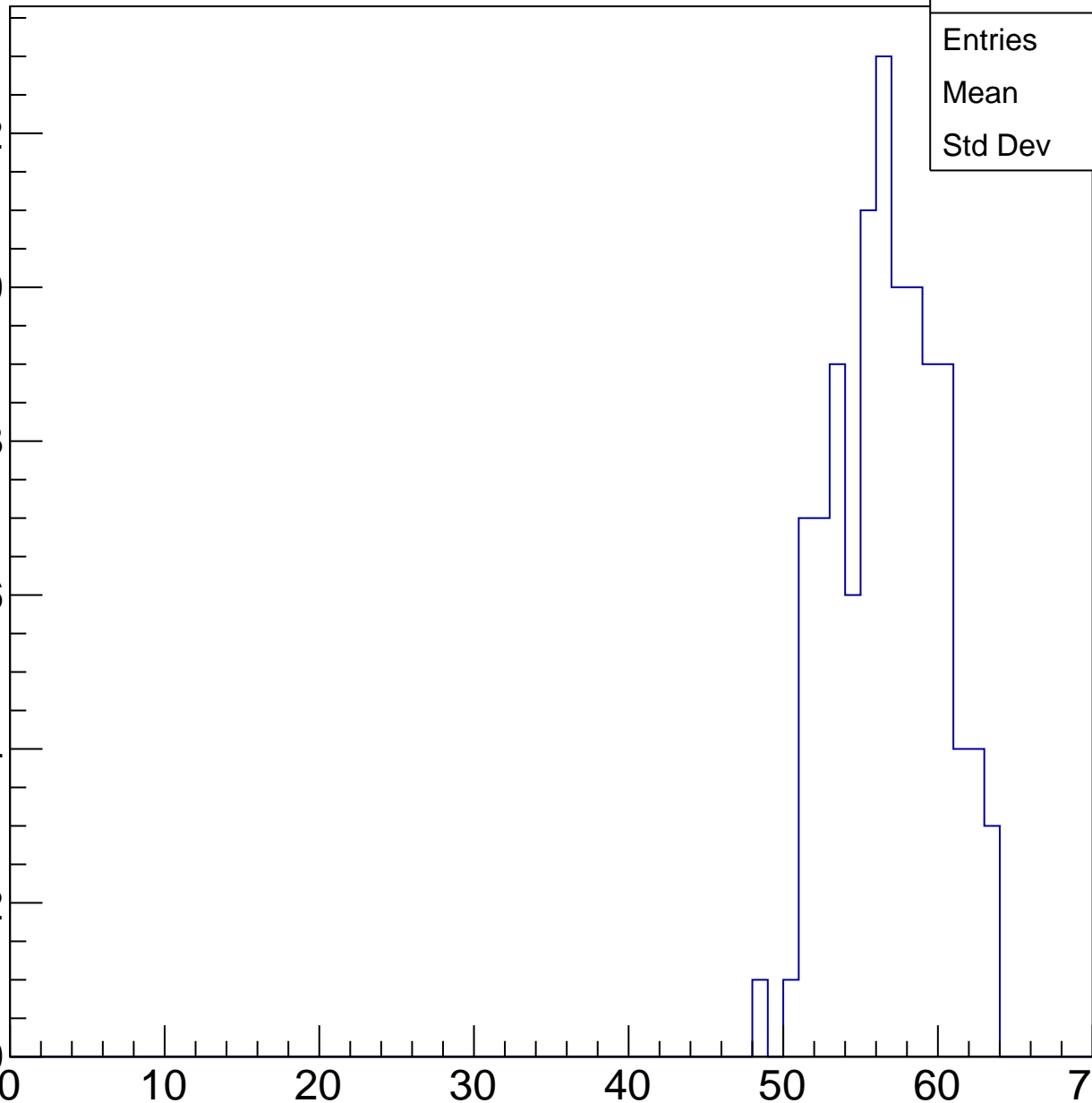
30

40

50

ampl

Entries	104
Mean	56.3
Std Dev	3.339



B1L001S, U20-ch61, adc5

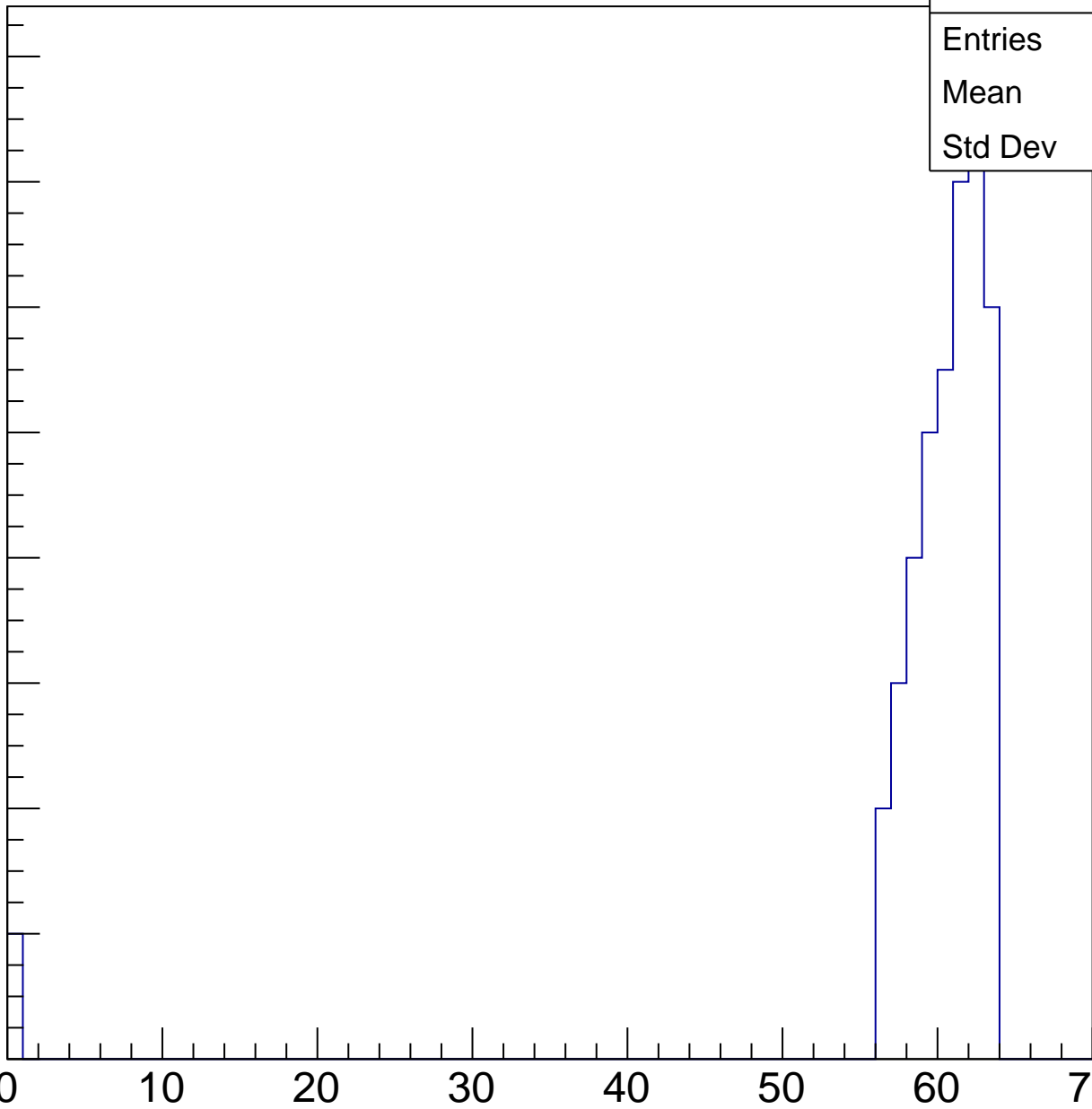
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	58.82
Std Dev	9.462

ampl

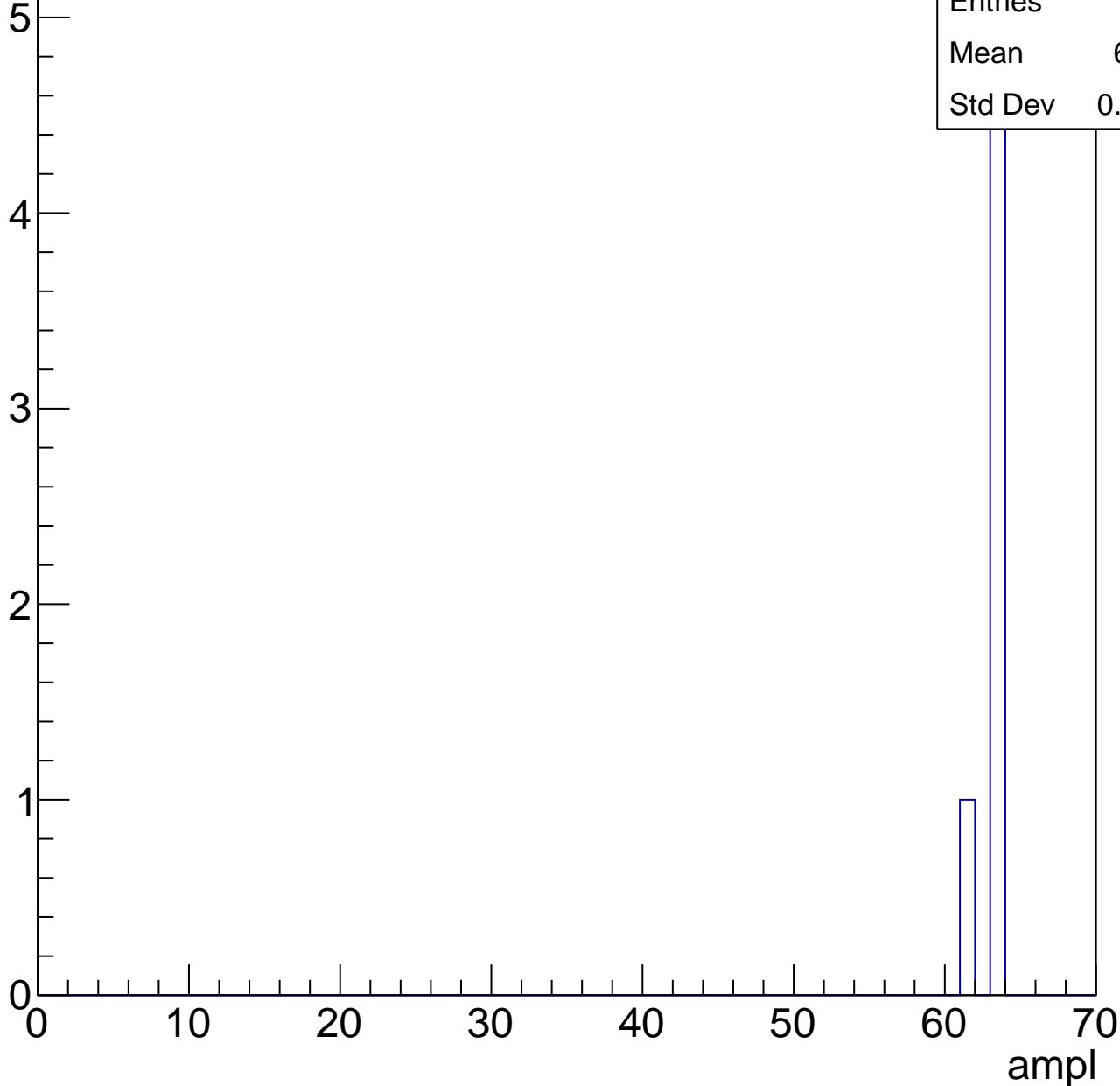


B1L001S, U20-ch61, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	6
Mean	62.67
Std Dev	0.7454



B1L001S, U20-ch61, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	21
Std Dev	0

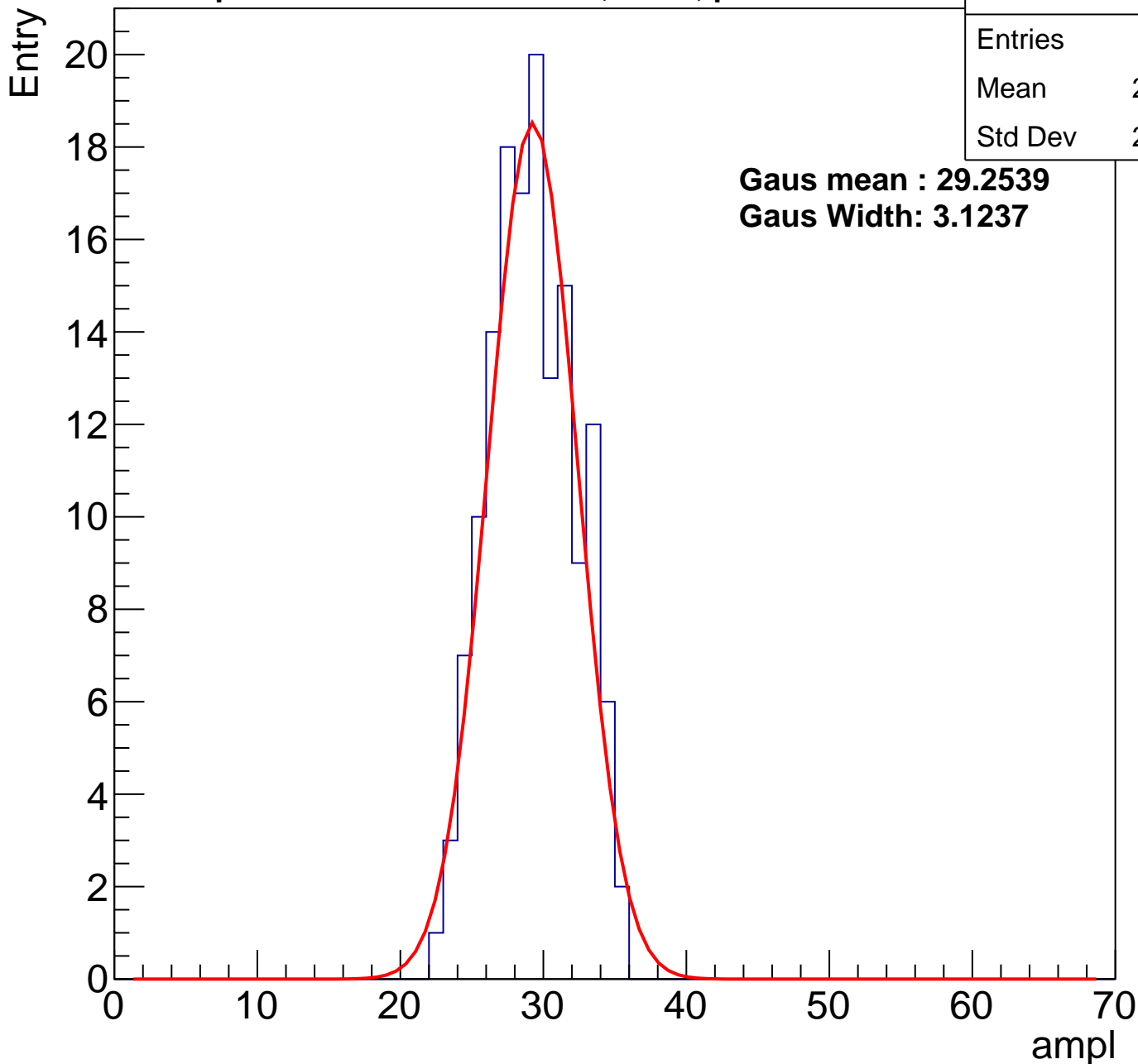
B1L001S, U20-ch62, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	28.76
Std Dev	2.935

Gaus mean : 29.2539

Gaus Width: 3.1237



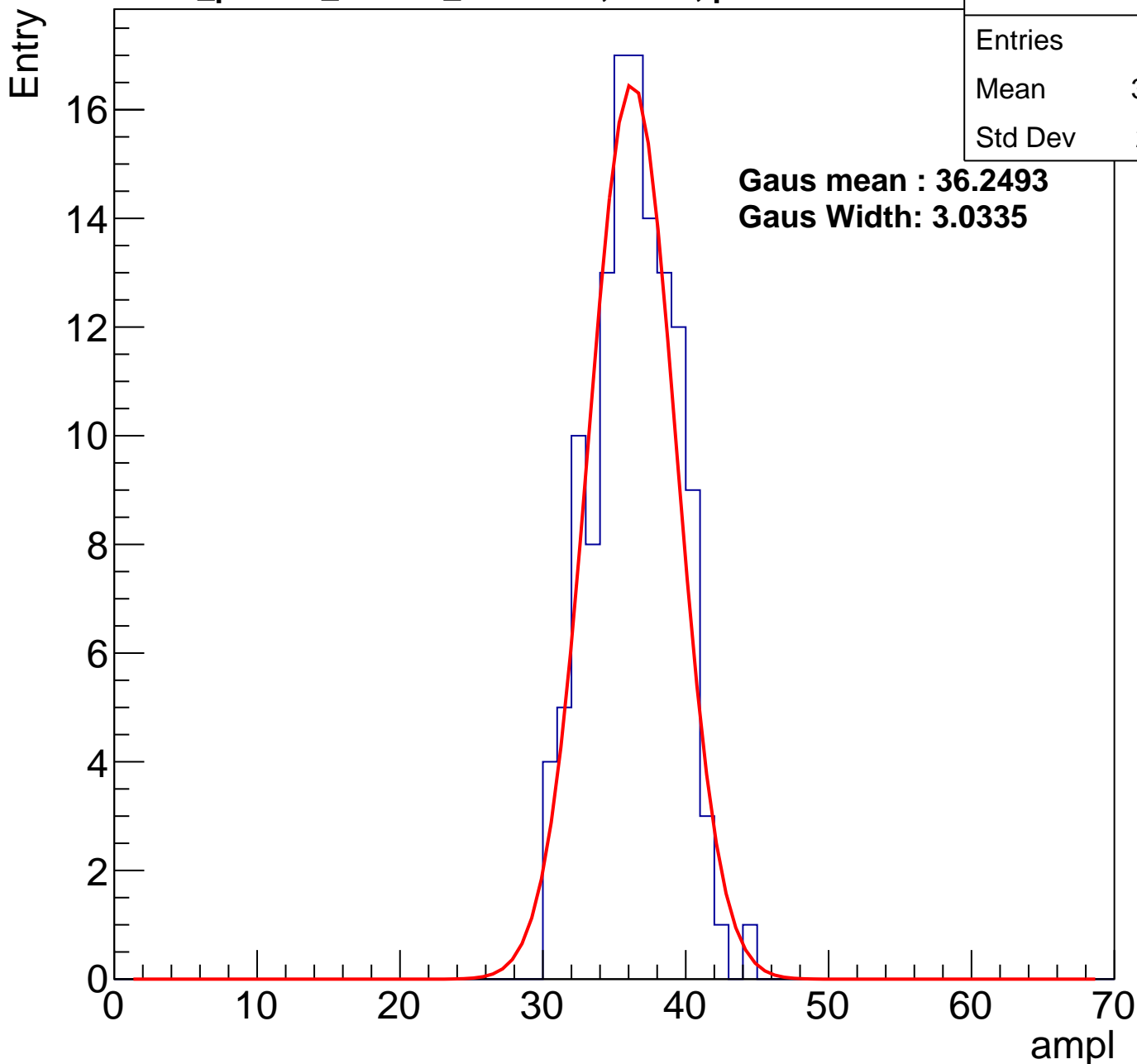
B1L001S, U20-ch62, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	35.88
Std Dev	2.891

Gaus mean : 36.2493

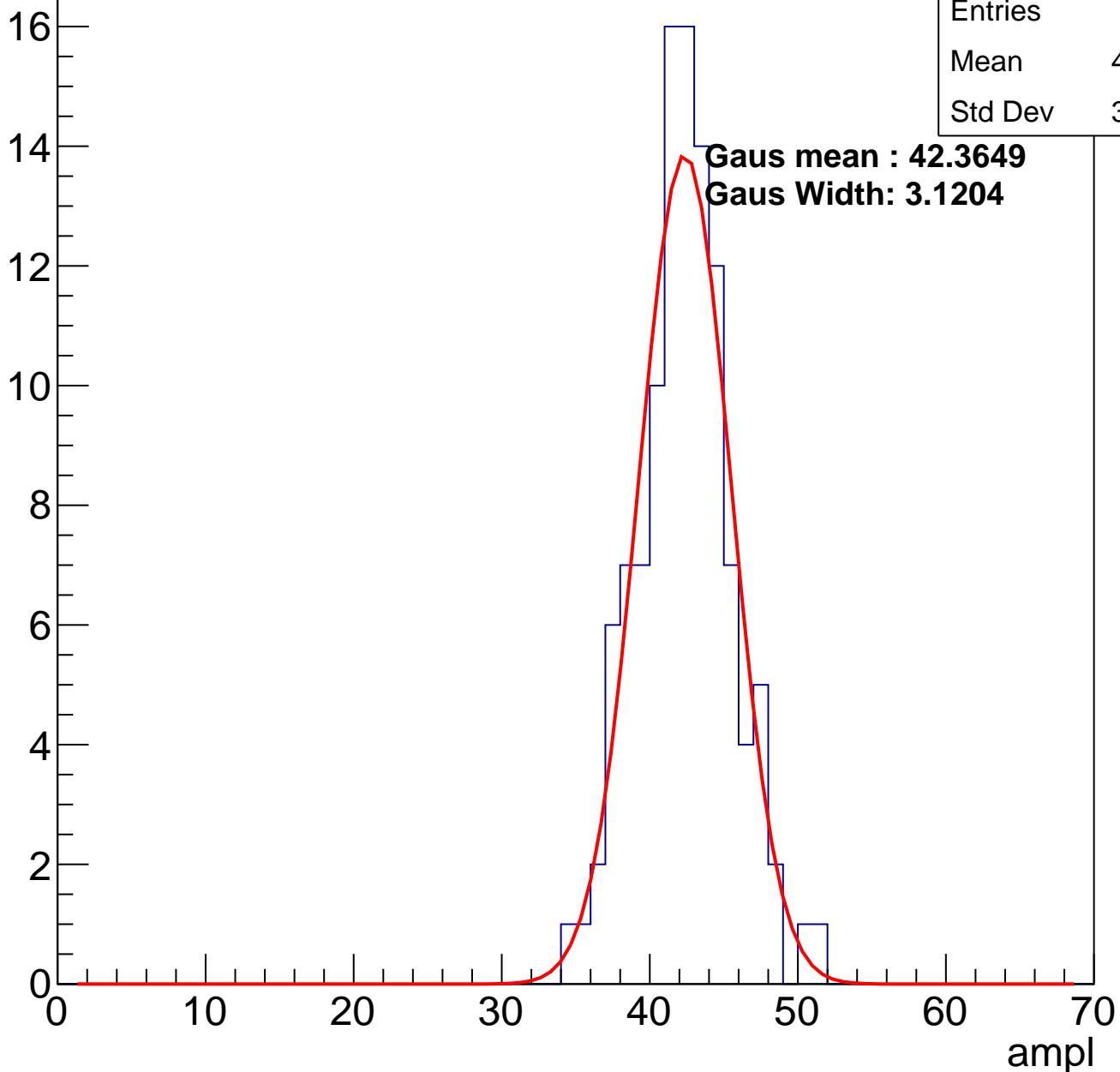
Gaus Width: 3.0335



B1L001S, U20-ch62, adc2

calib_packv5_042523_0143.root, FC#2, port C2

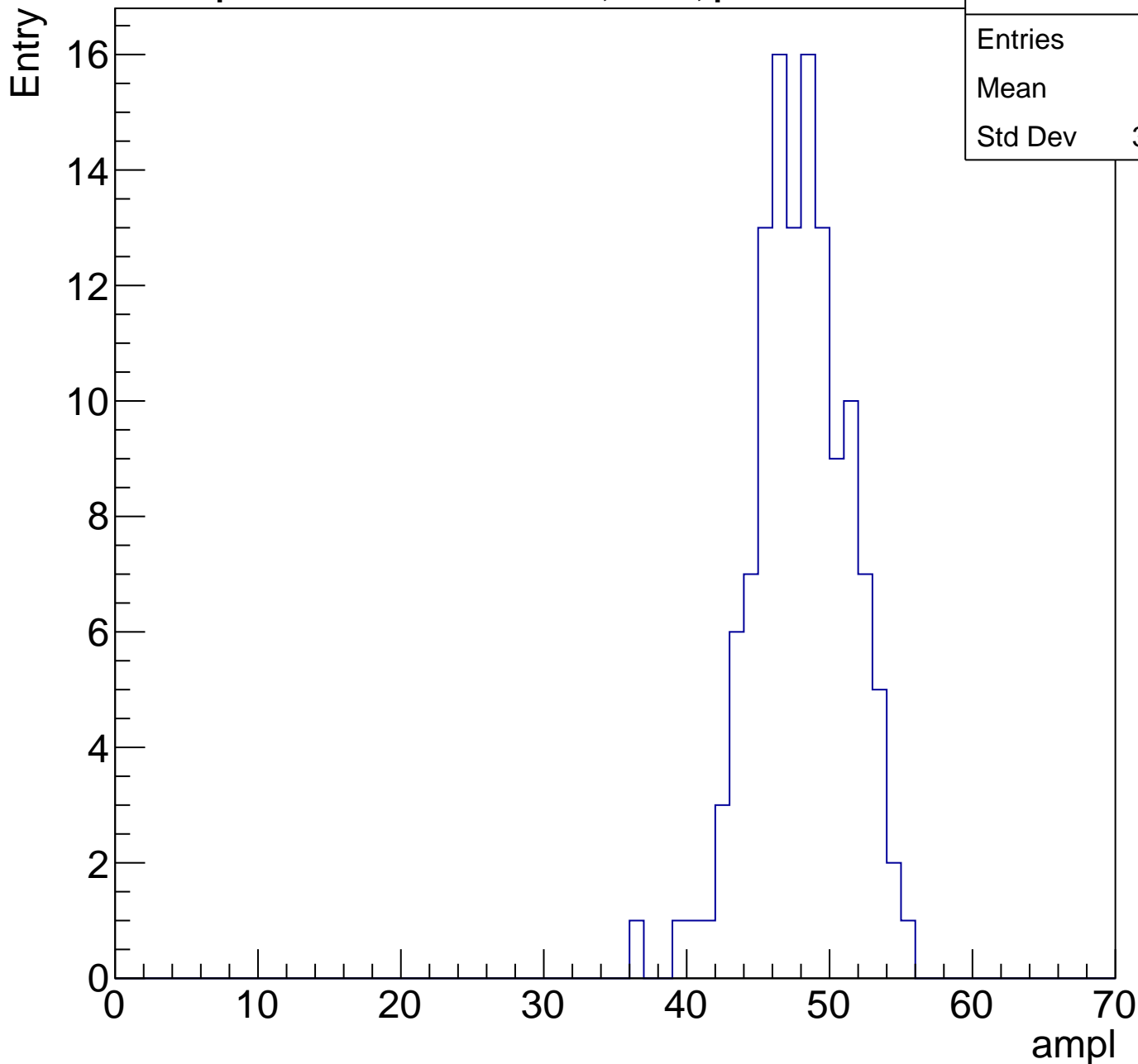
Entry



B1L001S, U20-ch62, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	47.5
Std Dev	3.307



B1L001S, U20-ch62, adc4

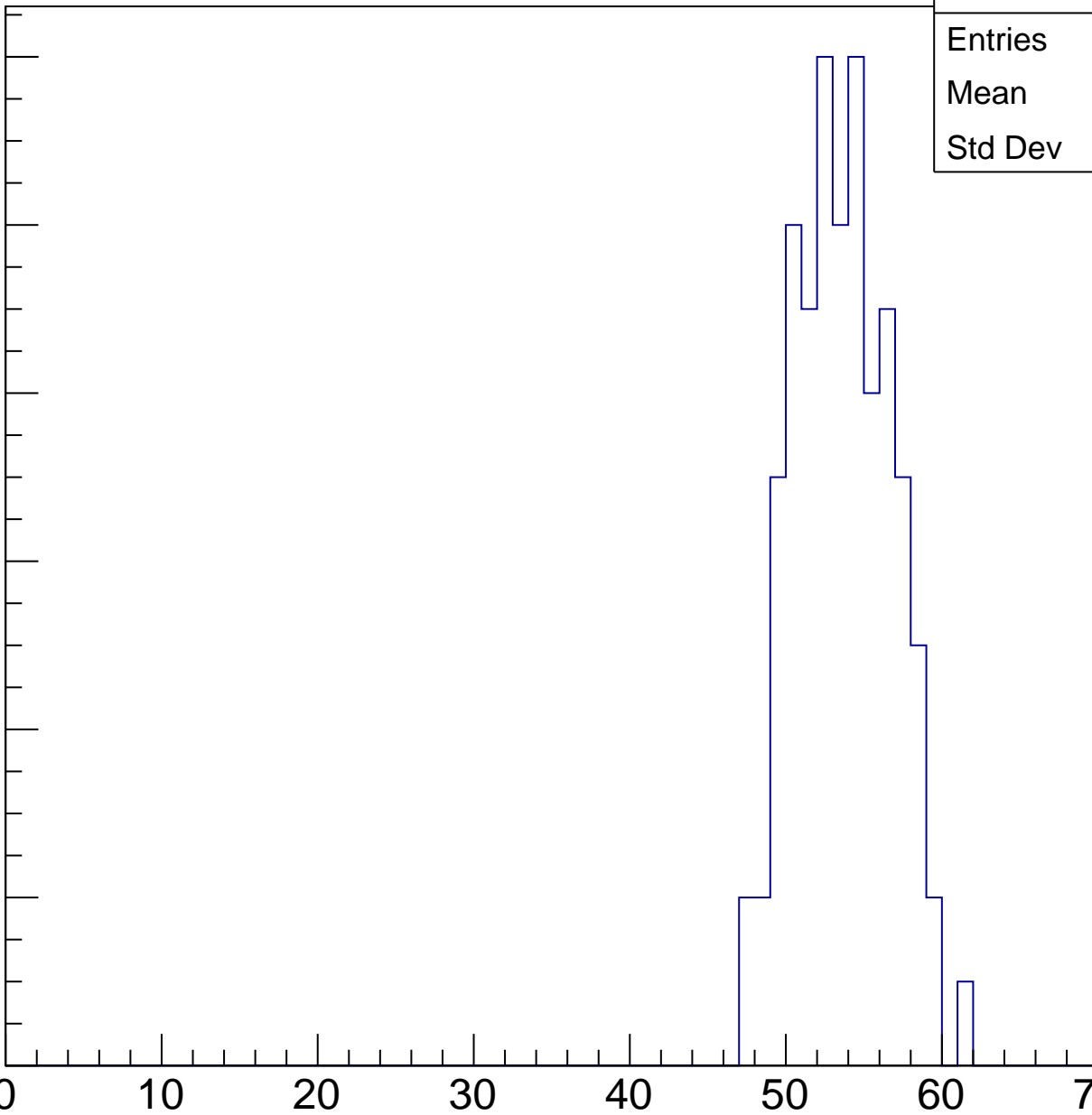
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	96
Mean	53.19
Std Dev	3.005

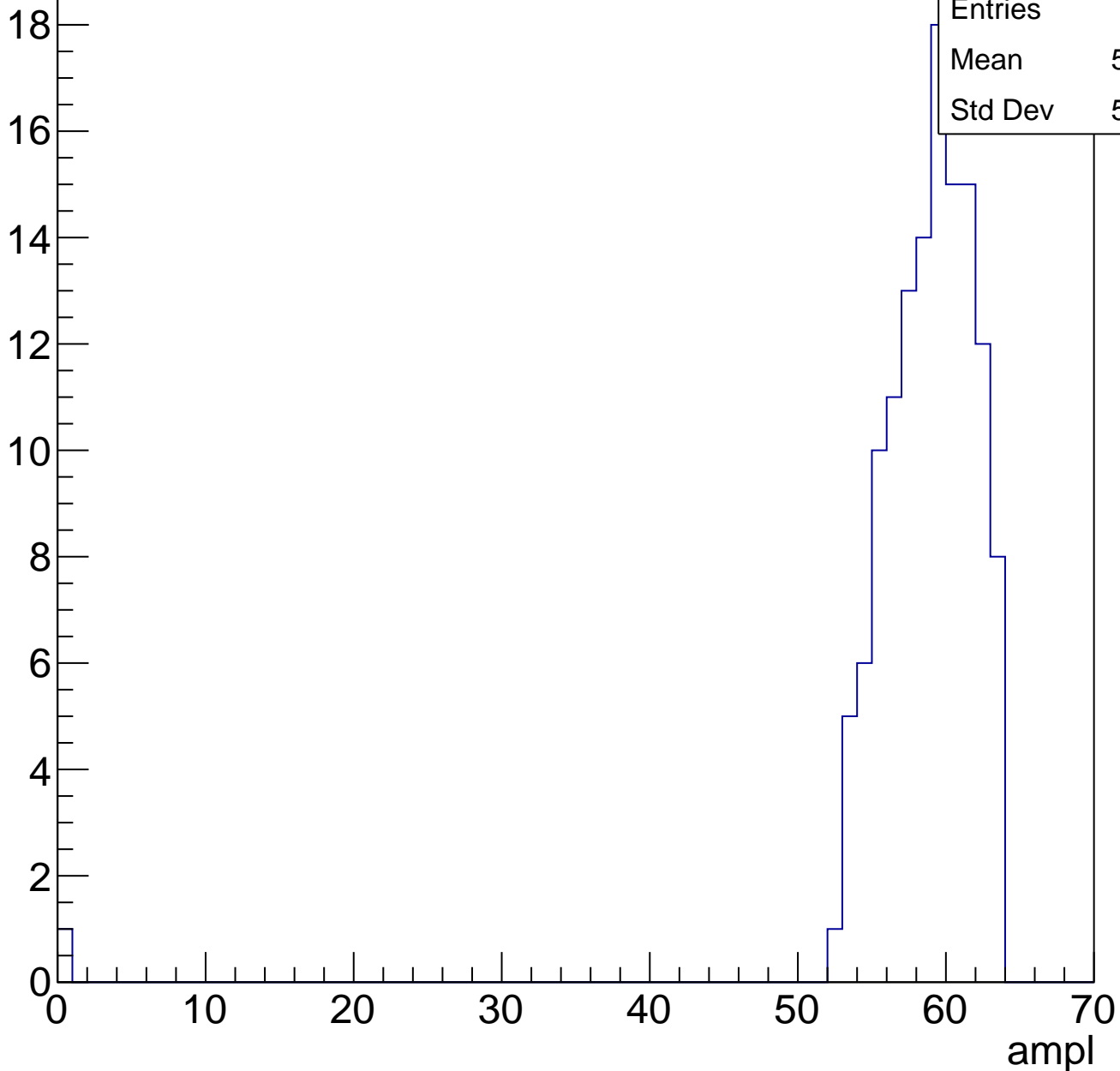
ampl



B1L001S, U20-ch62, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch62, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	31
Mean	61.58
Std Dev	1.756

0

0

10

20

30

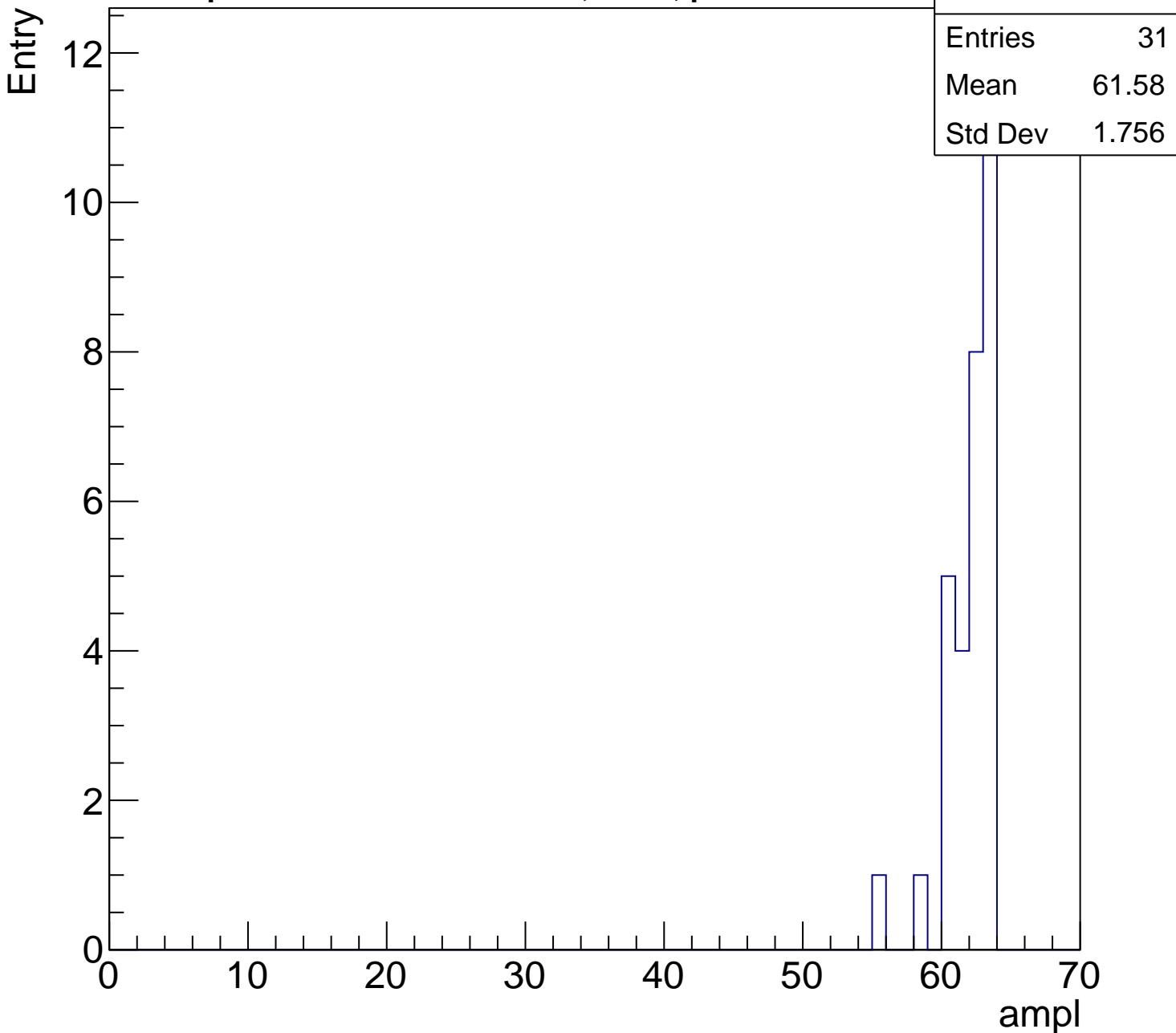
40

50

60

70

ampl



B1L001S, U20-ch62, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch63, adc0

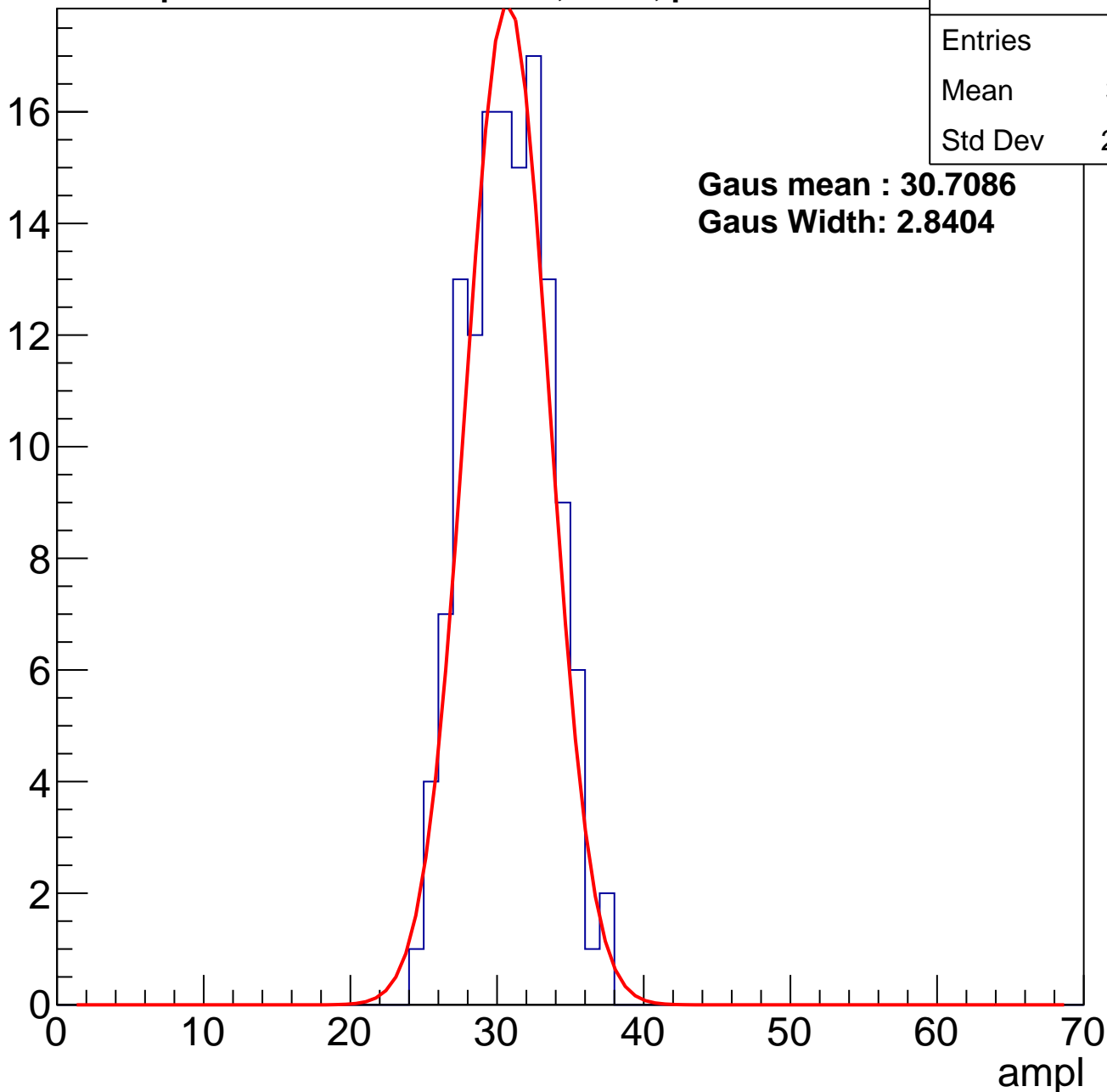
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	30.31
Std Dev	2.813

Gaus mean : 30.7086

Gaus Width: 2.8404

Entry



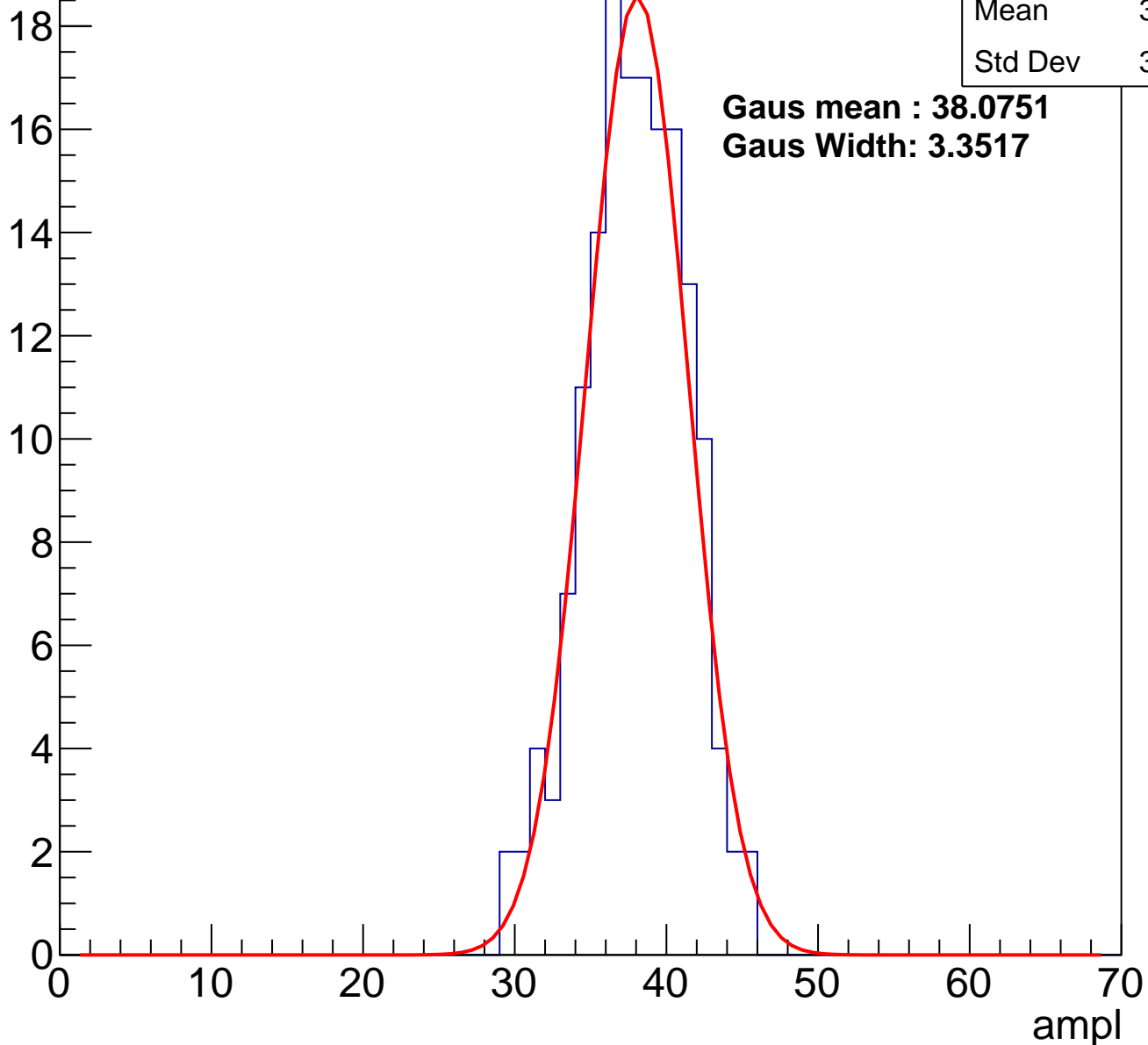
B1L001S, U20-ch63, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	37.48
Std Dev	3.315

Gaus mean : 38.0751
Gaus Width: 3.3517

Entry



B1L001S, U20-ch63, adc2

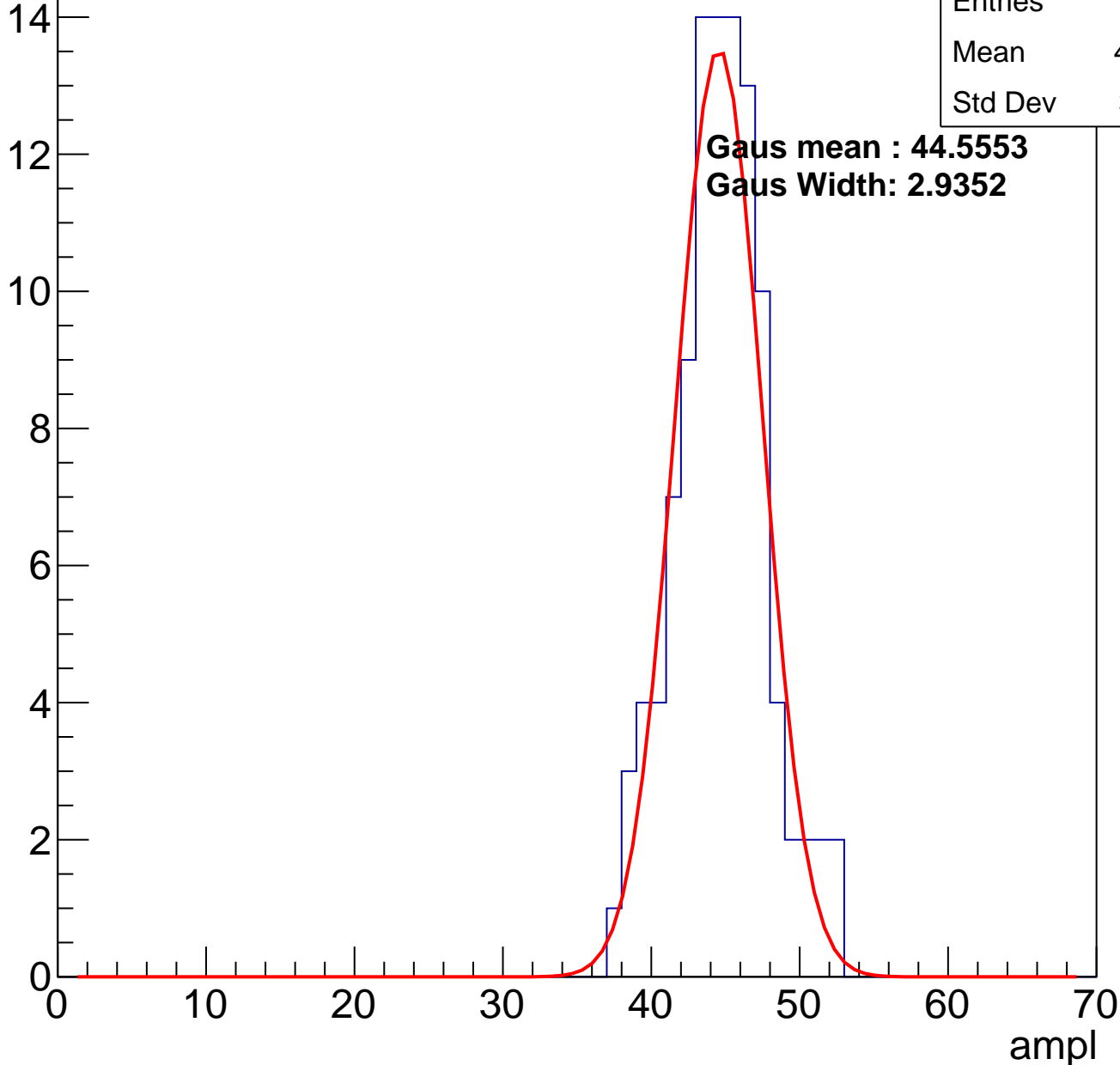
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	44.23
Std Dev	3.081

Gaus mean : 44.5553

Gaus Width: 2.9352

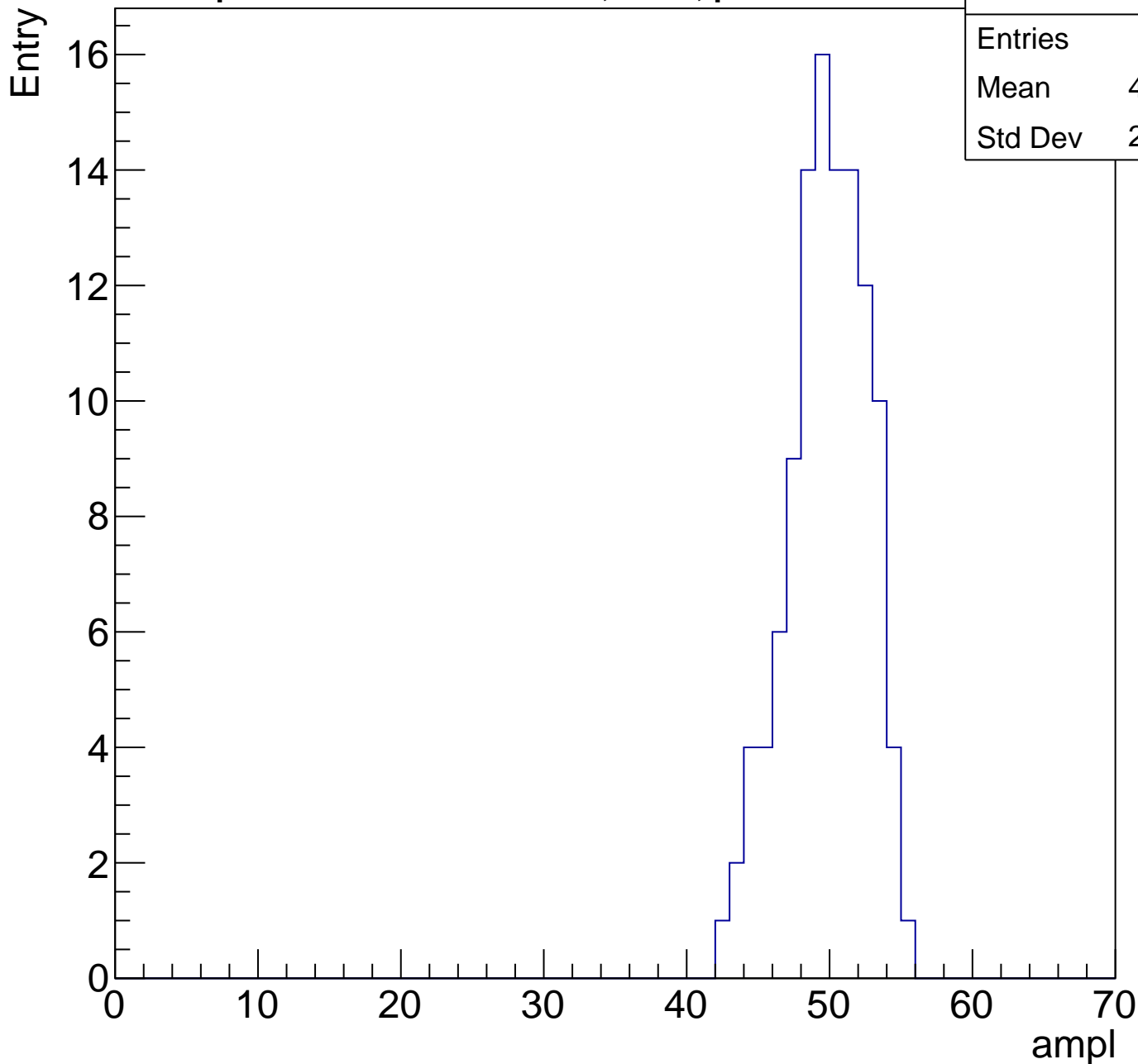
Entry



B1L001S, U20-ch63, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	49.35
Std Dev	2.766



B1L001S, U20-ch63, adc4

calib_packv5_042523_0143.root, FC#2, port C2

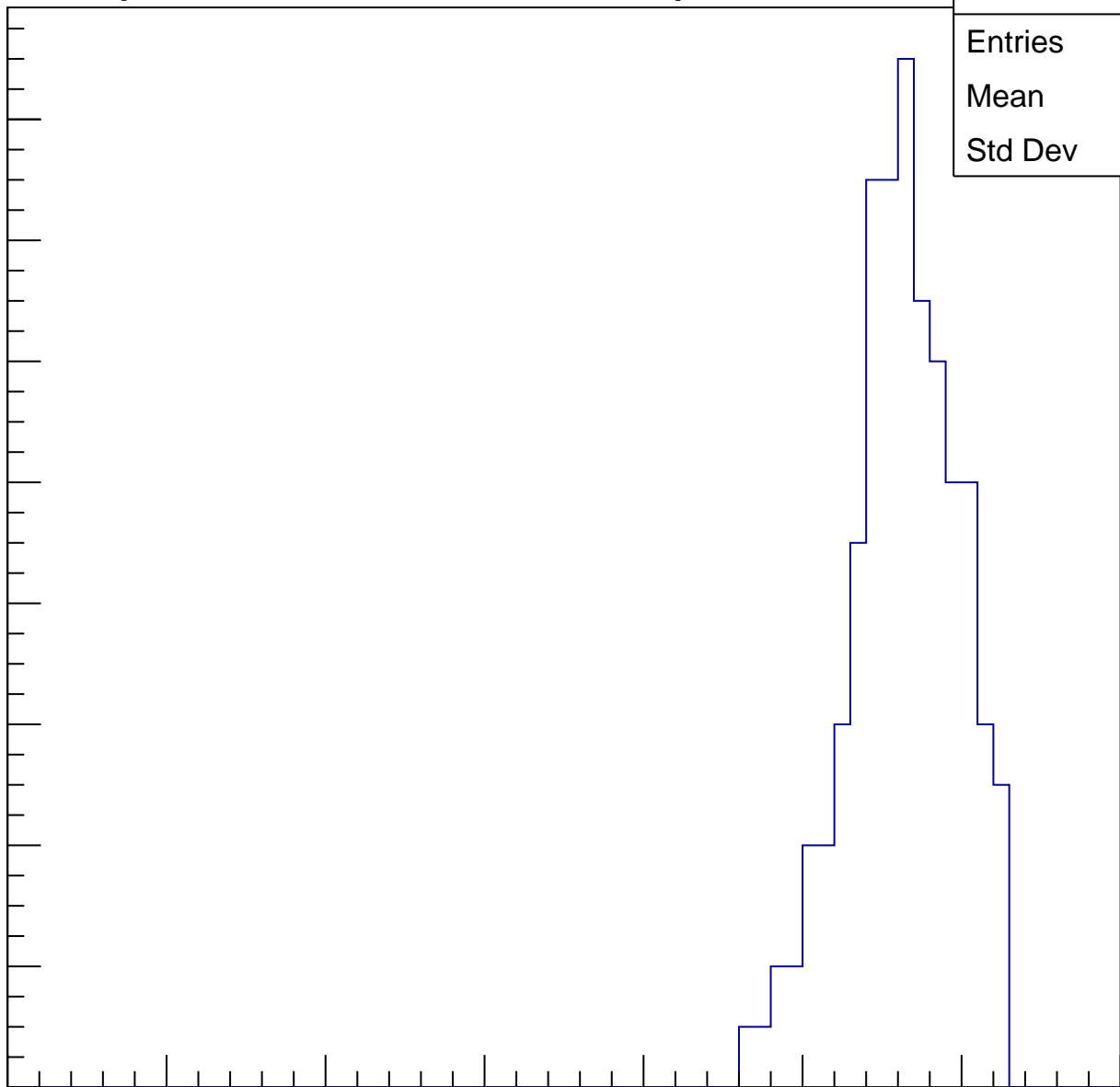
Entries	132
Mean	55.83
Std Dev	3.416

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch63, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

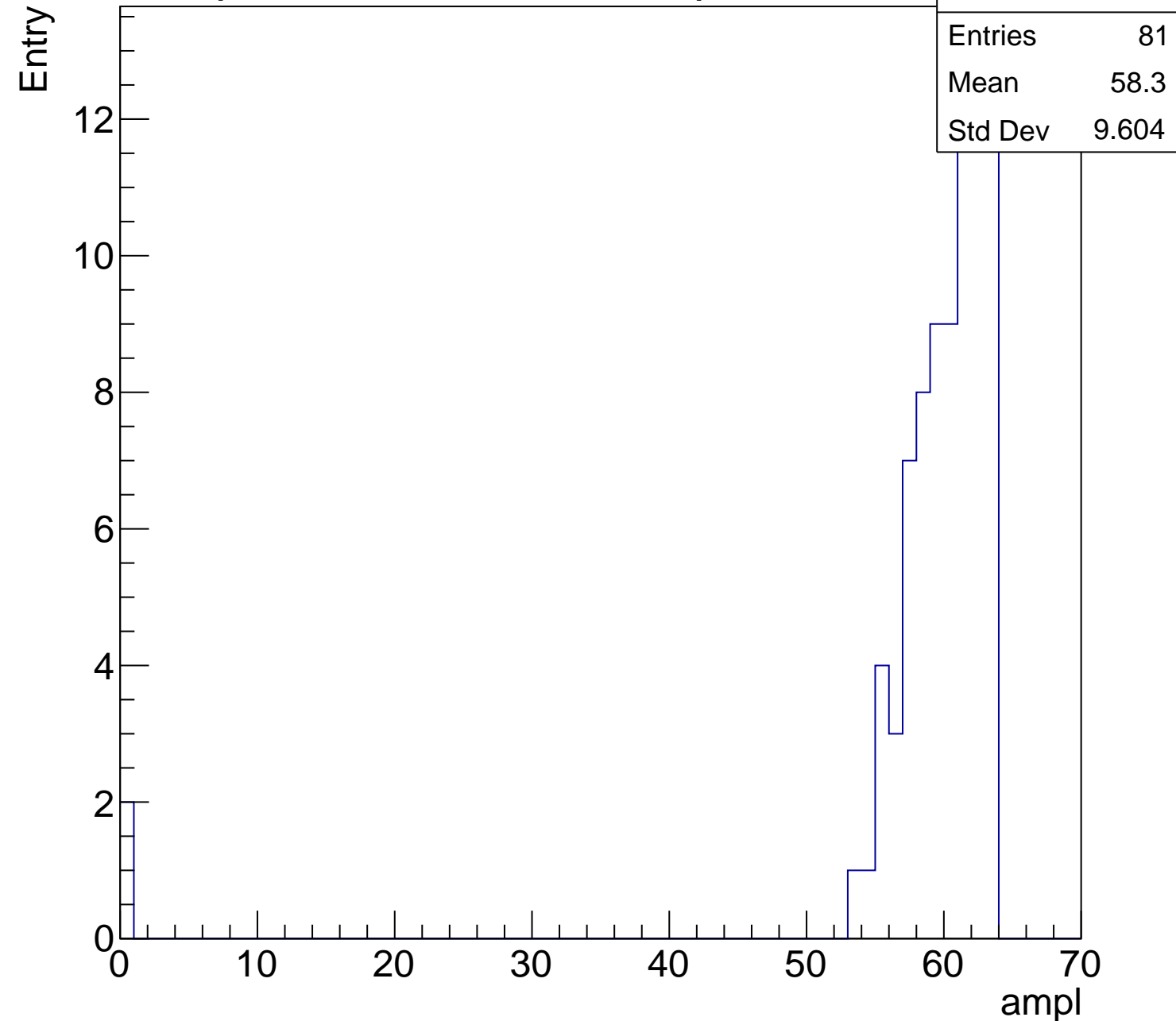
40

50

60

ampl

Entries	81
Mean	58.3
Std Dev	9.604

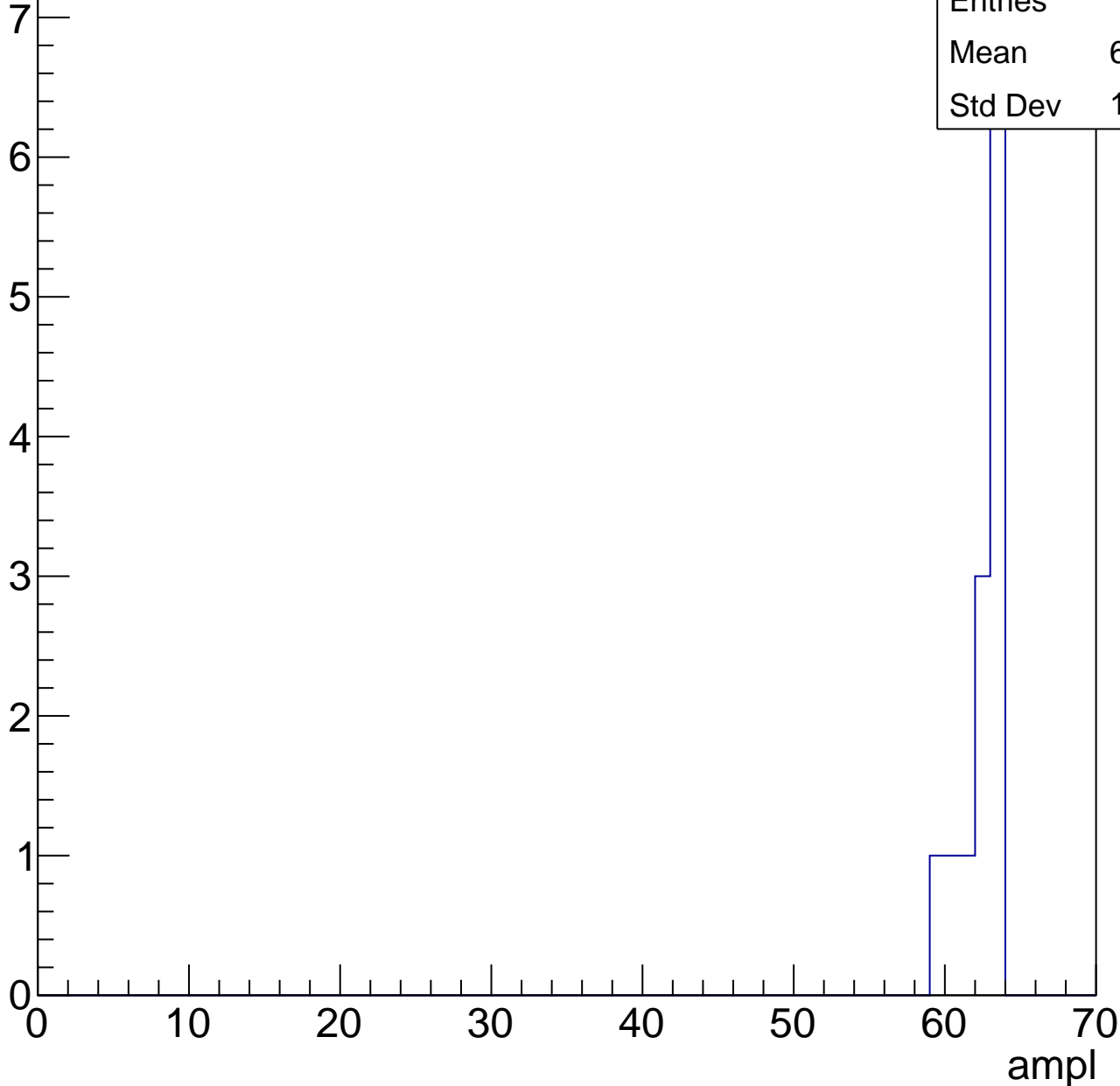


B1L001S, U20-ch63, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	62.08
Std Dev	1.269



B1L001S, U20-ch63, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch64, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	29.98
Std Dev	3.318

Gaus mean : 30.2241

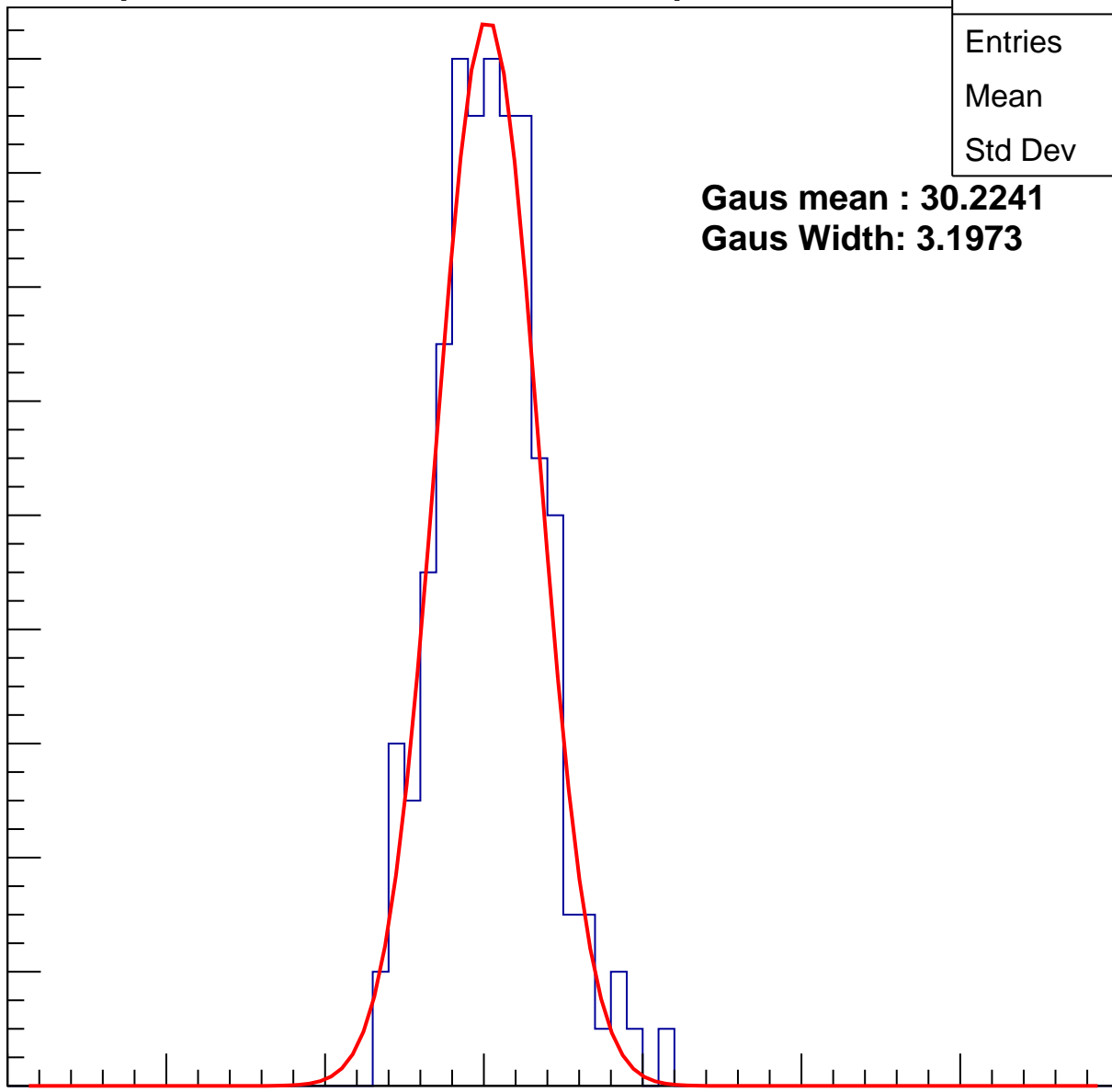
Gaus Width: 3.1973

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch64, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	37.25
Std Dev	3.064

Gaus mean : 37.4924

Gaus Width: 2.8854

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

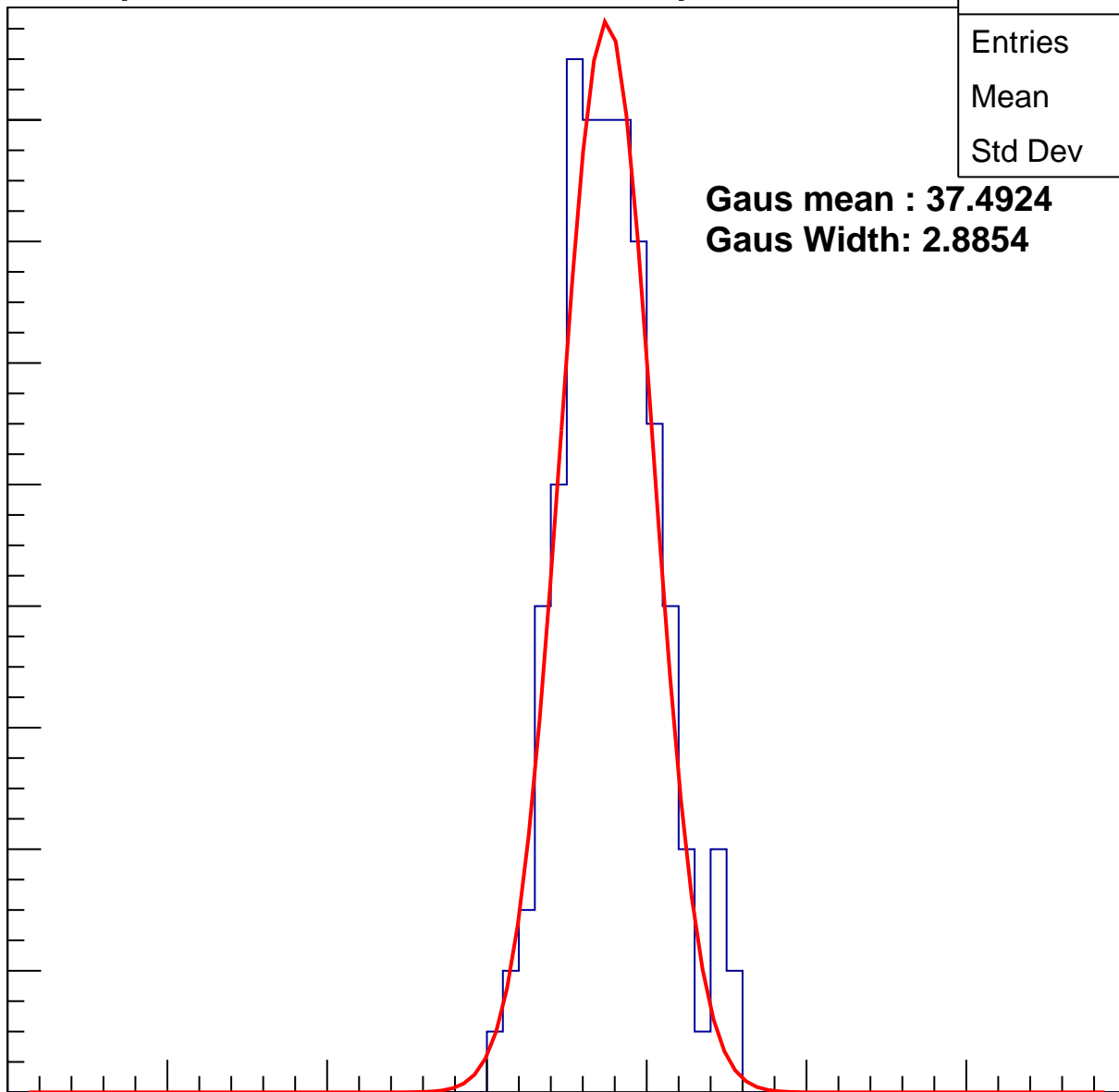
40

50

60

70

ampl



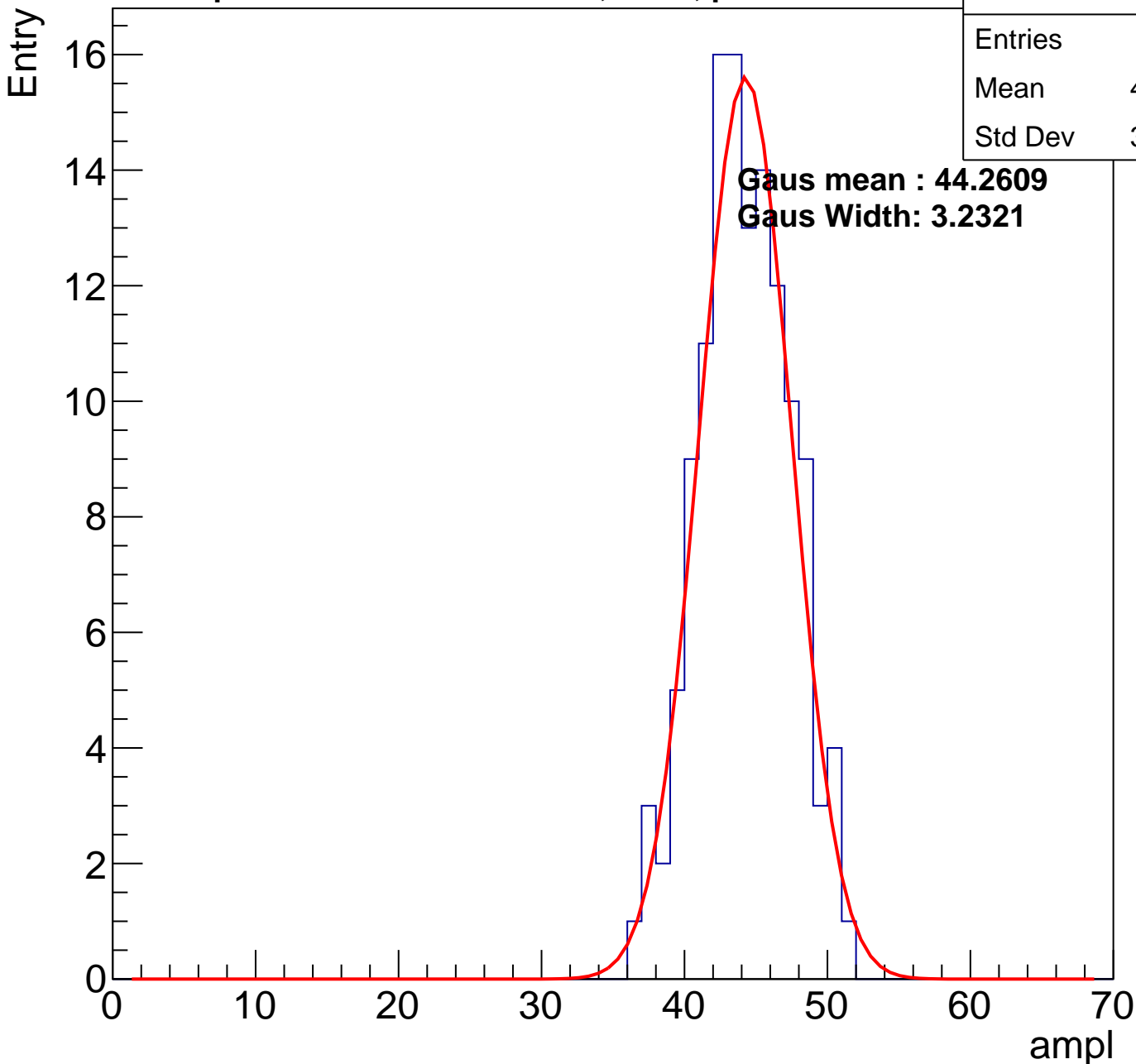
B1L001S, U20-ch64, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	43.74
Std Dev	3.158

Gaus mean : 44.2609

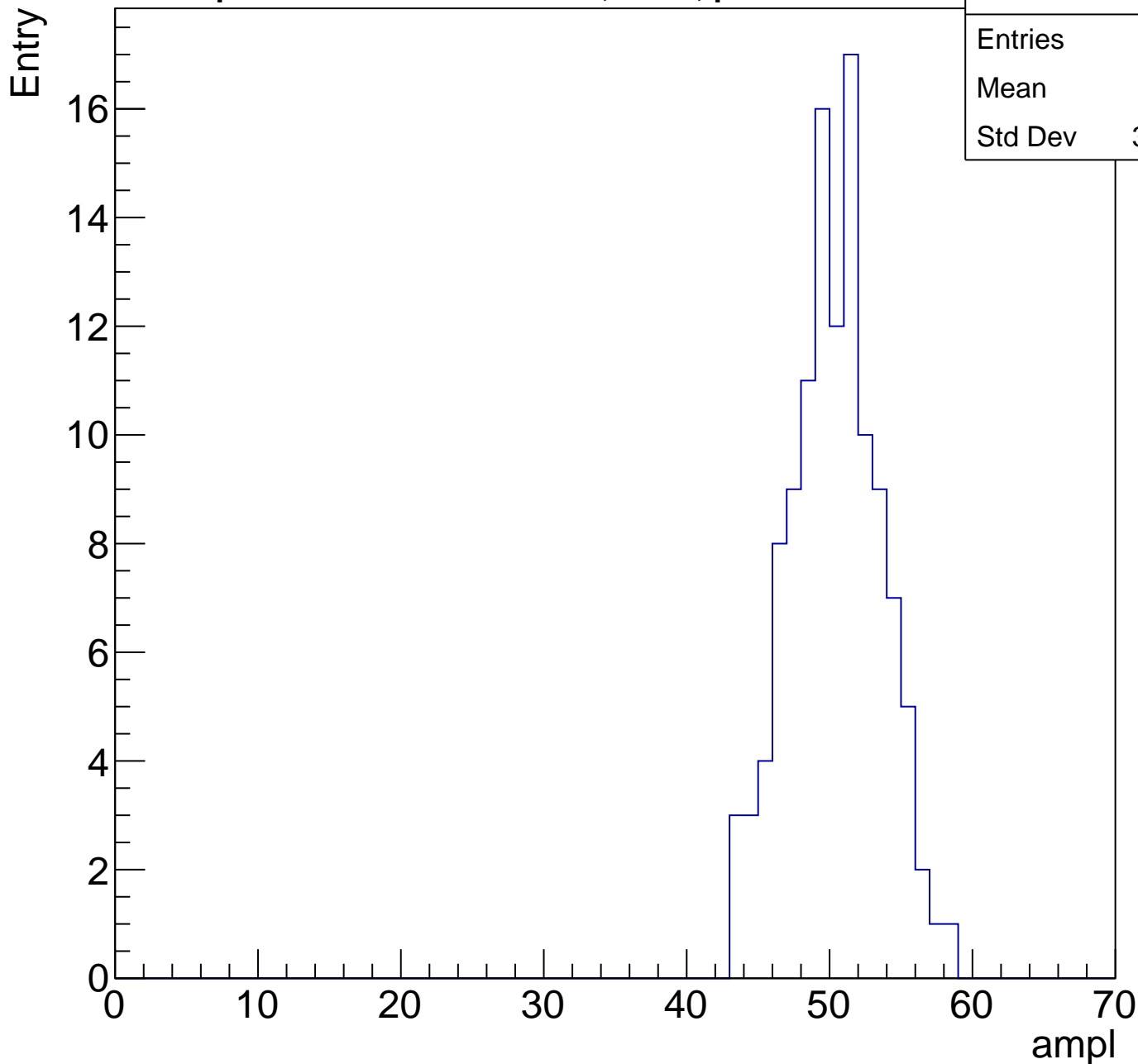
Gaus Width: 3.2321



B1L001S, U20-ch64, adc3

calib_packv5_042523_0143.root, FC#2, port C2

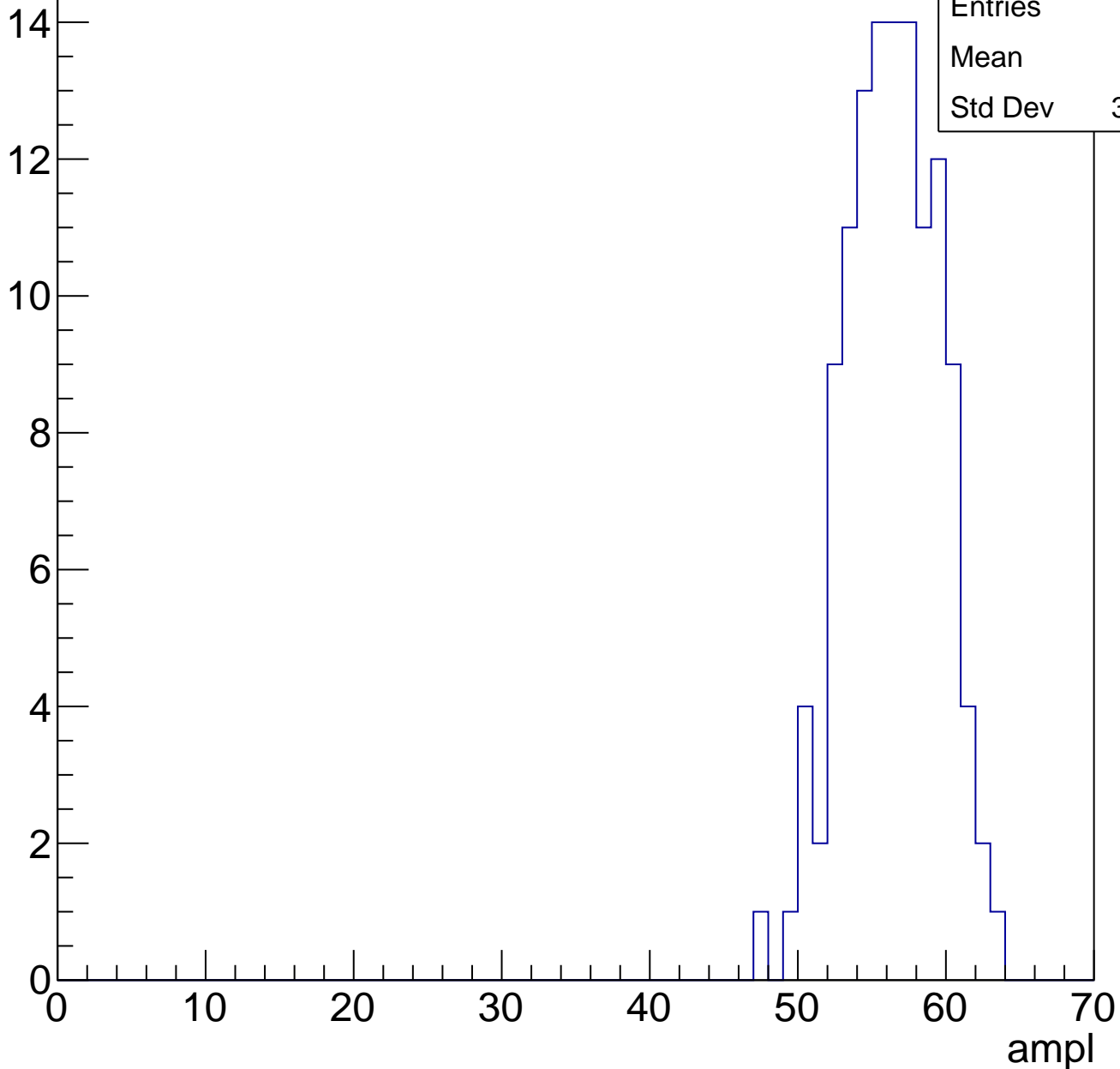
Entries	118
Mean	49.9
Std Dev	3.166



B1L001S, U20-ch64, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch64, adc5

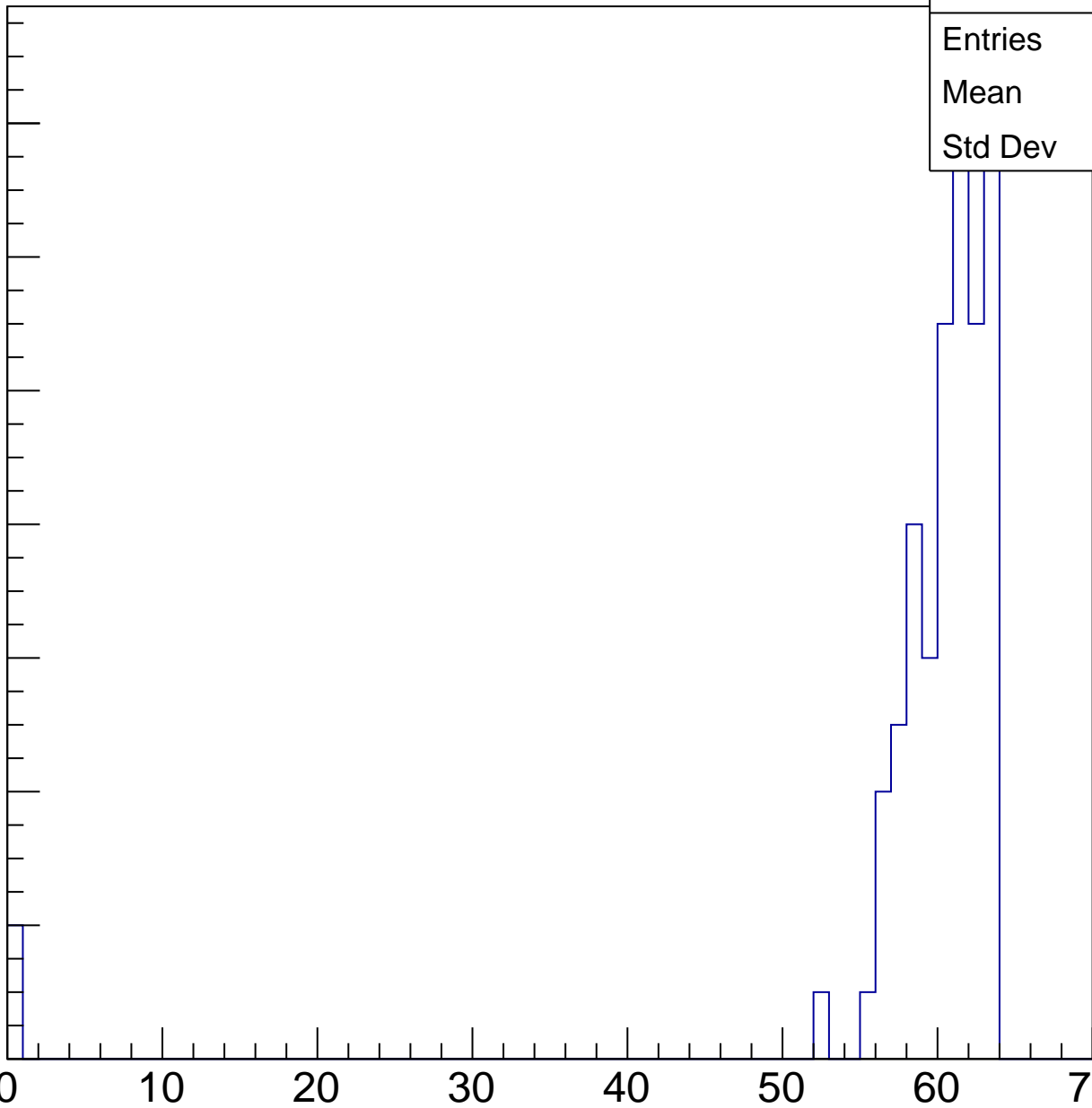
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	58.65
Std Dev	9.798

ampl

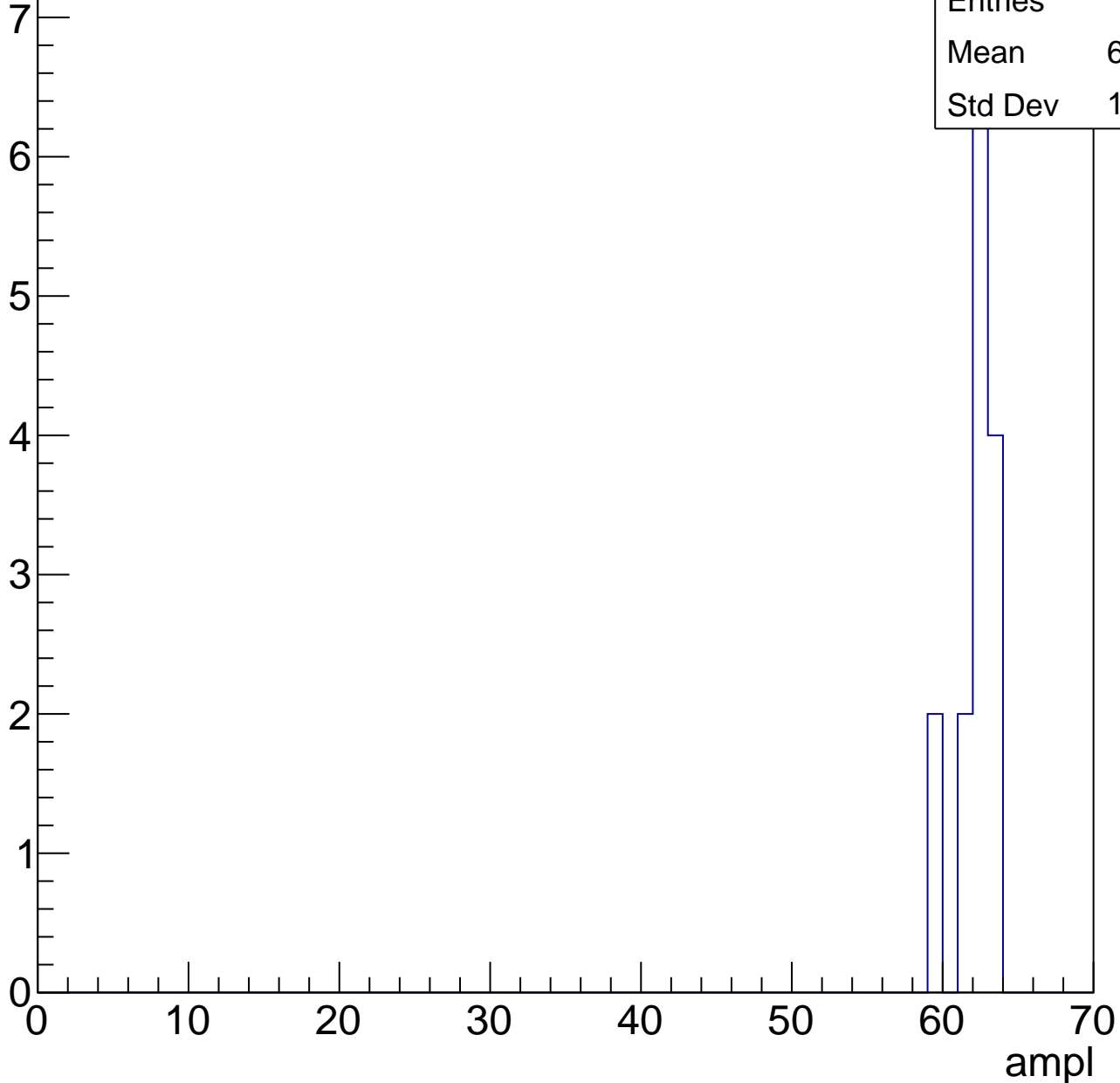


B1L001S, U20-ch64, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

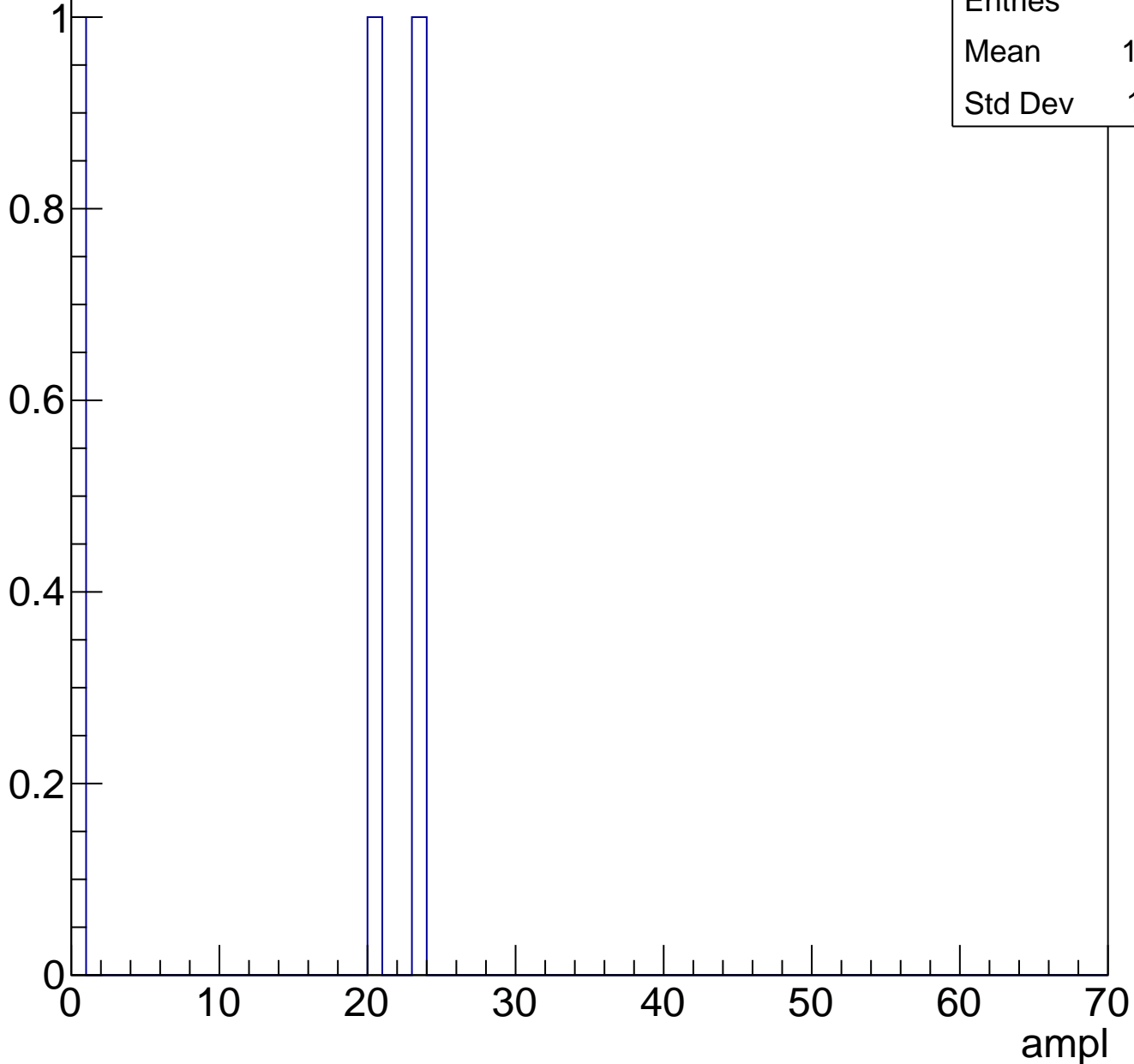
Entries	15
Mean	61.73
Std Dev	1.236



B1L001S, U20-ch64, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch65, adc0

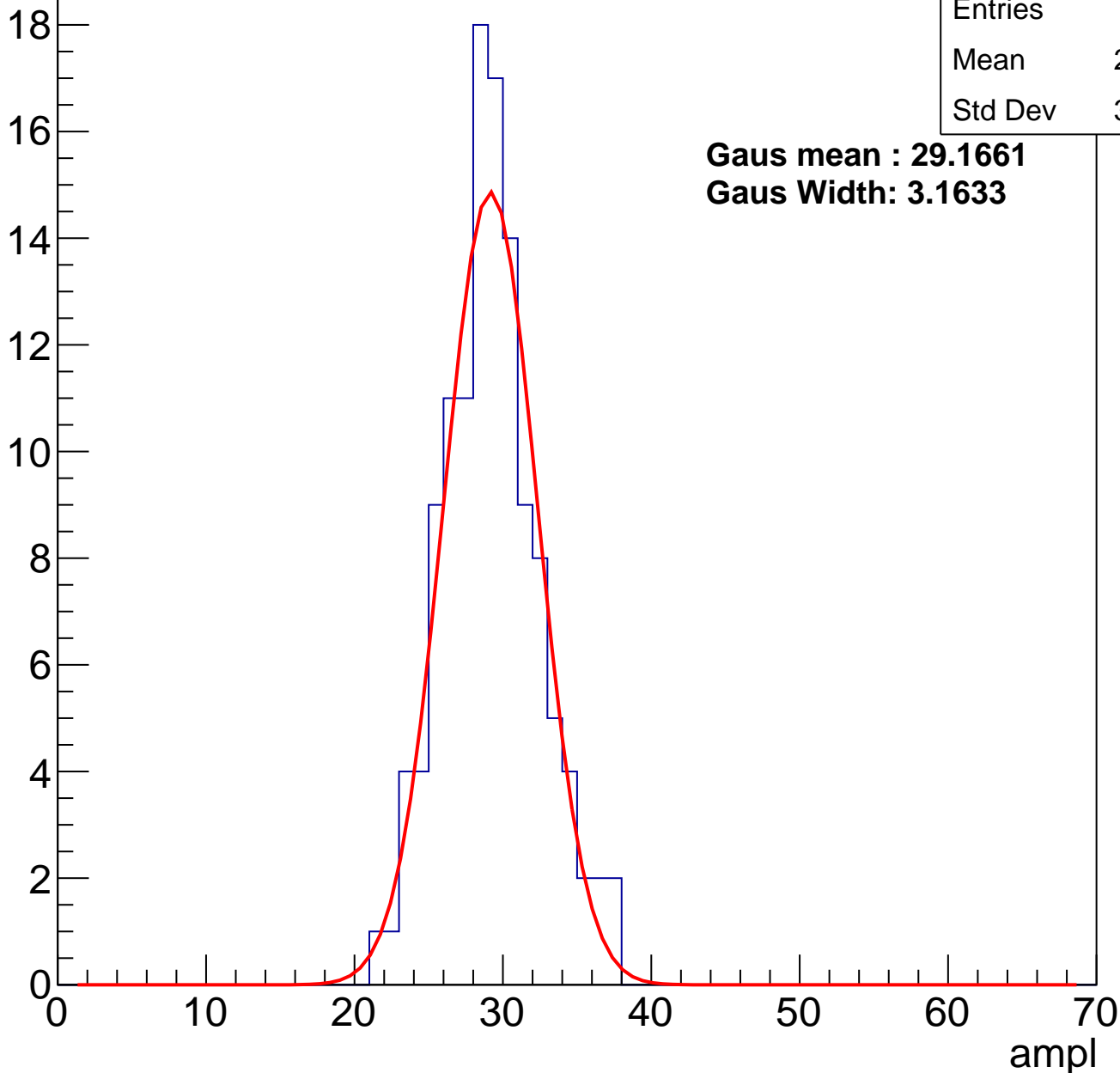
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	28.75
Std Dev	3.207

Gaus mean : 29.1661

Gaus Width: 3.1633

Entry



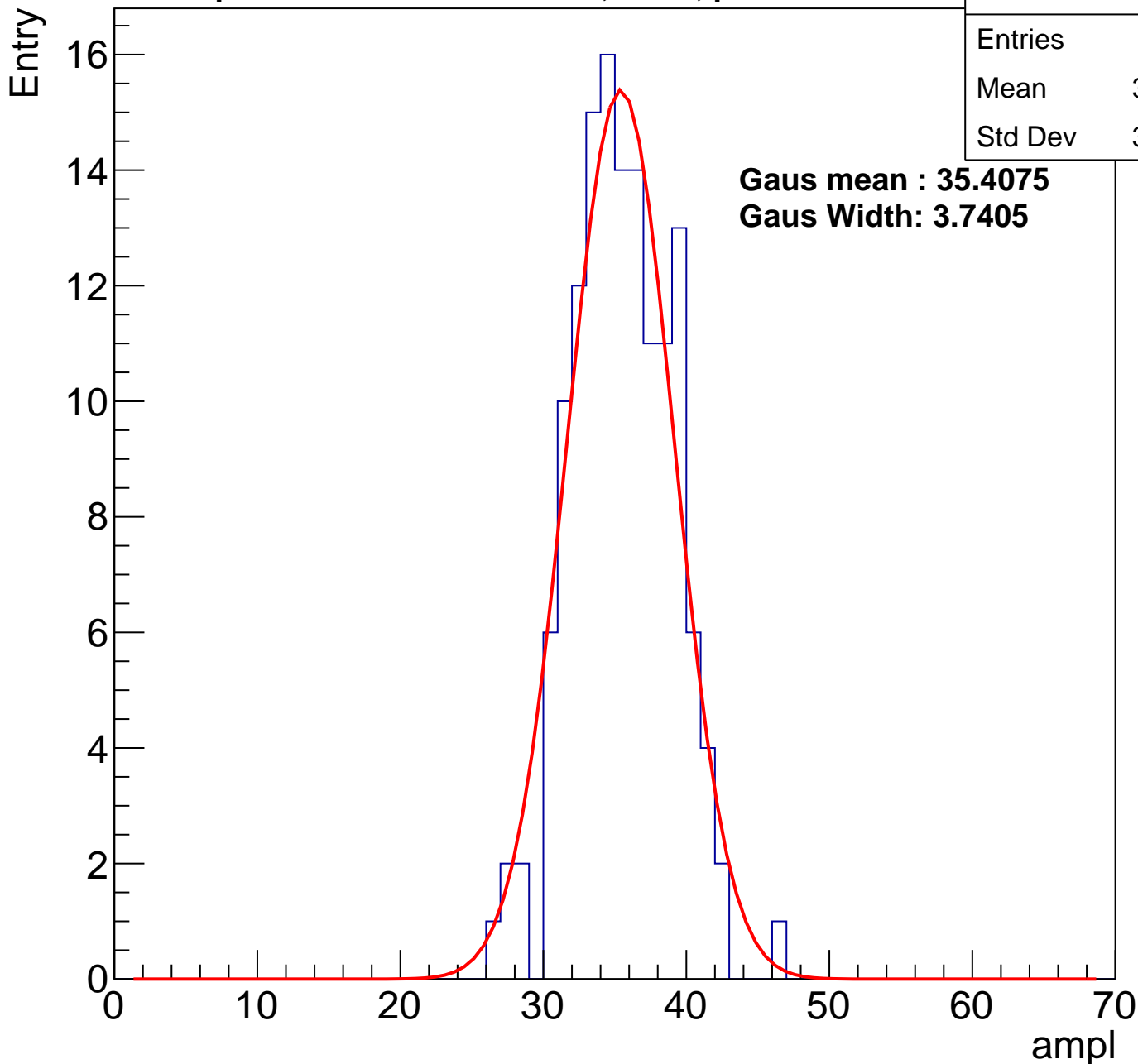
B1L001S, U20-ch65, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	35.06
Std Dev	3.462

Gaus mean : 35.4075

Gaus Width: 3.7405



B1L001S, U20-ch65, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

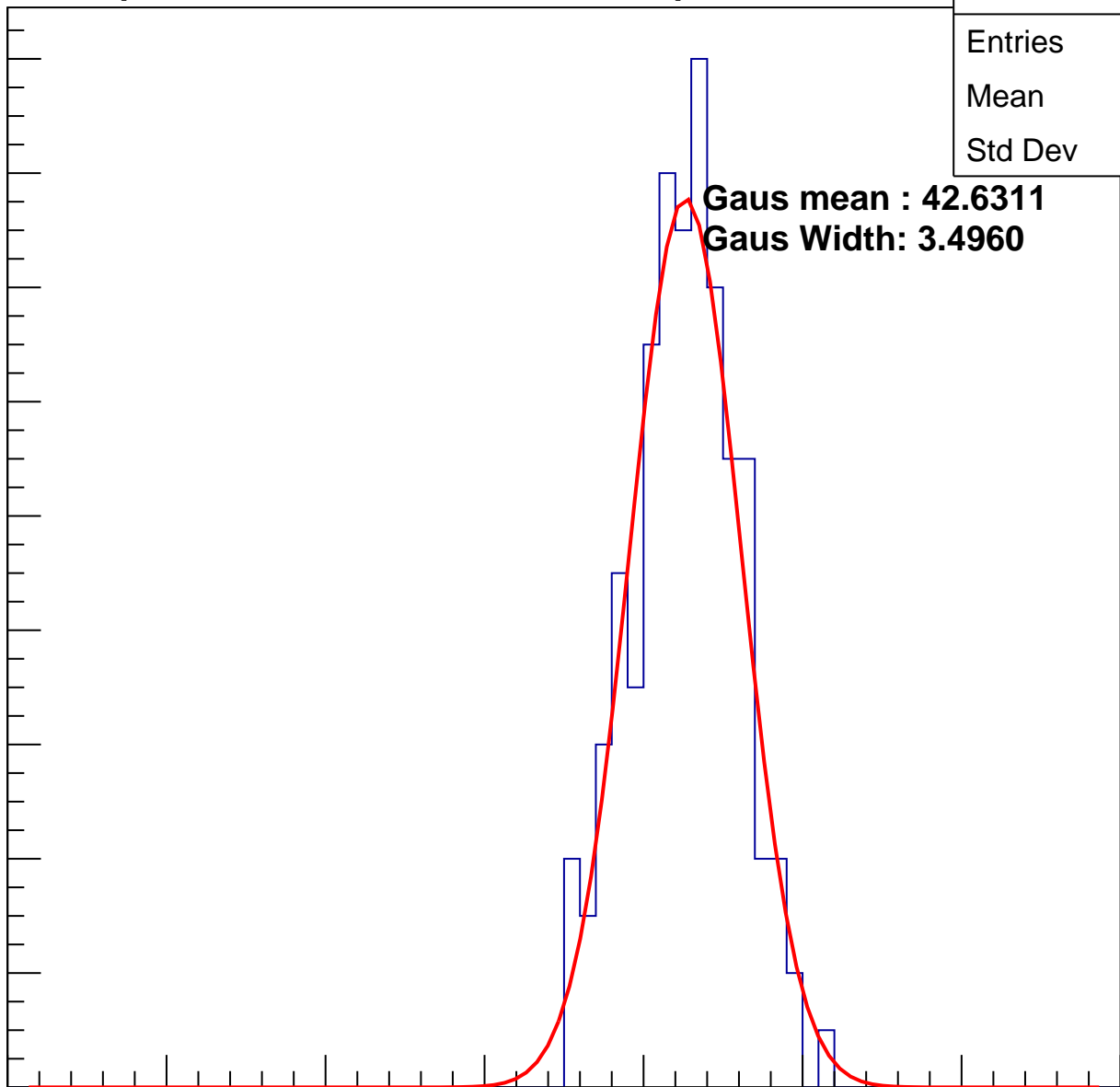
Entries	138
Mean	42.11
Std Dev	3.296

Gaus mean : 42.6311

Gaus Width: 3.4960

0 10 20 30 40 50 60 70

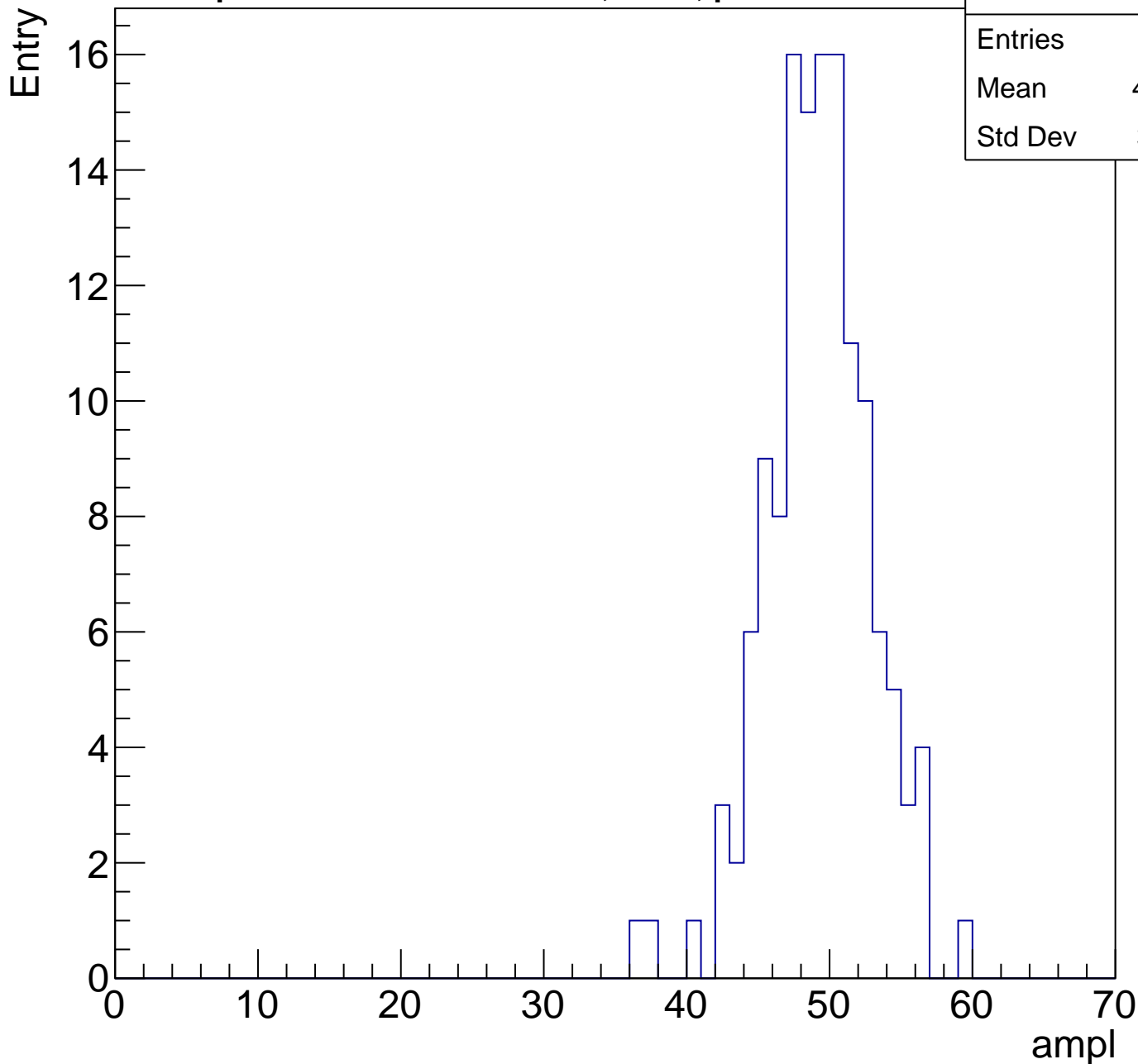
ampl



B1L001S, U20-ch65, adc3

calib_packv5_042523_0143.root, FC#2, port C2

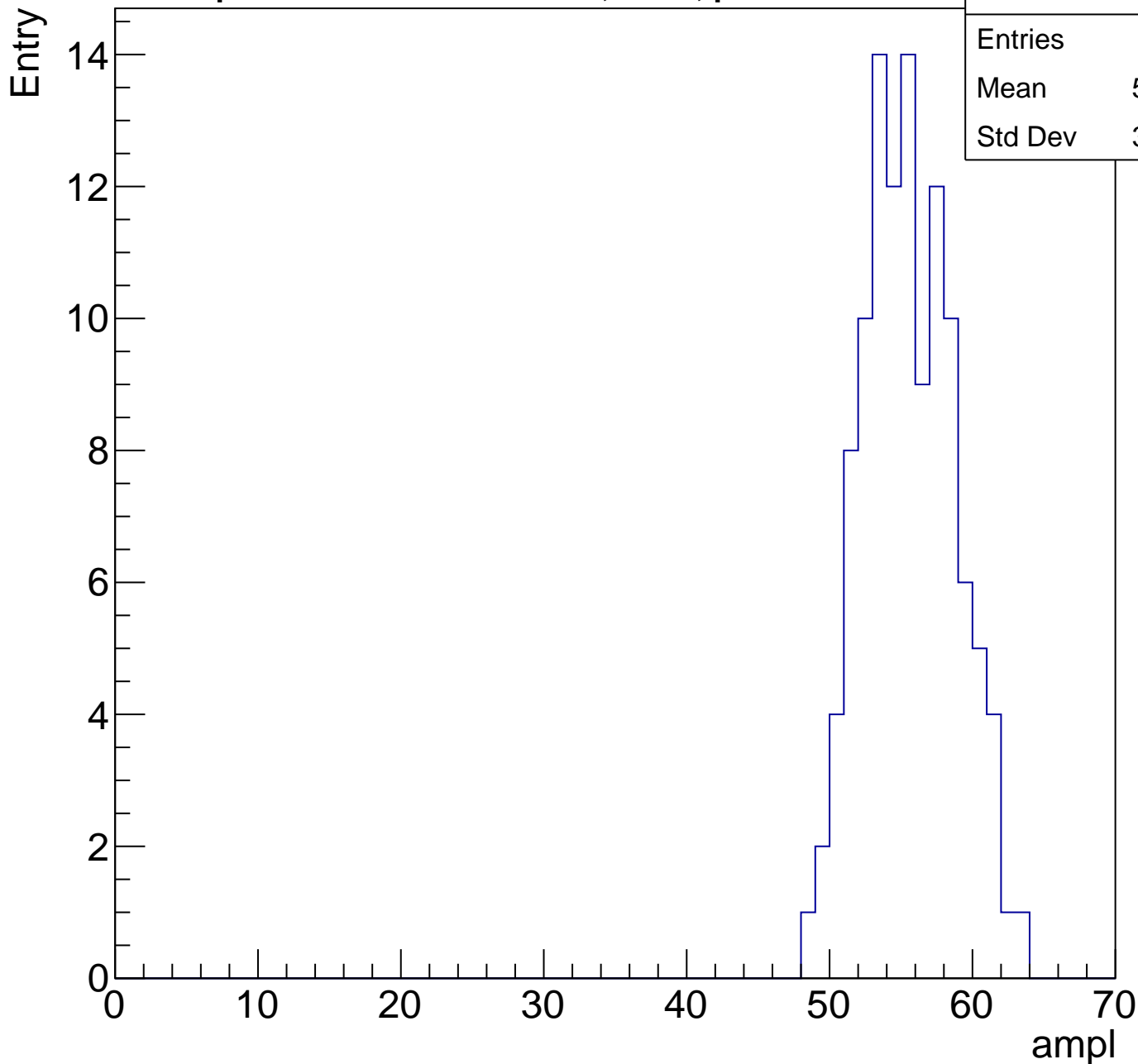
Entries	134
Mean	48.77
Std Dev	3.691



B1L001S, U20-ch65, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	55.09
Std Dev	3.164



B1L001S, U20-ch65, adc5

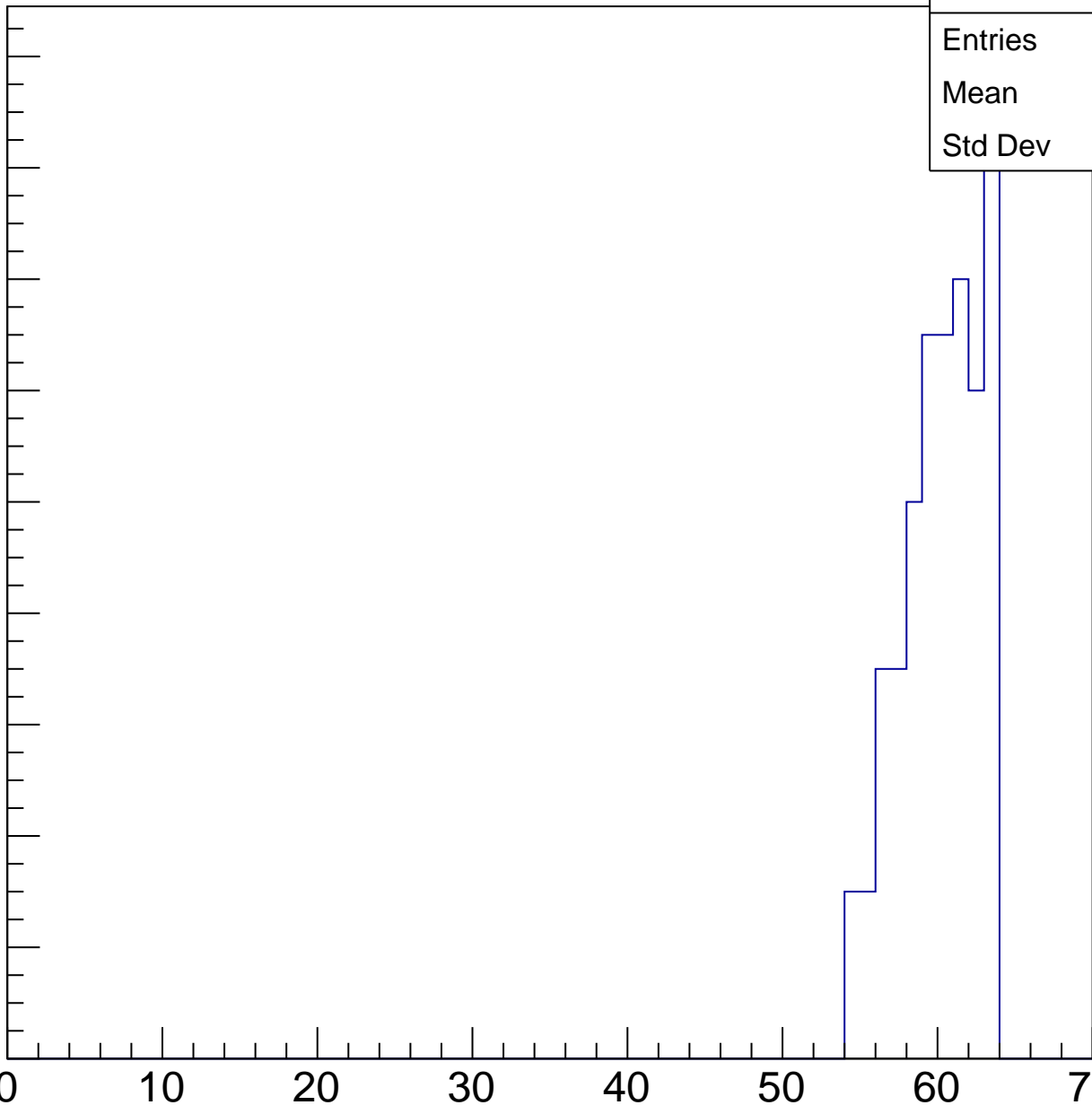
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.77
Std Dev	2.509

ampl

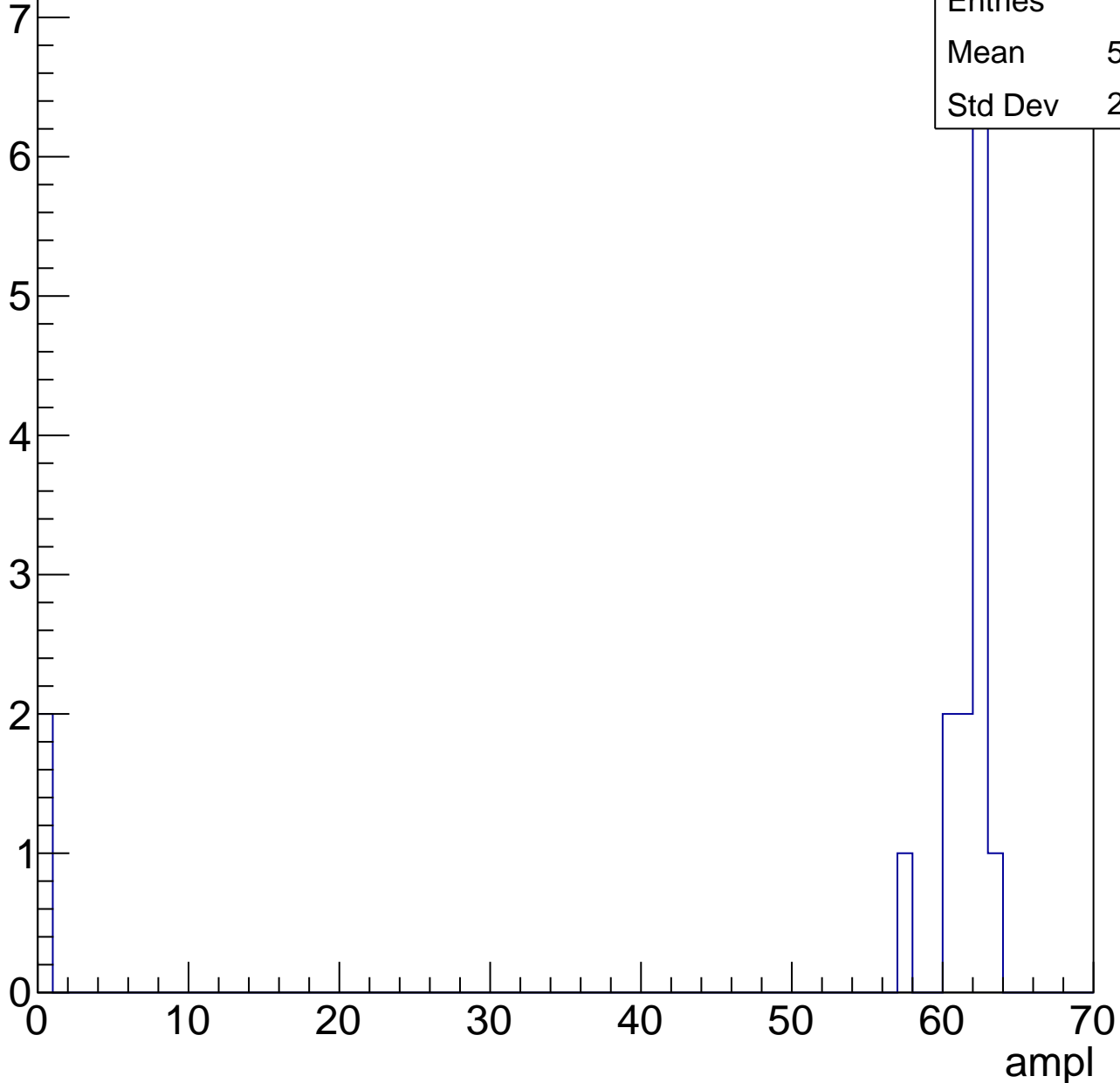


B1L001S, U20-ch65, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	53.07
Std Dev	20.86



B1L001S, U20-ch65, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch66, adc0

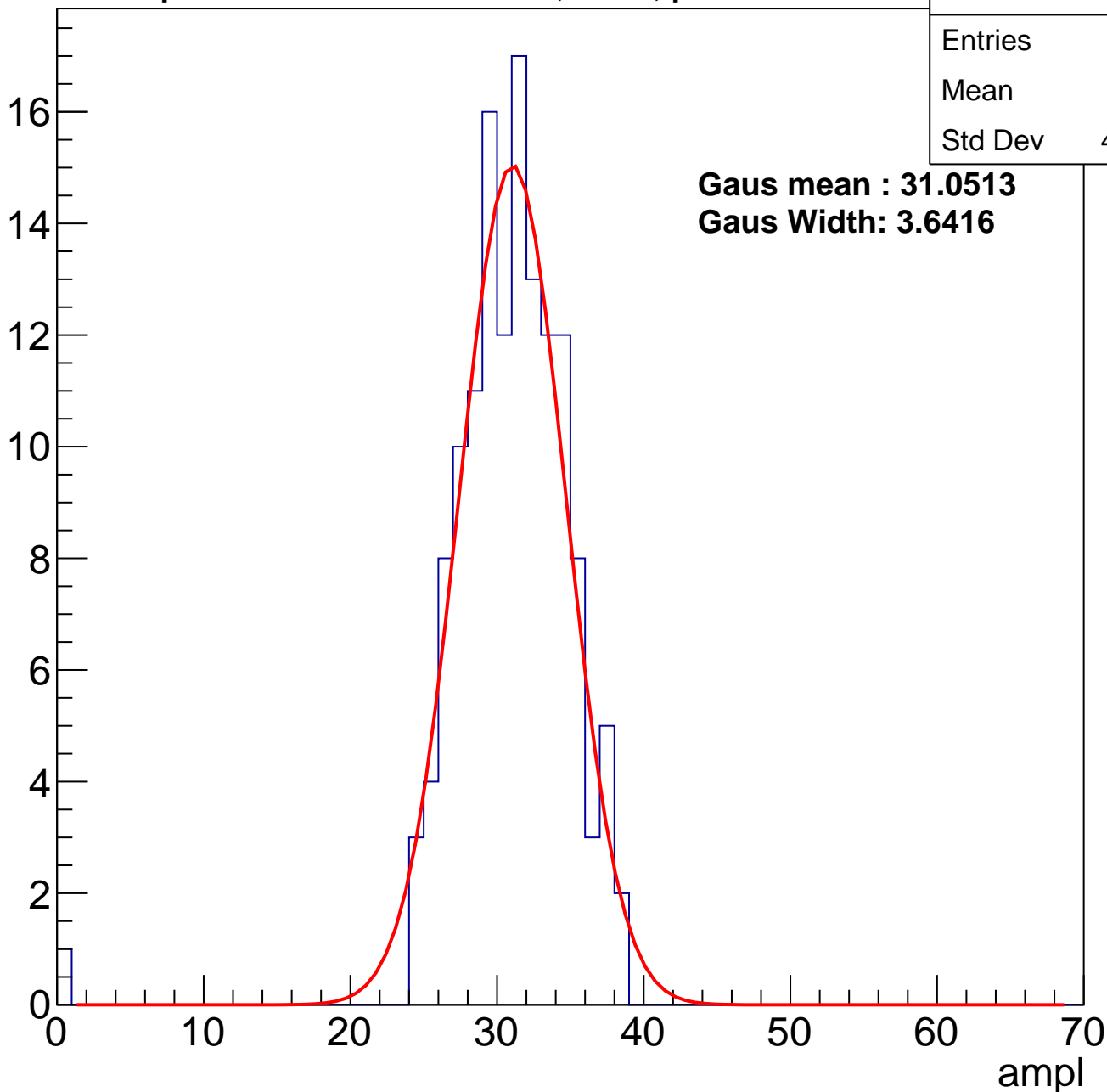
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	30.5
Std Dev	4.187

Gaus mean : 31.0513

Gaus Width: 3.6416

Entry

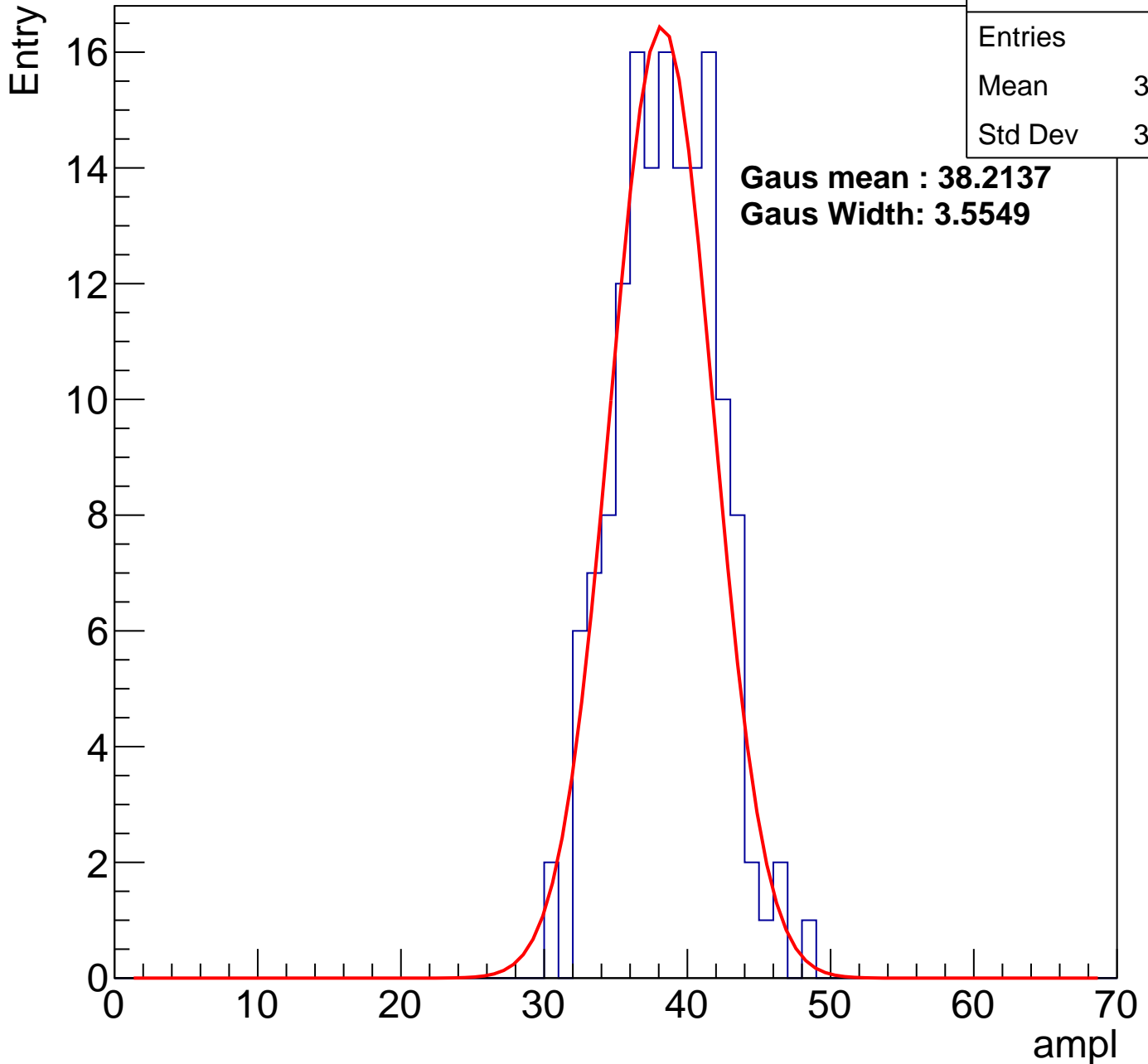


B1L001S, U20-ch66, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	38.09
Std Dev	3.432

Gaus mean : 38.2137
Gaus Width: 3.5549



B1L001S, U20-ch66, adc2

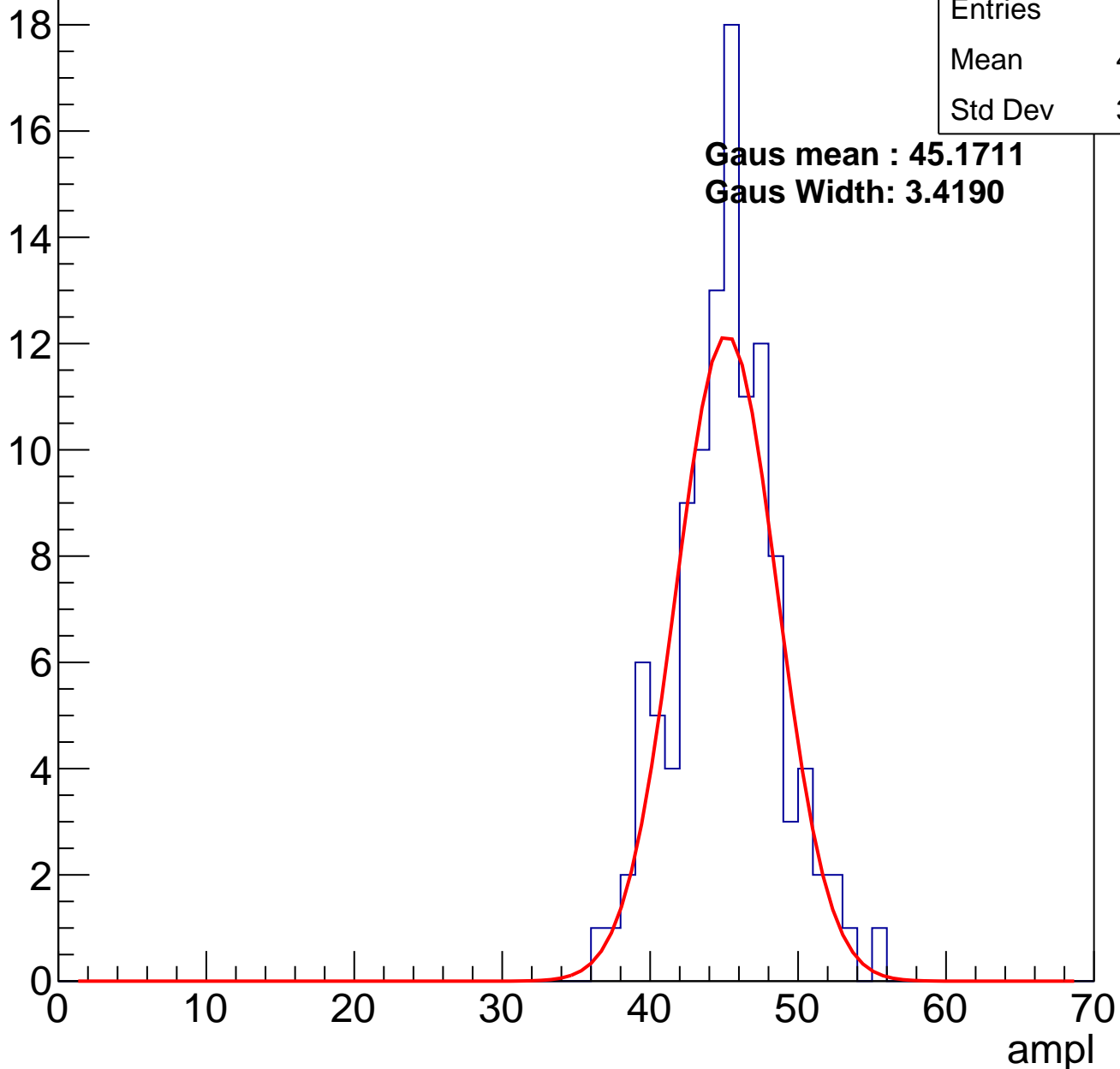
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	44.71
Std Dev	3.501

Gaus mean : 45.1711

Gaus Width: 3.4190

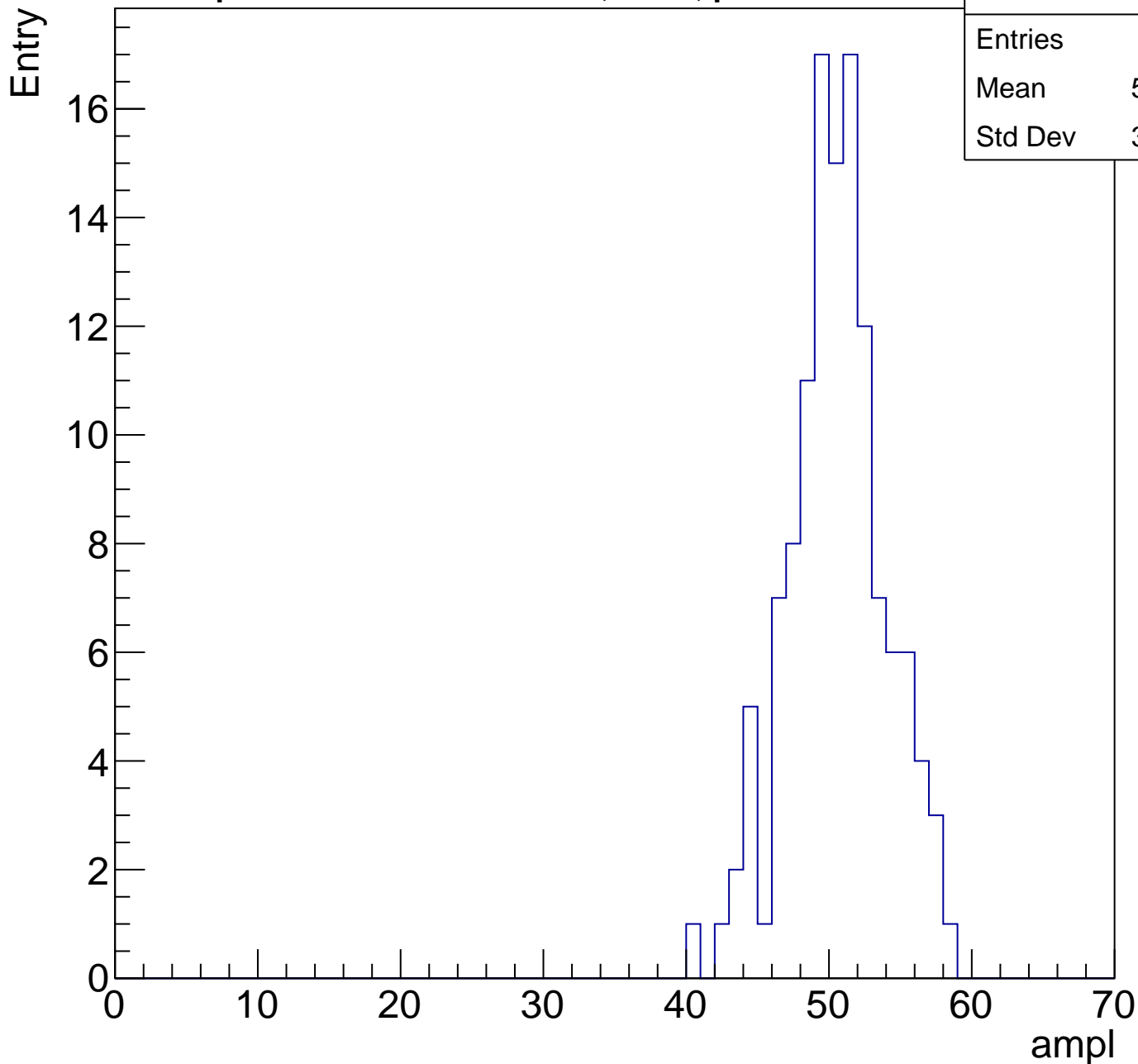
Entry



B1L001S, U20-ch66, adc3

calib_packv5_042523_0143.root, FC#2, port C2

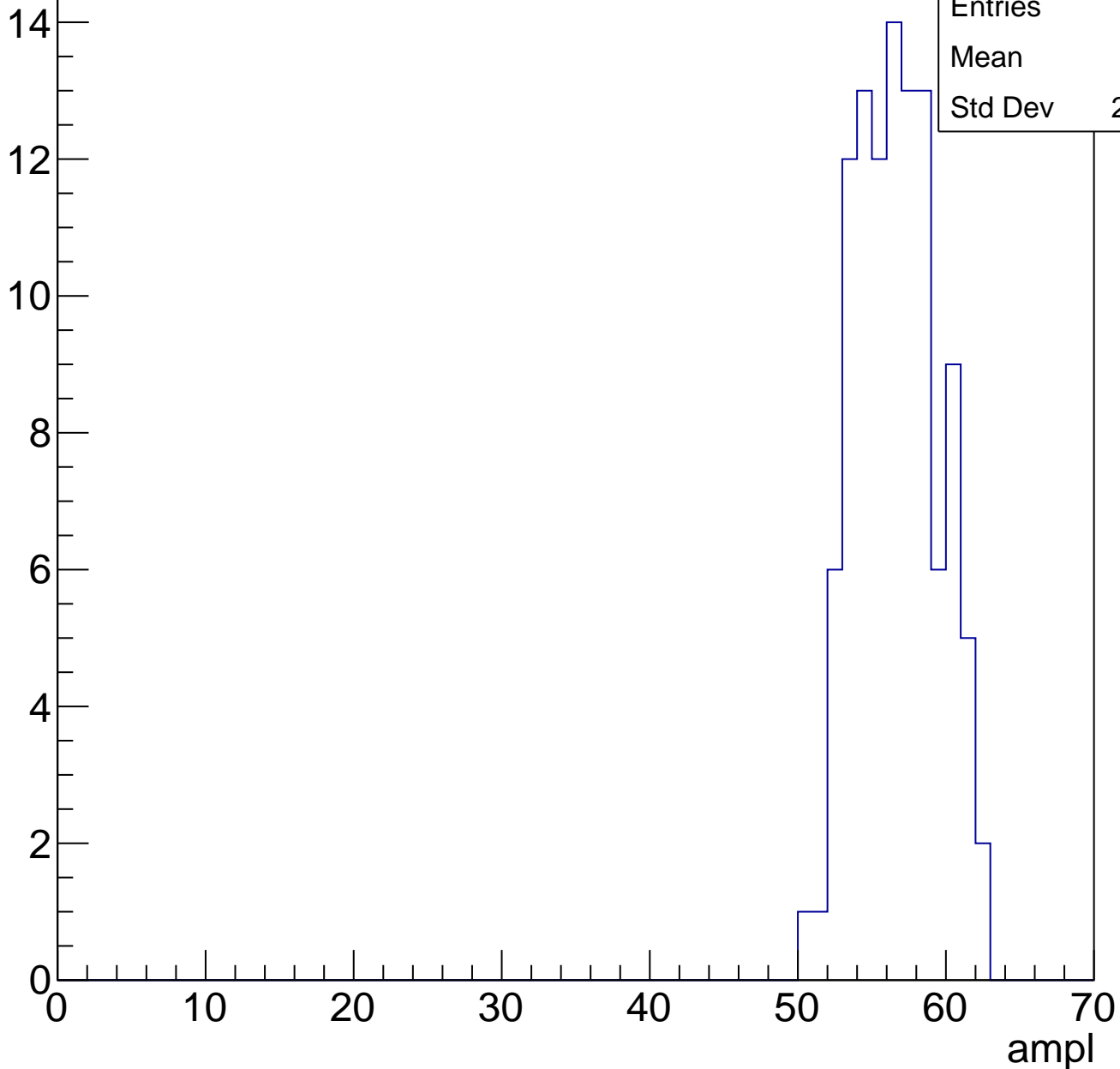
Entries	124
Mean	50.09
Std Dev	3.408



B1L001S, U20-ch66, adc4

calib_packv5_042523_0143.root, FC#2, port C2

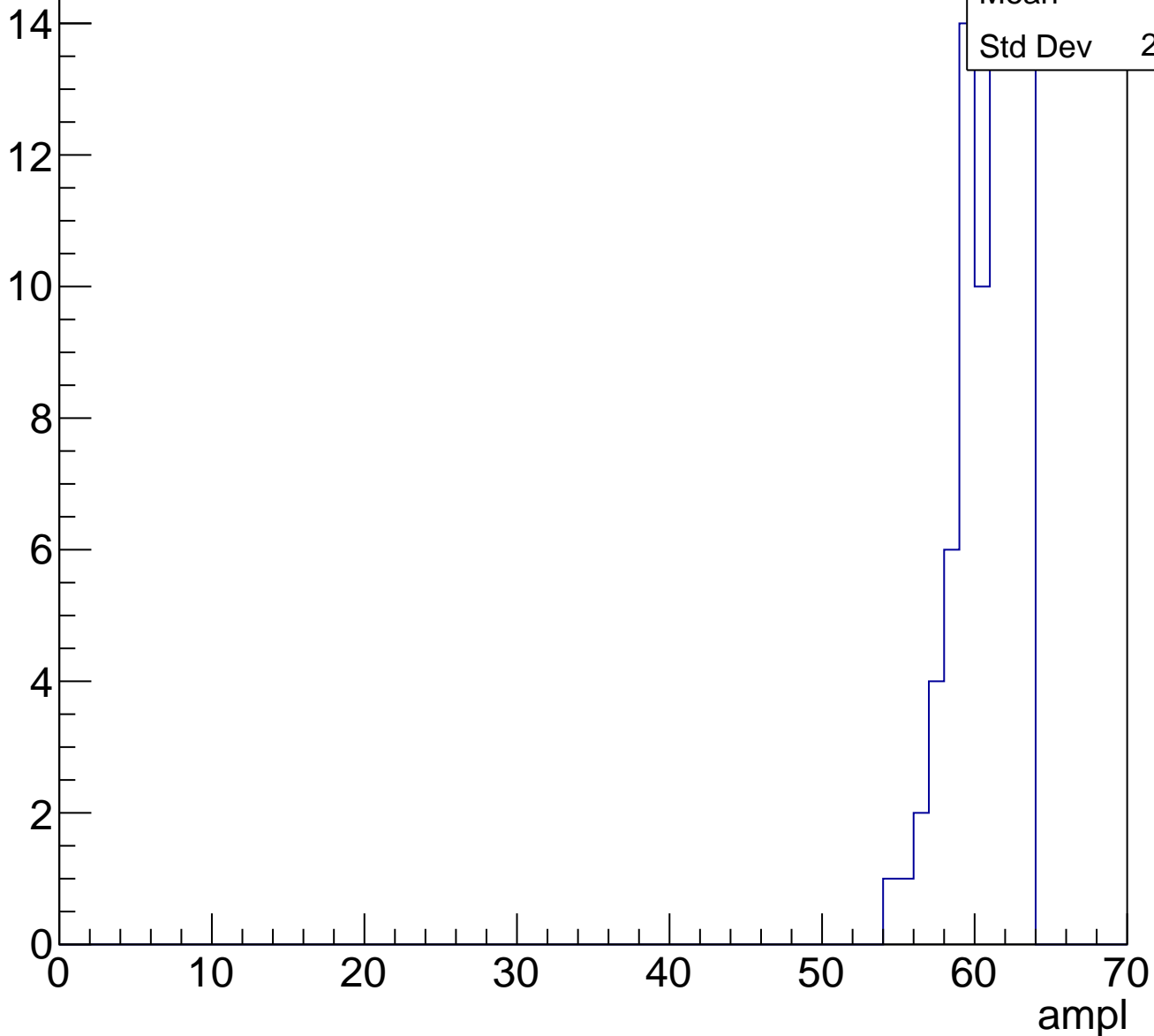
Entry



B1L001S, U20-ch66, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

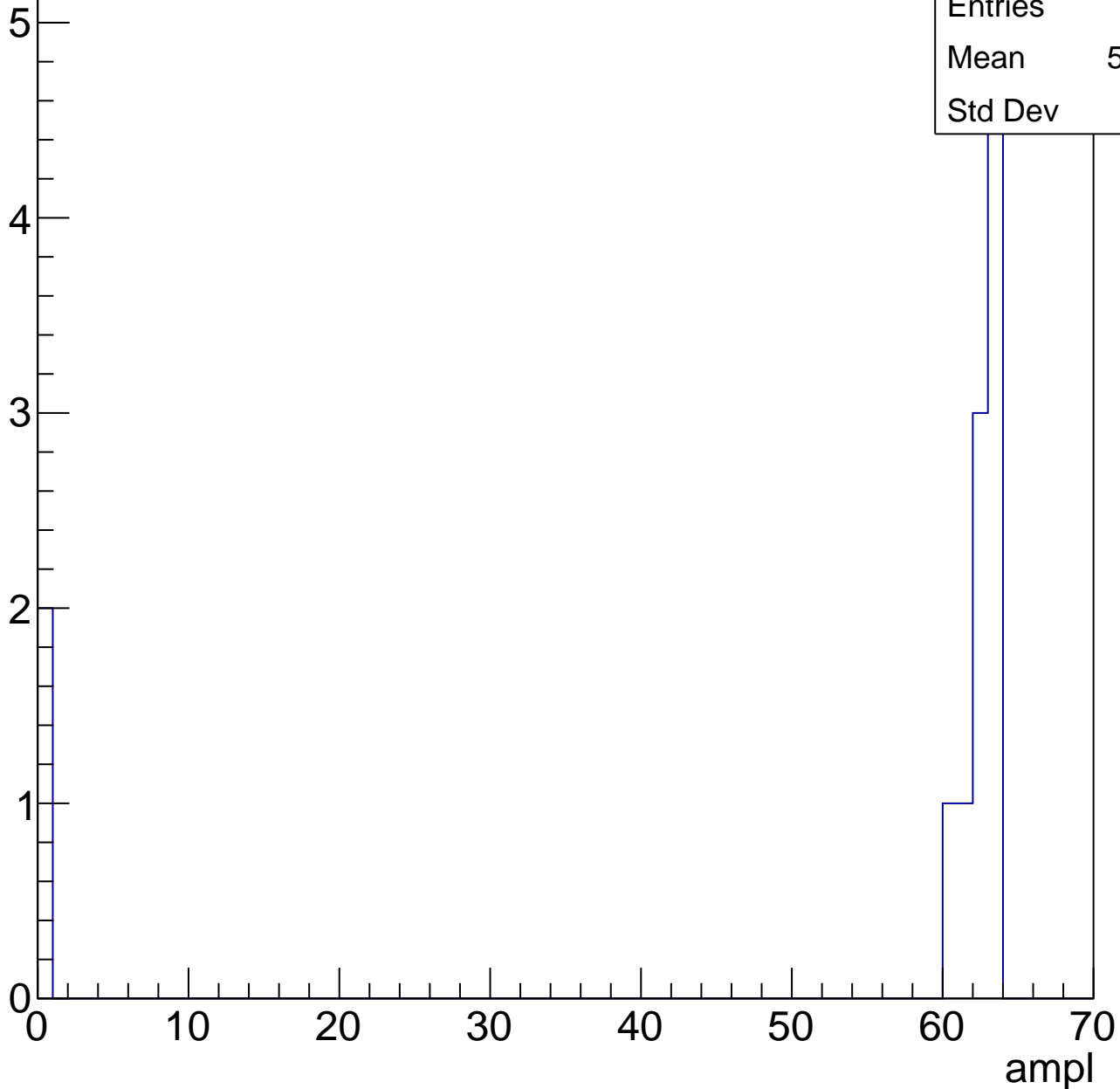


B1L001S, U20-ch66, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

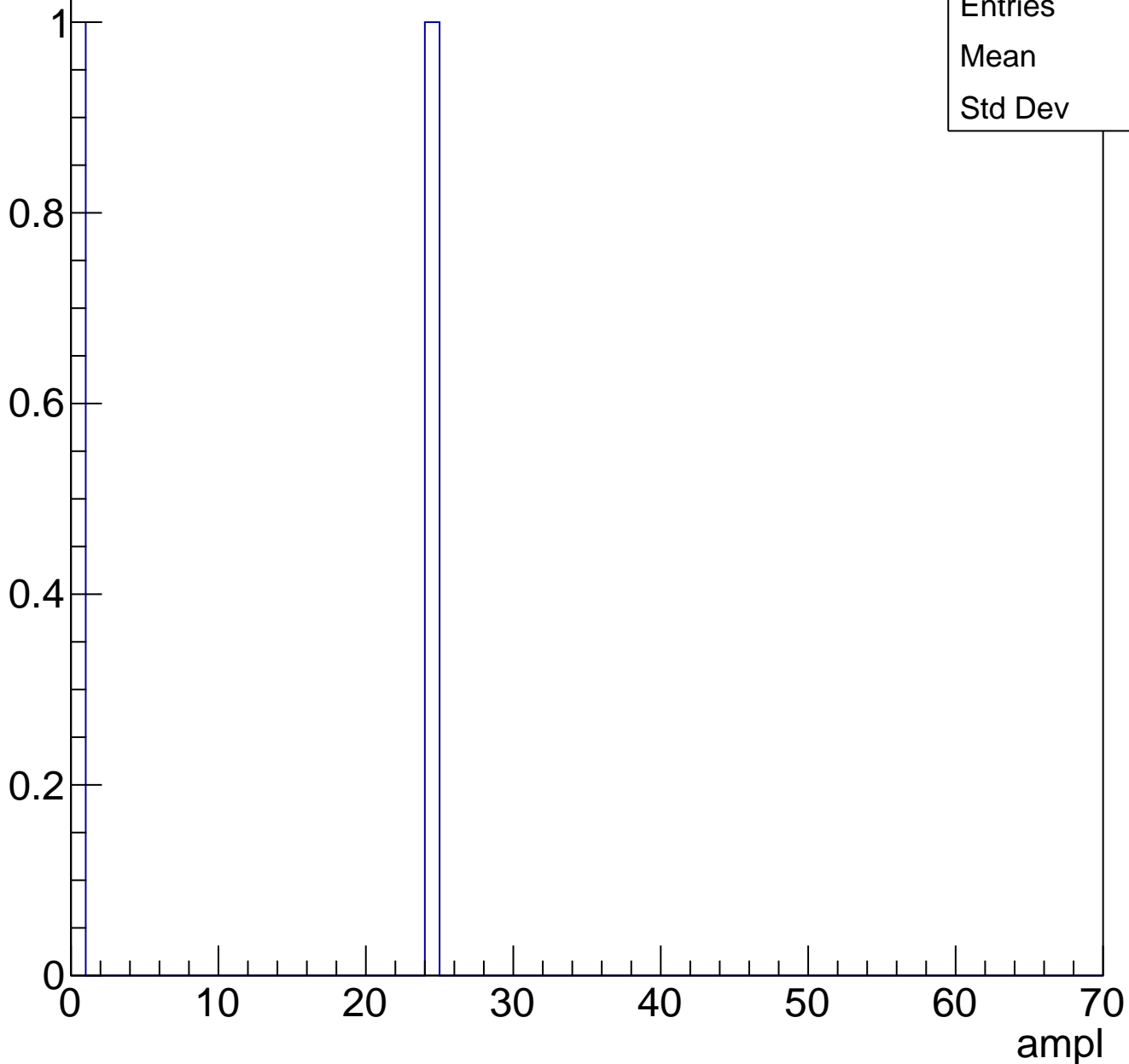
Entries	12
Mean	51.83
Std Dev	23.2



B1L001S, U20-ch66, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



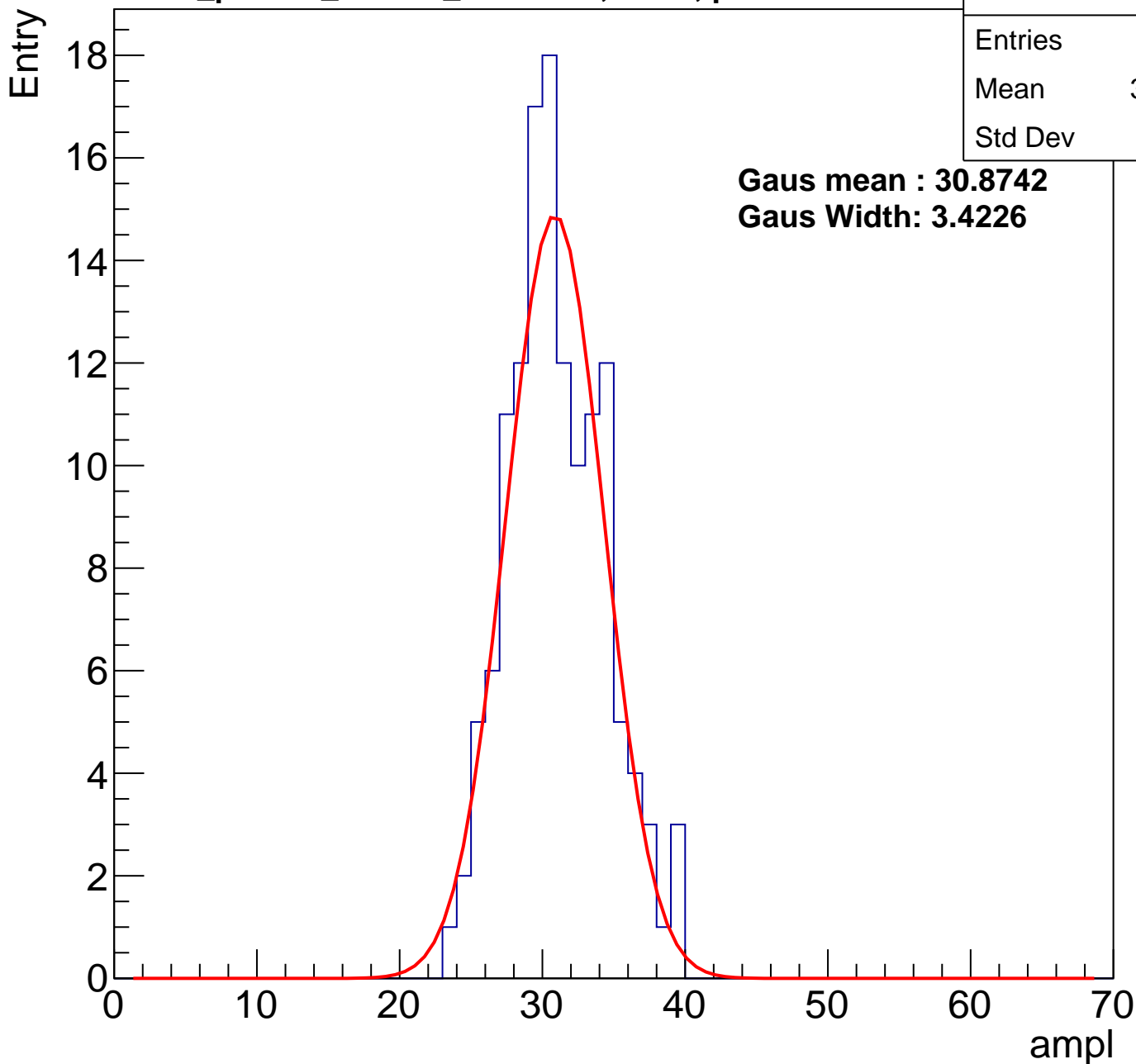
B1L001S, U20-ch67, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	30.57
Std Dev	3.39

Gaus mean : 30.8742

Gaus Width: 3.4226



B1L001S, U20-ch67, adc1

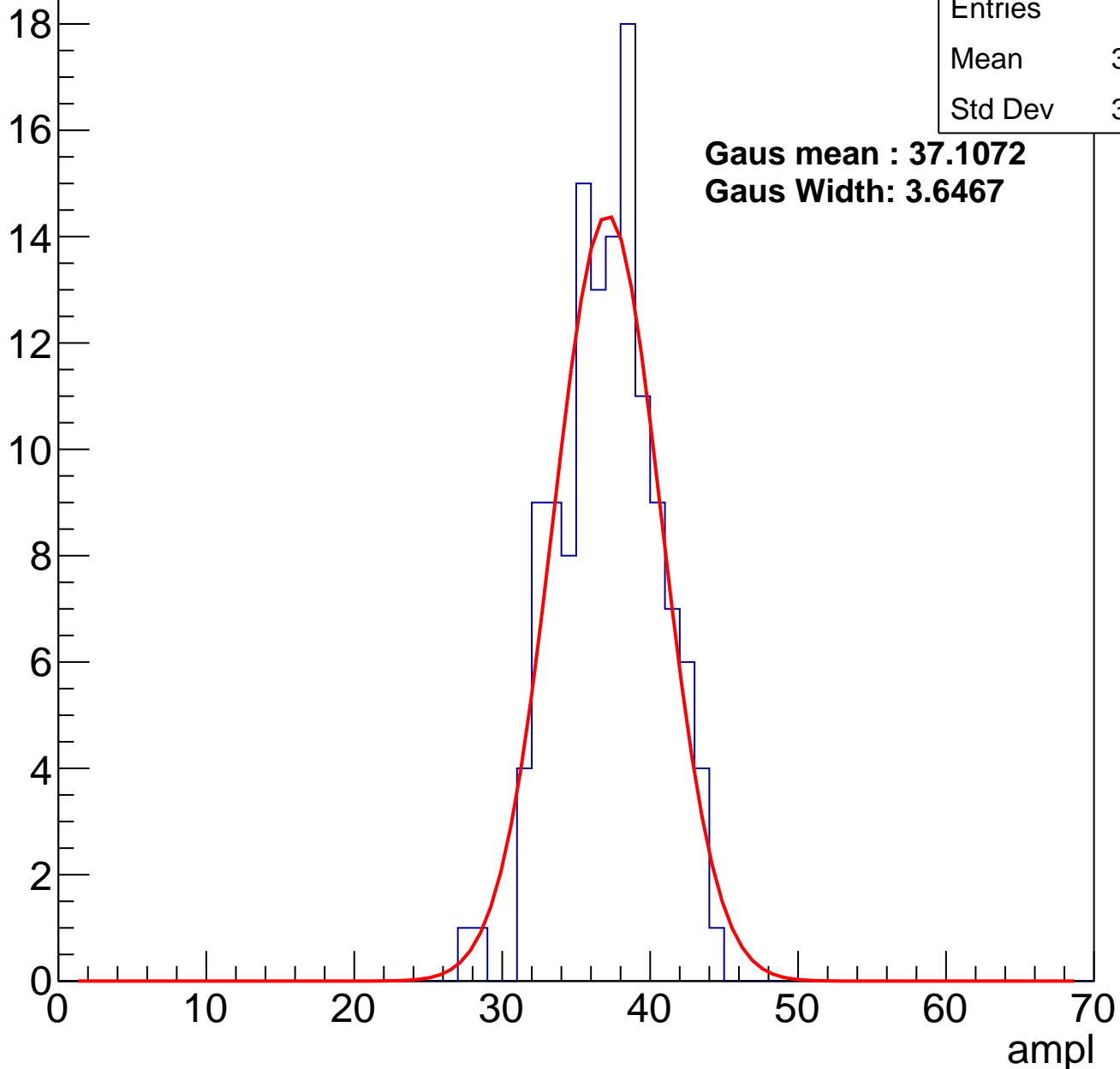
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	36.73
Std Dev	3.314

Gaus mean : 37.1072

Gaus Width: 3.6467

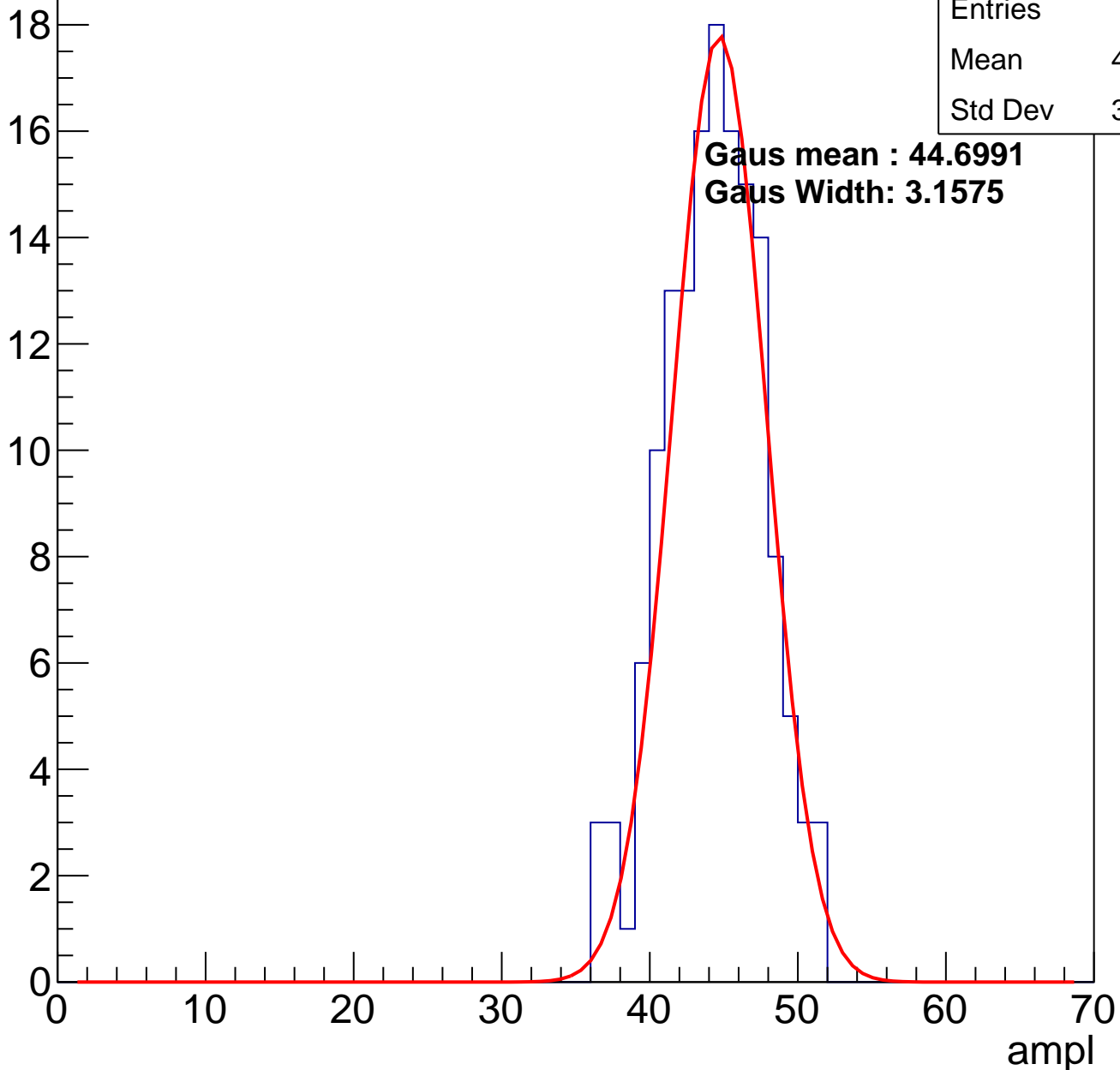
Entry



B1L001S, U20-ch67, adc2

calib_packv5_042523_0143.root, FC#2, port C2

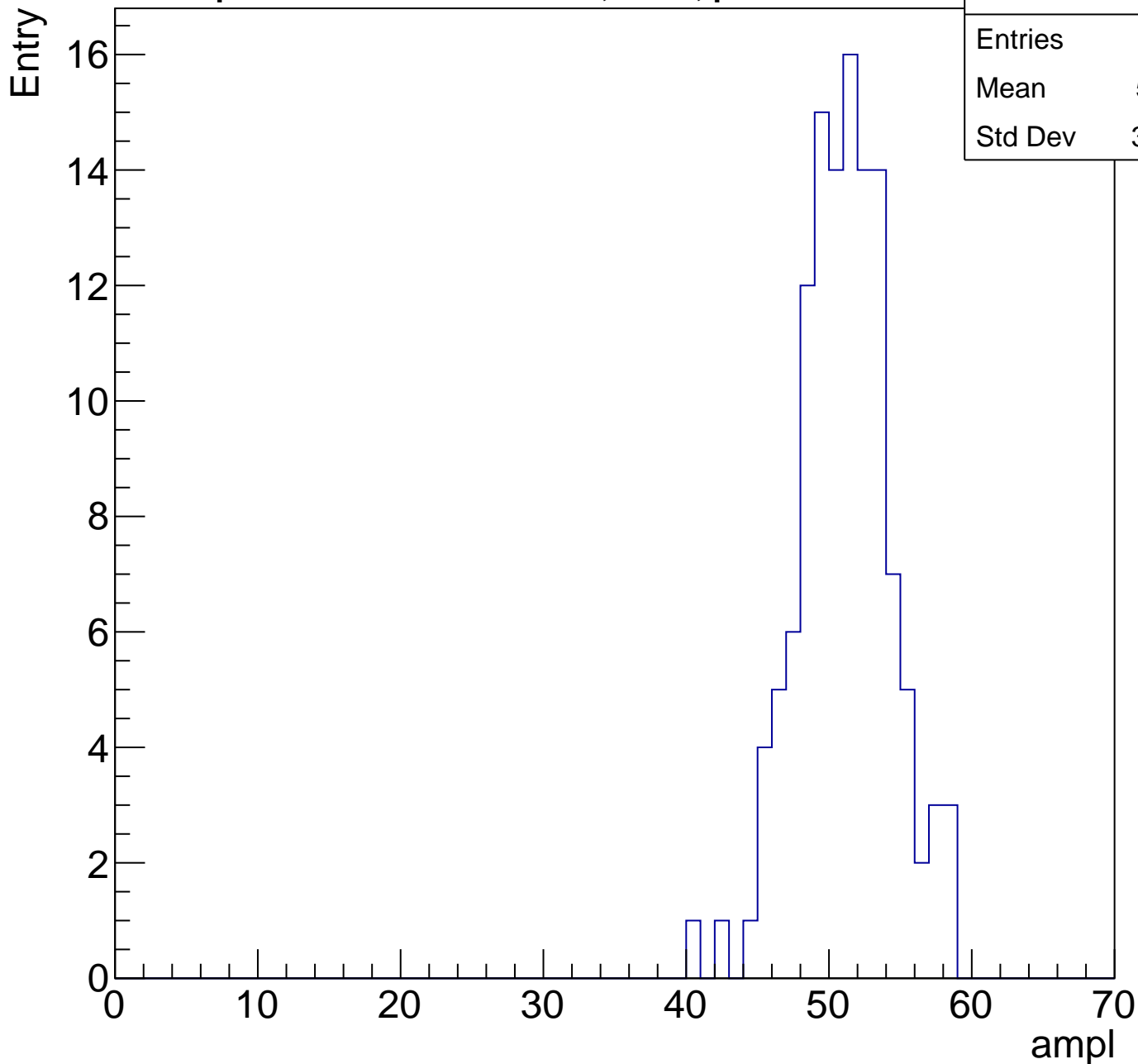
Entry



B1L001S, U20-ch67, adc3

calib_packv5_042523_0143.root, FC#2, port C2

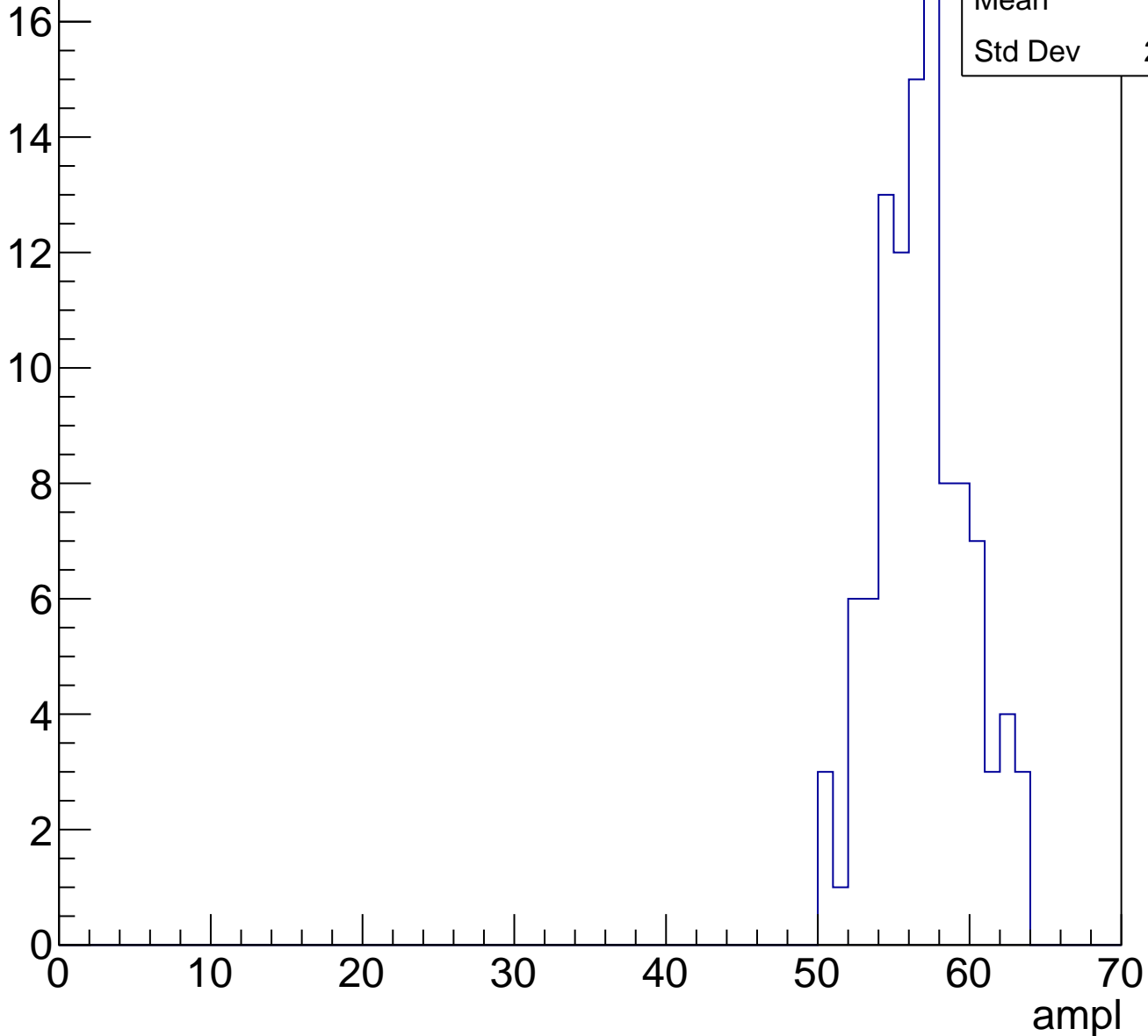
Entries	123
Mean	50.61
Std Dev	3.248



B1L001S, U20-ch67, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch67, adc5

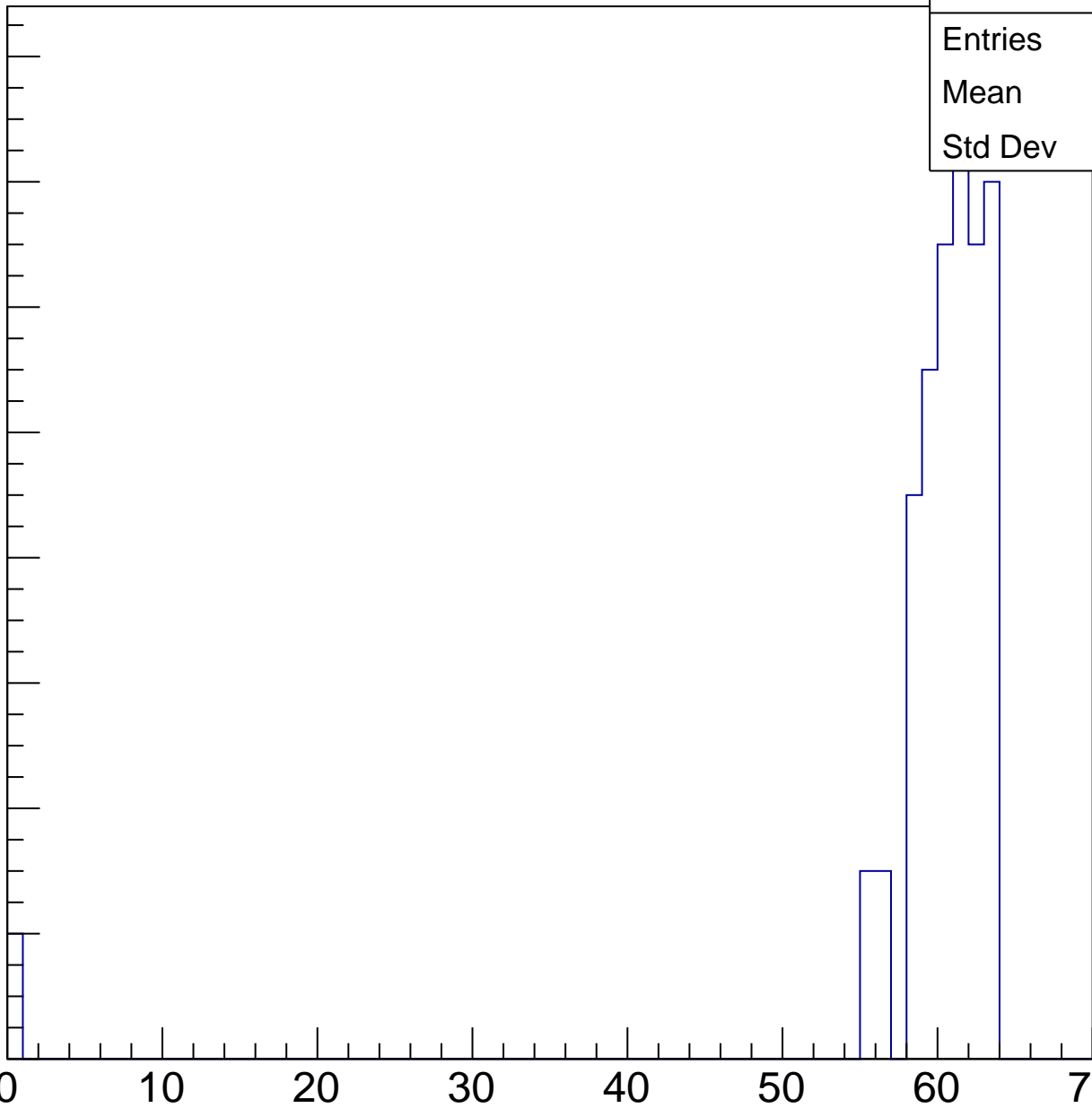
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	58.9
Std Dev	9.426

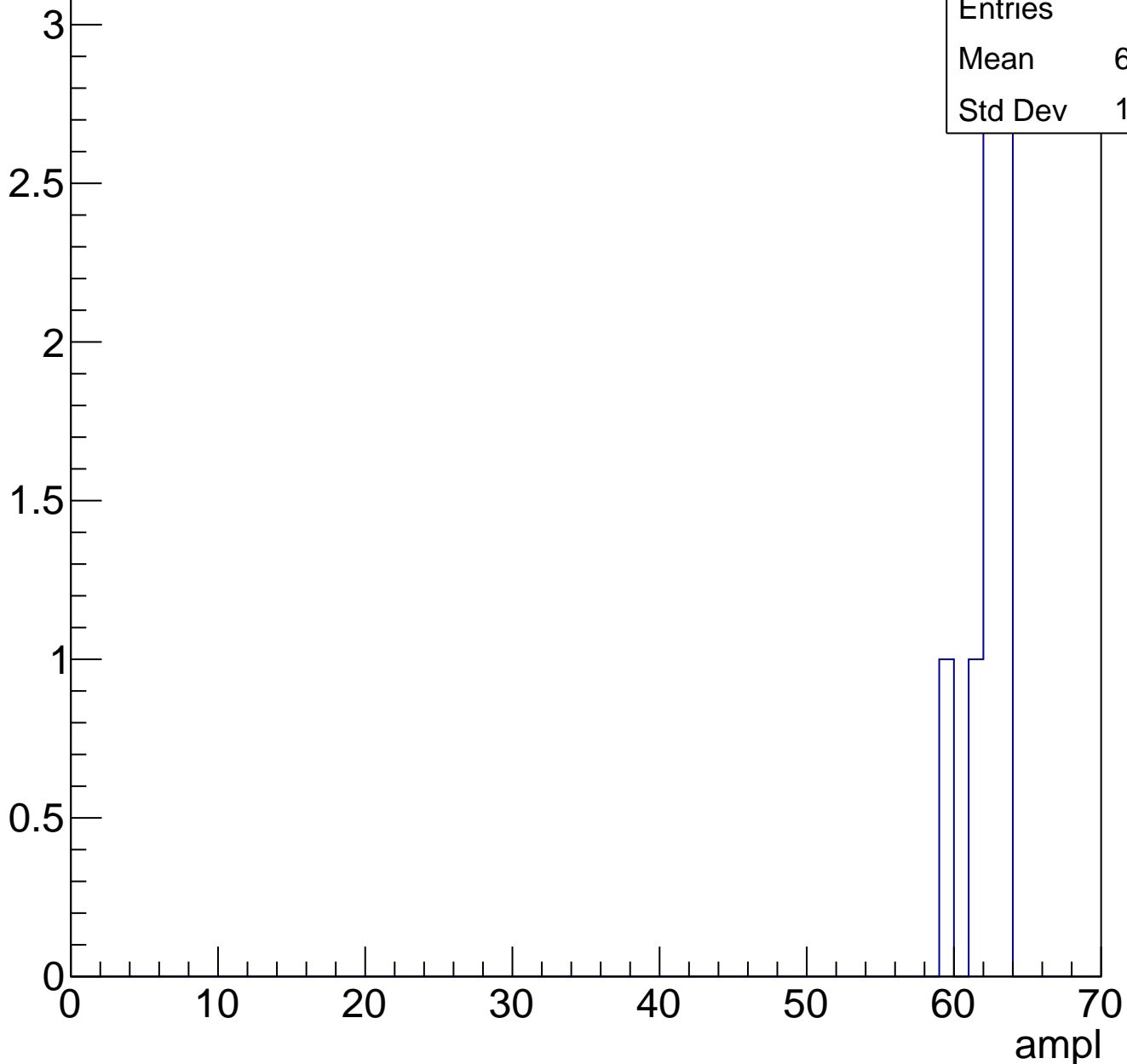
ampl



B1L001S, U20-ch67, adc6

calib_packv5_042523_0143.root, FC#2, port C2

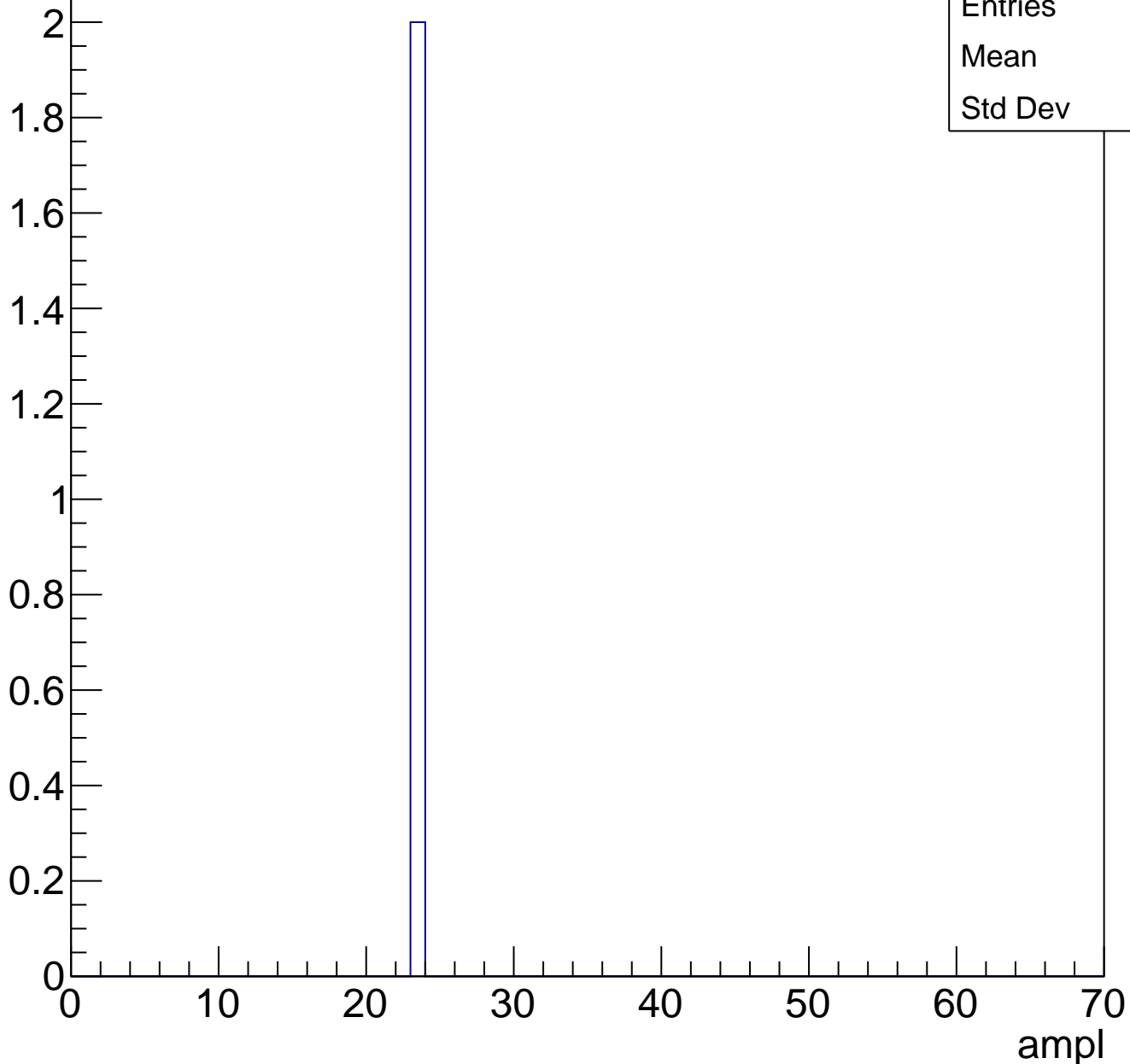
Entry



B1L001S, U20-ch67, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	23
Std Dev	0

B1L001S, U20-ch68, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	29.88
Std Dev	2.803

Gaus mean : 30.4716

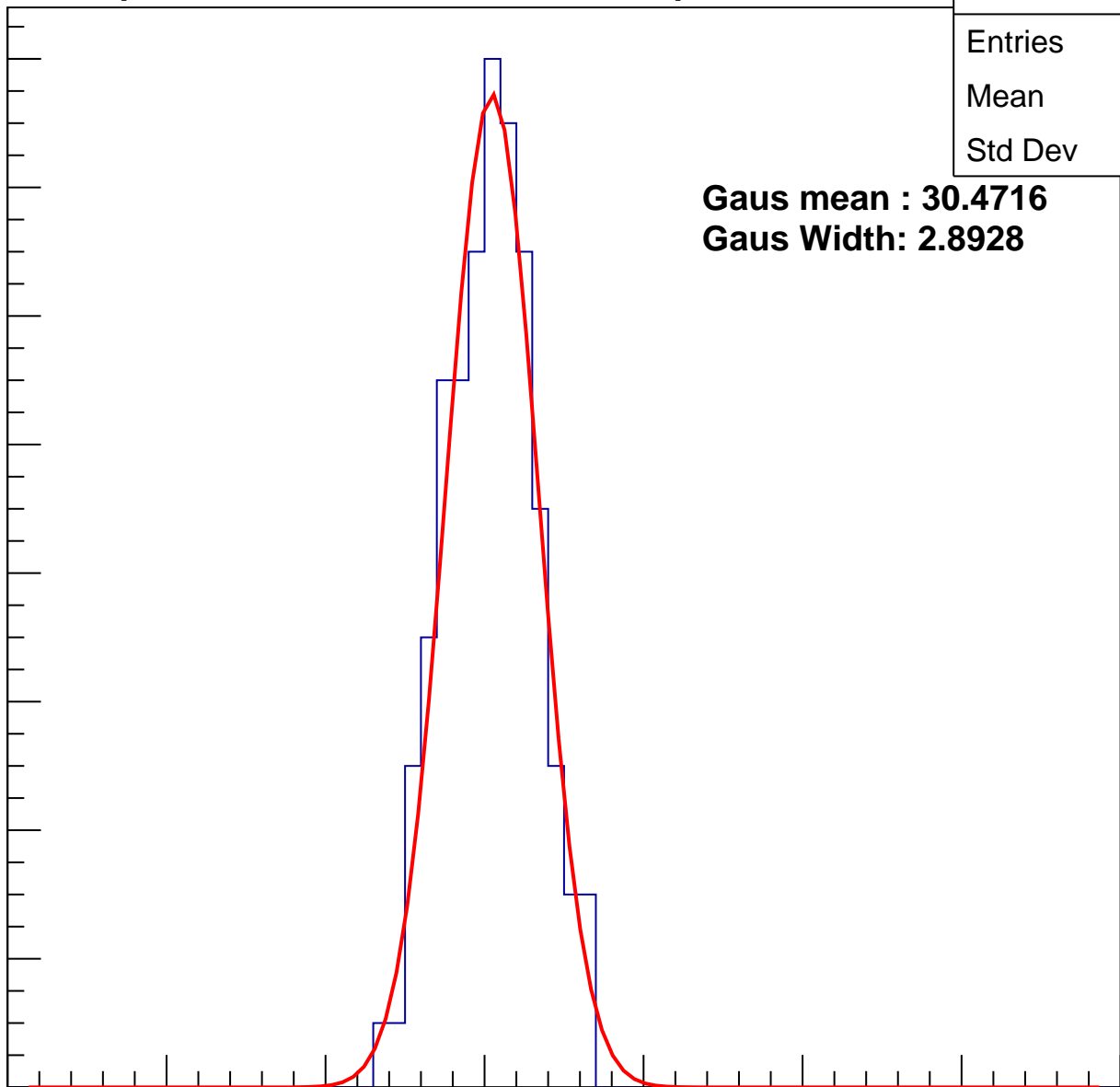
Gaus Width: 2.8928

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

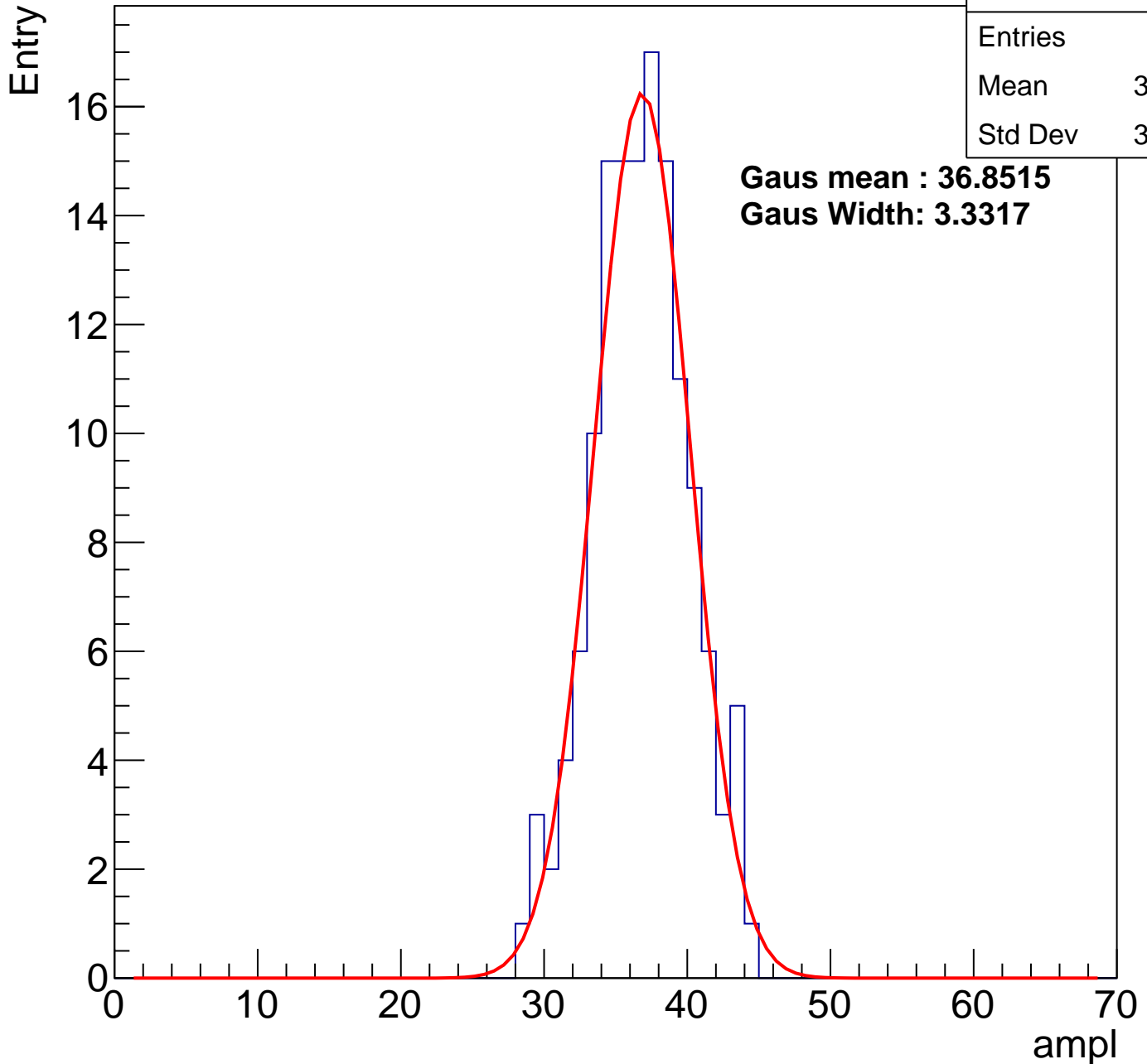


B1L001S, U20-ch68, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	36.34
Std Dev	3.313

Gaus mean : 36.8515
Gaus Width: 3.3317

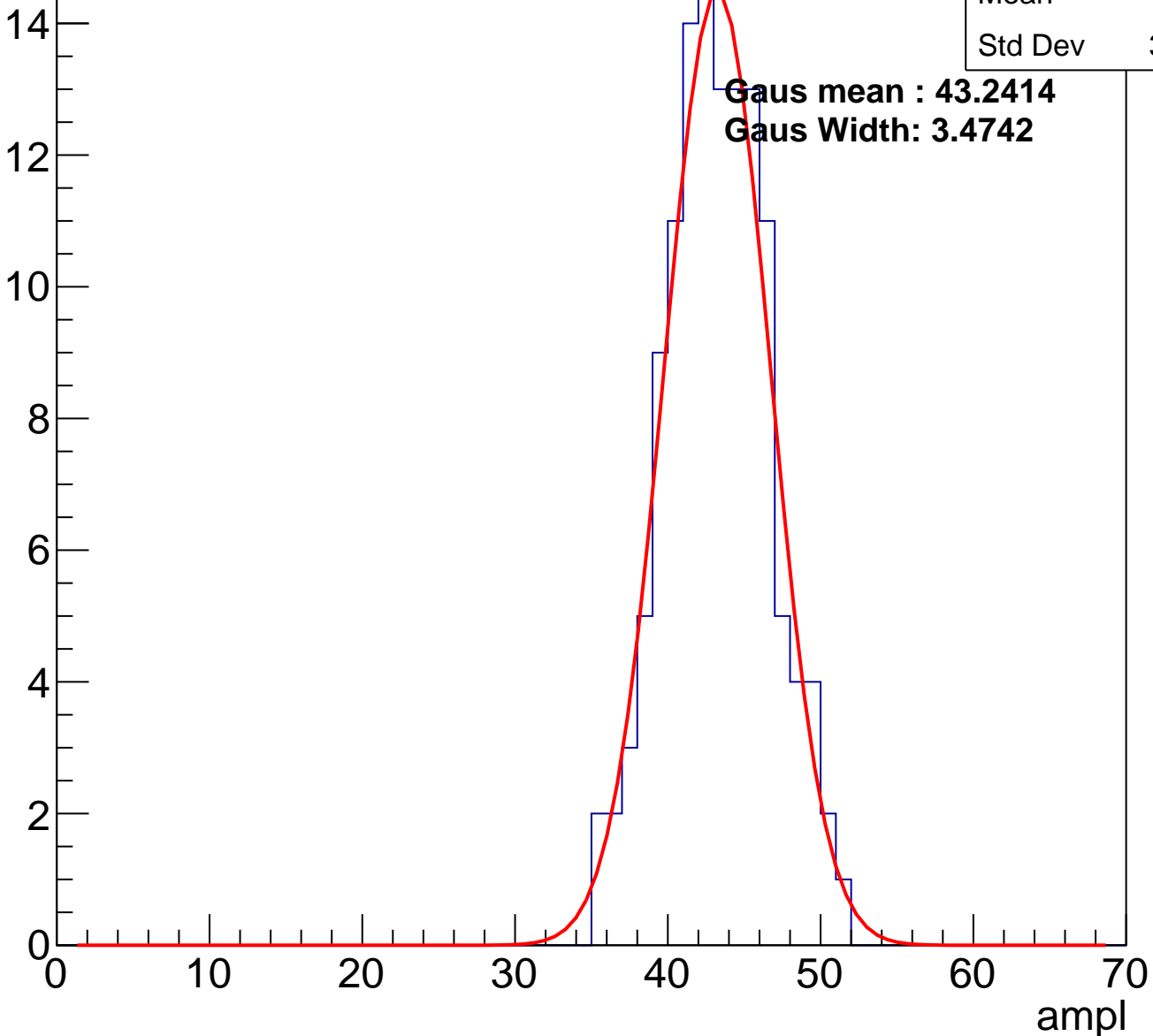


B1L001S, U20-ch68, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	42.79
Std Dev	3.341

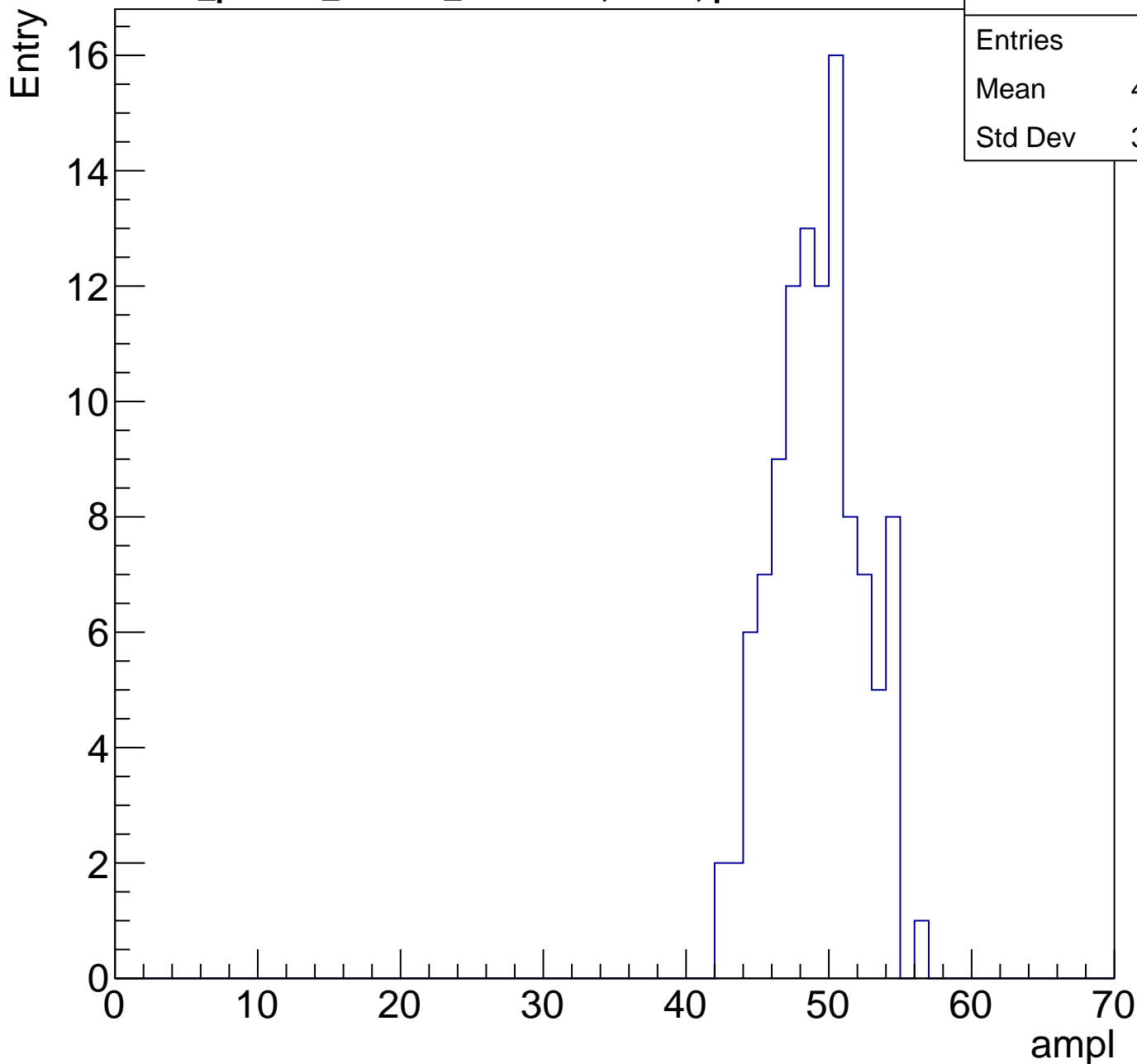
Entry



B1L001S, U20-ch68, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	48.74
Std Dev	3.053



B1L001S, U20-ch68, adc4

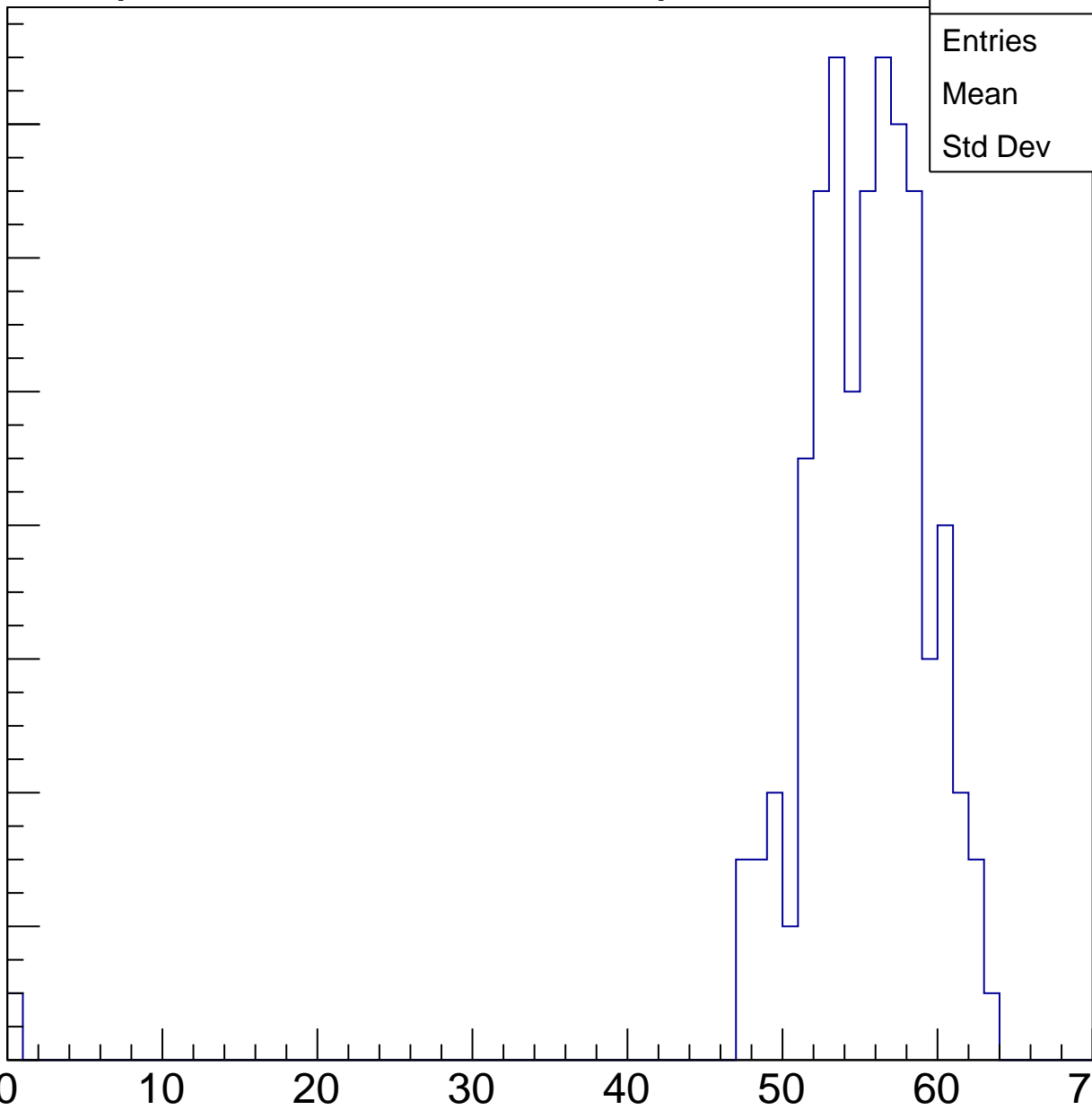
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	137
Mean	54.64
Std Dev	5.863

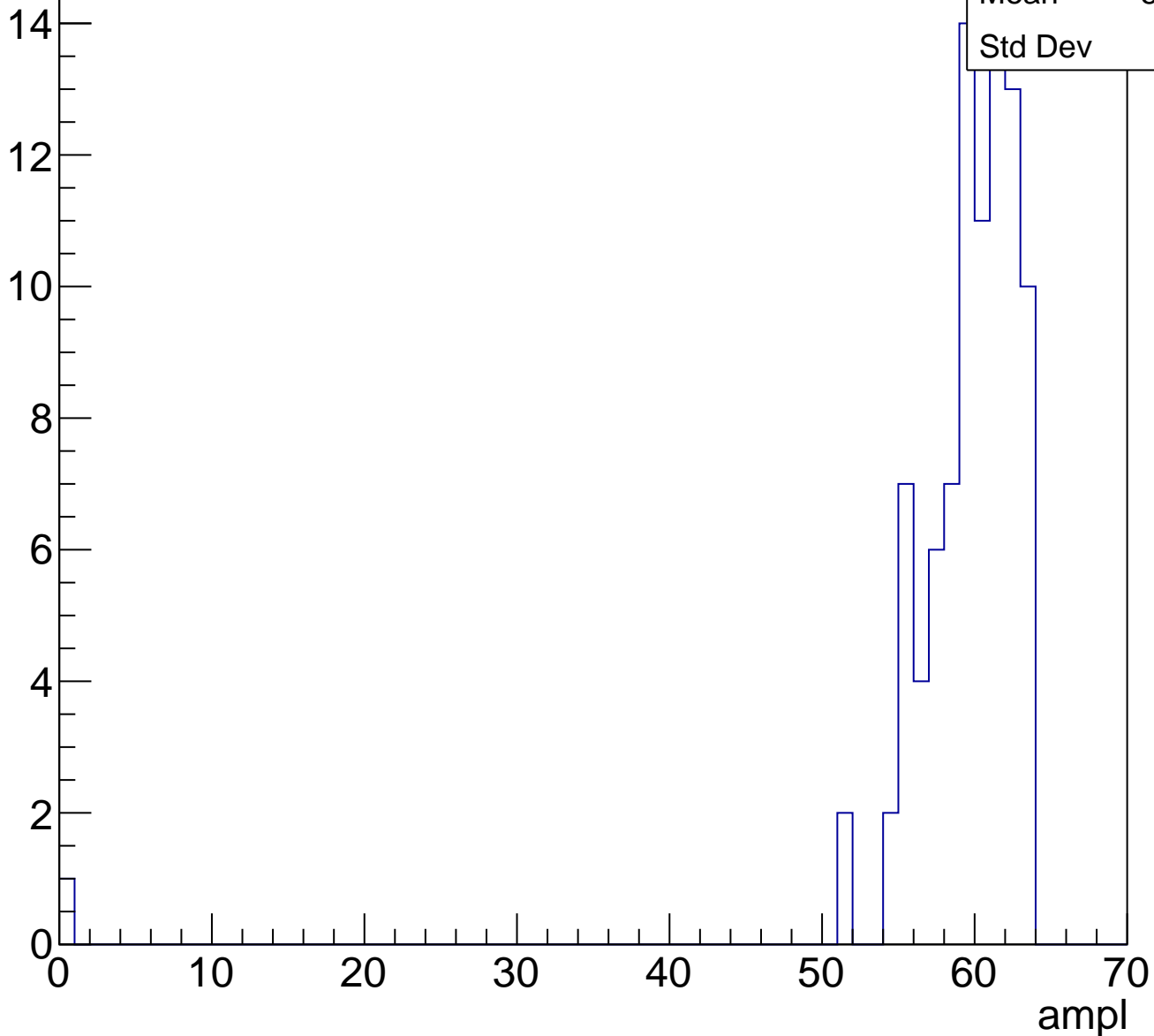
ampl



B1L001S, U20-ch68, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

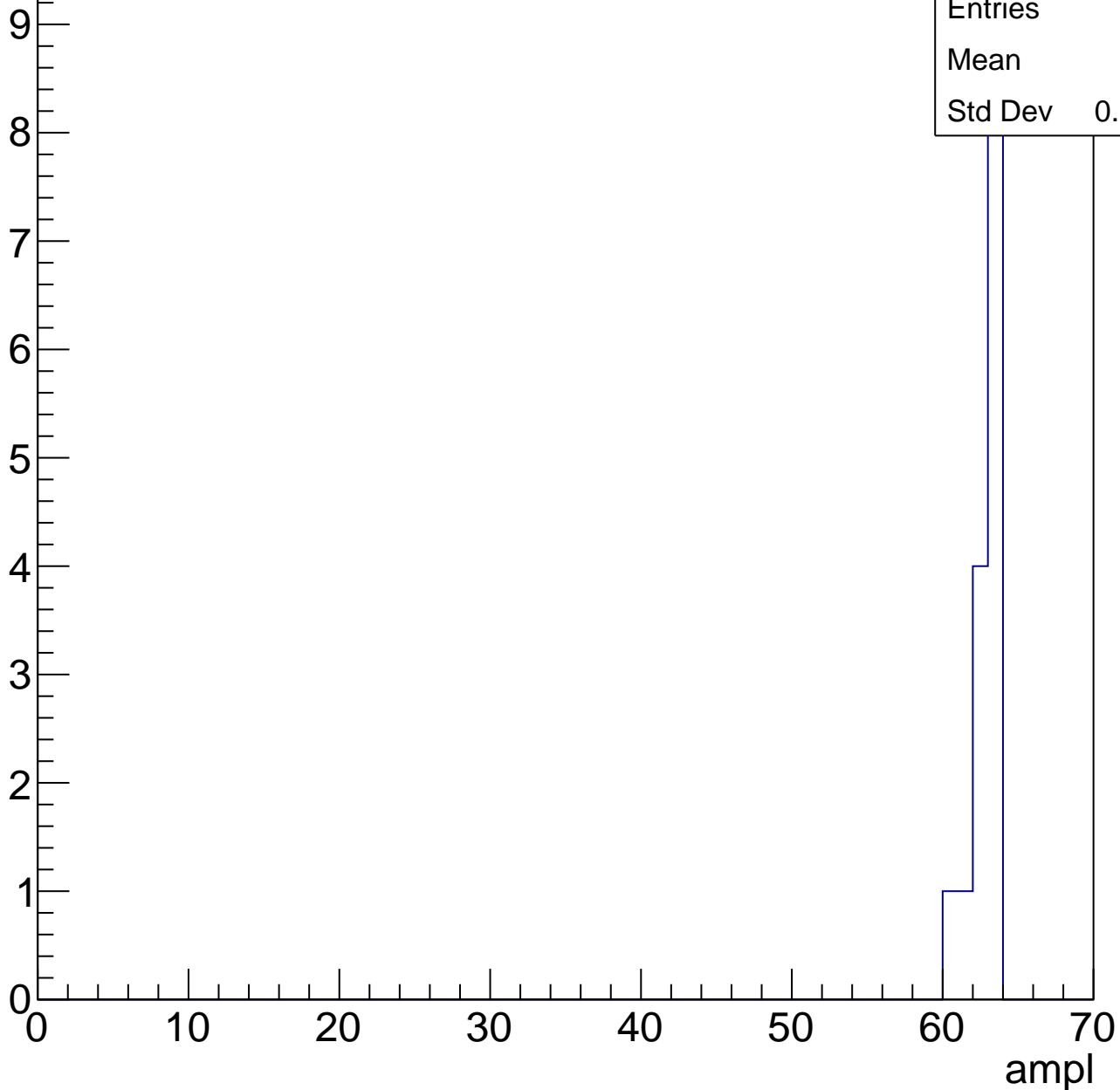


B1L001S, U20-ch68, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	62.4
Std Dev	0.8794



B1L001S, U20-ch68, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U20-ch69, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	30.65
Std Dev	3.11

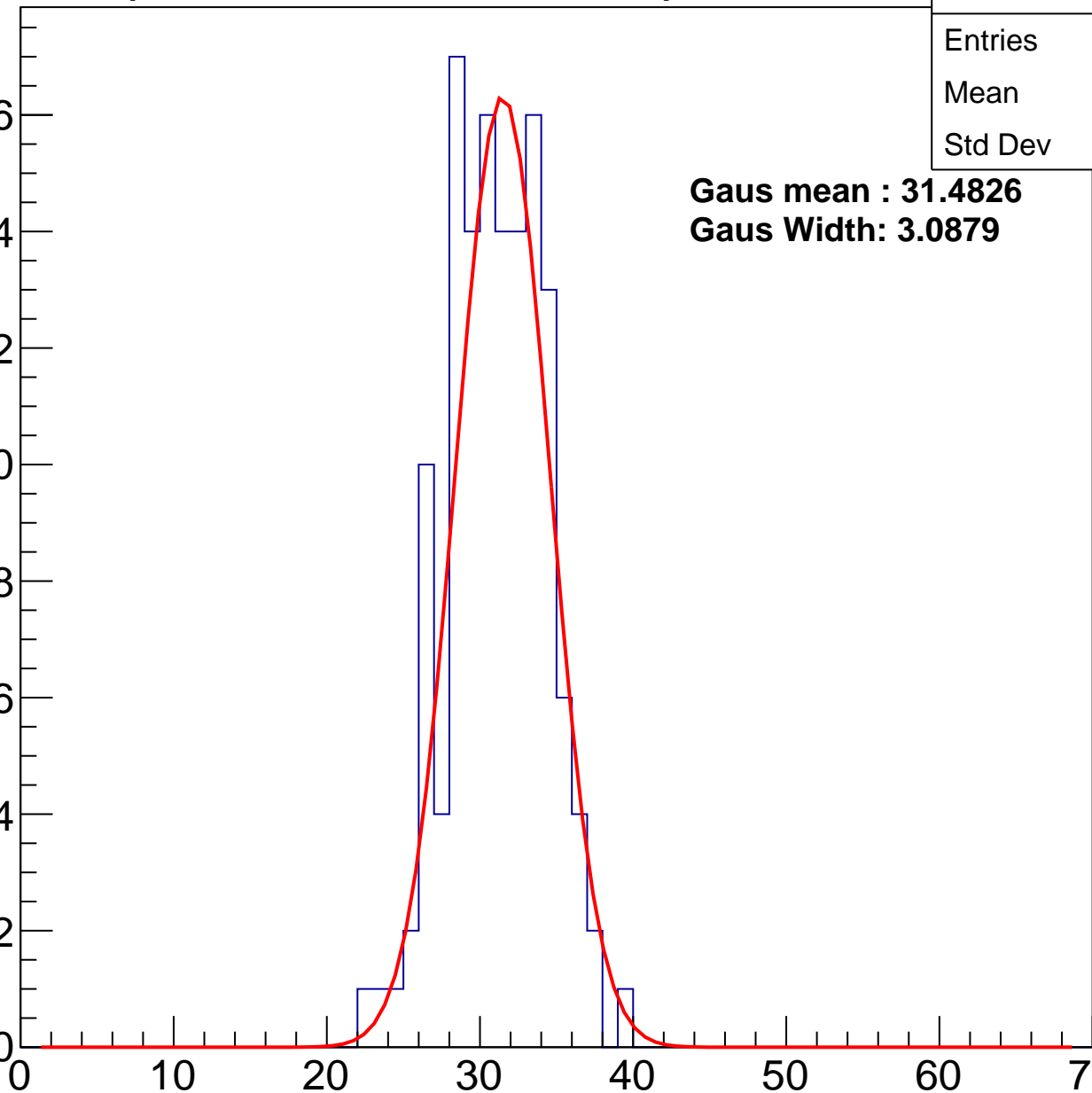
Gaus mean : 31.4826

Gaus Width: 3.0879

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch69, adc1

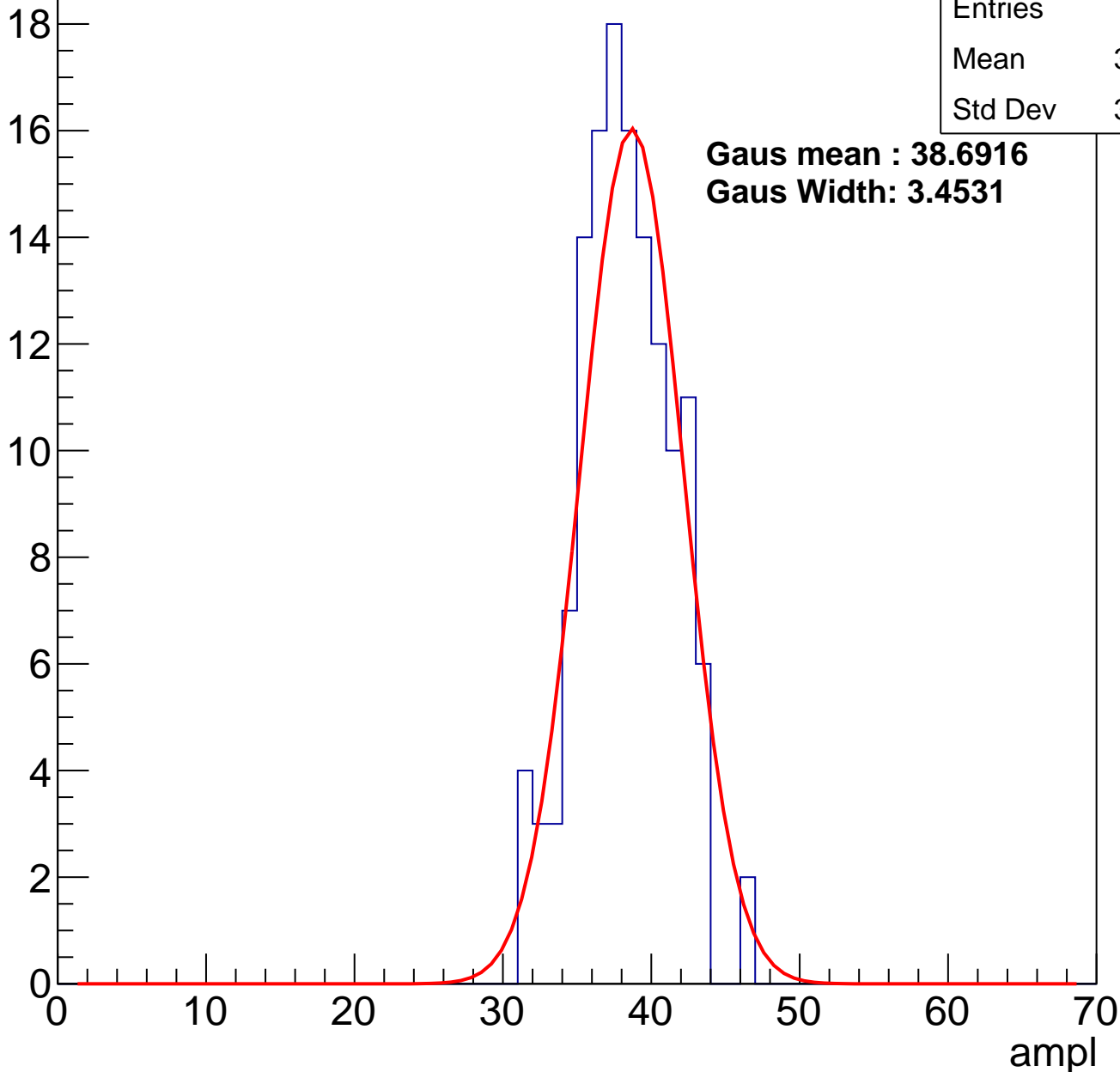
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	37.83
Std Dev	3.093

Gaus mean : 38.6916

Gaus Width: 3.4531

Entry



B1L001S, U20-ch69, adc2

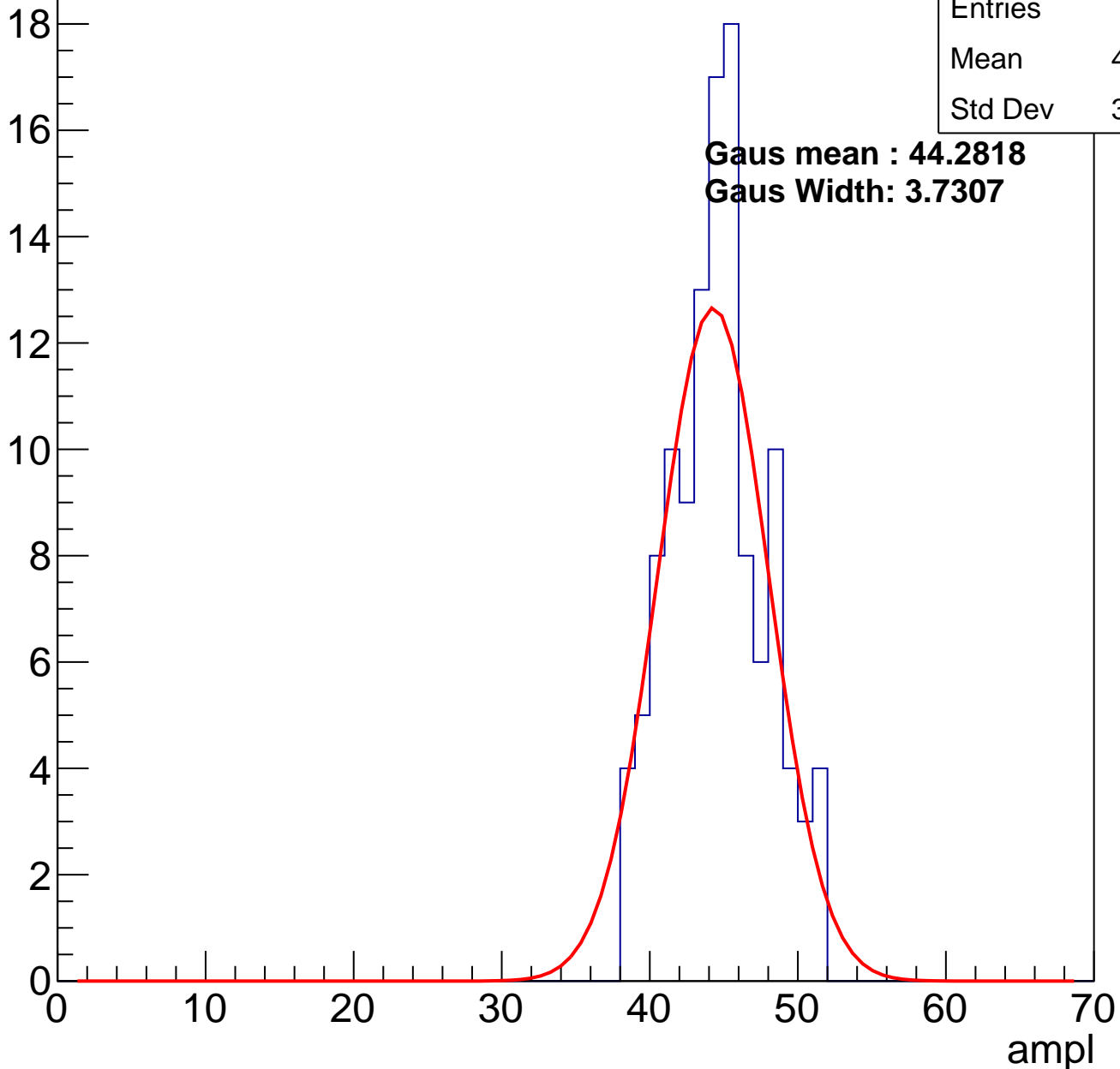
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	44.13
Std Dev	3.178

Gaus mean : 44.2818

Gaus Width: 3.7307

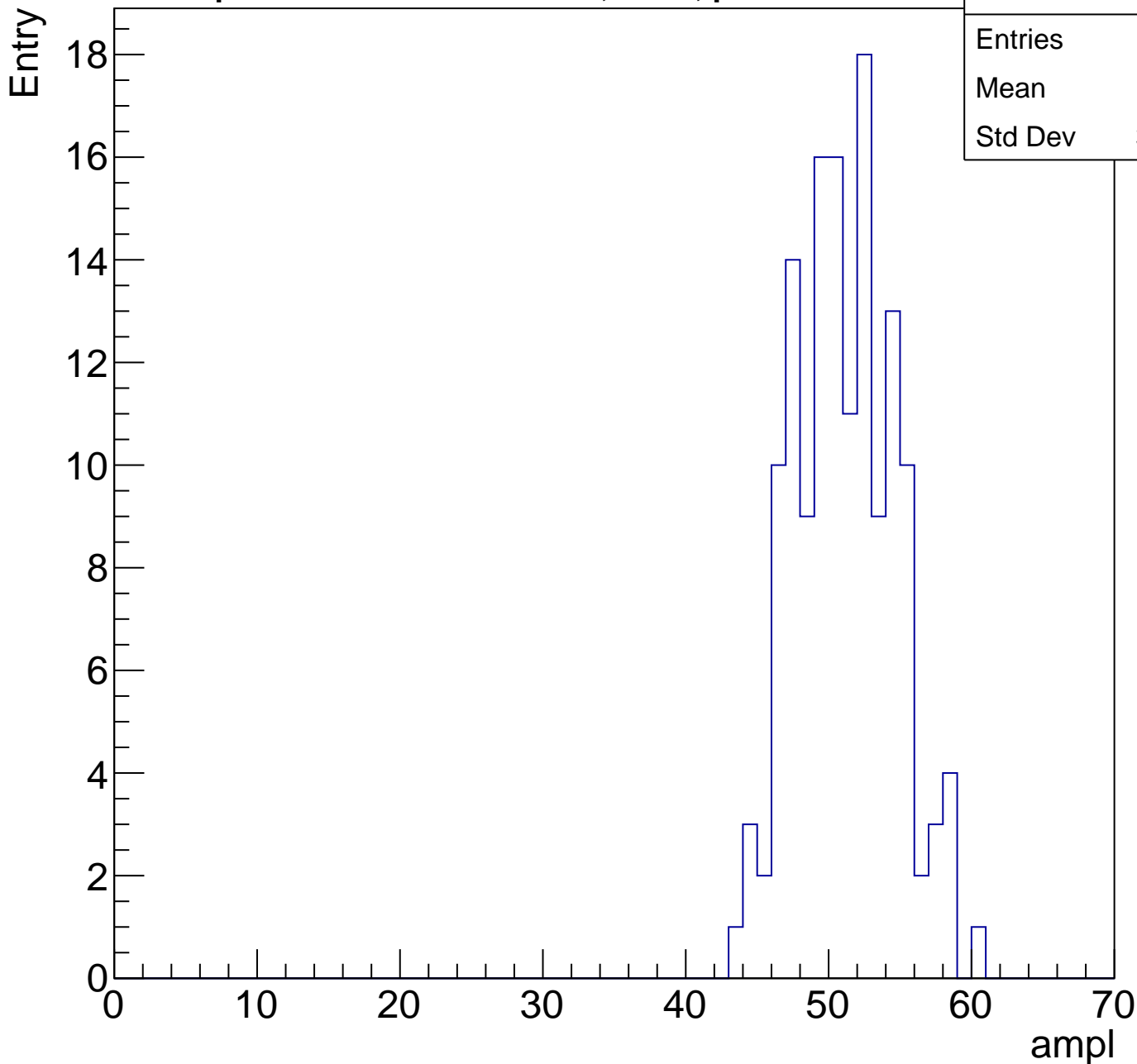
Entry



B1L001S, U20-ch69, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	50.7
Std Dev	3.431



B1L001S, U20-ch69, adc4

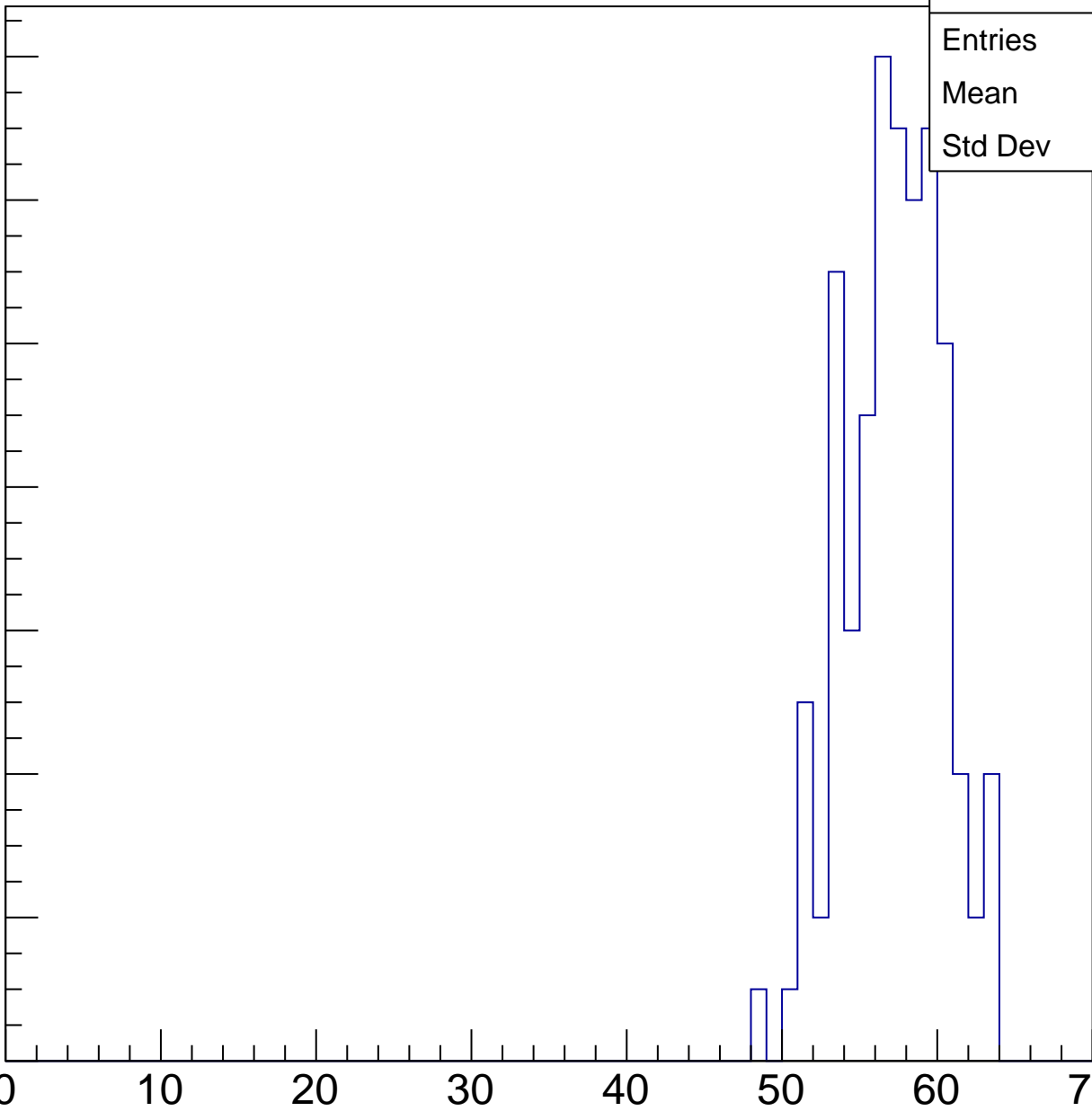
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	56.7
Std Dev	3.121

ampl



B1L001S, U20-ch69, adc5

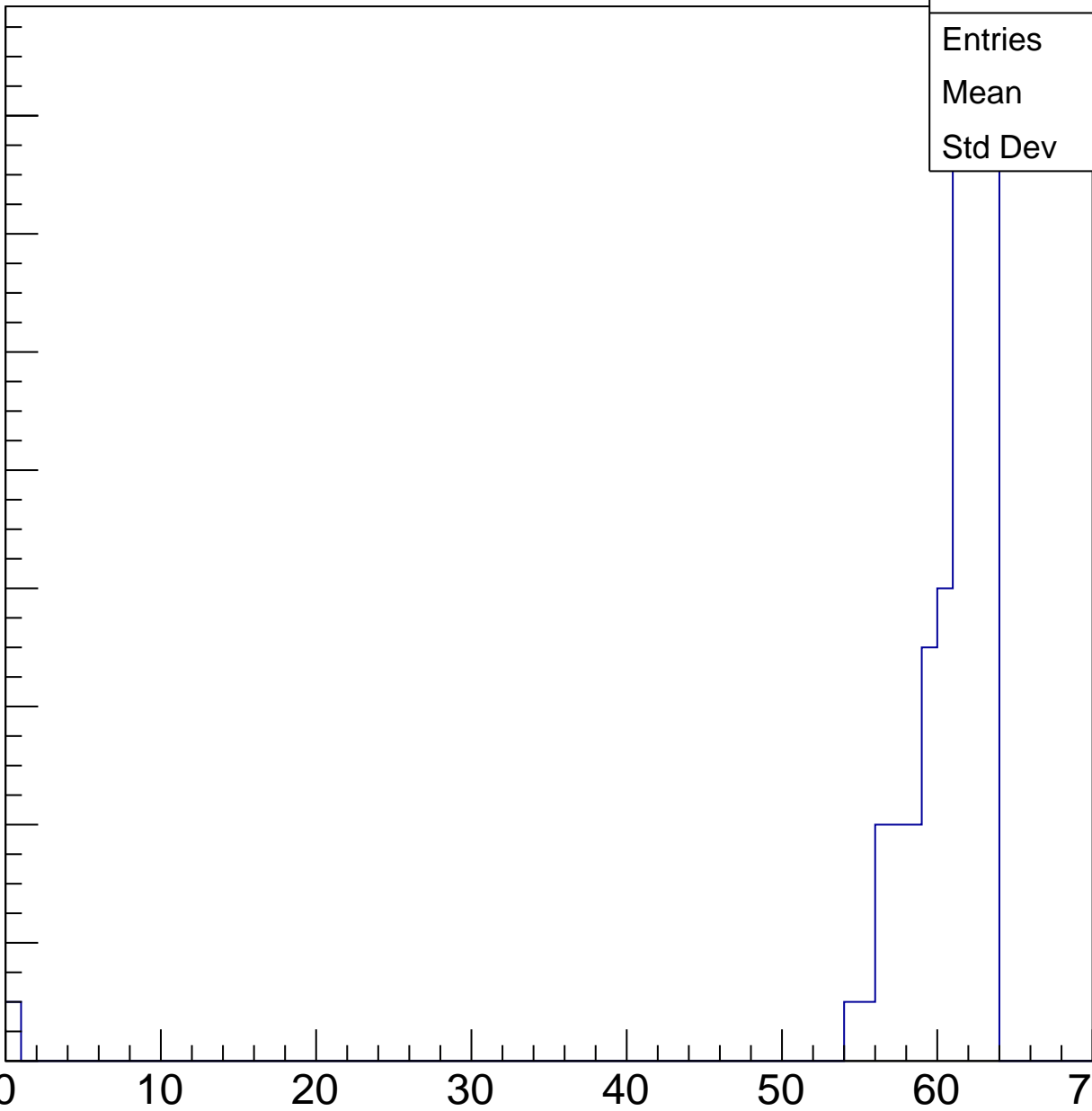
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	79
Mean	59.8
Std Dev	7.123

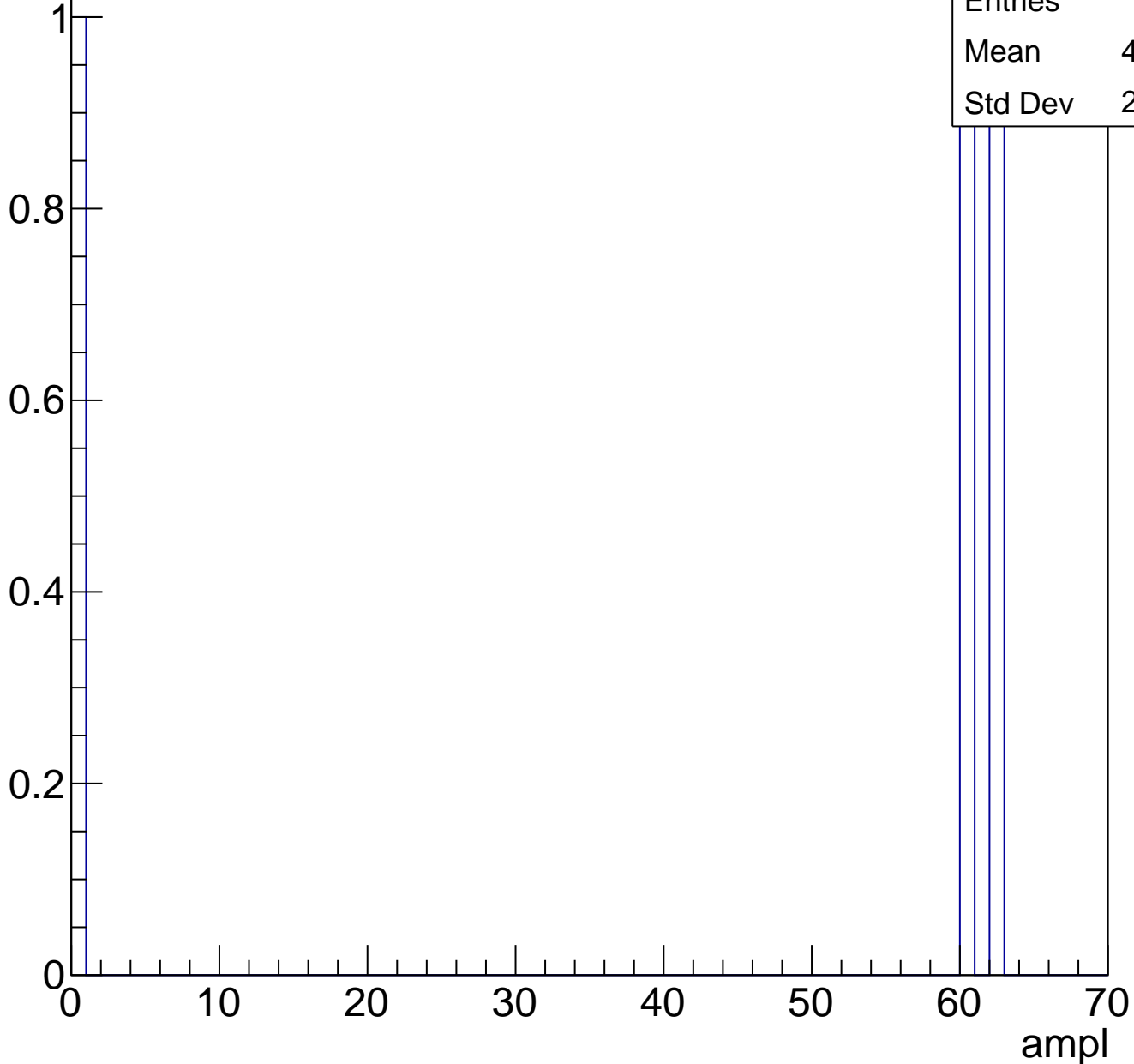
ampl



B1L001S, U20-ch69, adc6

calib_packv5_042523_0143.root, FC#2, port C2

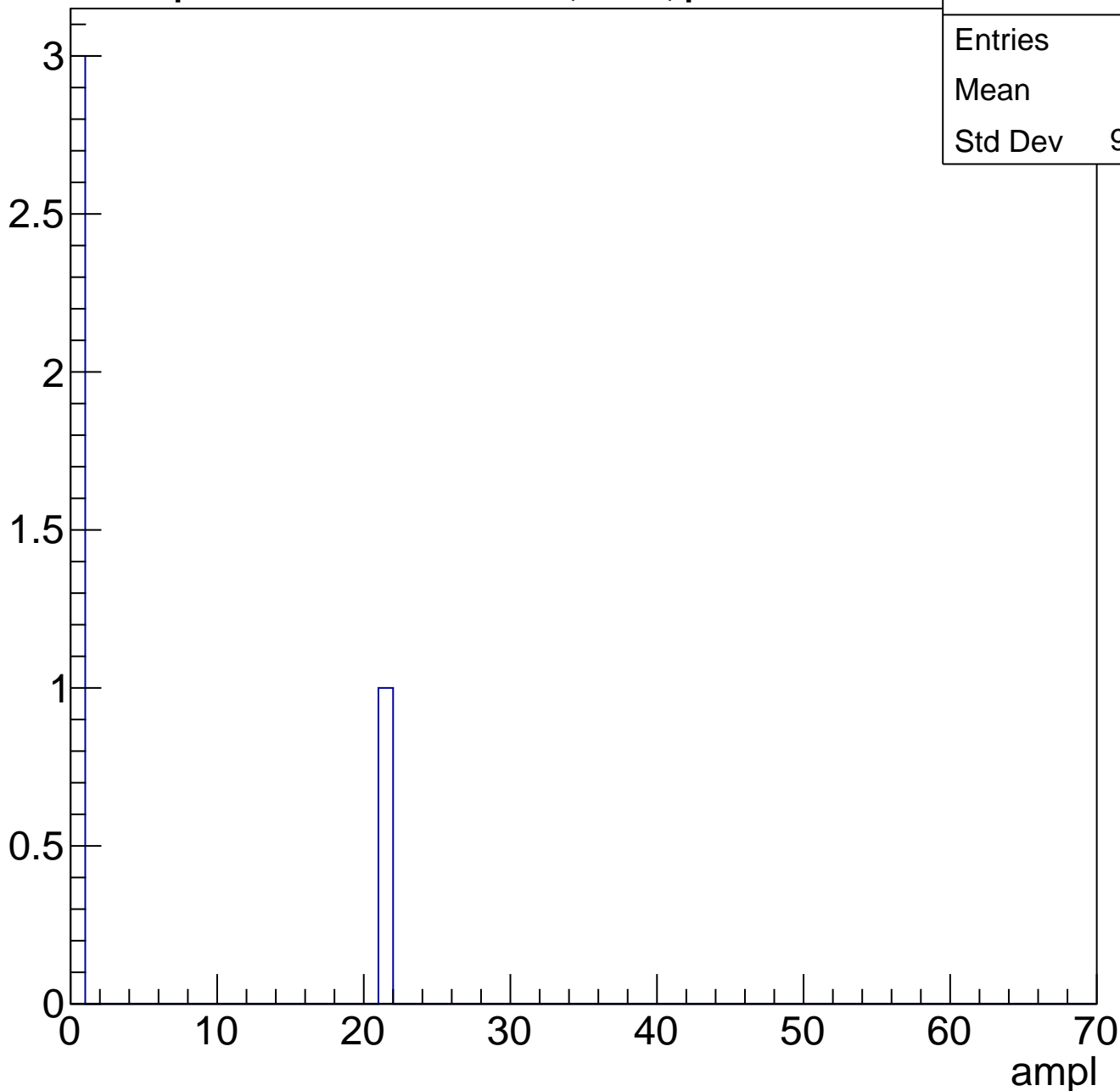
Entry



B1L001S, U20-ch69, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	5.25
Std Dev	9.093

B1L001S, U20-ch70, adc0

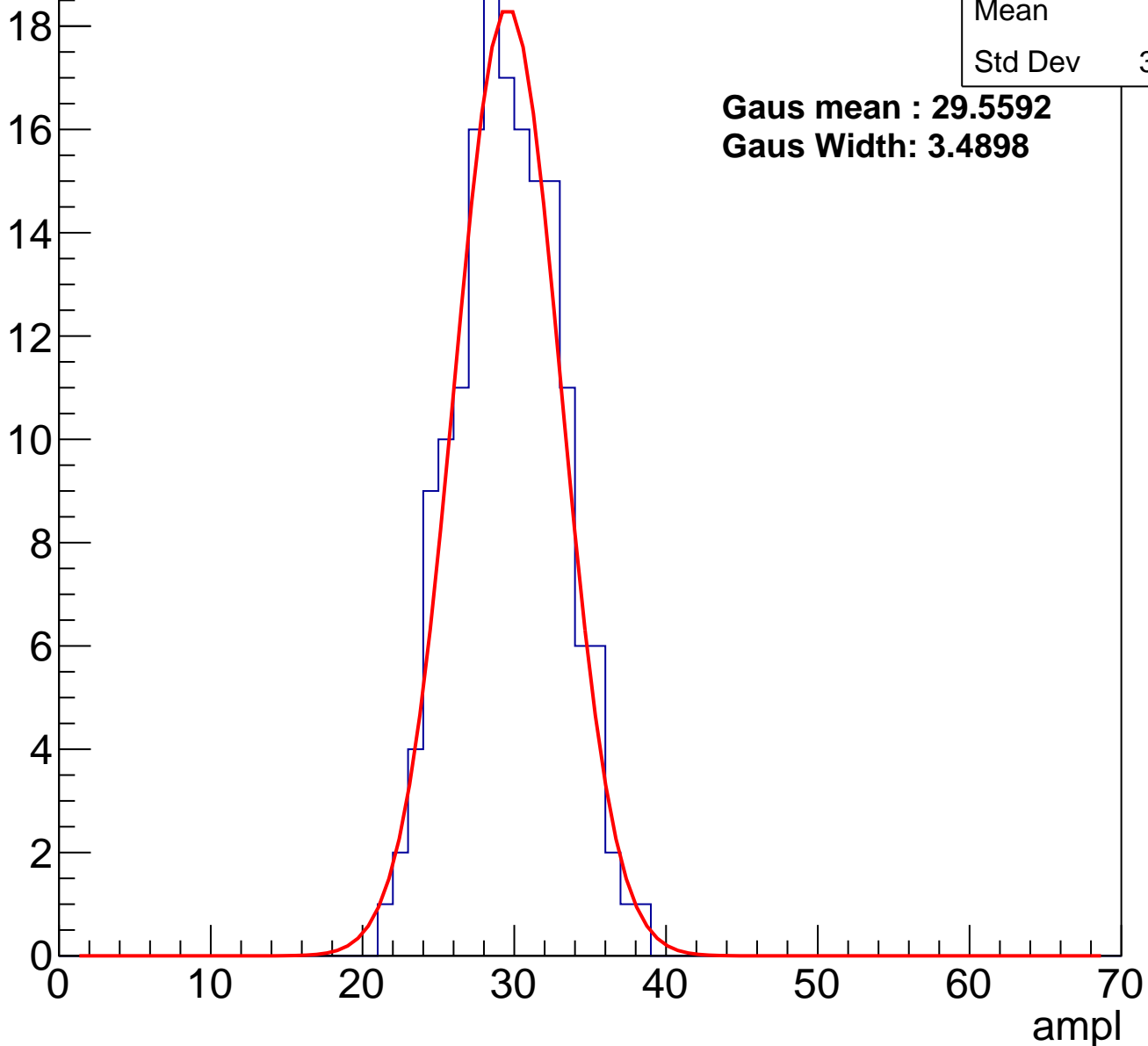
calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	29.1
Std Dev	3.395

Gaus mean : 29.5592

Gaus Width: 3.4898

Entry

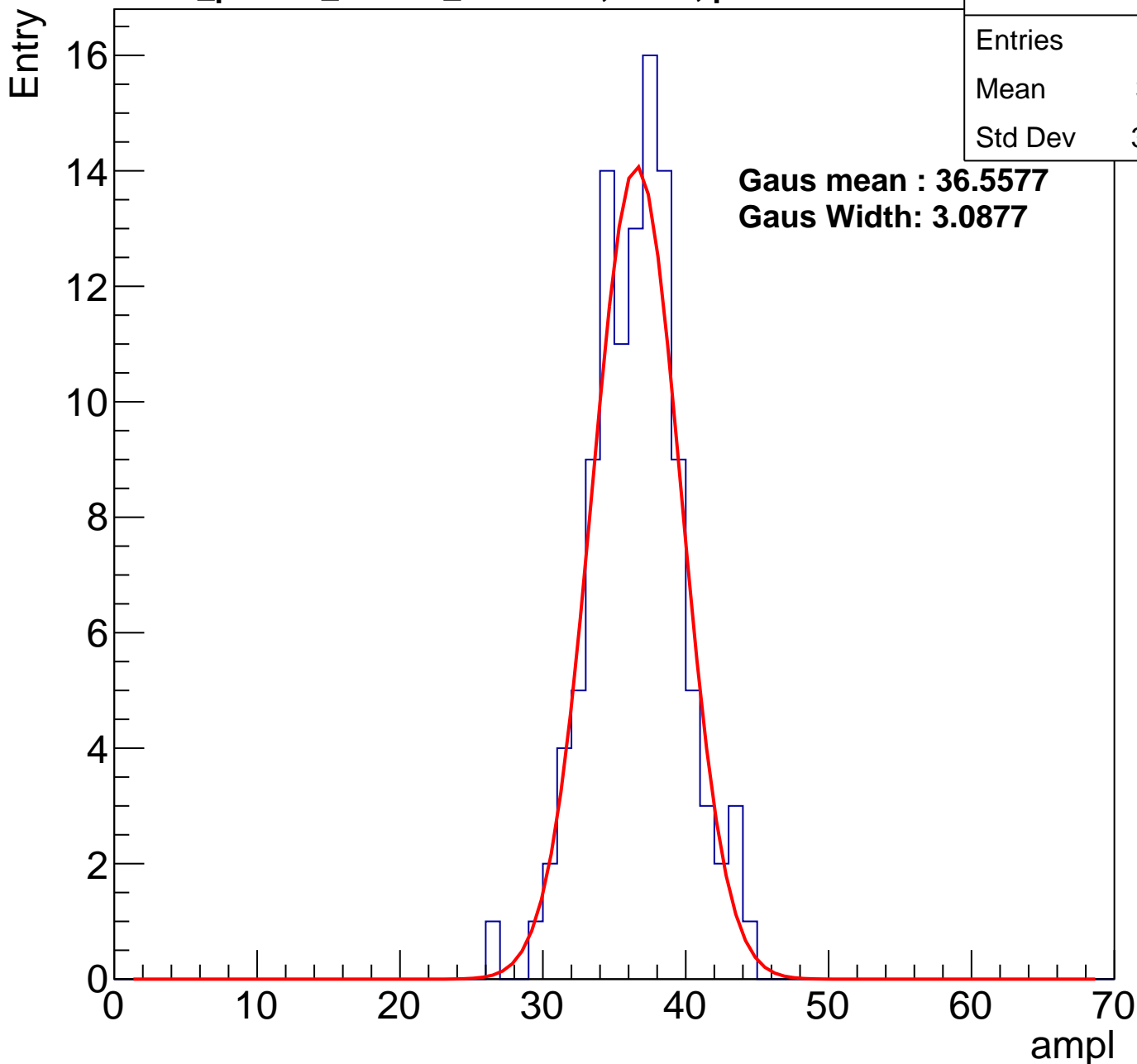


B1L001S, U20-ch70, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	36.11
Std Dev	3.174

Gaus mean : 36.5577
Gaus Width: 3.0877



B1L001S, U20-ch70, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

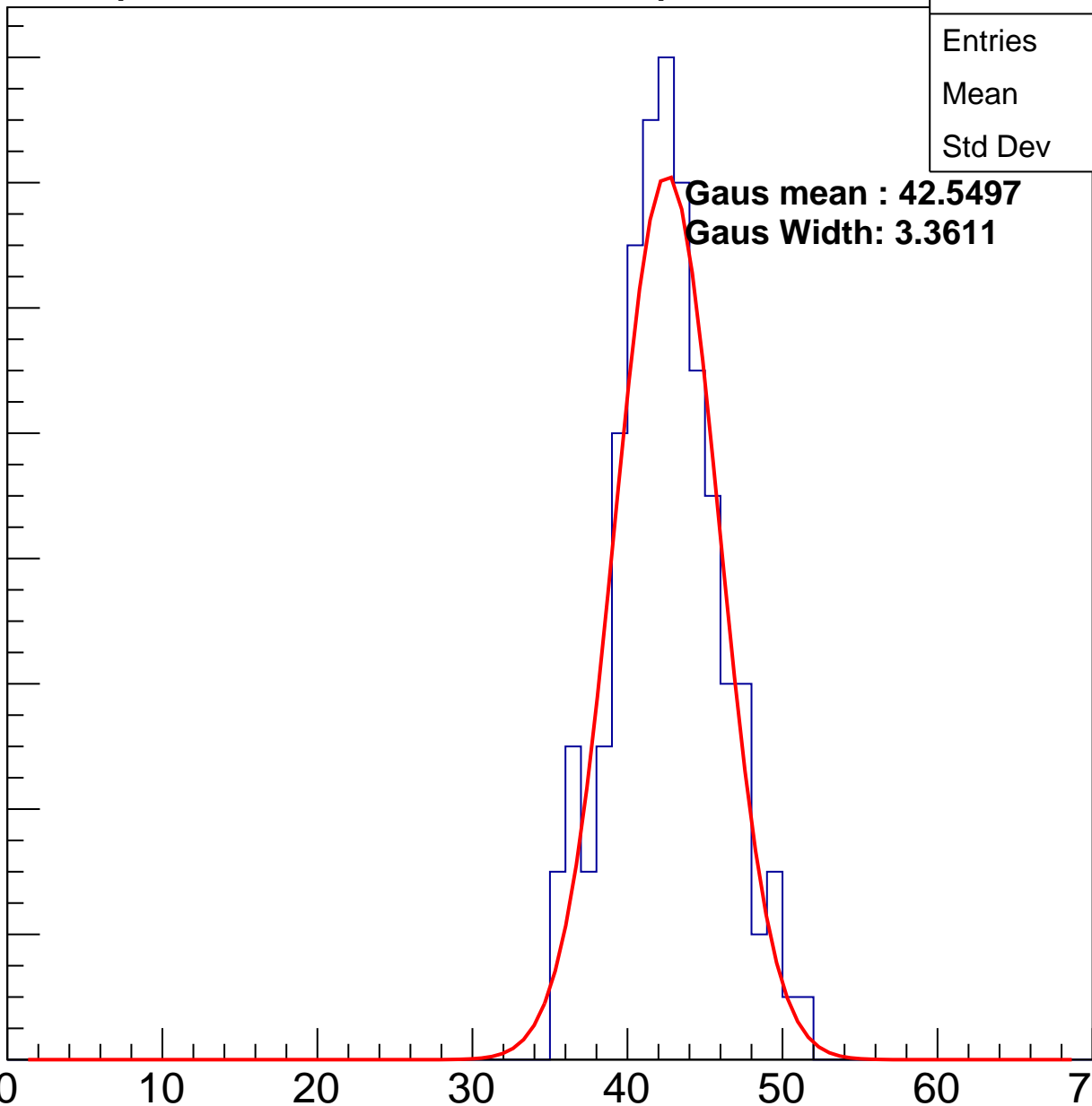
16
14
12
10
8
6
4
2
0

Entries	123
Mean	42.08
Std Dev	3.358

Gaus mean : 42.5497

Gaus Width: 3.3611

ampl



B1L001S, U20-ch70, adc3

calib_packv5_042523_0143.root, FC#2, port C2

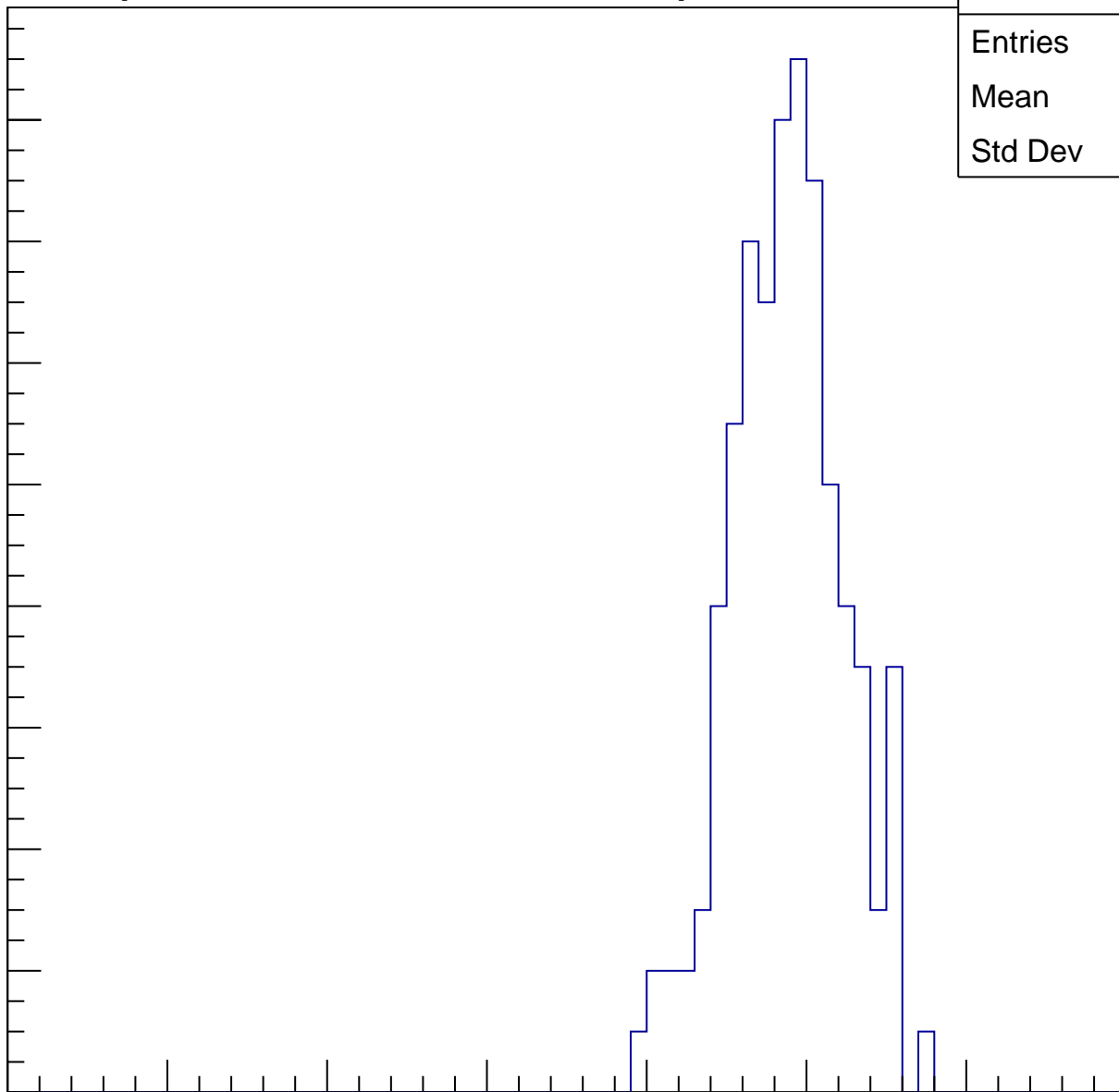
Entries	140
Mean	48.34
Std Dev	3.521

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

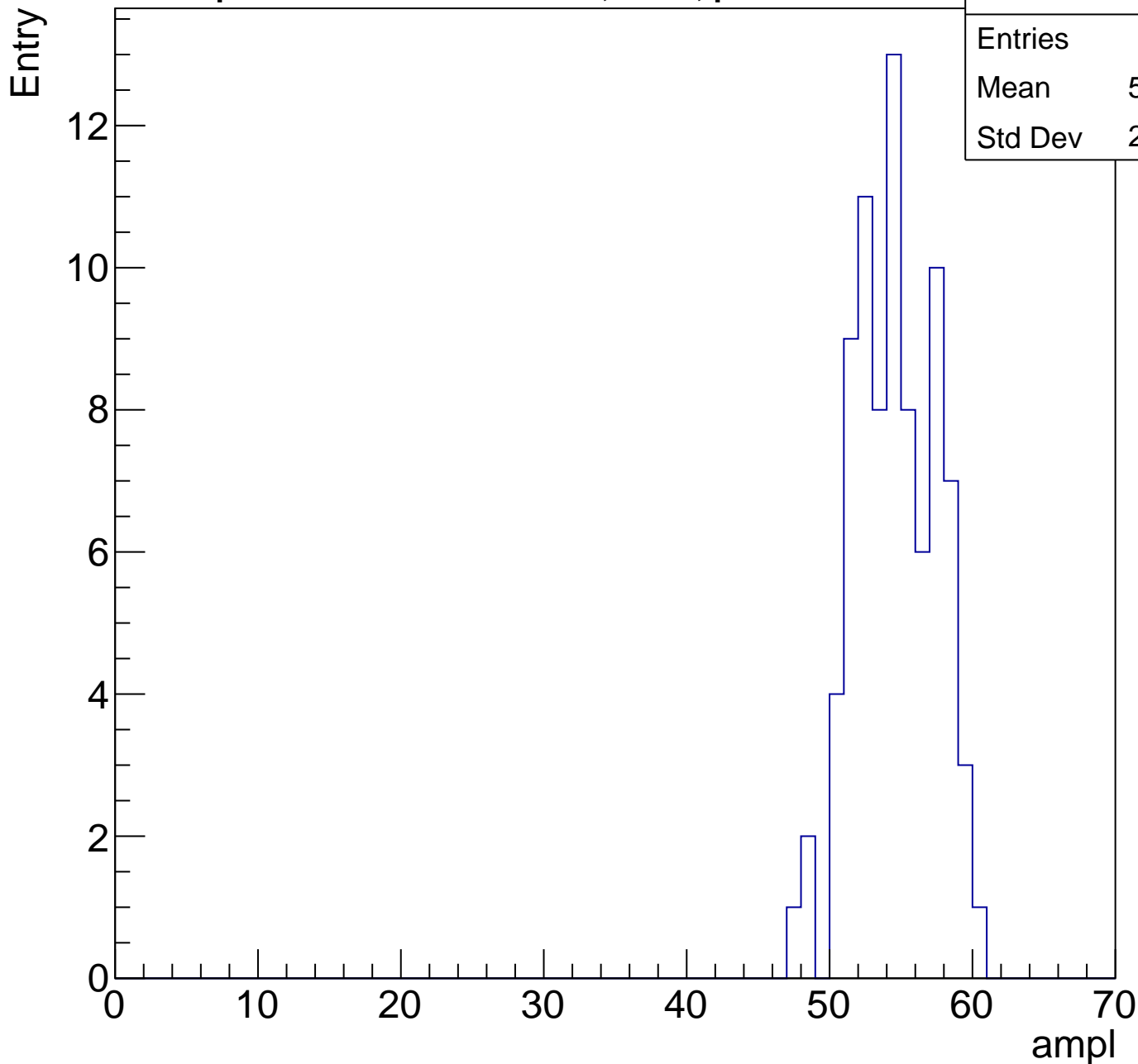
ampl



B1L001S, U20-ch70, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	83
Mean	54.08
Std Dev	2.825



B1L001S, U20-ch70, adc5

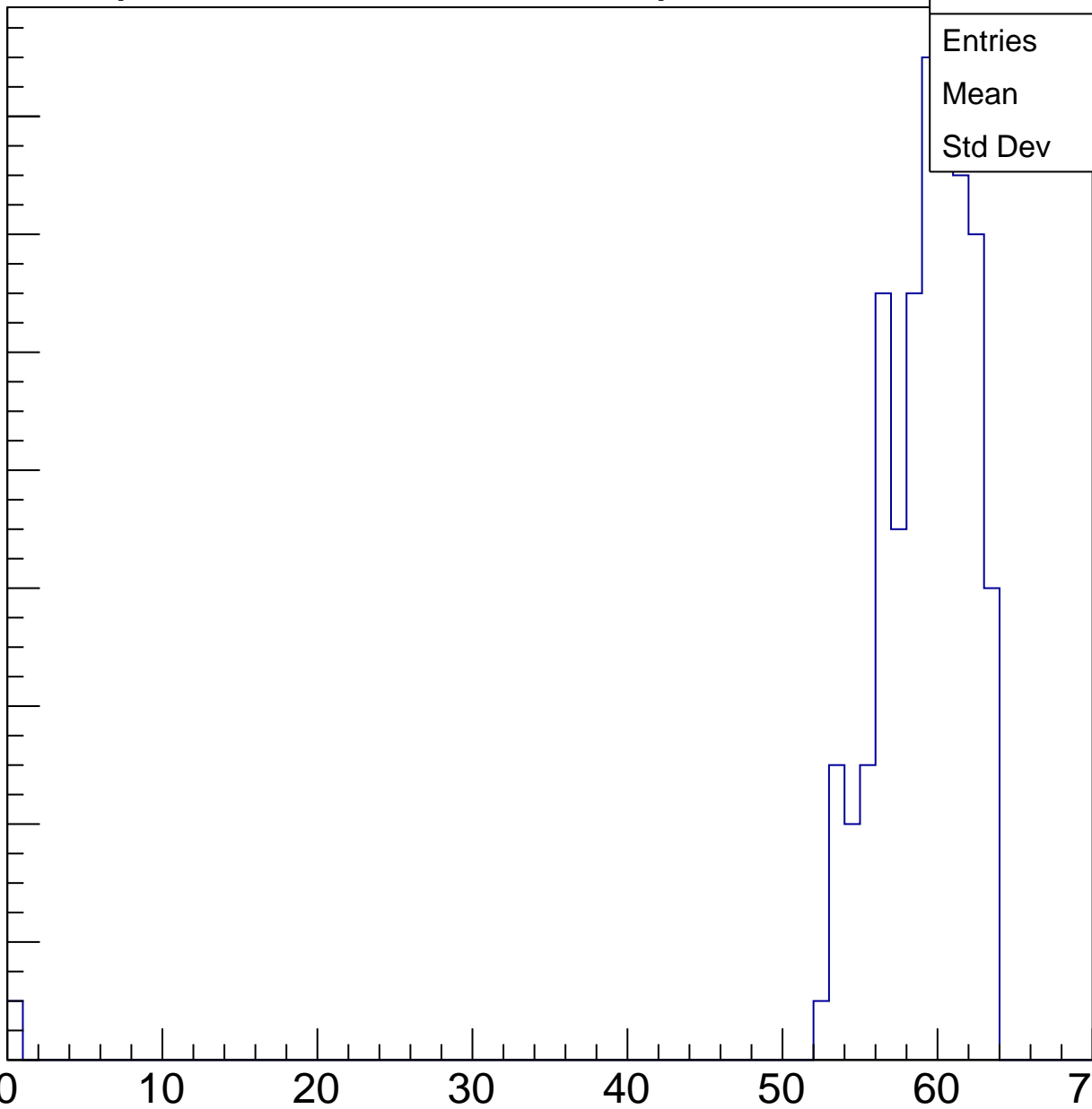
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	58.3
Std Dev	5.956

ampl



B1L001S, U20-ch70, adc6

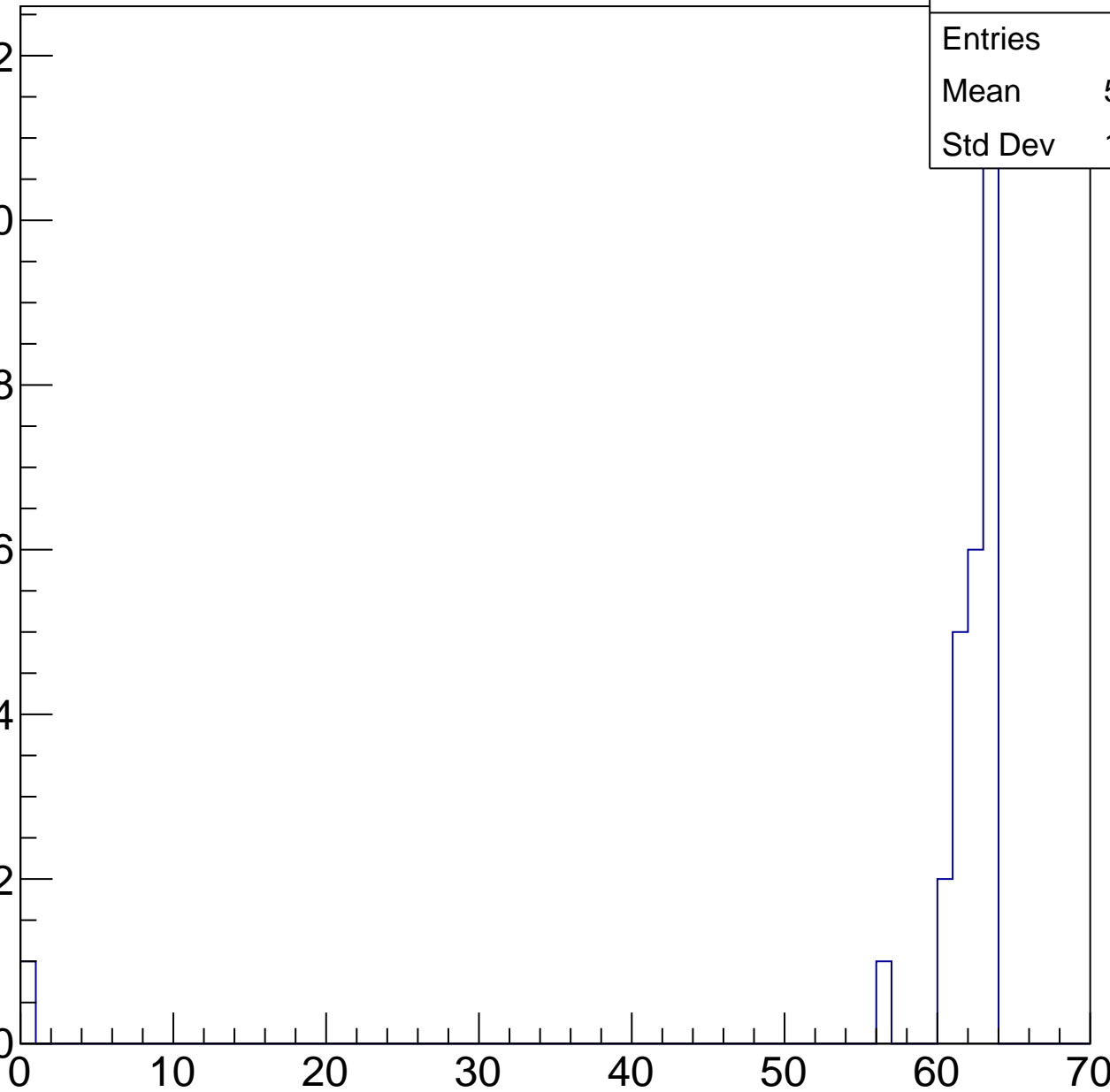
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	27
Mean	59.59
Std Dev	11.78

ampl



B1L001S, U20-ch70, adc7

calib_packv5_042523_0143.root, FC#2, port C2

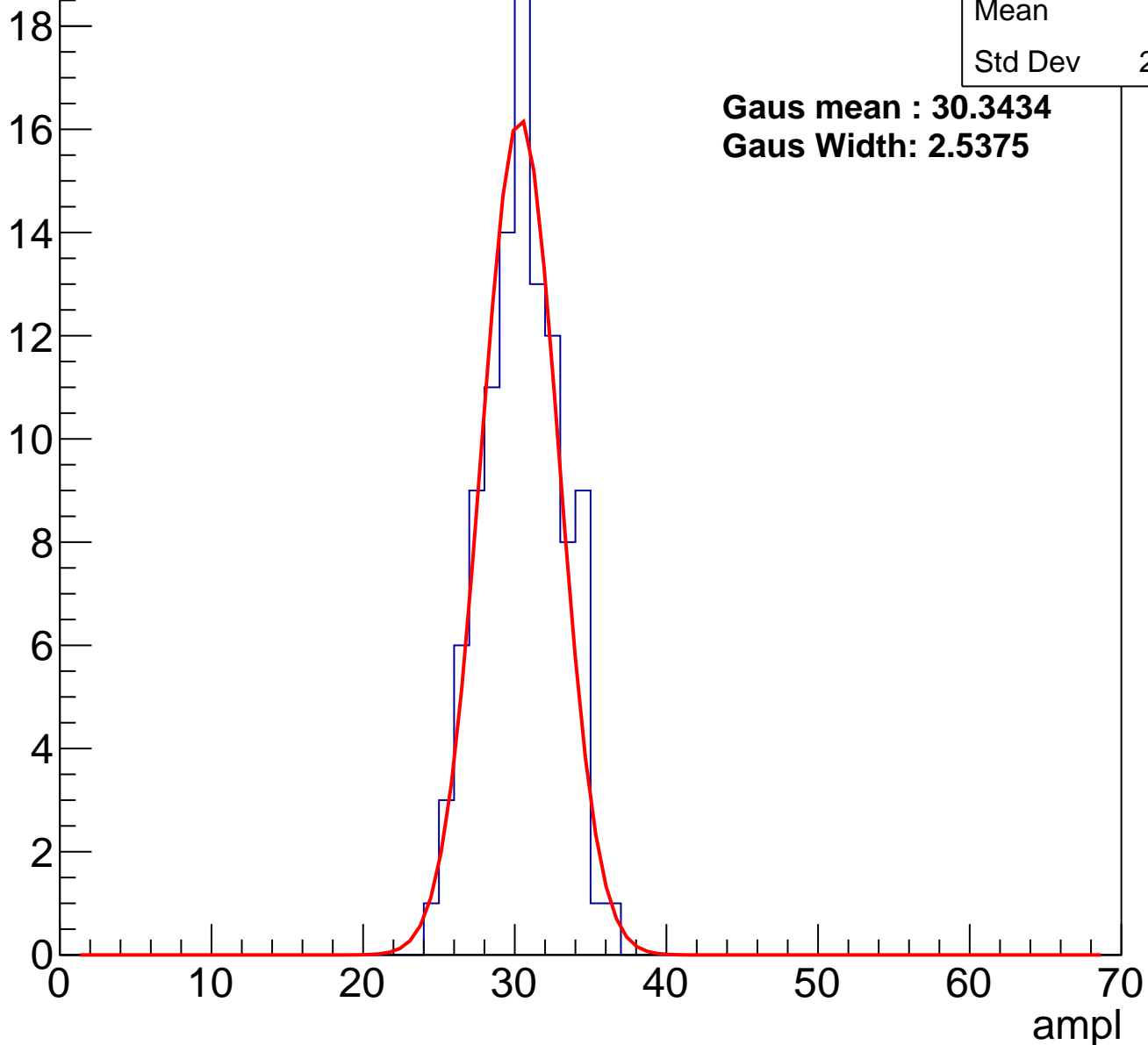
Entry



B1L001S, U20-ch71, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch71, adc1

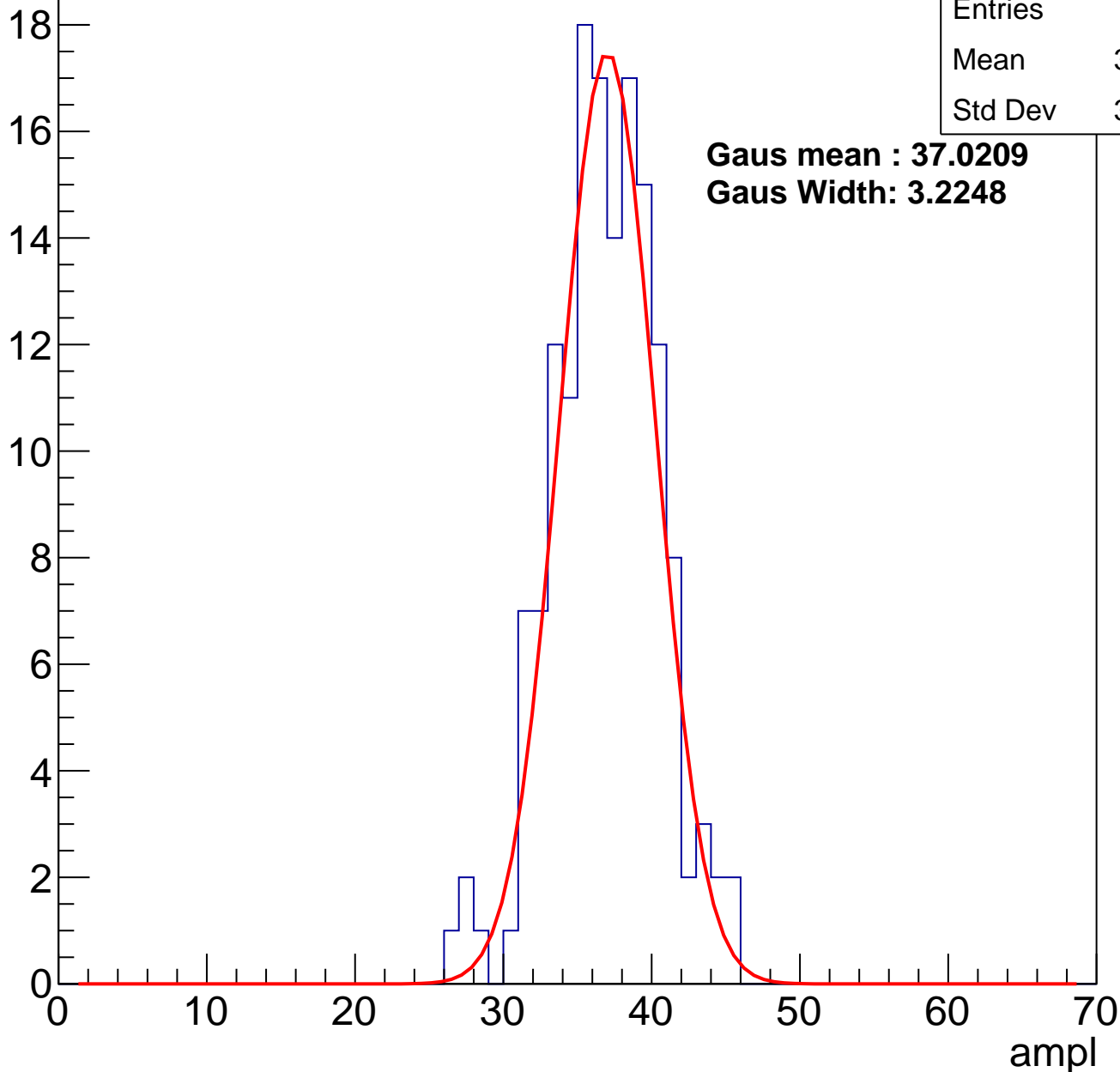
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	36.44
Std Dev	3.542

Gaus mean : 37.0209

Gaus Width: 3.2248

Entry



B1L001S, U20-ch71, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	43.82
Std Dev	3.544

Gaus mean : 44.5400

Gaus Width: 3.7600

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

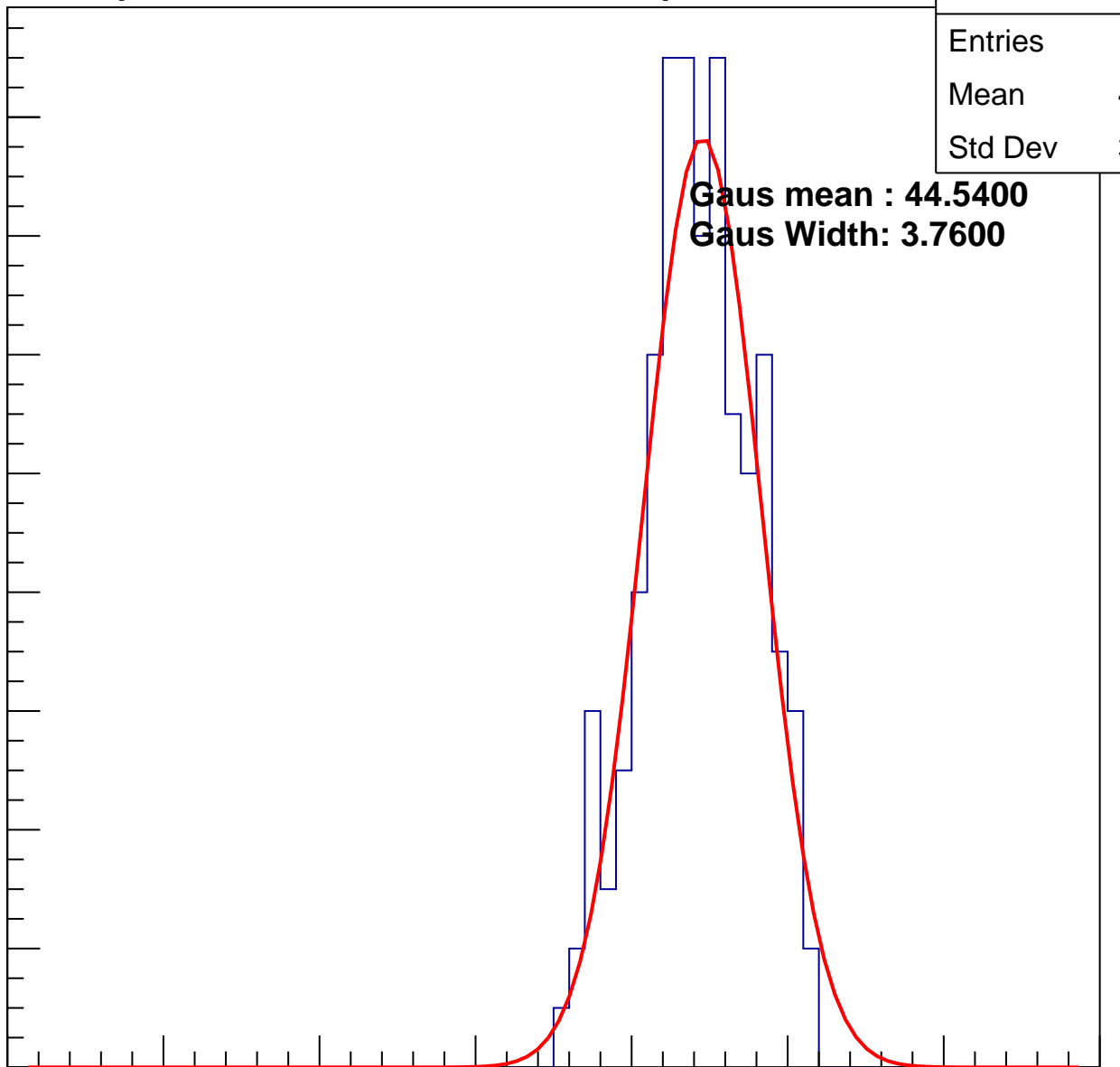
40

50

60

70

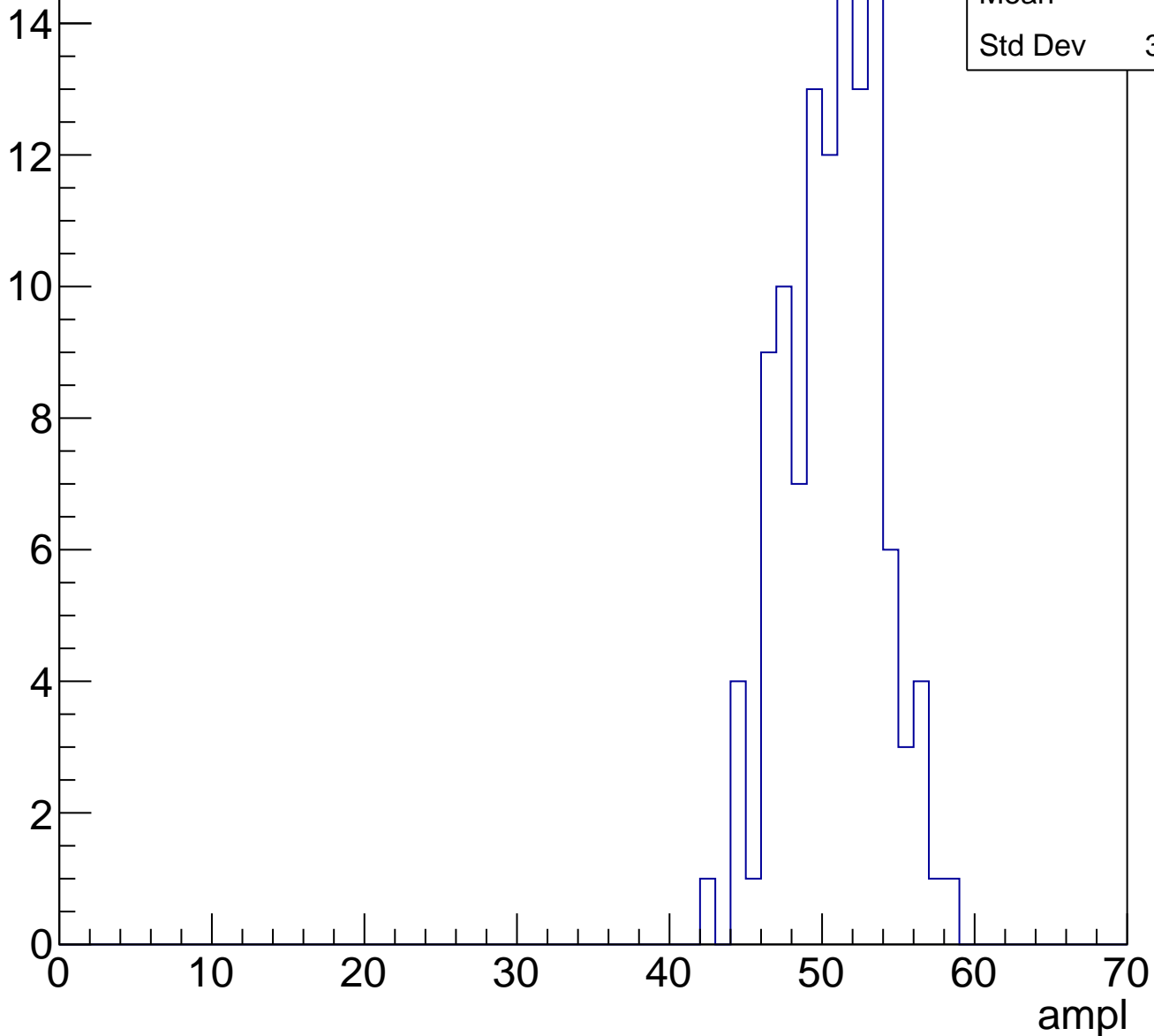
ampl



B1L001S, U20-ch71, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

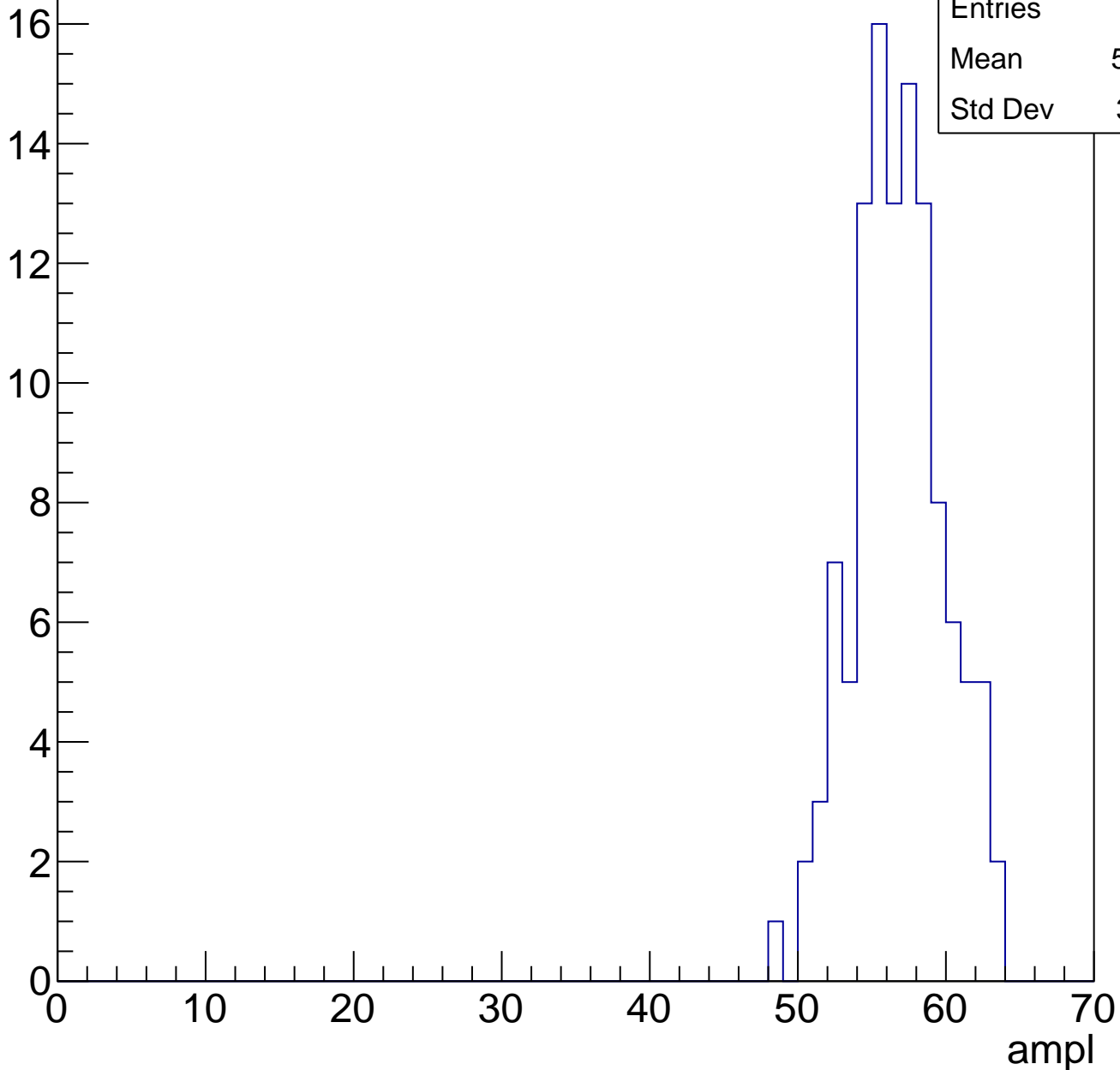


B1L001S, U20-ch71, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	56.33
Std Dev	3.051

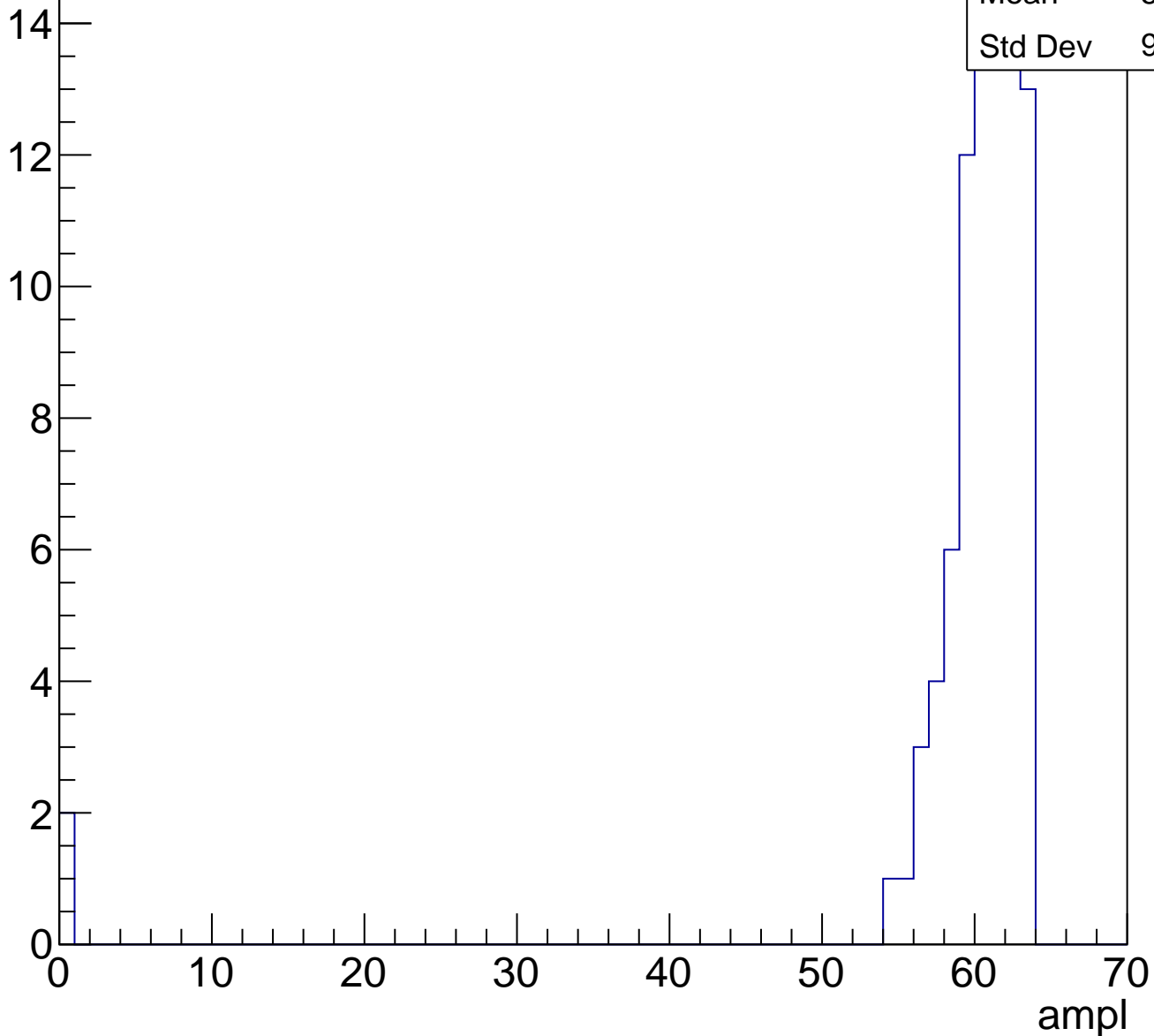
Entry



B1L001S, U20-ch71, adc5

calib_packv5_042523_0143.root, FC#2, port C2

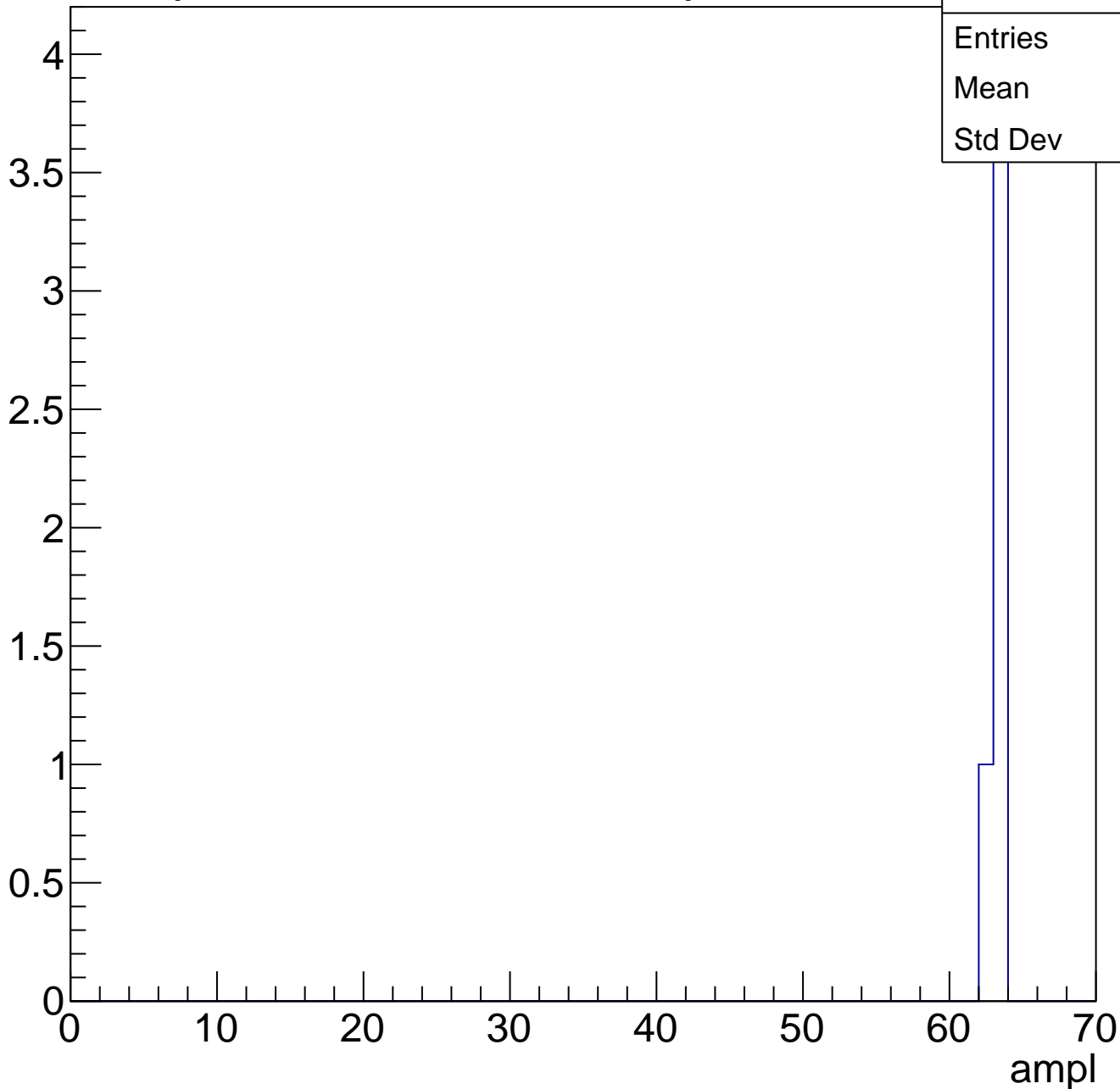
Entry



B1L001S, U20-ch71, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	62.8
Std Dev	0.4

B1L001S, U20-ch71, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	36.67
Std Dev	18.62

ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch72, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	29.3
Std Dev	4.091

Gaus mean : 29.7539

Gaus Width: 3.1472

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

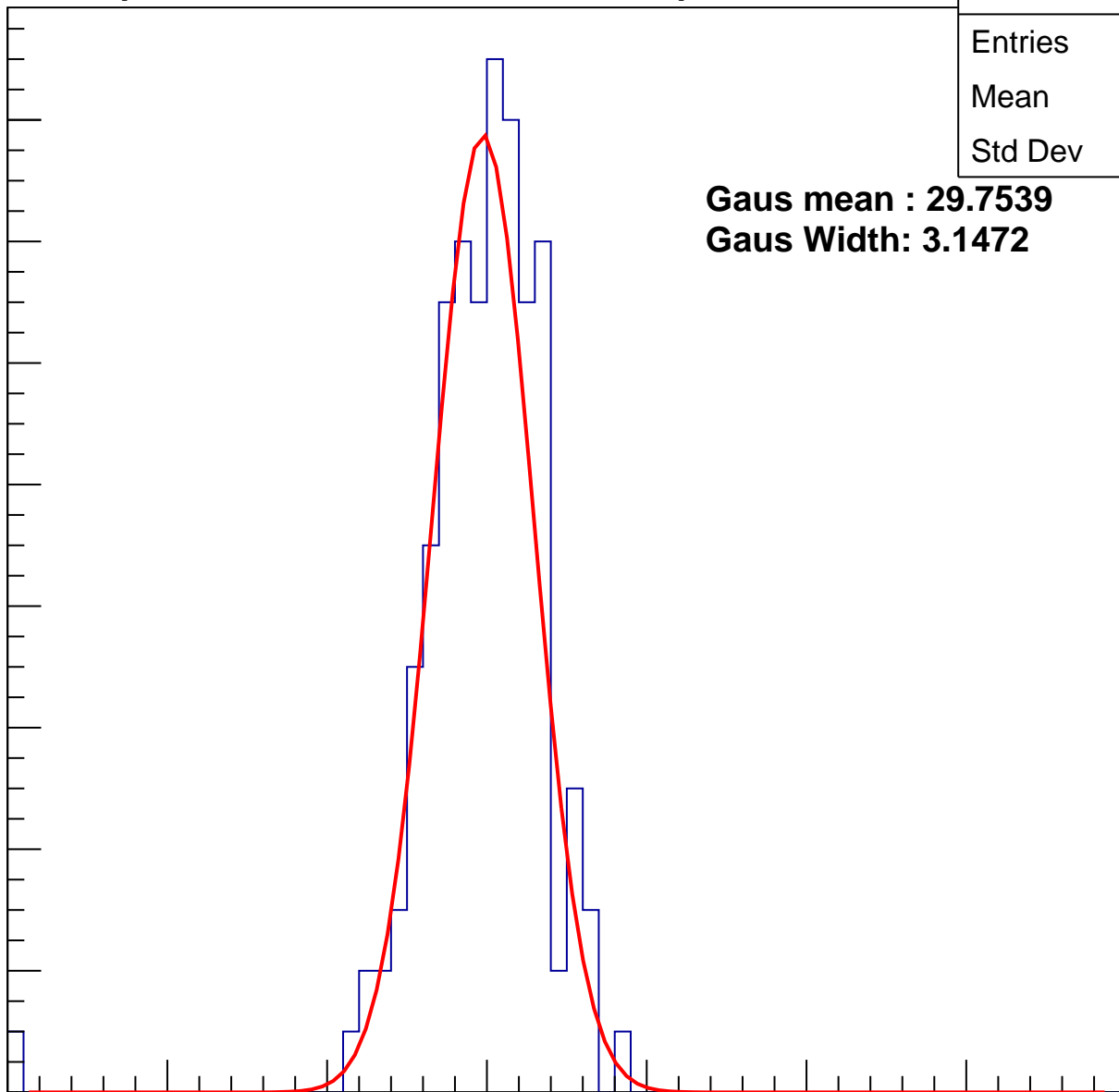
40

50

60

70

ampl



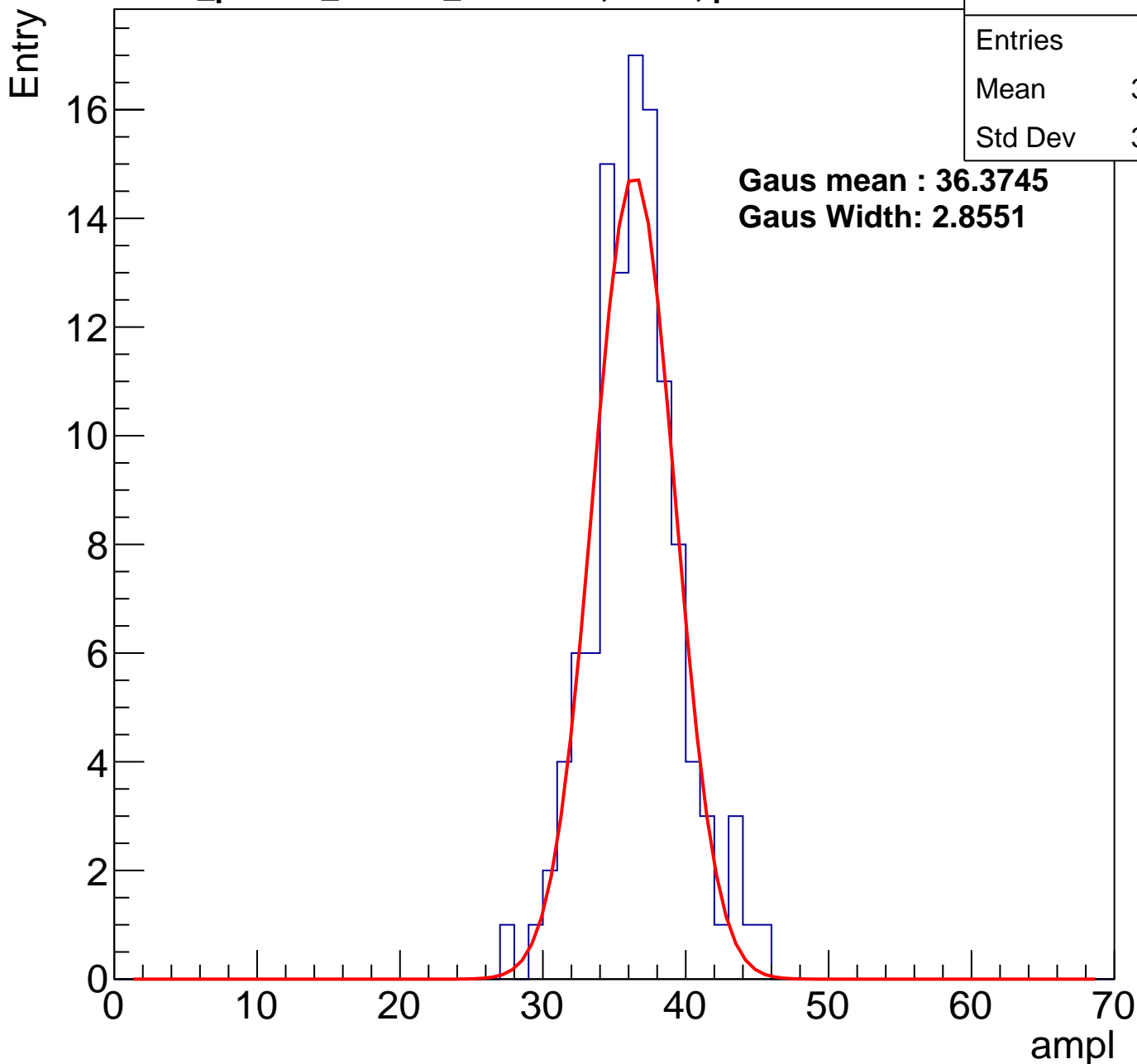
B1L001S, U20-ch72, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	36.04
Std Dev	3.154

Gaus mean : 36.3745

Gaus Width: 2.8551



B1L001S, U20-ch72, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

143

Mean

42.41

Std Dev

3.663

Gaus mean : 42.7341

Gaus Width: 3.6987

0

10

20

30

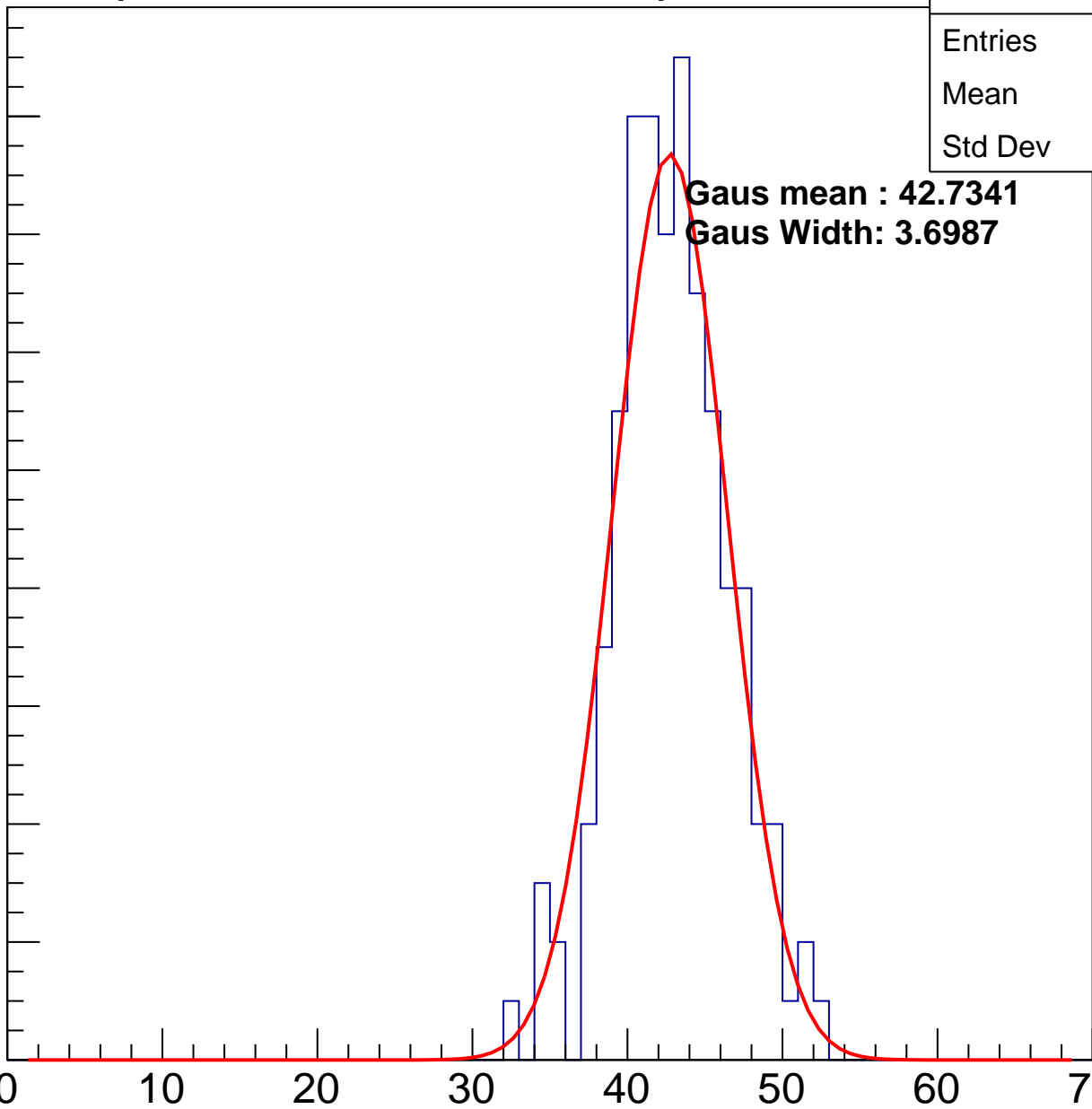
40

50

60

70

ampl

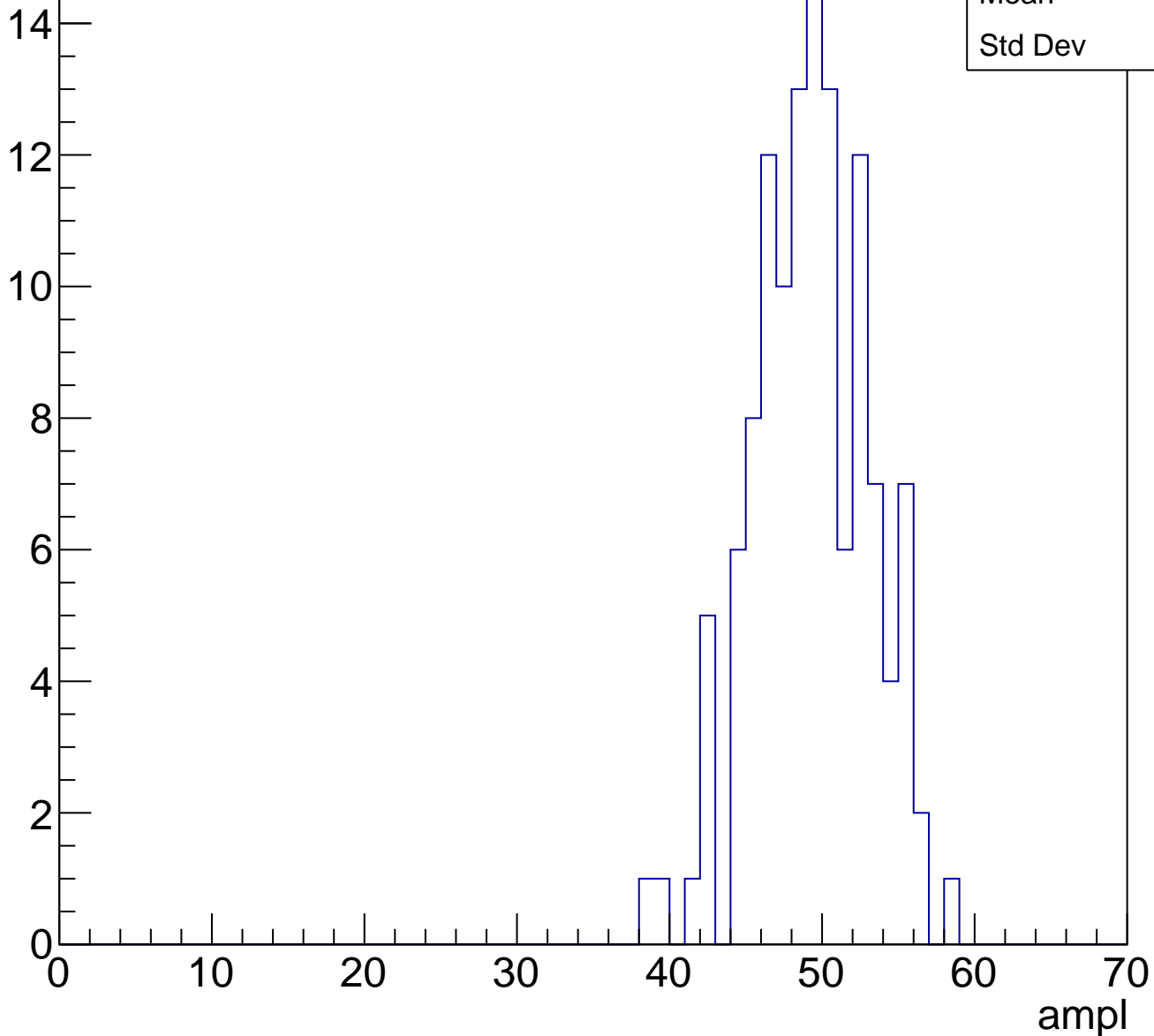


B1L001S, U20-ch72, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	48.83
Std Dev	3.76

Entry



B1L001S, U20-ch72, adc4

calib_packv5_042523_0143.root, FC#2, port C2

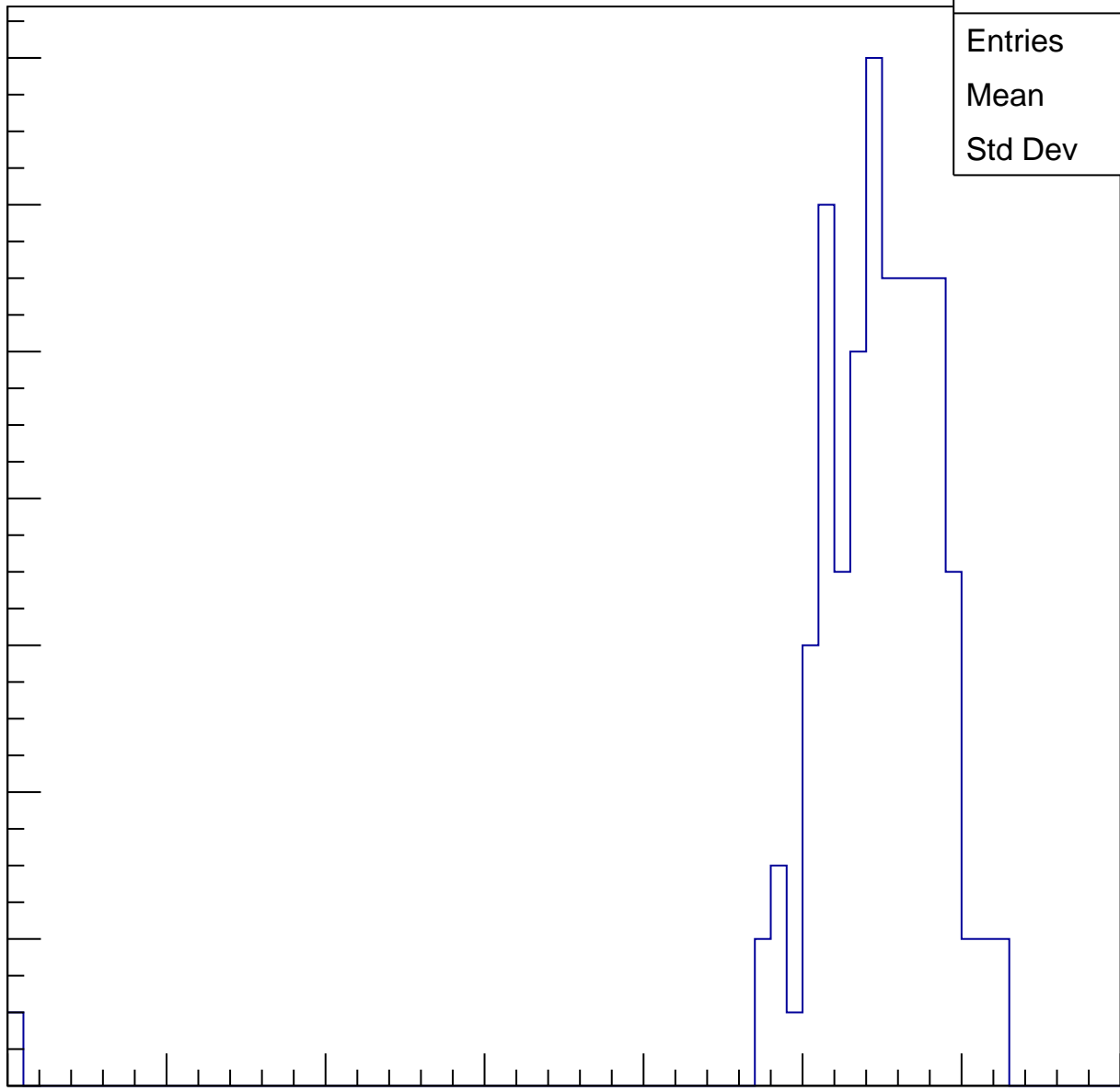
Entries	113
Mean	54.11
Std Dev	6.09

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch72, adc5

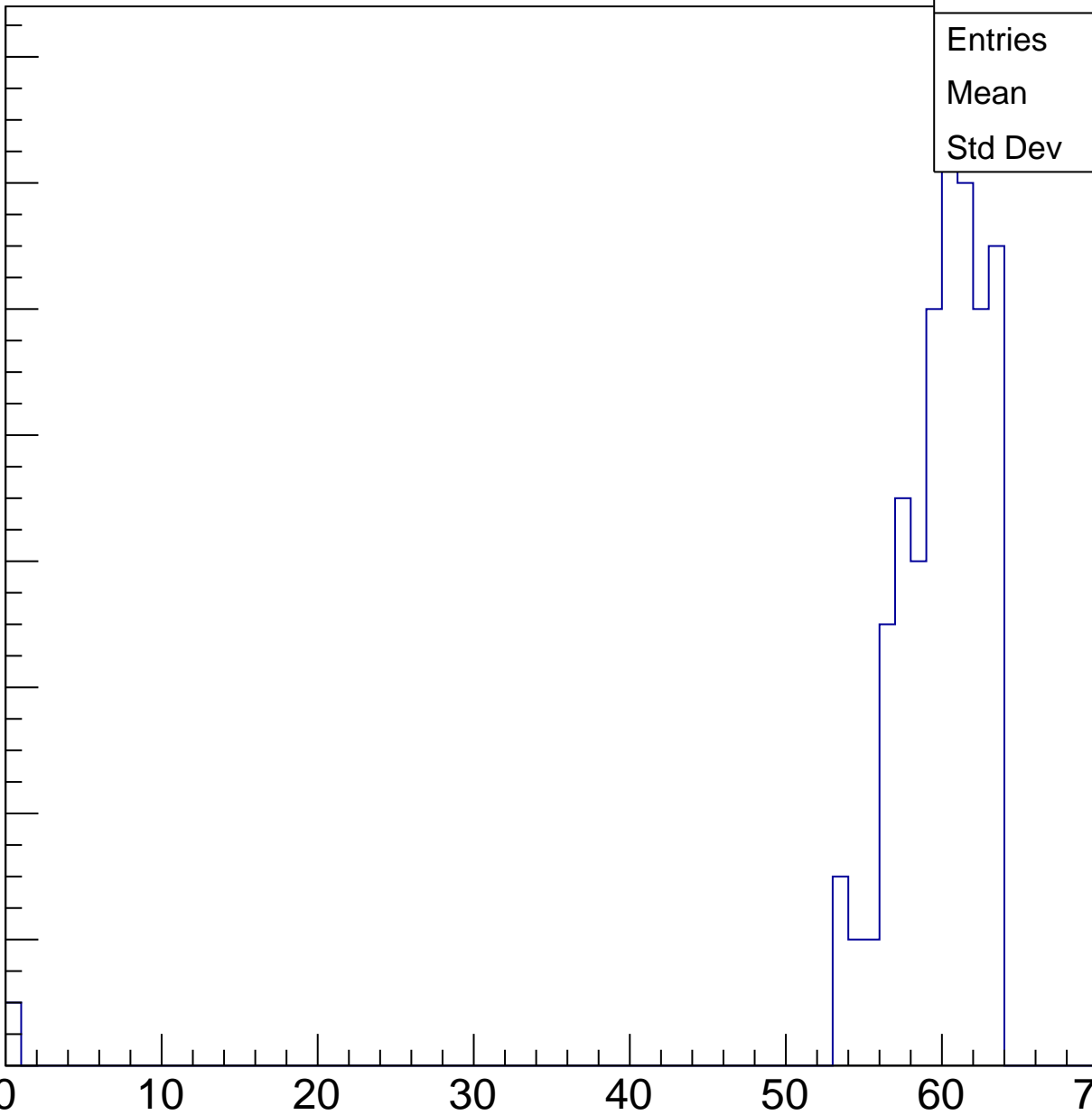
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	58.9
Std Dev	6.486

ampl

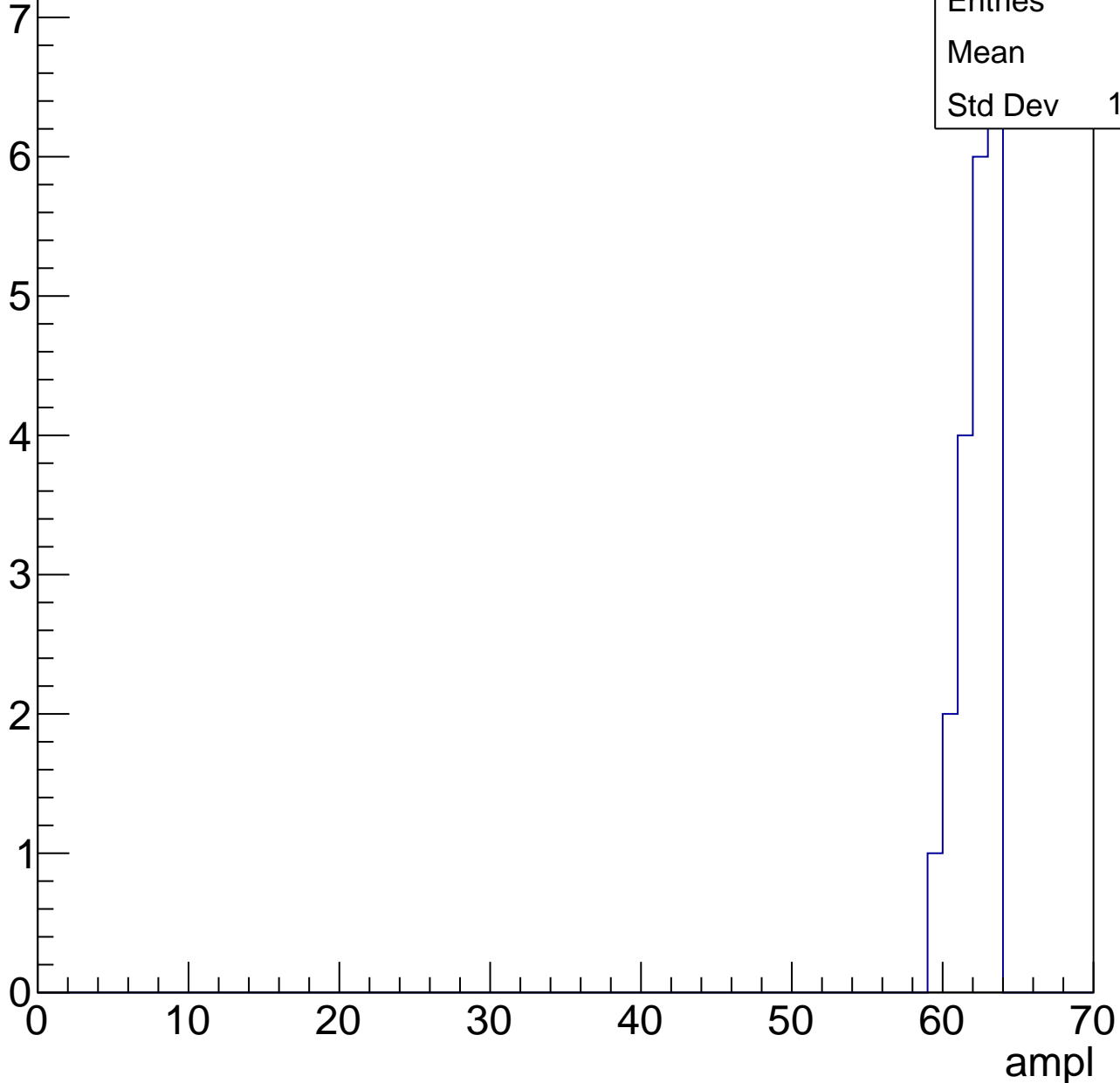


B1L001S, U20-ch72, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	61.8
Std Dev	1.166



B1L001S, U20-ch72, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch73, adc0

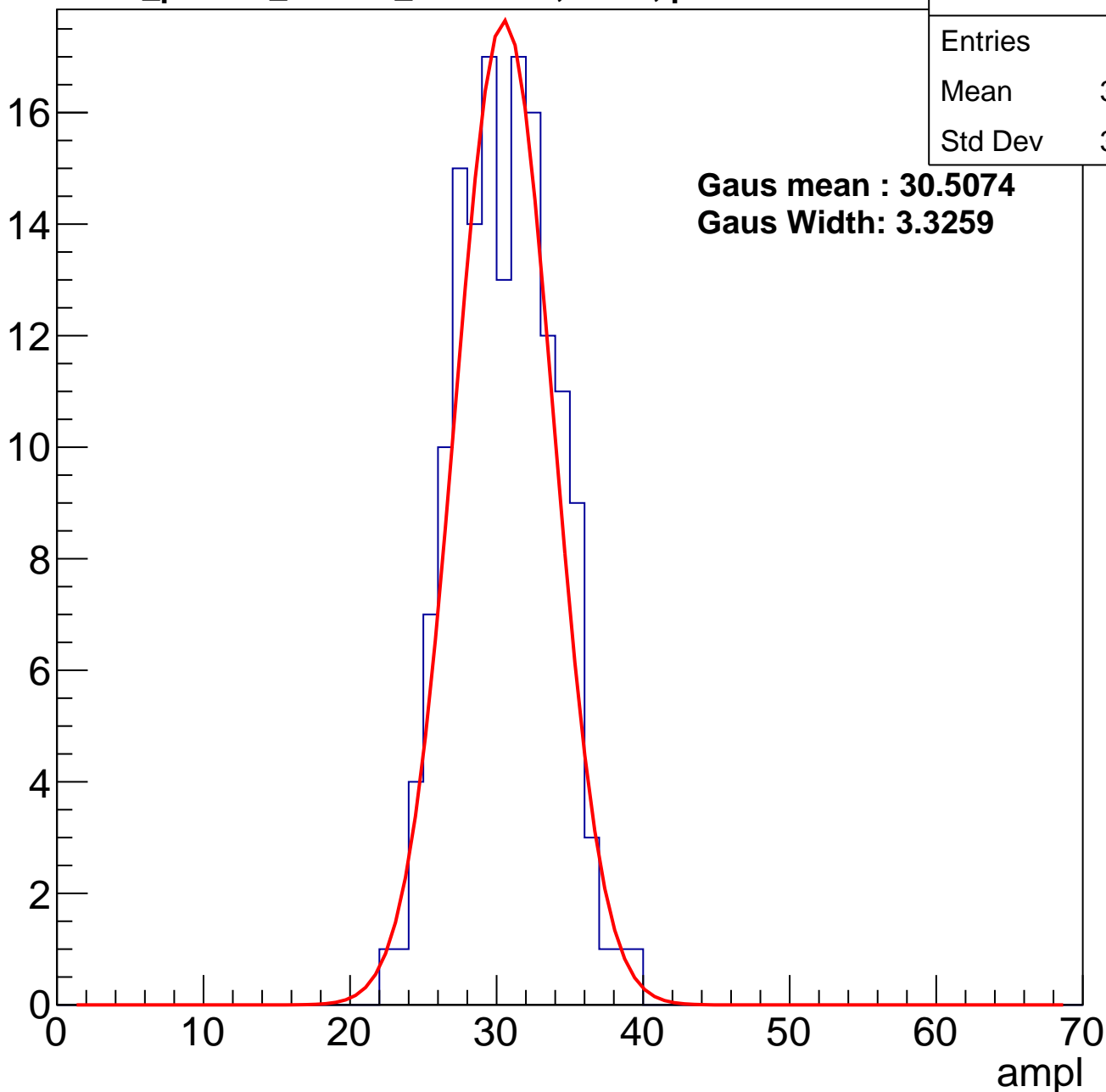
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	30.08
Std Dev	3.325

Gaus mean : 30.5074

Gaus Width: 3.3259

Entry



B1L001S, U20-ch73, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	99
Mean	36.34
Std Dev	3.257

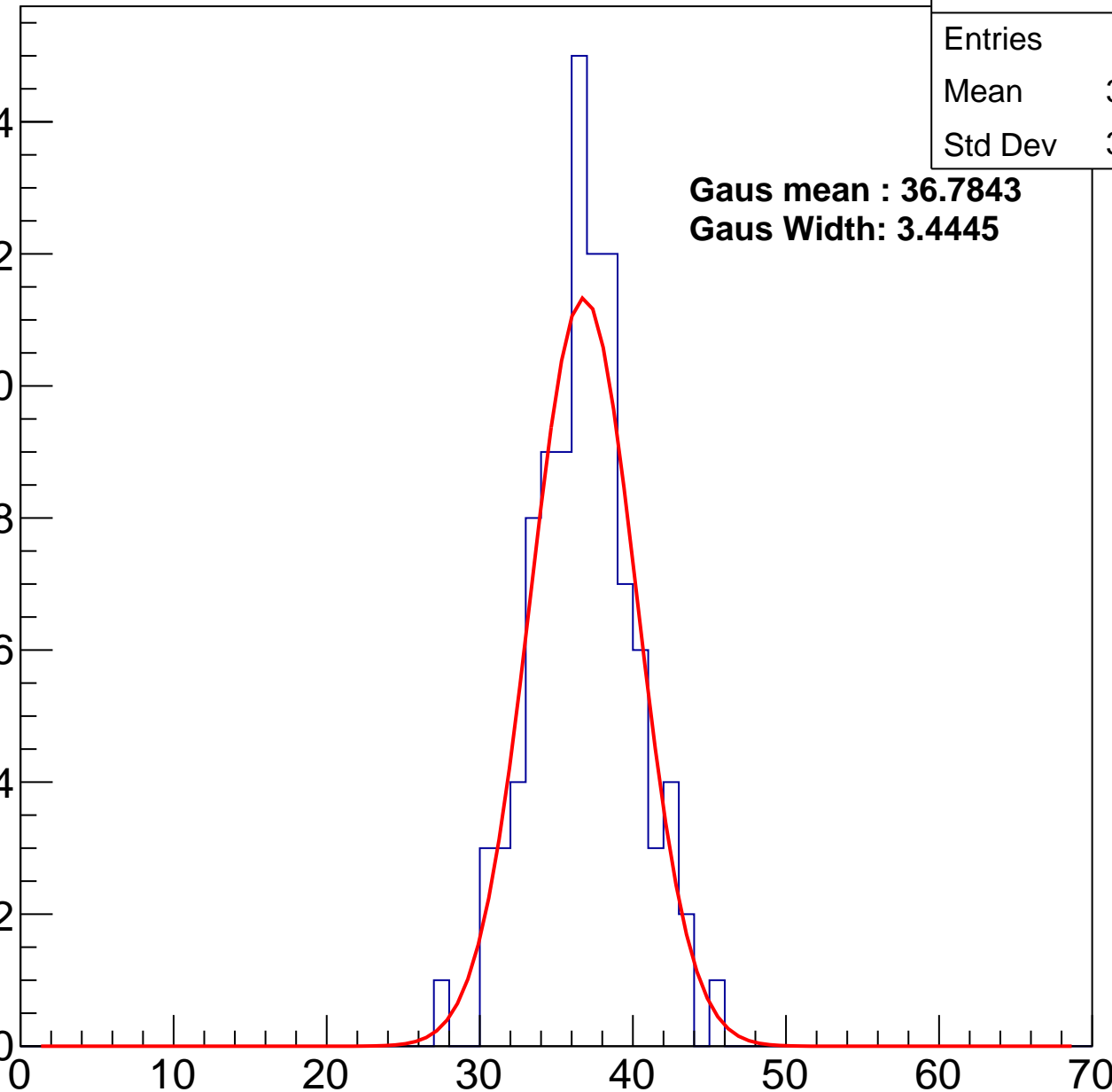
Gaus mean : 36.7843

Gaus Width: 3.4445

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch73, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

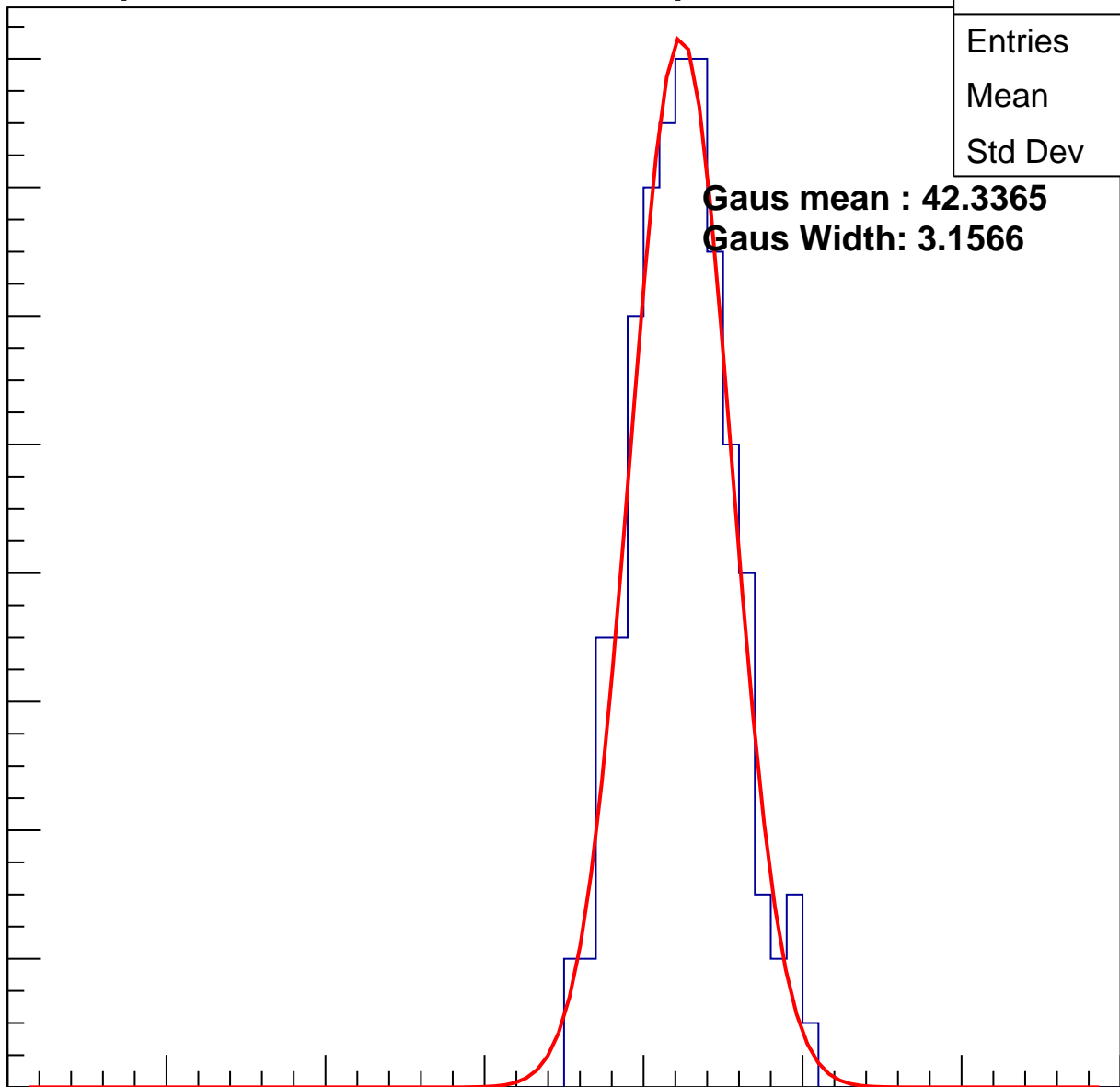
Entries	131
Mean	41.94
Std Dev	3.125

Gaus mean : 42.3365

Gaus Width: 3.1566

ampl

0 10 20 30 40 50 60 70

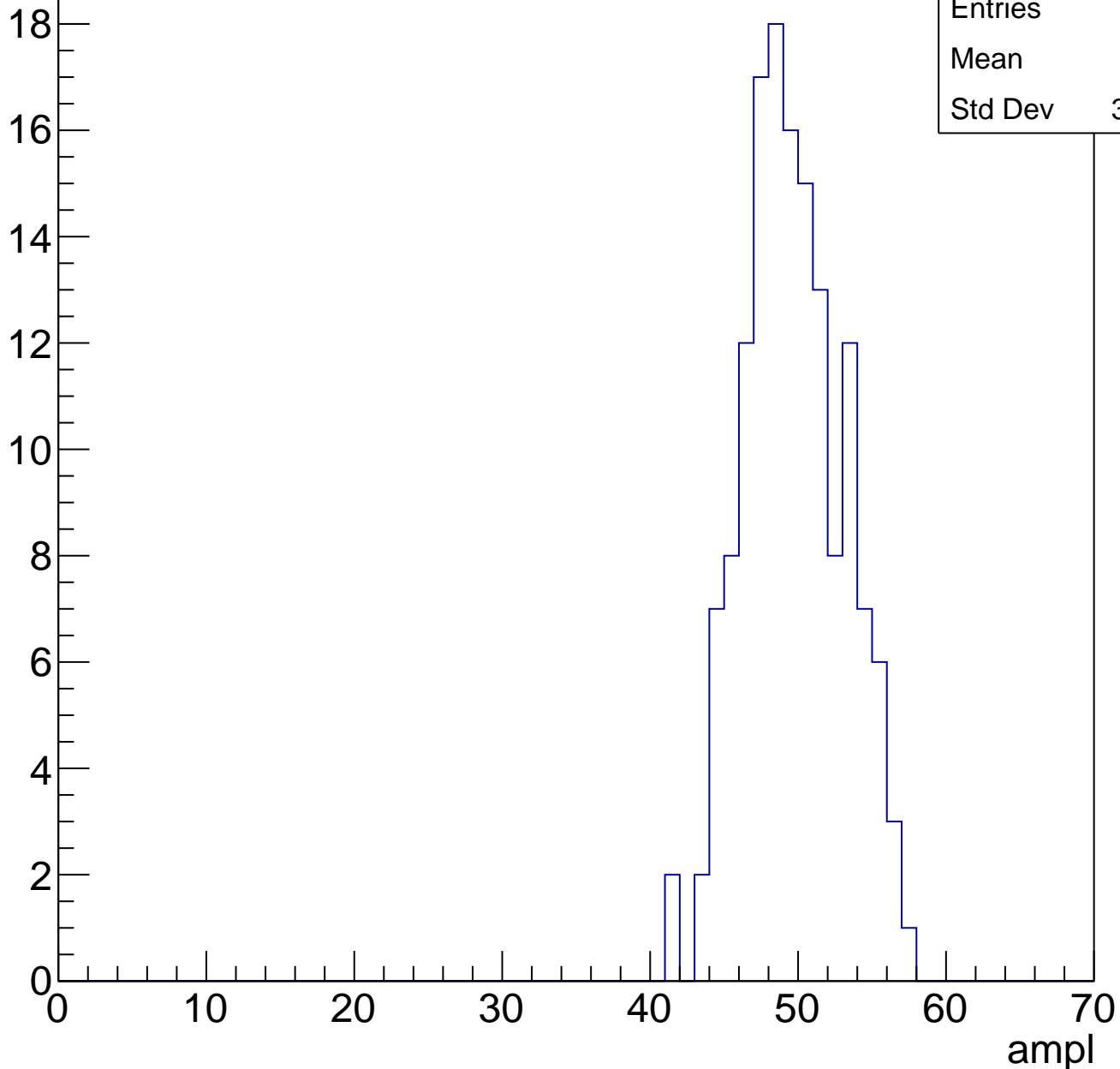


B1L001S, U20-ch73, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	49.2
Std Dev	3.324

Entry



B1L001S, U20-ch73, adc4

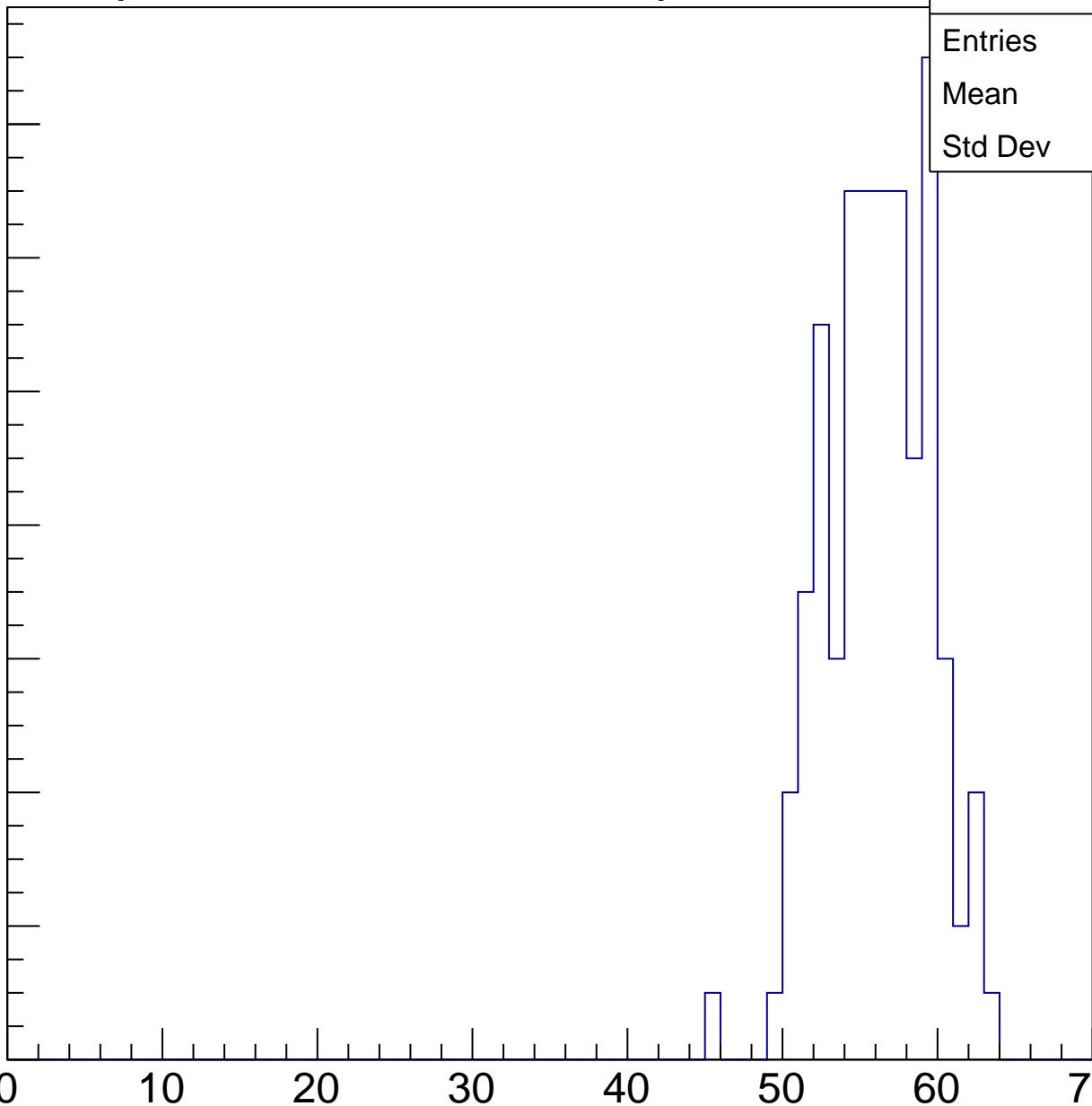
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	55.69
Std Dev	3.299

ampl



B1L001S, U20-ch73, adc5

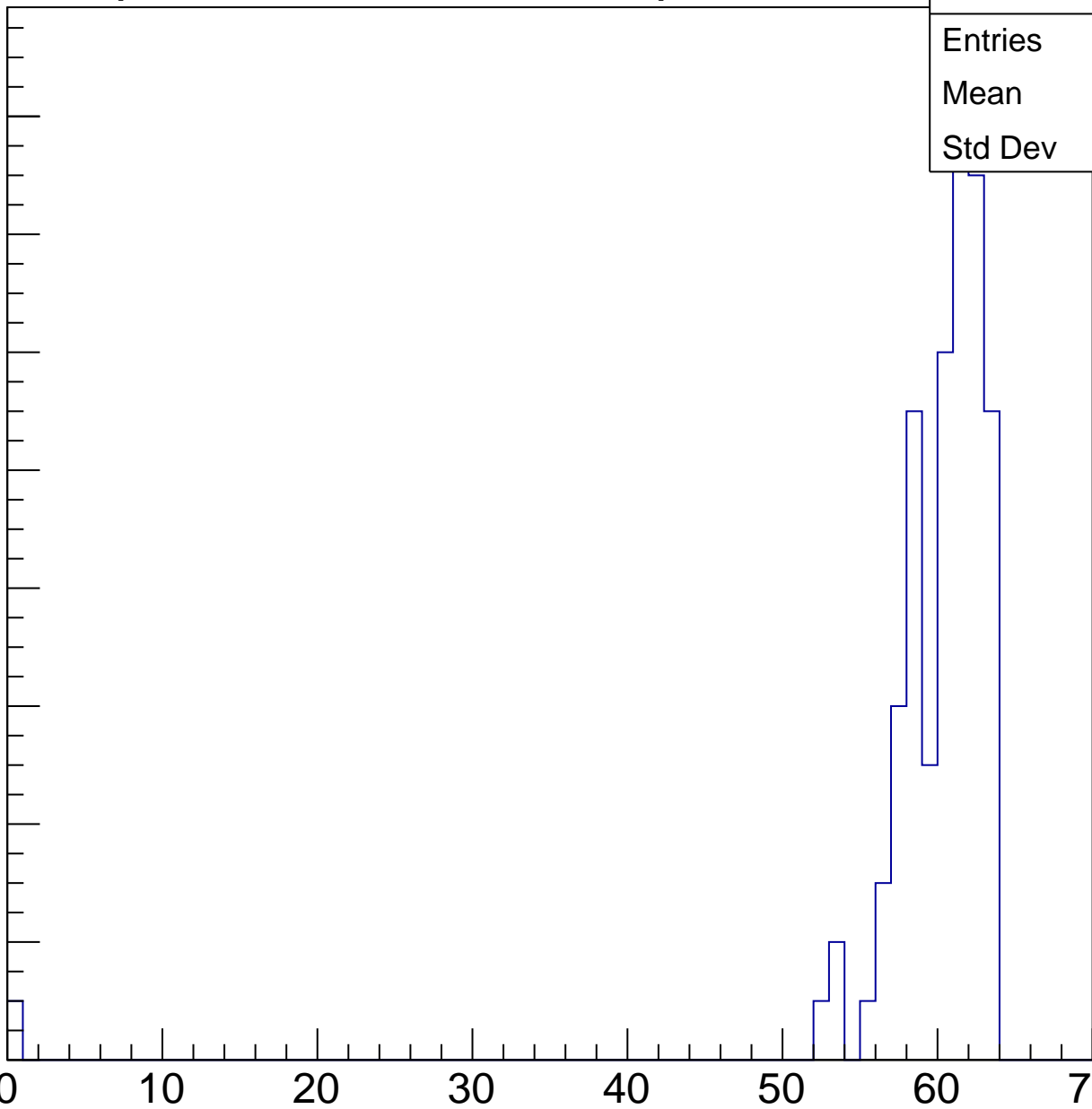
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.25
Std Dev	6.916

ampl

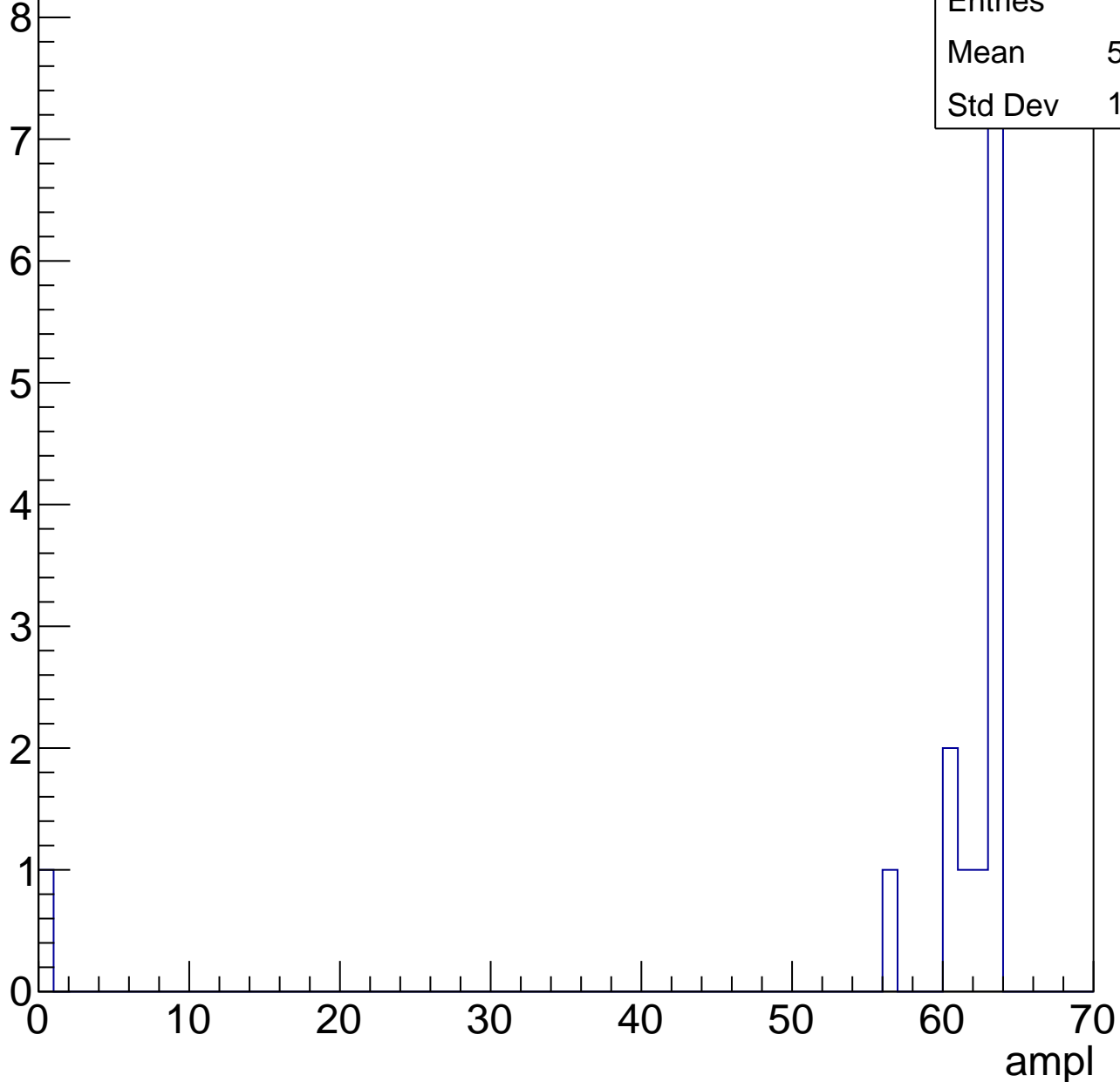


B1L001S, U20-ch73, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.36
Std Dev	16.03



B1L001S, U20-ch73, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L001S, U20-ch74, adc0

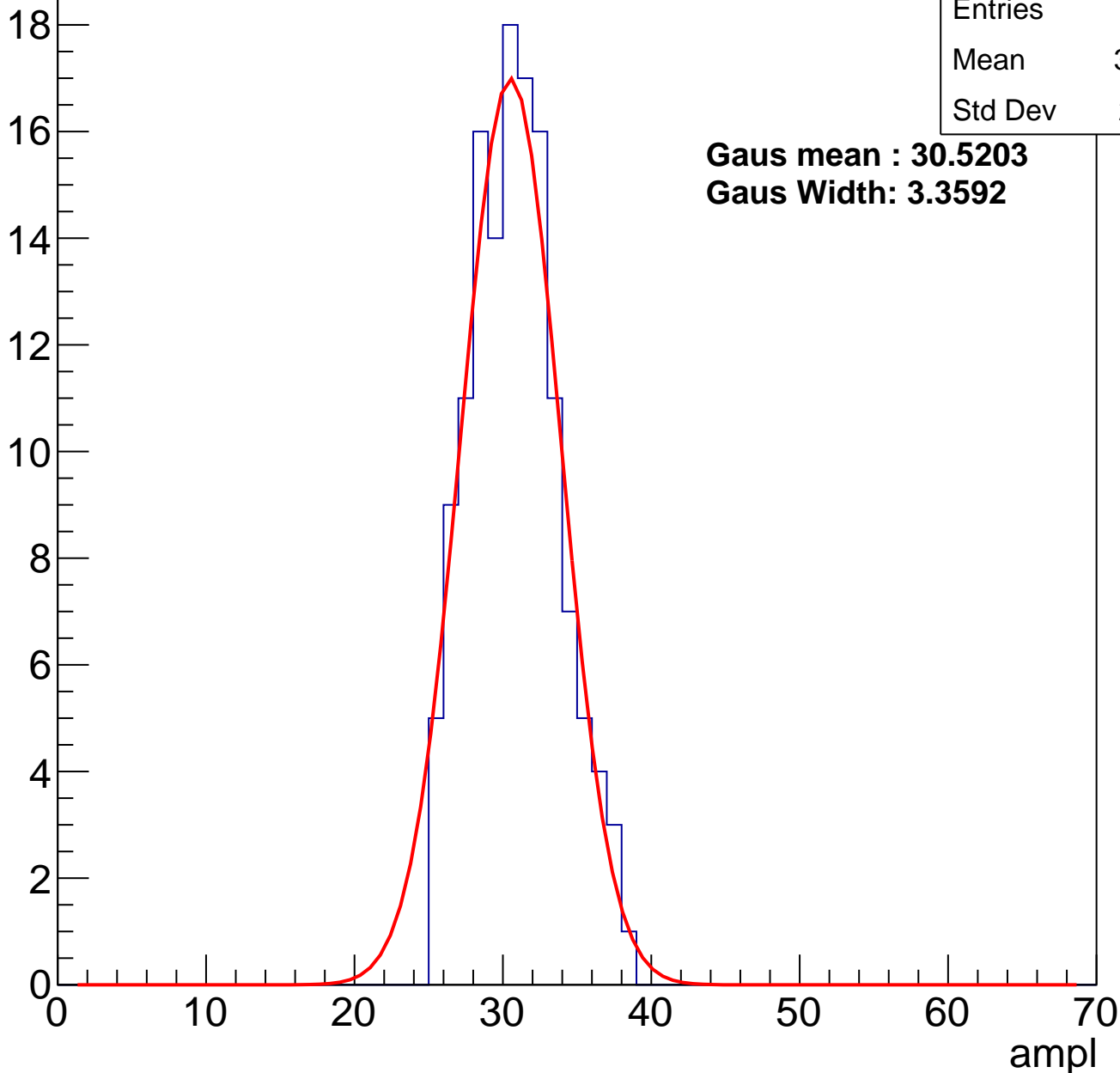
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	30.35
Std Dev	2.961

Gaus mean : 30.5203

Gaus Width: 3.3592

Entry

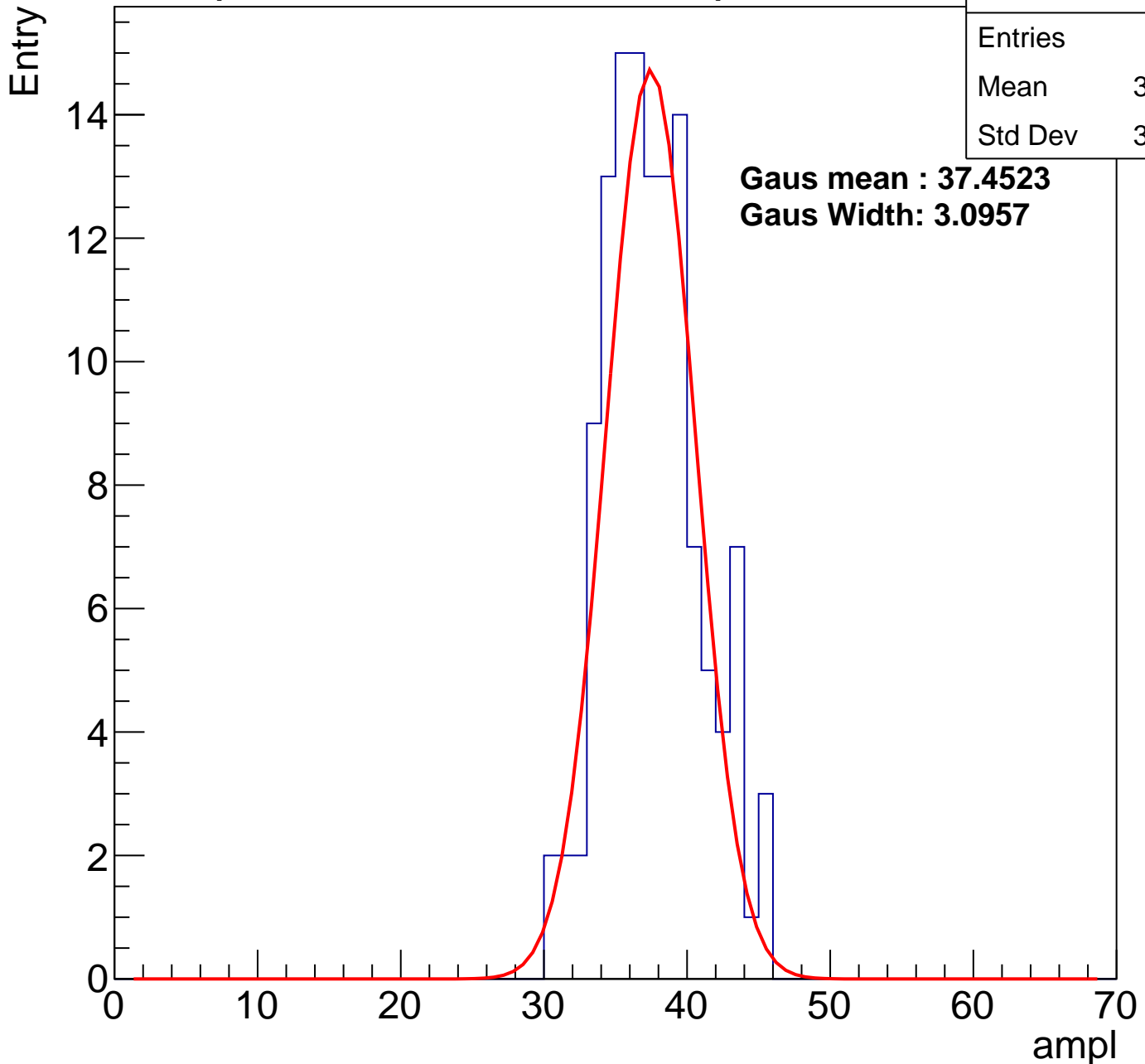


B1L001S, U20-ch74, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	37.15
Std Dev	3.296

Gaus mean : 37.4523
Gaus Width: 3.0957



B1L001S, U20-ch74, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	43.17
Std Dev	3.25

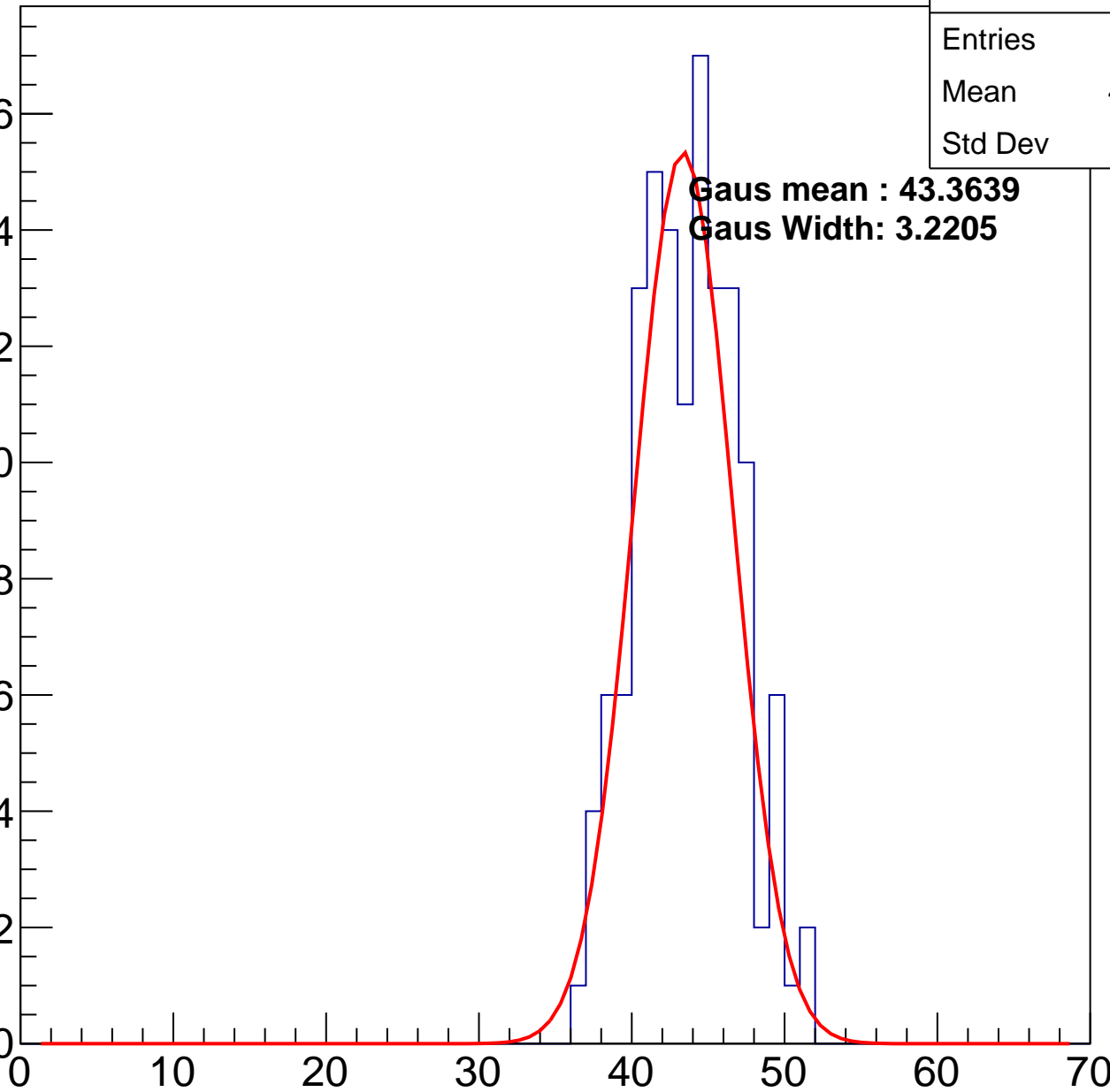
Gaus mean : 43.3639

Gaus Width: 3.2205

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch74, adc3

calib_packv5_042523_0143.root, FC#2, port C2

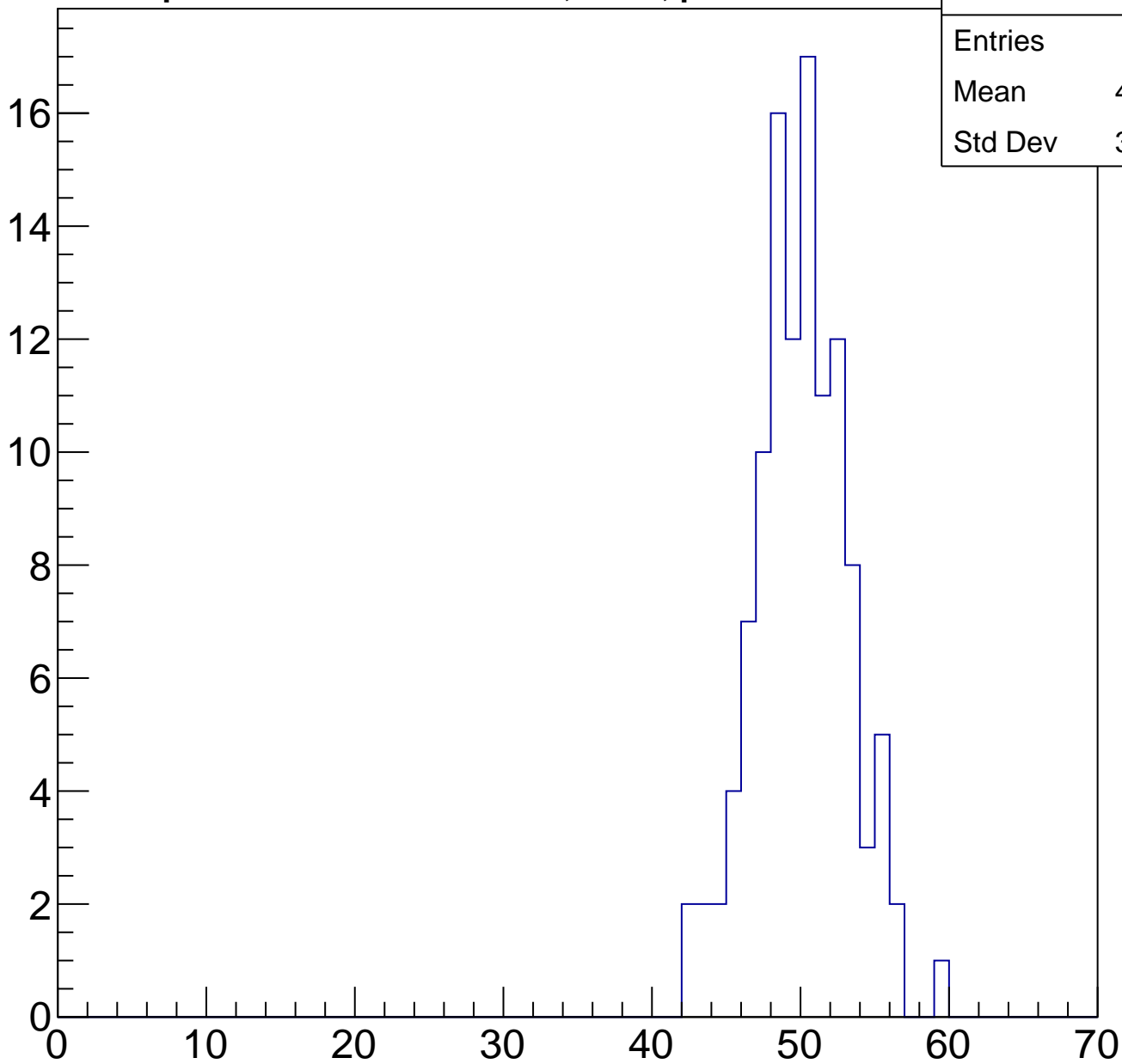
Entries	114
Mean	49.59
Std Dev	3.125

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch74, adc4

calib_packv5_042523_0143.root, FC#2, port C2

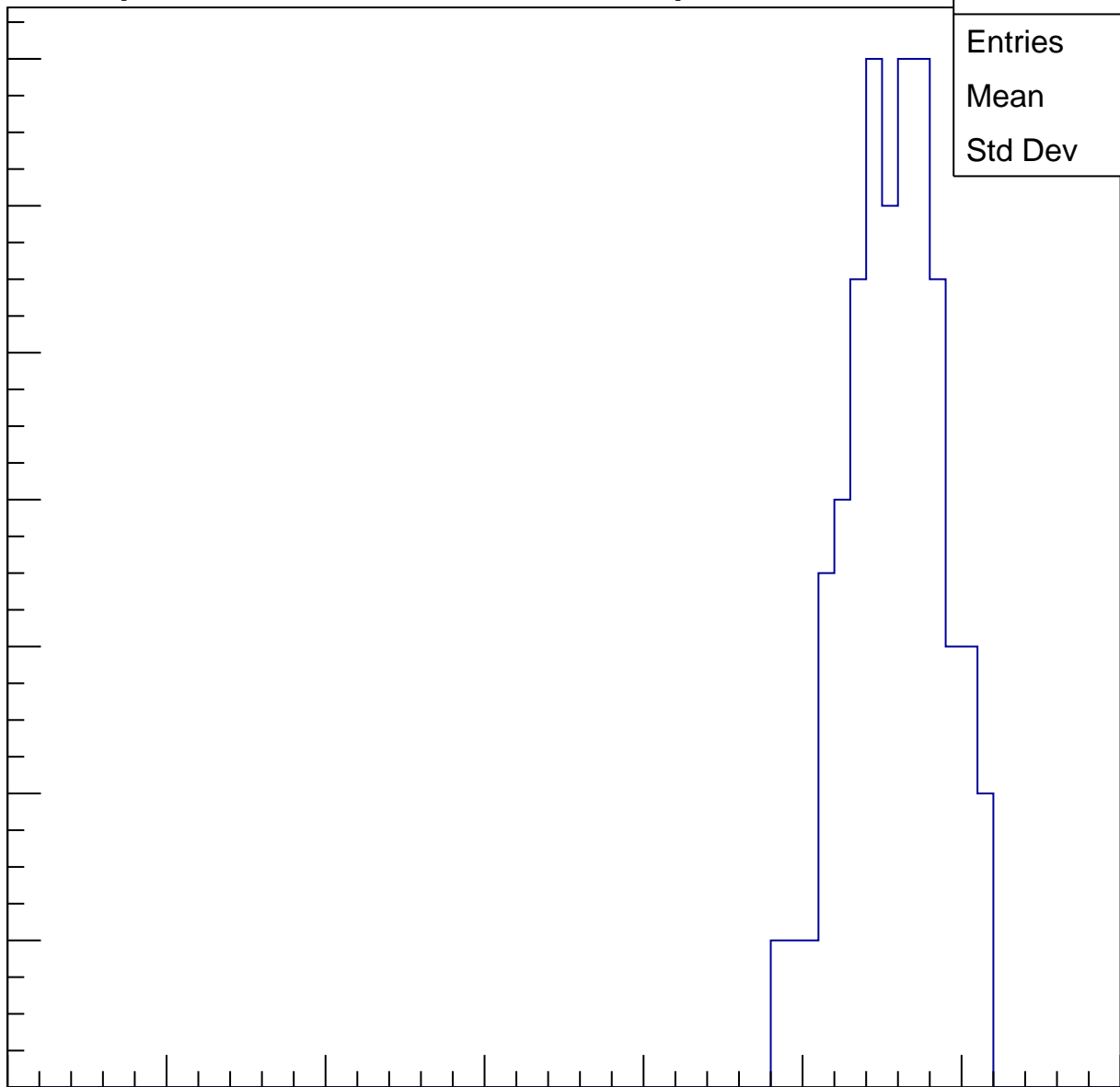
Entries	113
Mean	55.26
Std Dev	2.995

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch74, adc5

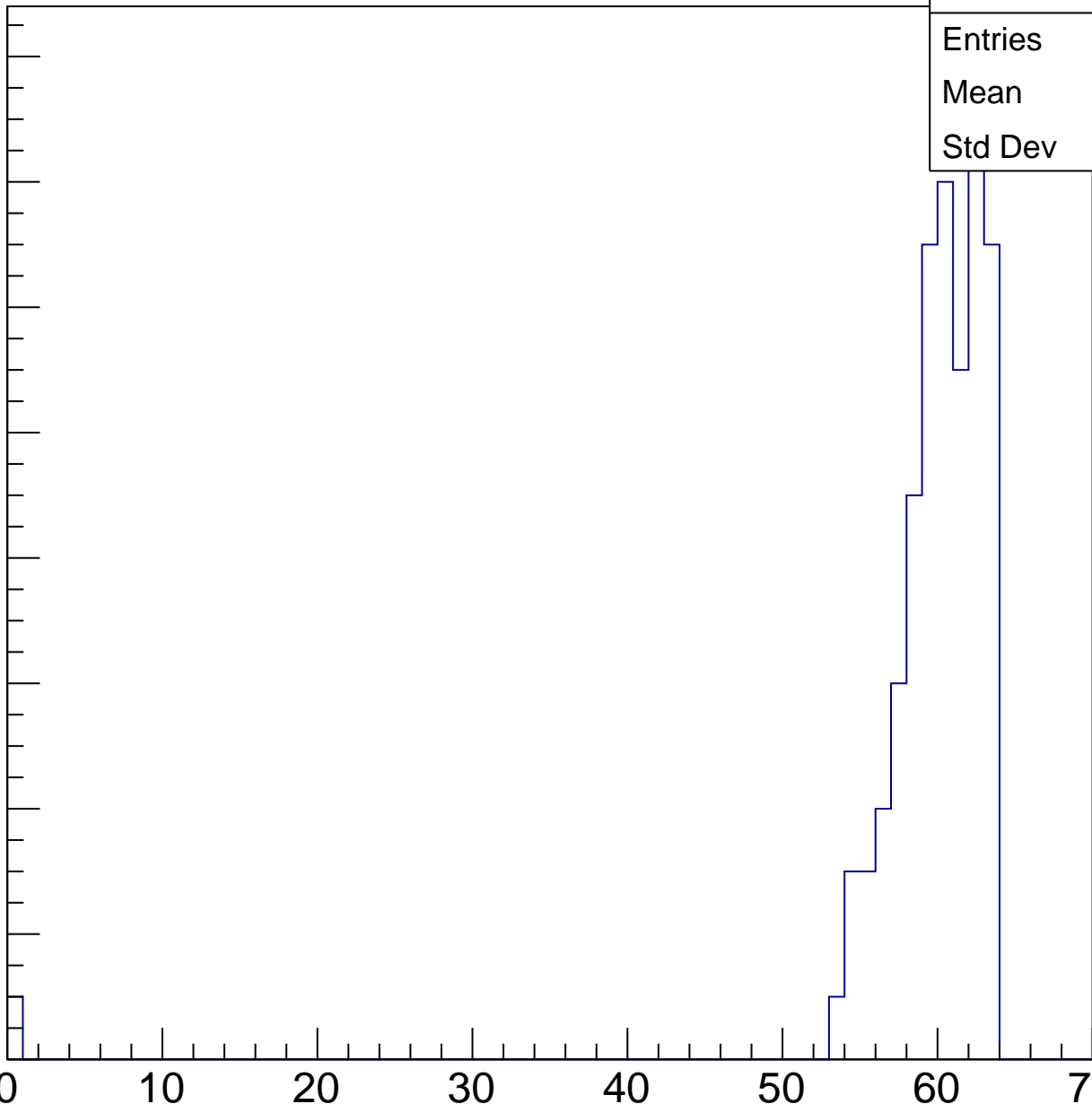
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	59.12
Std Dev	6.619

ampl

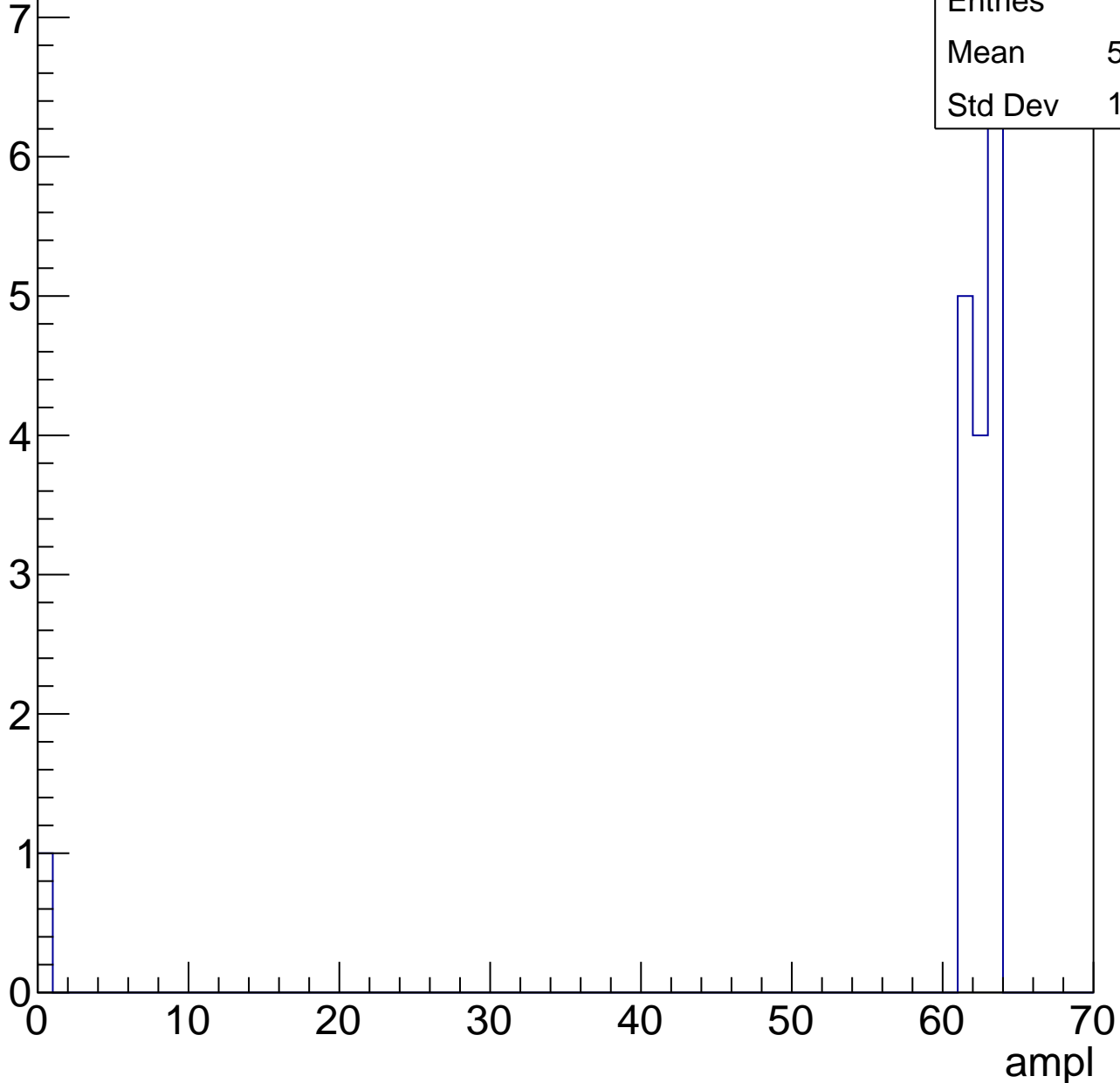


B1L001S, U20-ch74, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	58.47
Std Dev	14.64



B1L001S, U20-ch74, adc7

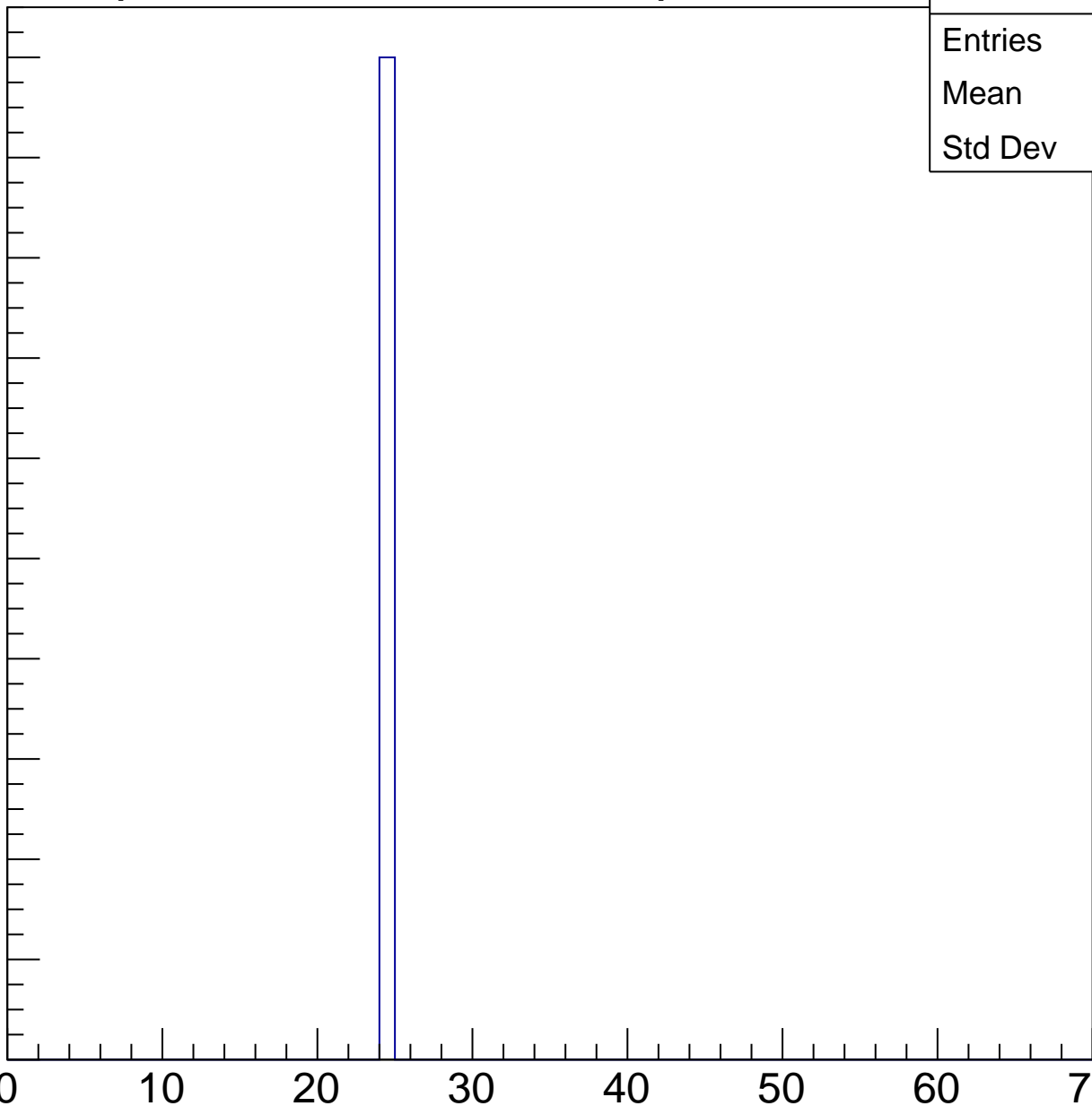
calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	24
Std Dev	0

ampl



B1L001S, U20-ch75, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	28.81
Std Dev	3.107

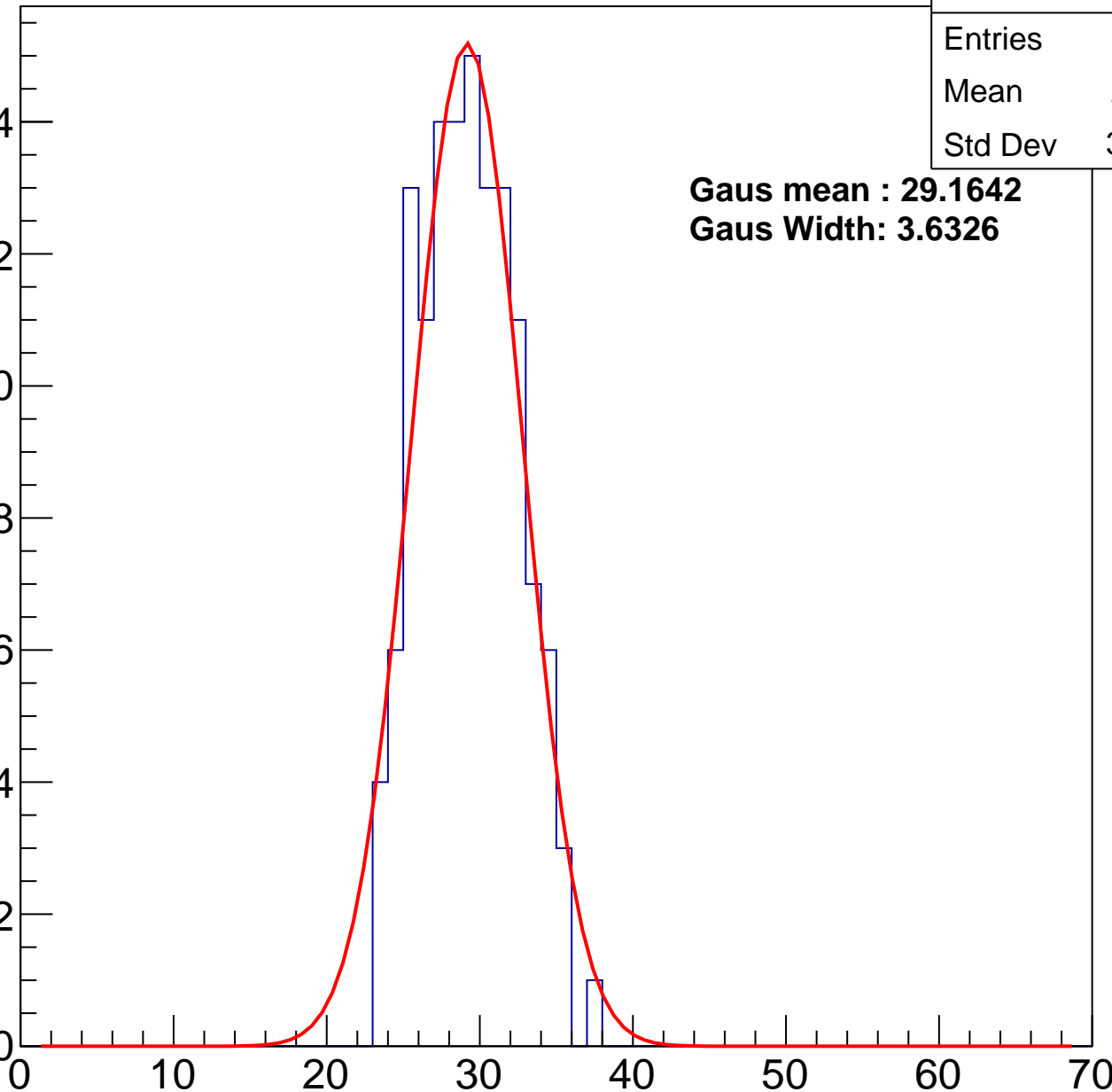
Gaus mean : 29.1642

Gaus Width: 3.6326

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch75, adc1

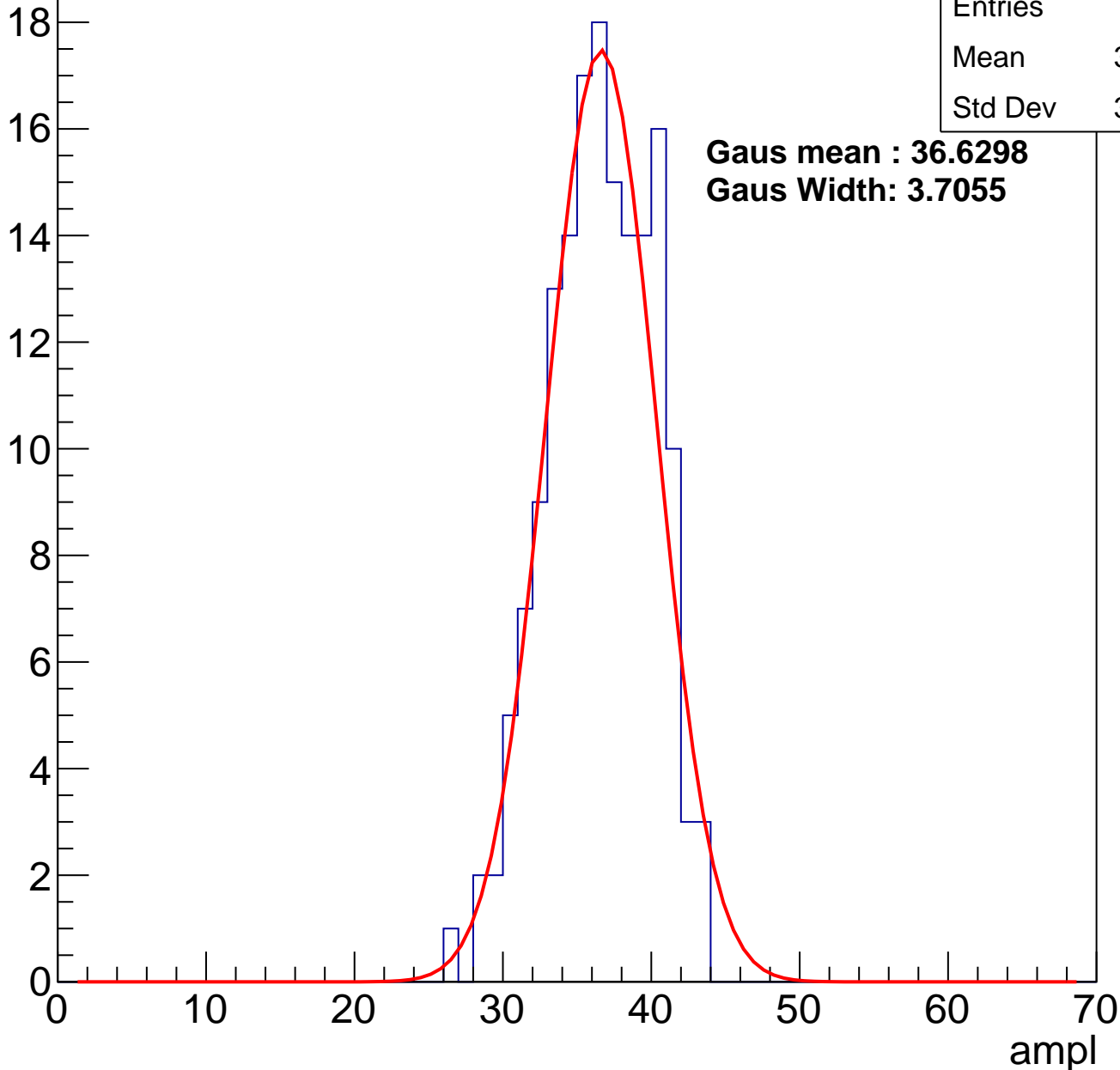
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	36.08
Std Dev	3.478

Gaus mean : 36.6298

Gaus Width: 3.7055

Entry



B1L001S, U20-ch75, adc2

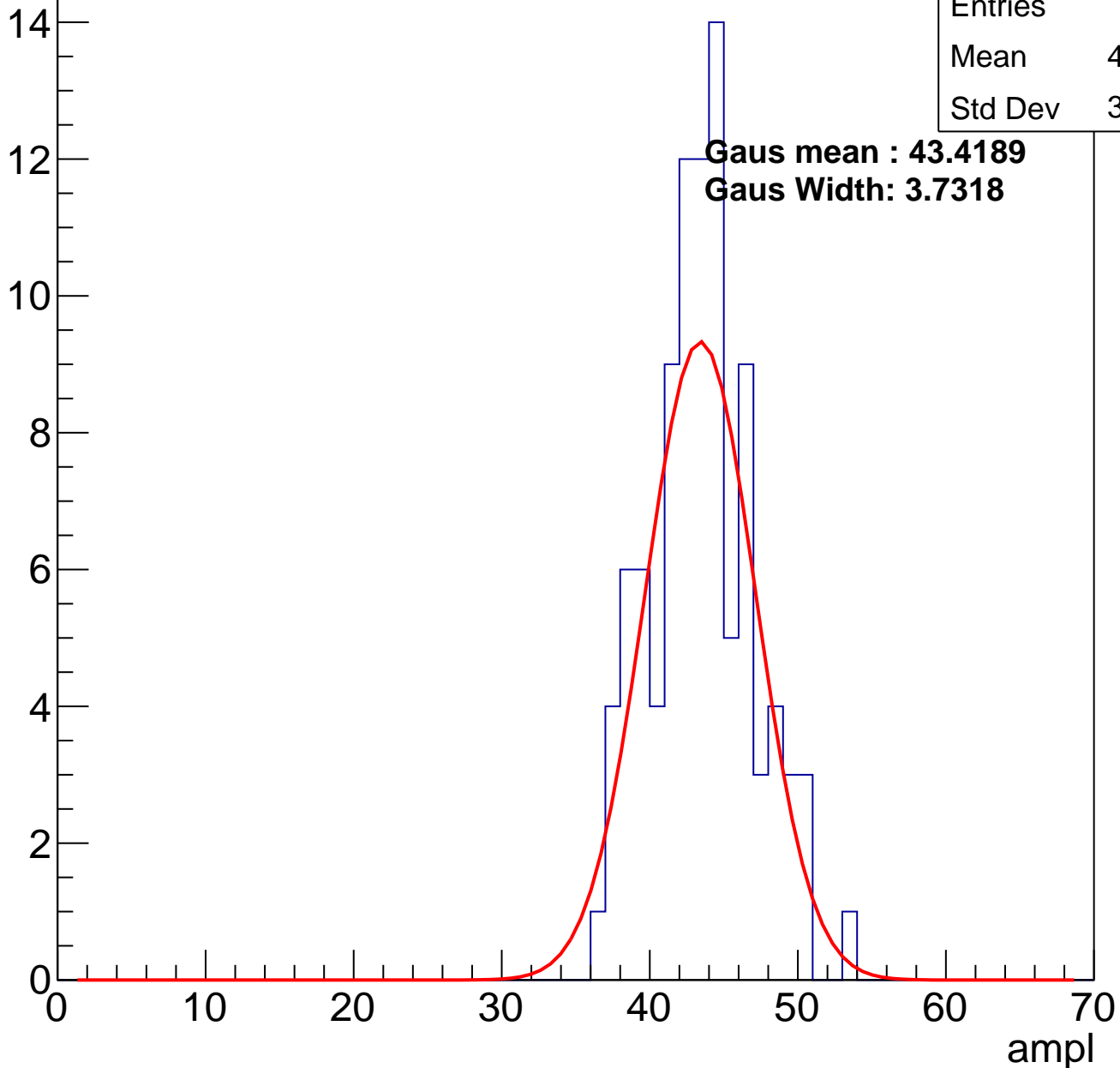
calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	43.05
Std Dev	3.447

Gaus mean : 43.4189

Gaus Width: 3.7318

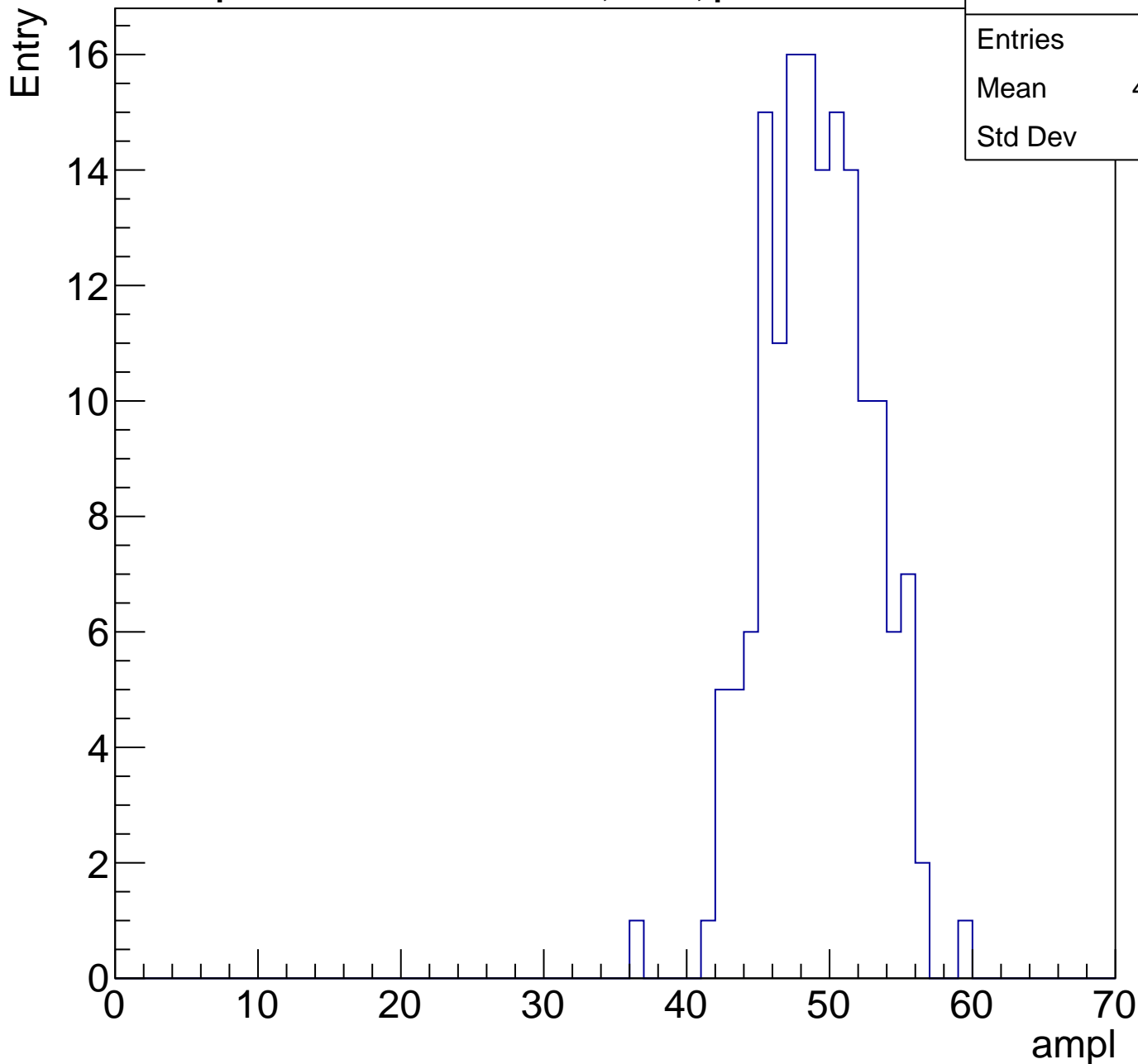
Entry



B1L001S, U20-ch75, adc3

calib_packv5_042523_0143.root, FC#2, port C2

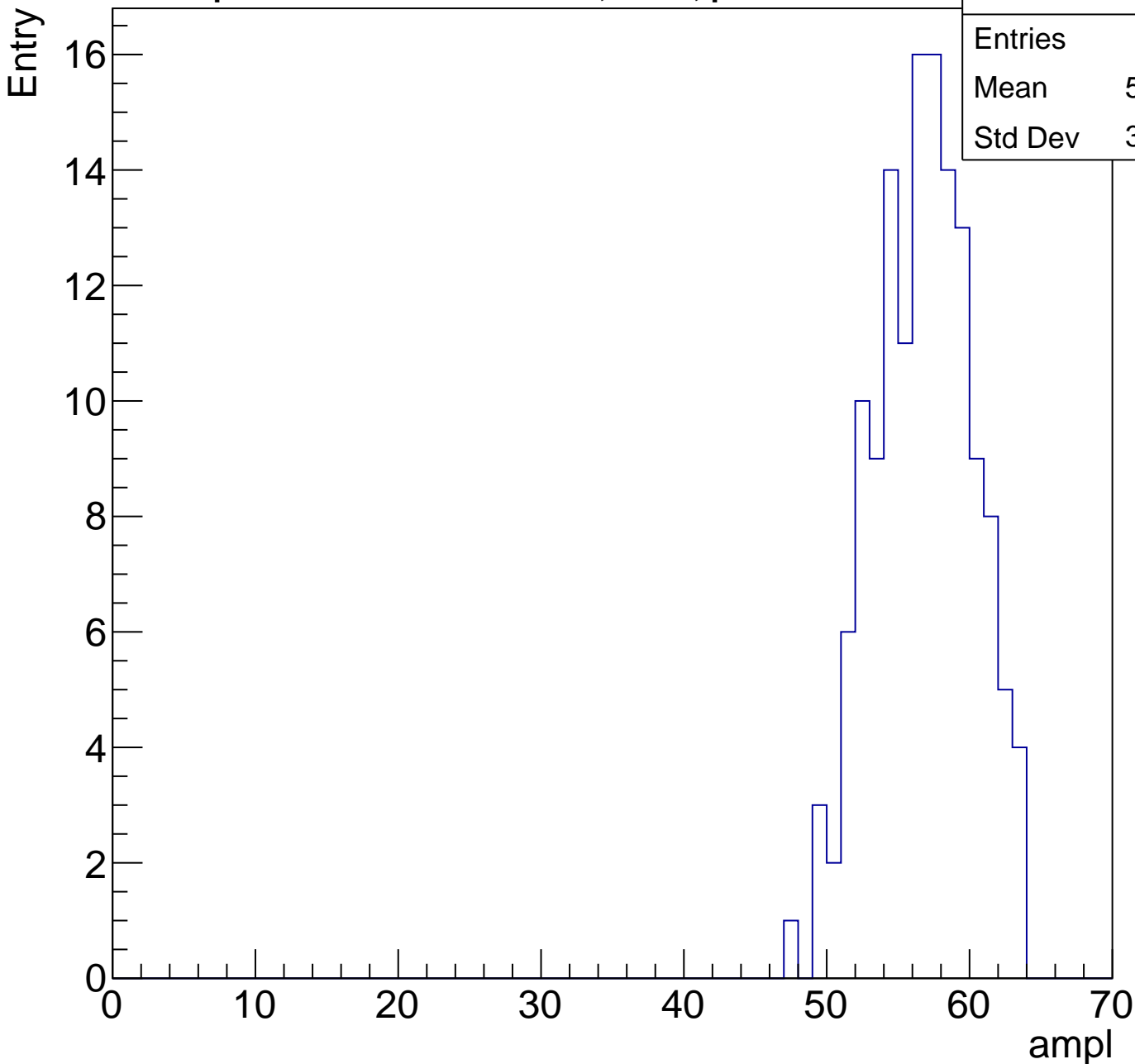
Entries	155
Mean	48.69
Std Dev	3.71



B1L001S, U20-ch75, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	56.28
Std Dev	3.427



B1L001S, U20-ch75, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

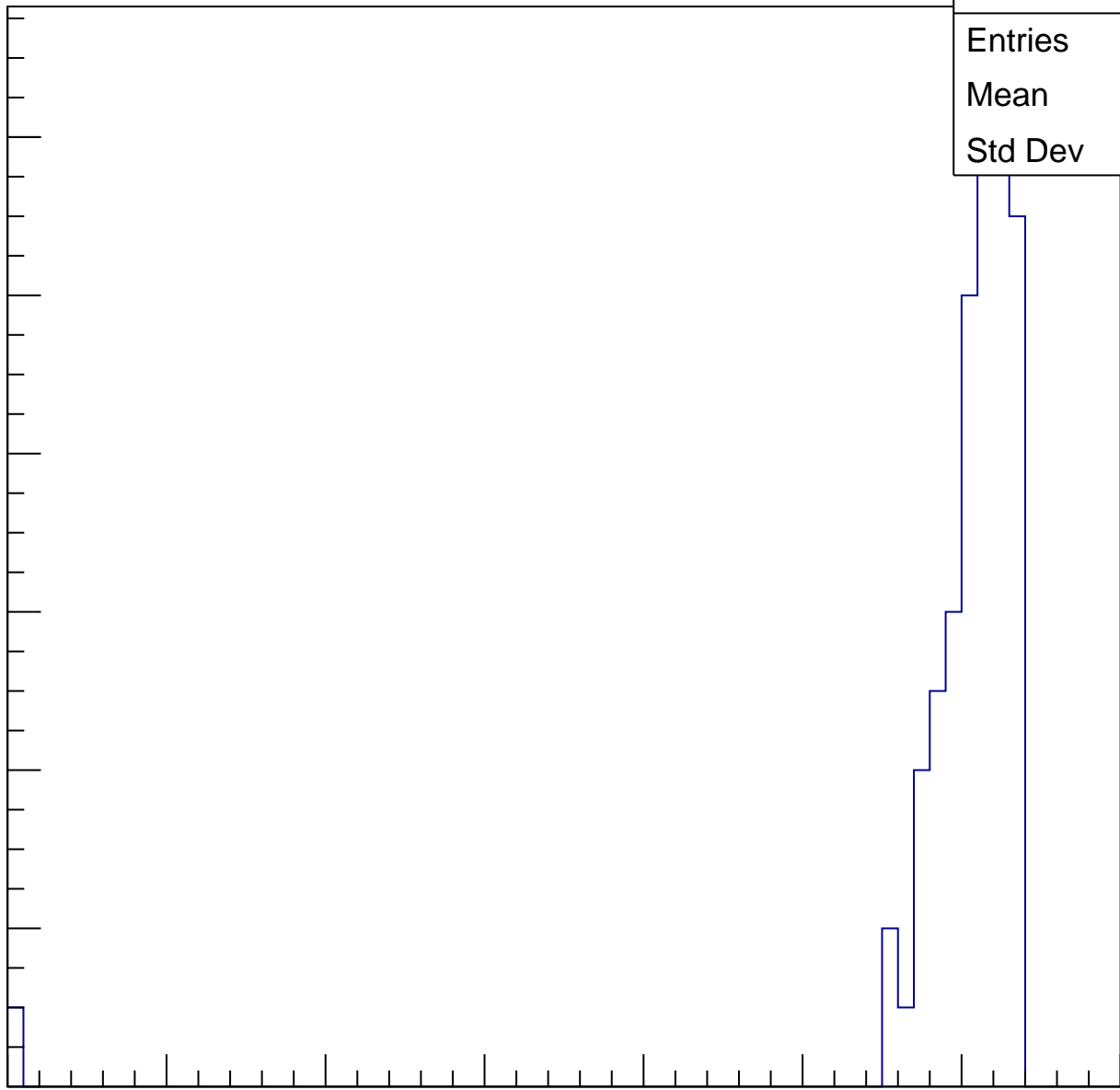
2

0

Entries	65
Mean	59.52
Std Dev	7.722

ampl

0 10 20 30 40 50 60 70

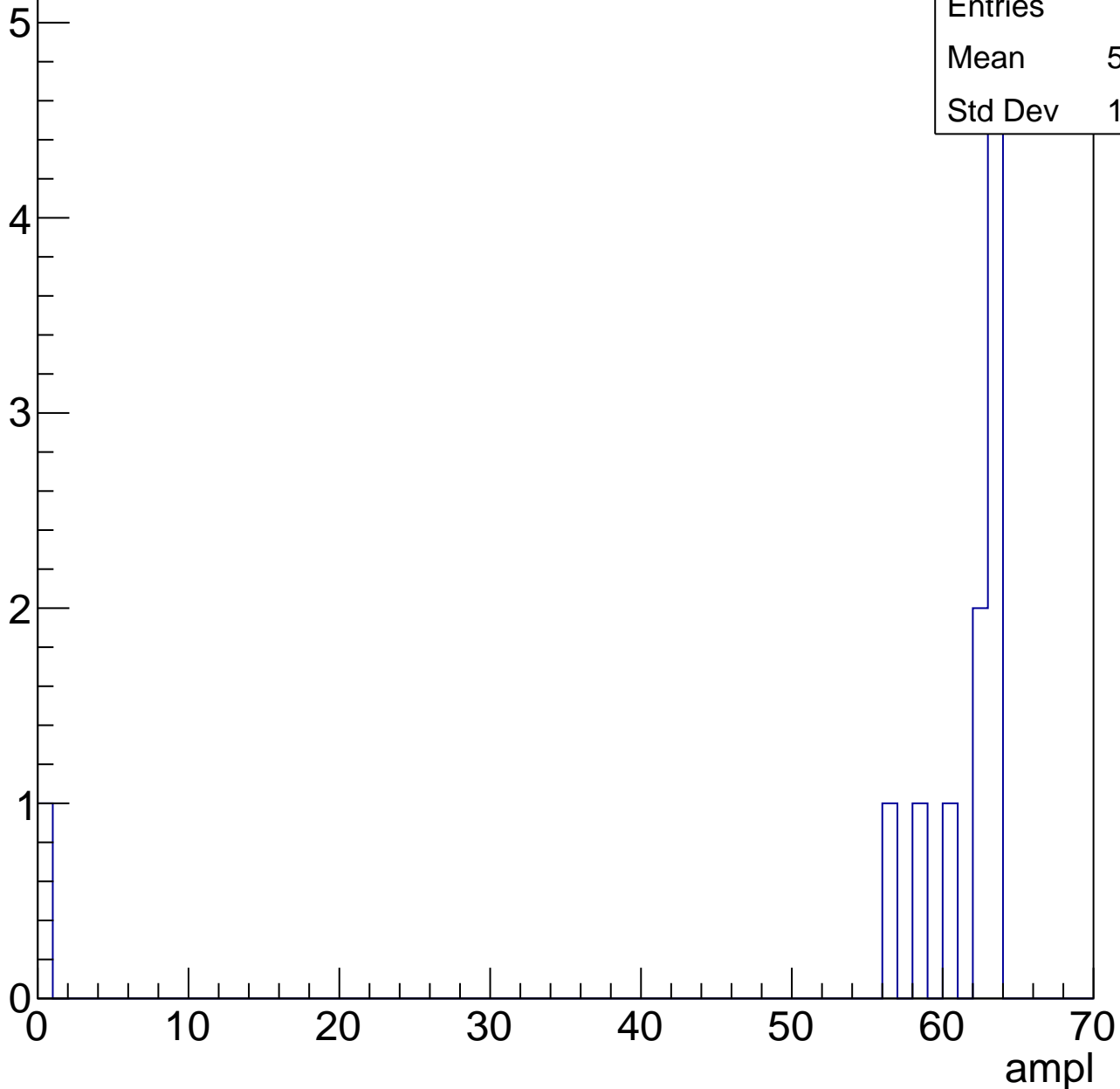


B1L001S, U20-ch75, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	55.73
Std Dev	17.77



B1L001S, U20-ch75, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	2
Mean	22.5
Std Dev	0.5

B1L001S, U20-ch76, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	27.86
Std Dev	3.2

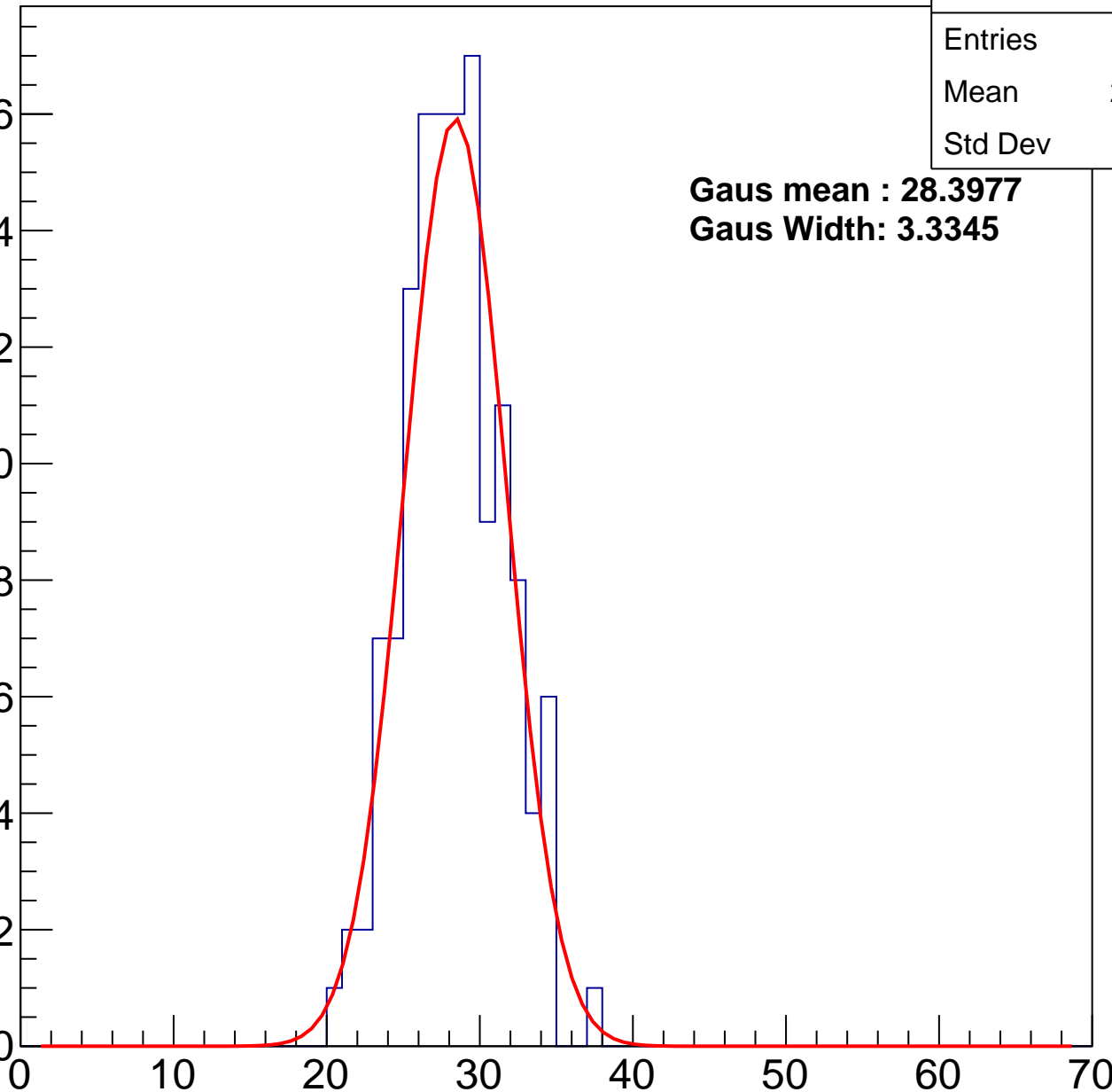
Gaus mean : 28.3977

Gaus Width: 3.3345

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch76, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	34.31
Std Dev	2.992

Gaus mean : 35.0197

Gaus Width: 3.0789

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U20-ch76, adc2

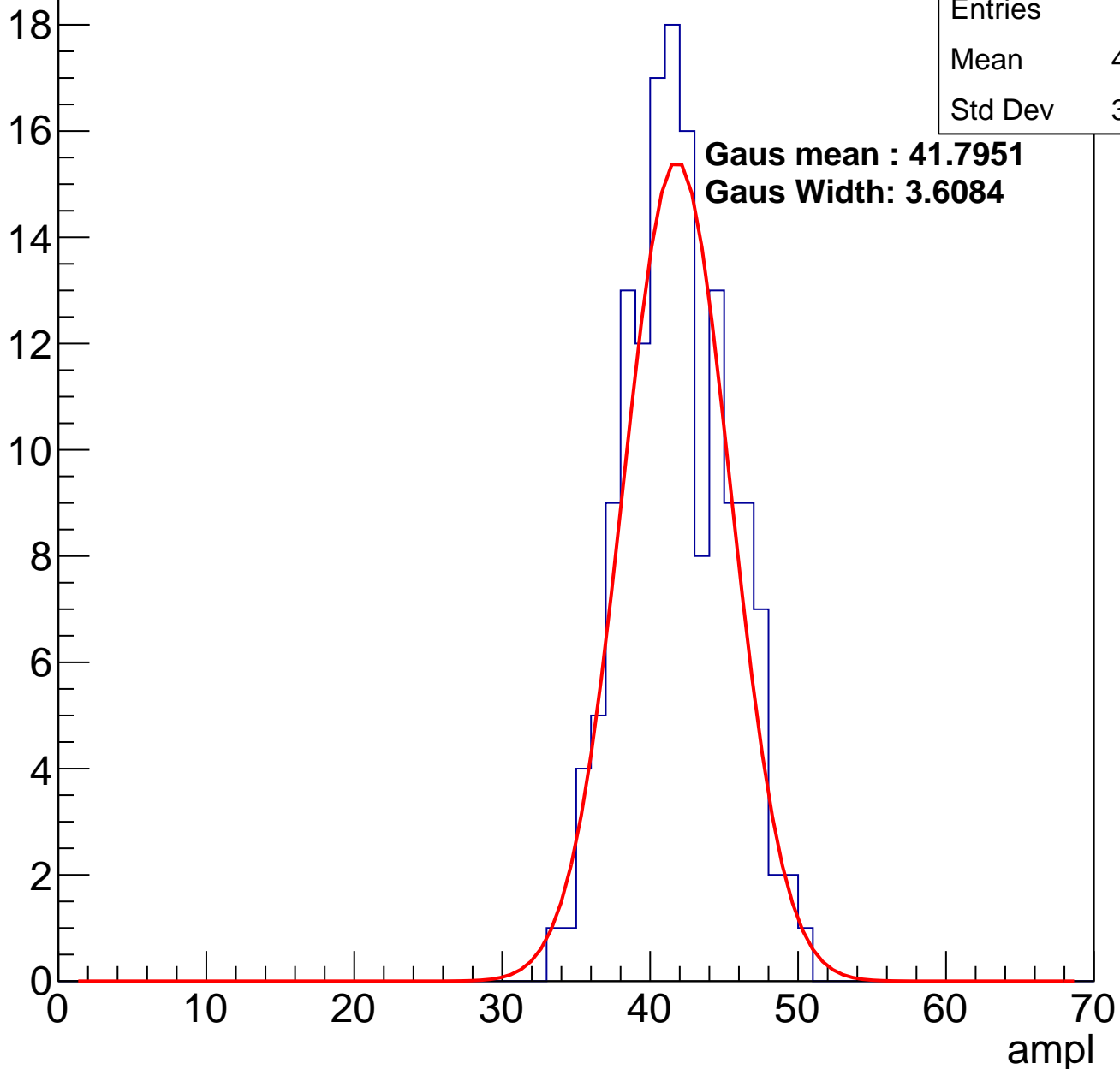
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	41.36
Std Dev	3.482

Gaus mean : 41.7951

Gaus Width: 3.6084

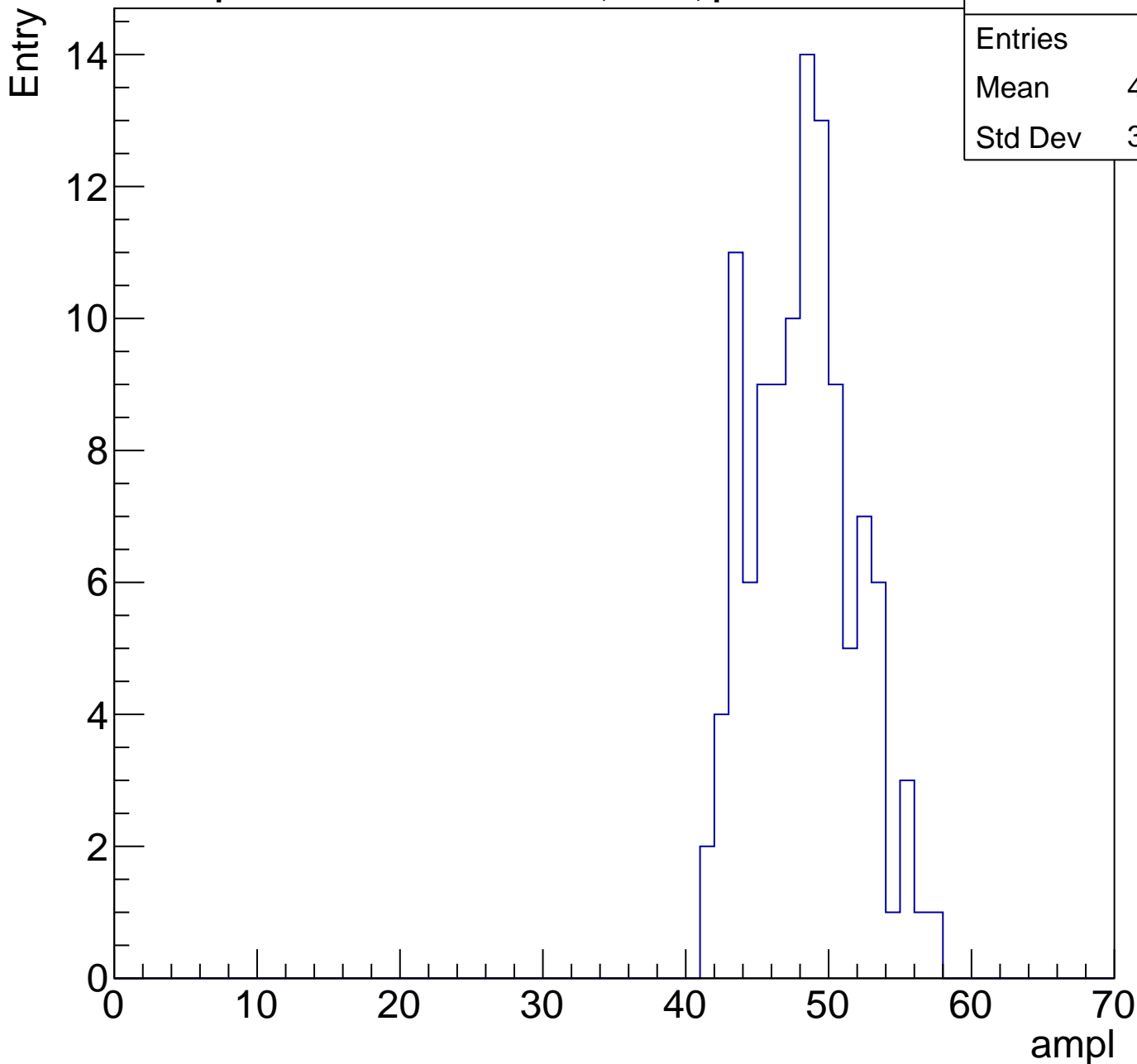
Entry



B1L001S, U20-ch76, adc3

calib_packv5_042523_0143.root, FC#2, port C2

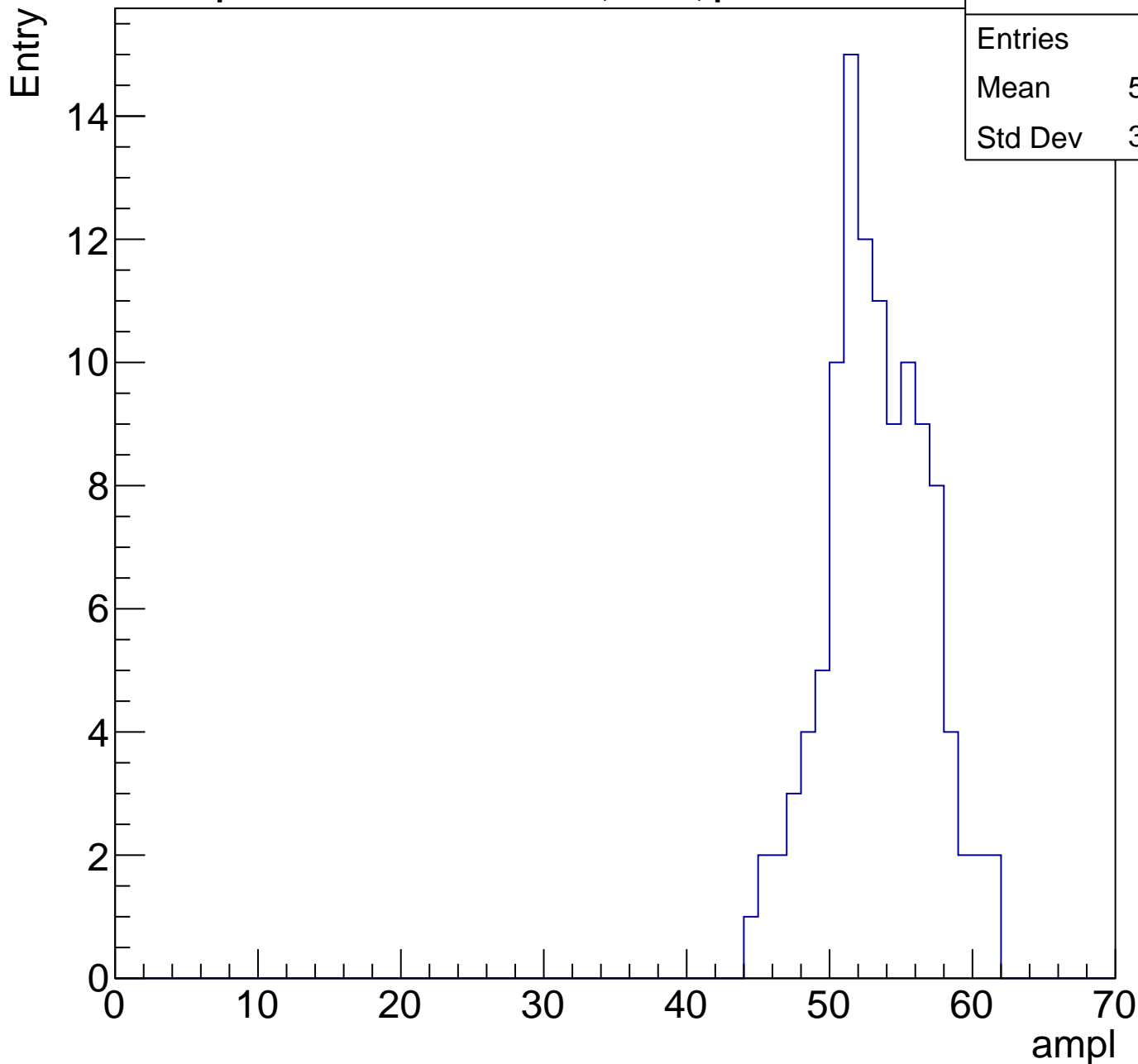
Entries	111
Mean	47.78
Std Dev	3.575



B1L001S, U20-ch76, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	52.83
Std Dev	3.564



B1L001S, U20-ch76, adc5

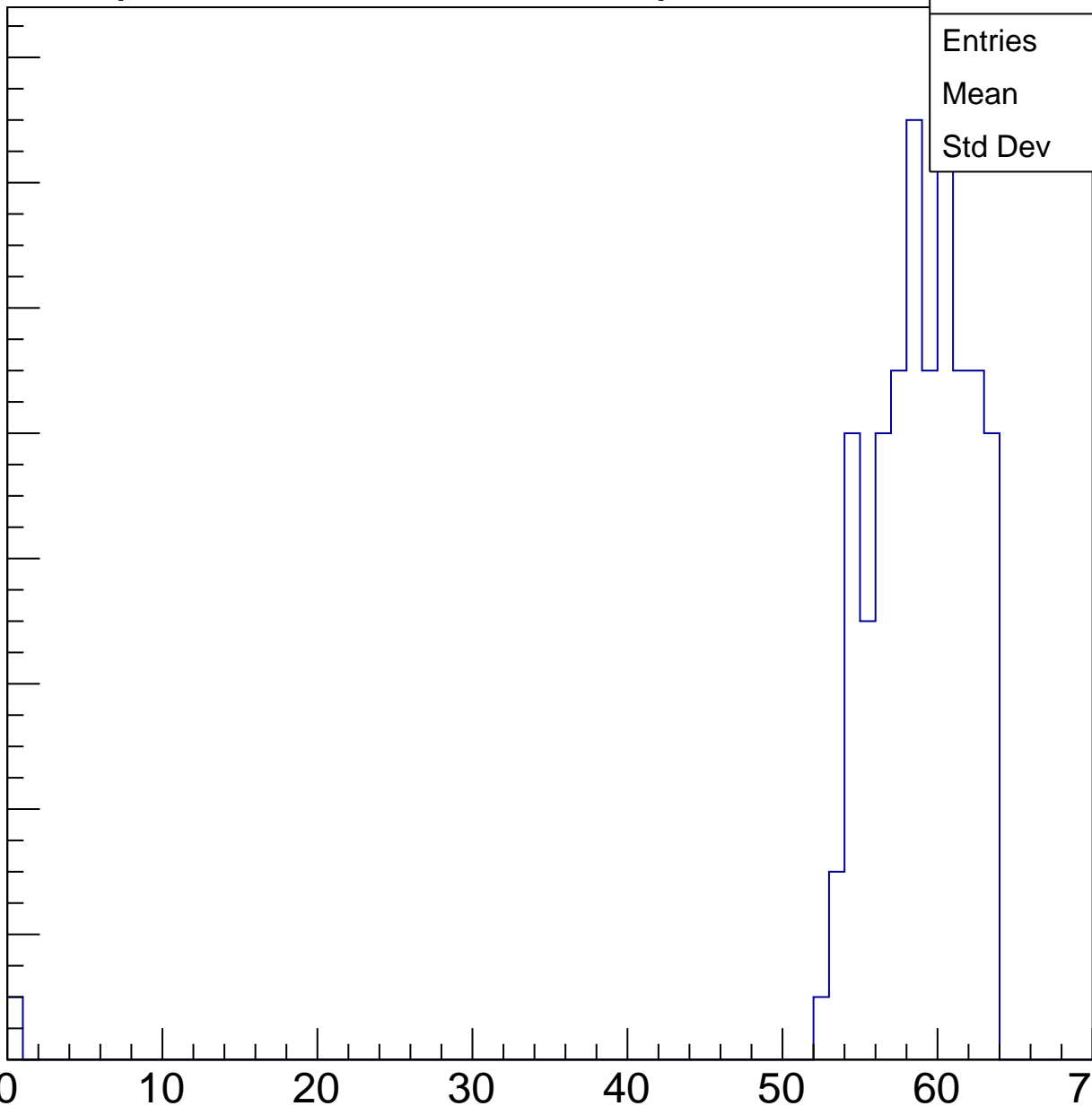
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	117
Mean	57.99
Std Dev	6.097

ampl

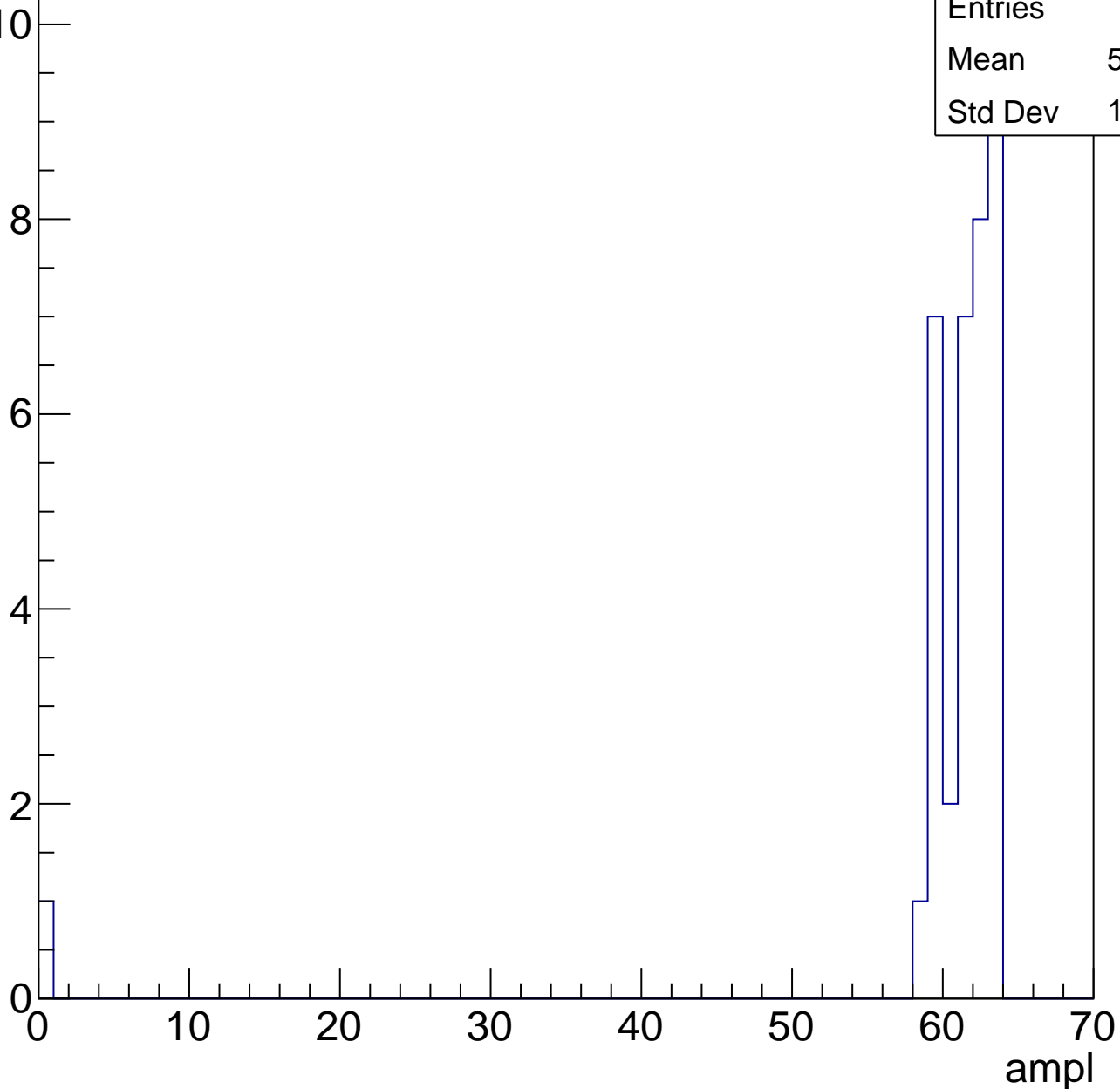


B1L001S, U20-ch76, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

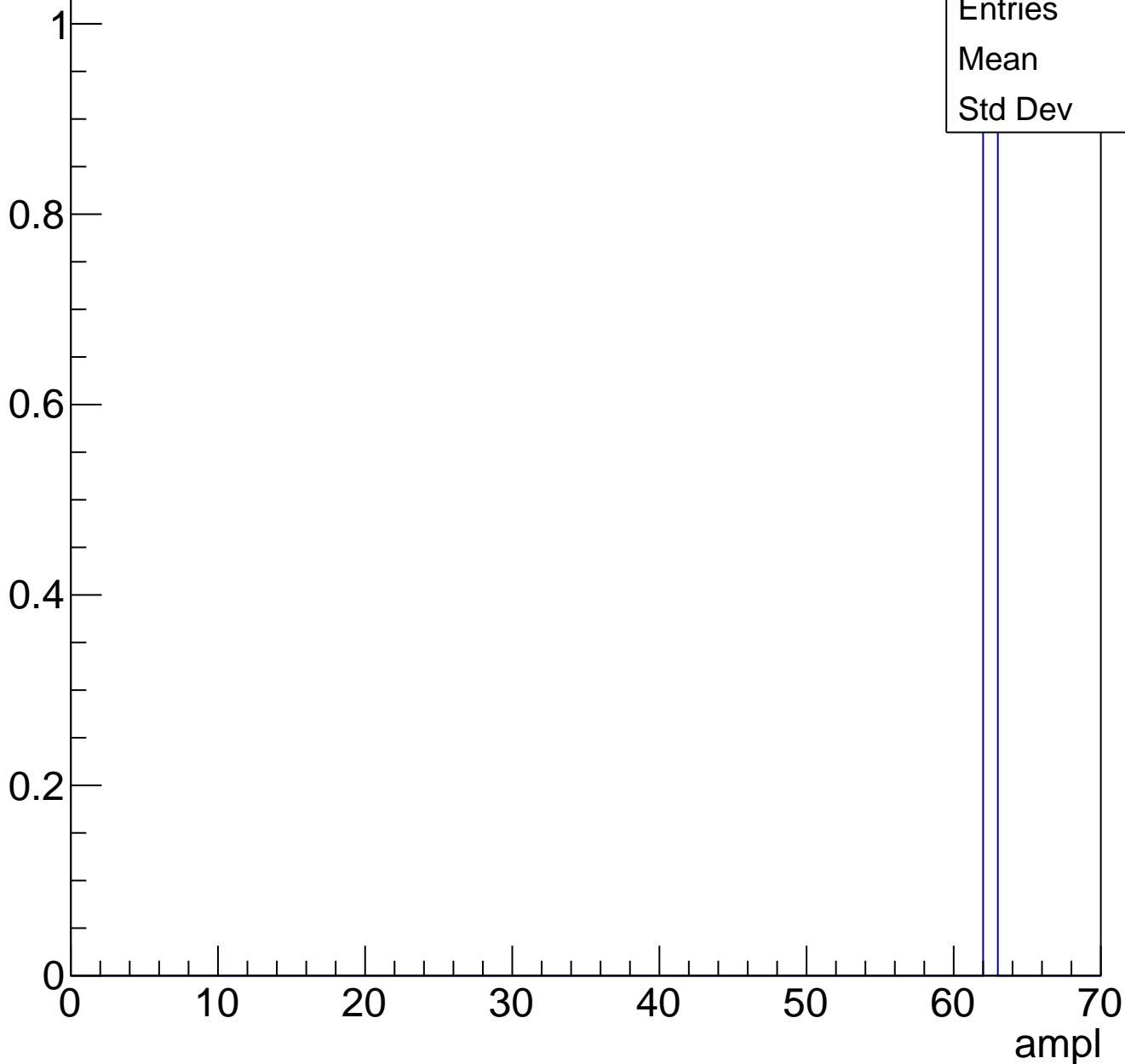
Entries	36
Mean	59.56
Std Dev	10.18



B1L001S, U20-ch76, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch77, adc0

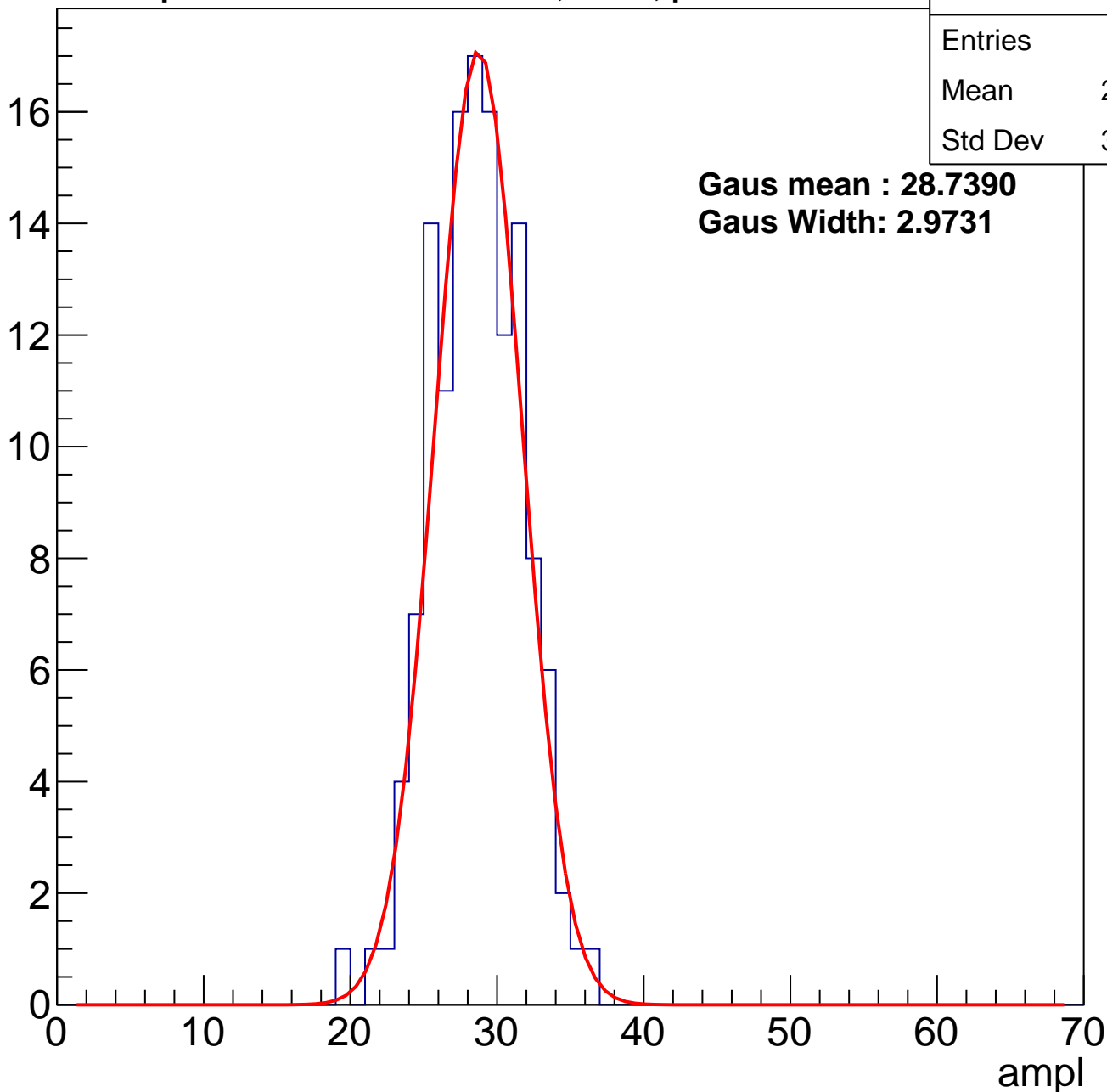
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	28.16
Std Dev	3.032

Gaus mean : 28.7390

Gaus Width: 2.9731

Entry



B1L001S, U20-ch77, adc1

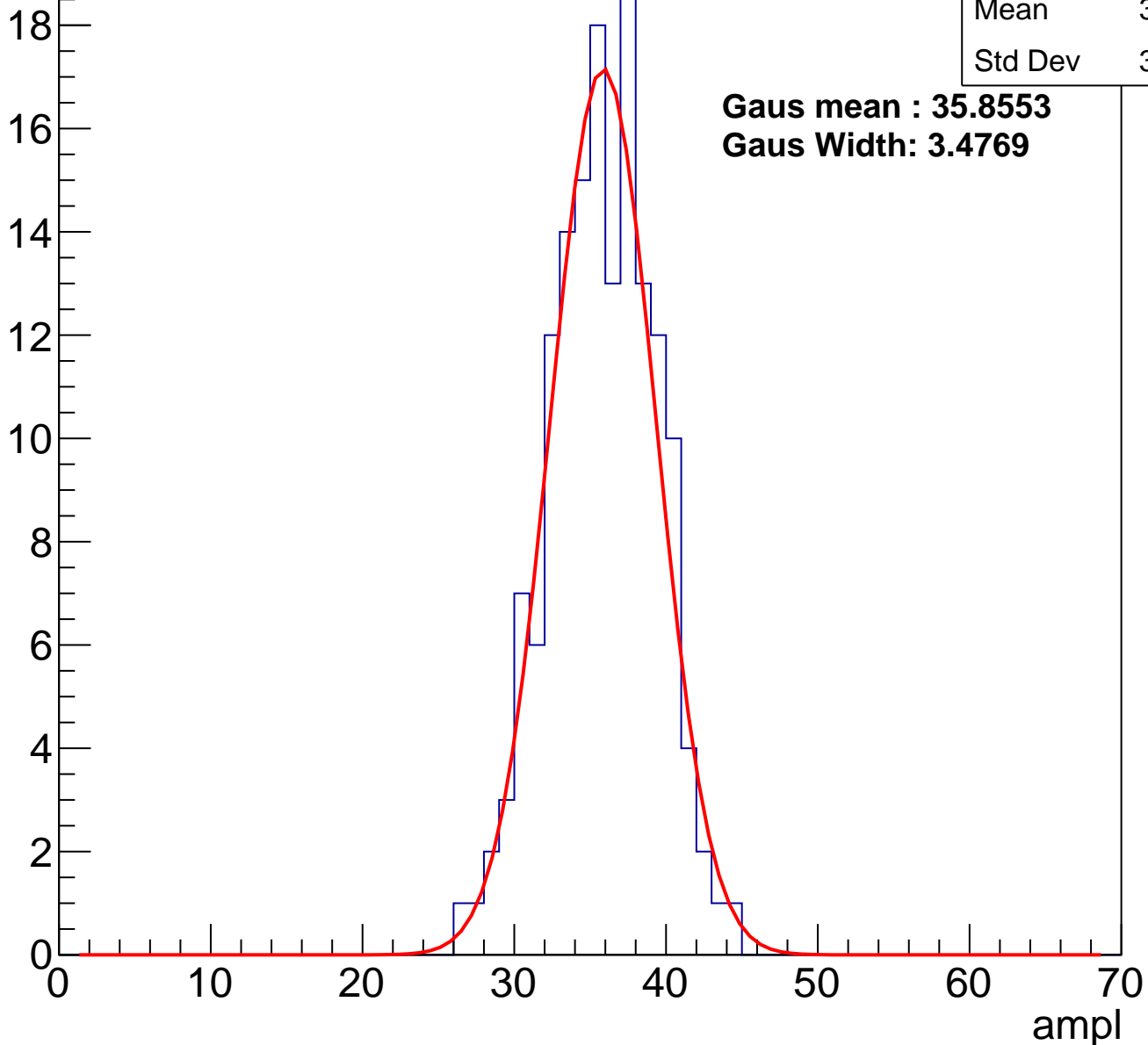
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	35.36
Std Dev	3.434

Gaus mean : 35.8553

Gaus Width: 3.4769

Entry



B1L001S, U20-ch77, adc2

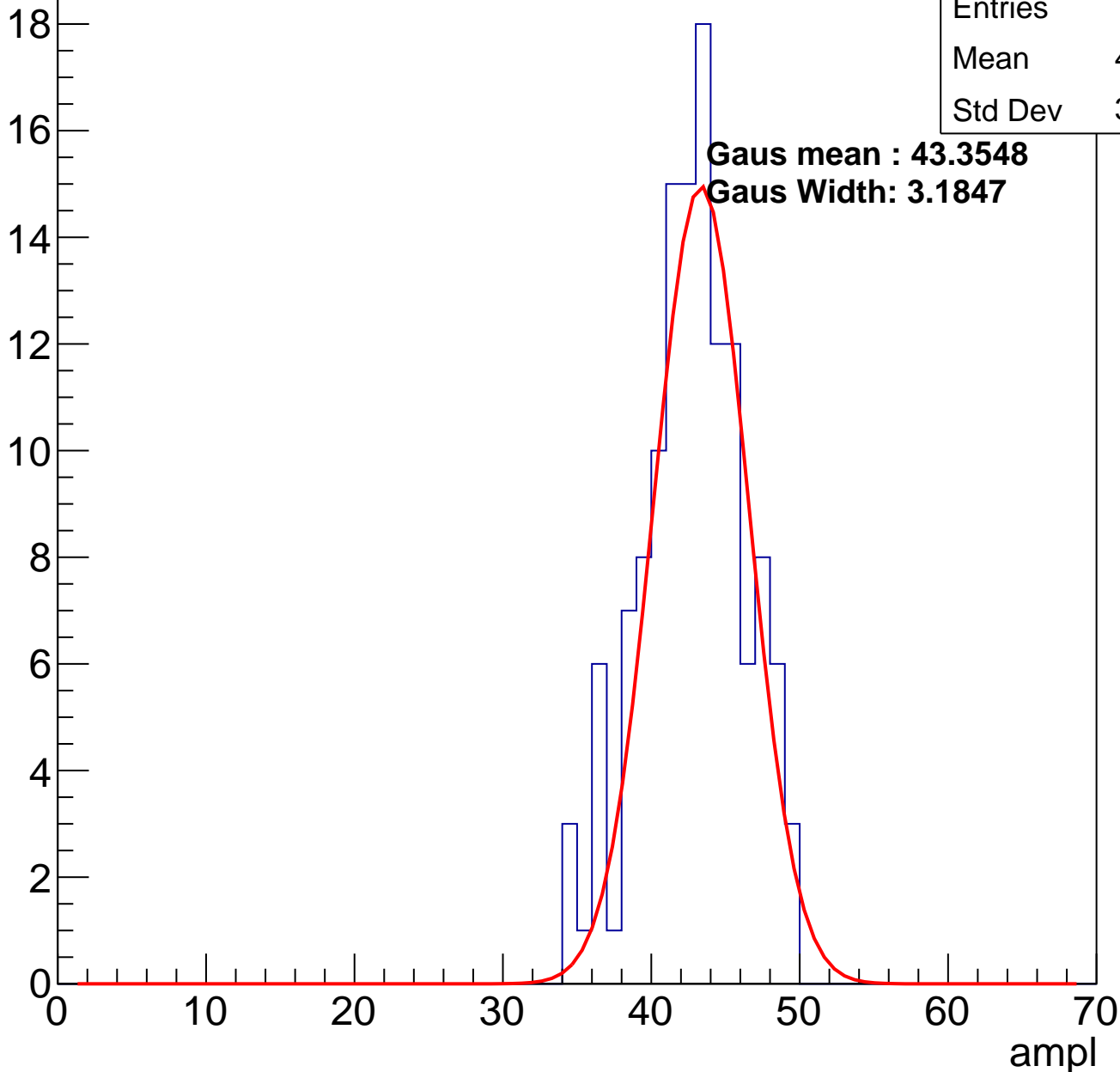
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	42.31
Std Dev	3.431

Gaus mean : 43.3548

Gaus Width: 3.1847

Entry



B1L001S, U20-ch77, adc3

calib_packv5_042523_0143.root, FC#2, port C2

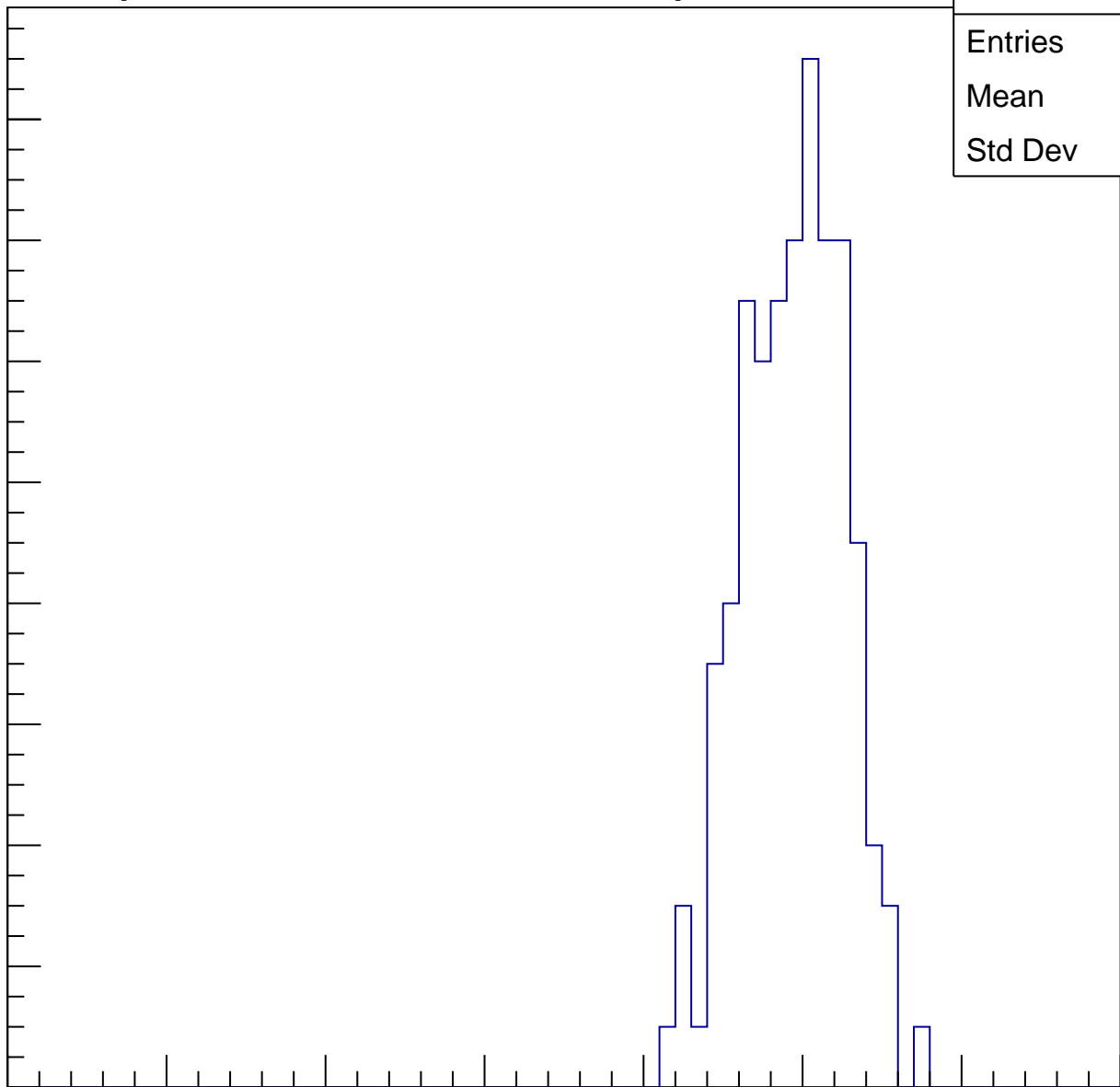
Entries	134
Mean	48.93
Std Dev	3.165

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch77, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

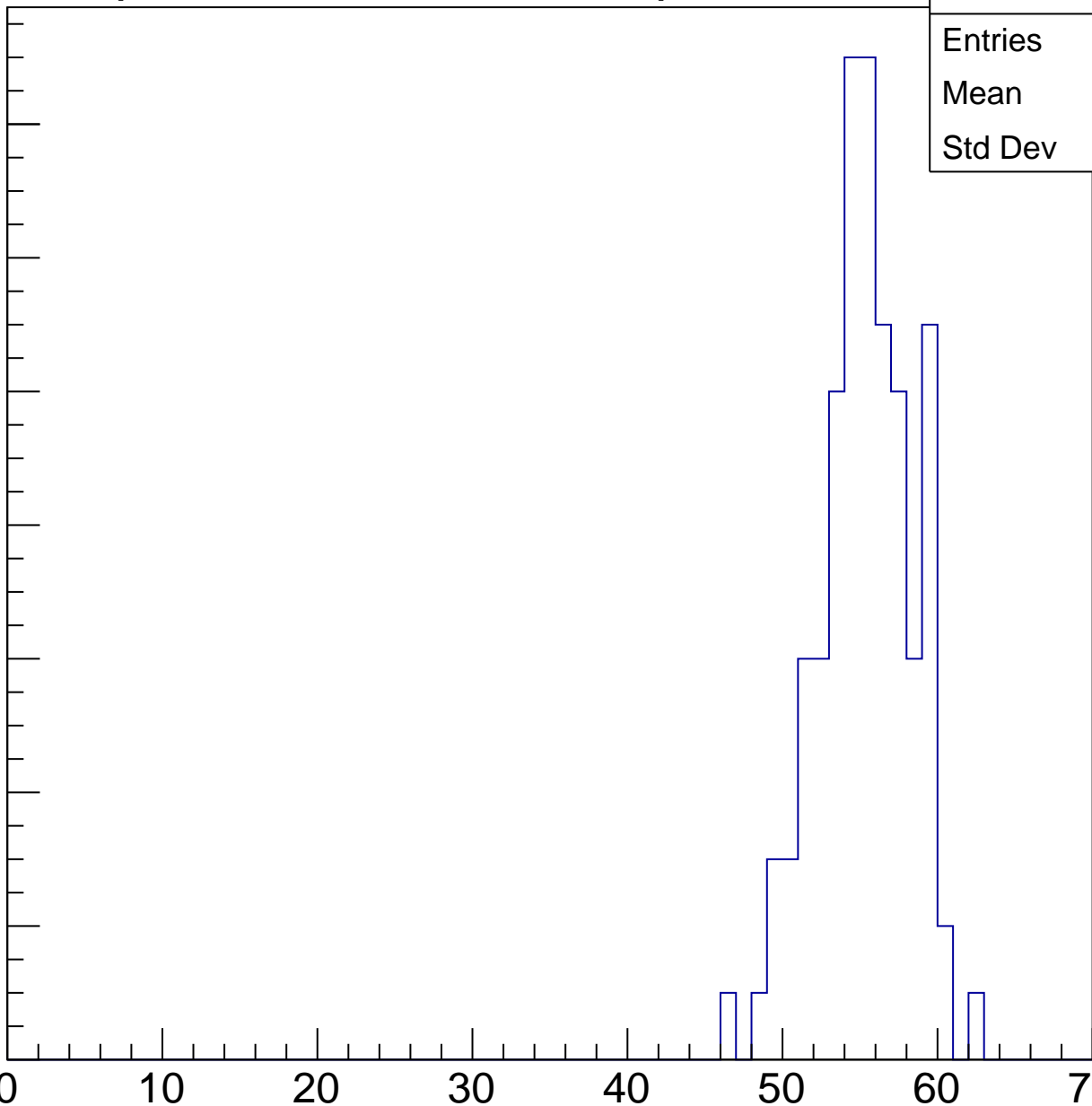
0

Entries 101

Mean 54.84

Std Dev 2.978

ampl



B1L001S, U20-ch77, adc5

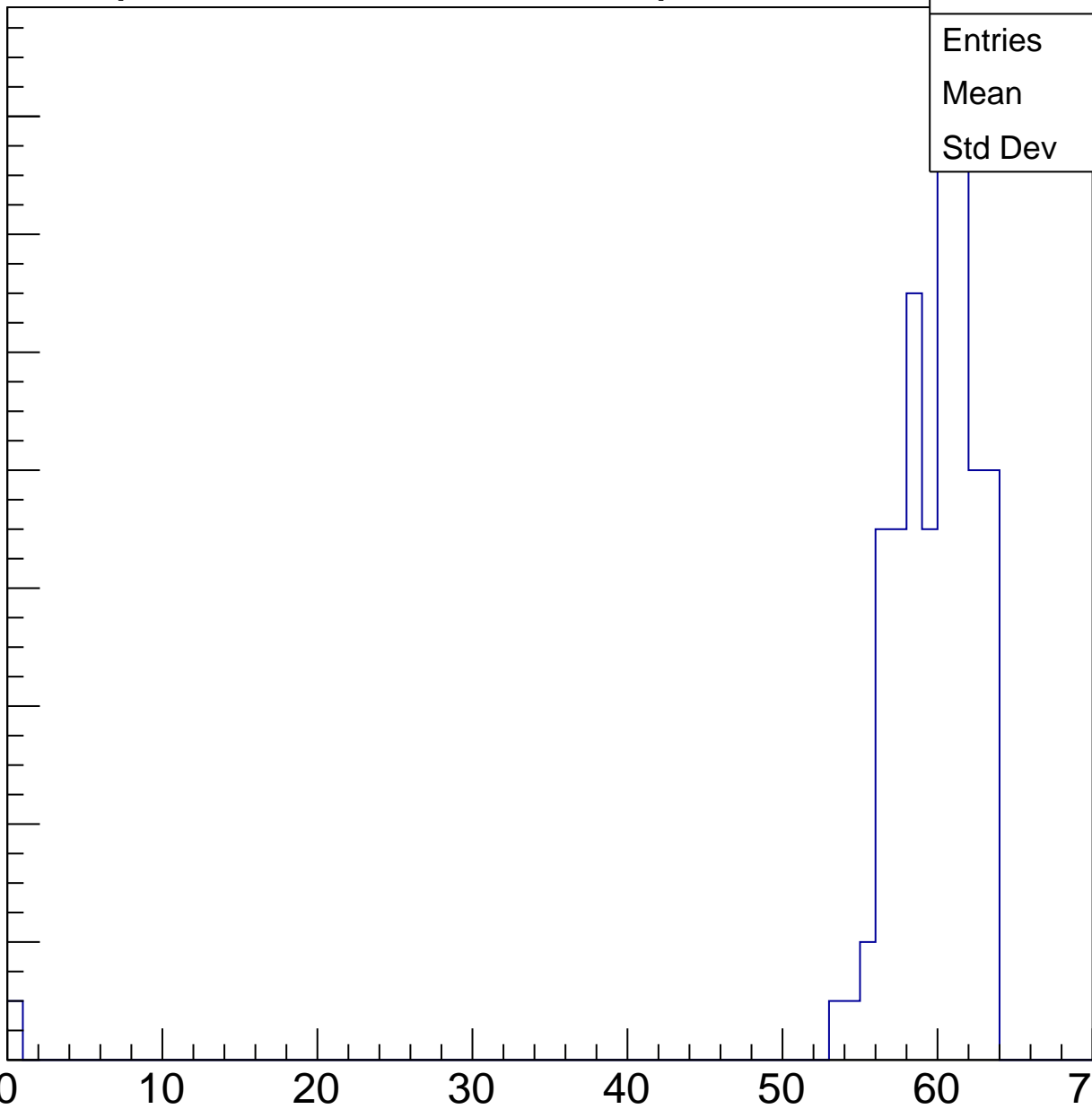
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.83
Std Dev	6.414

ampl

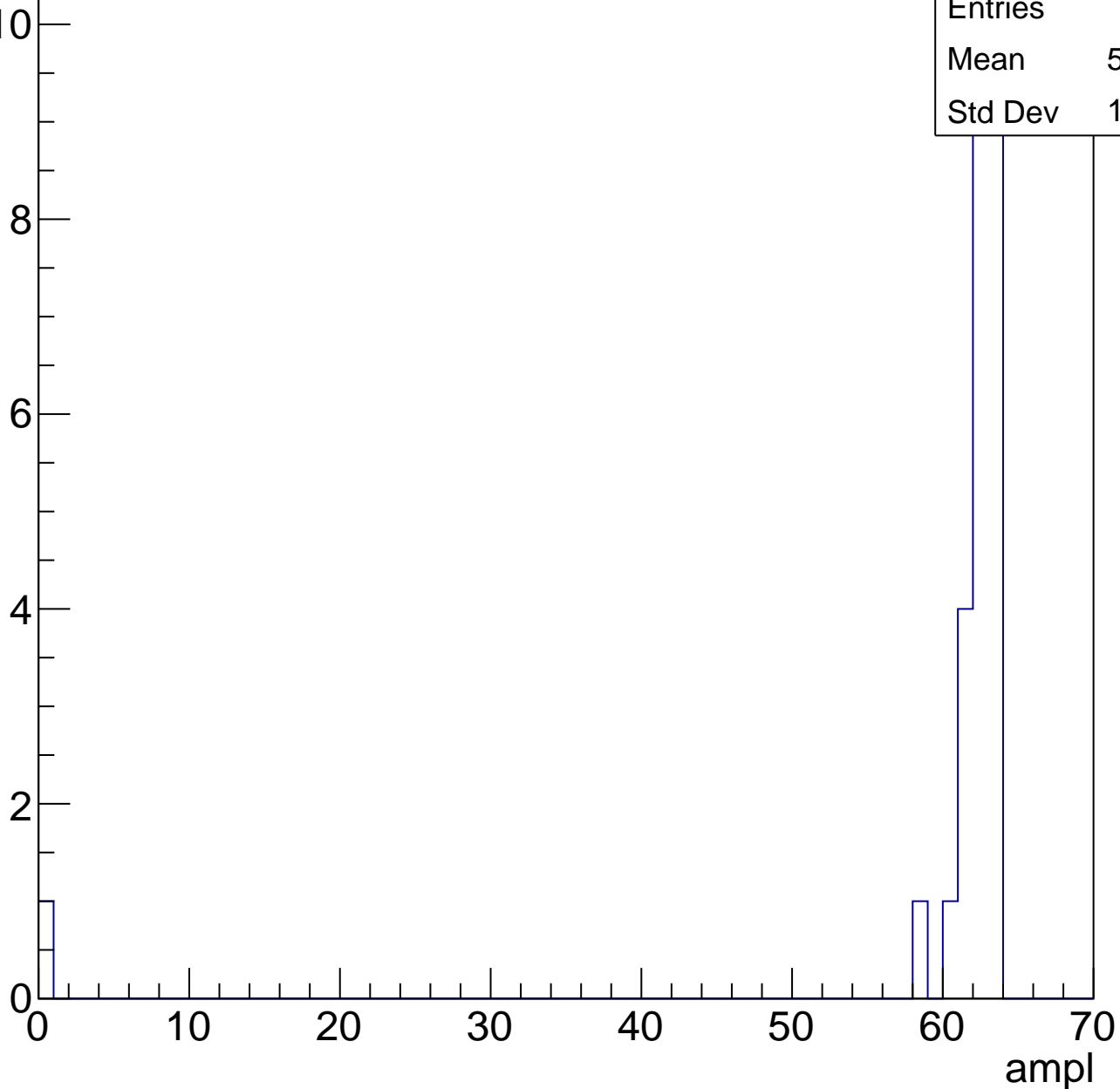


B1L001S, U20-ch77, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	59.62
Std Dev	11.98



B1L001S, U20-ch77, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch78, adc0

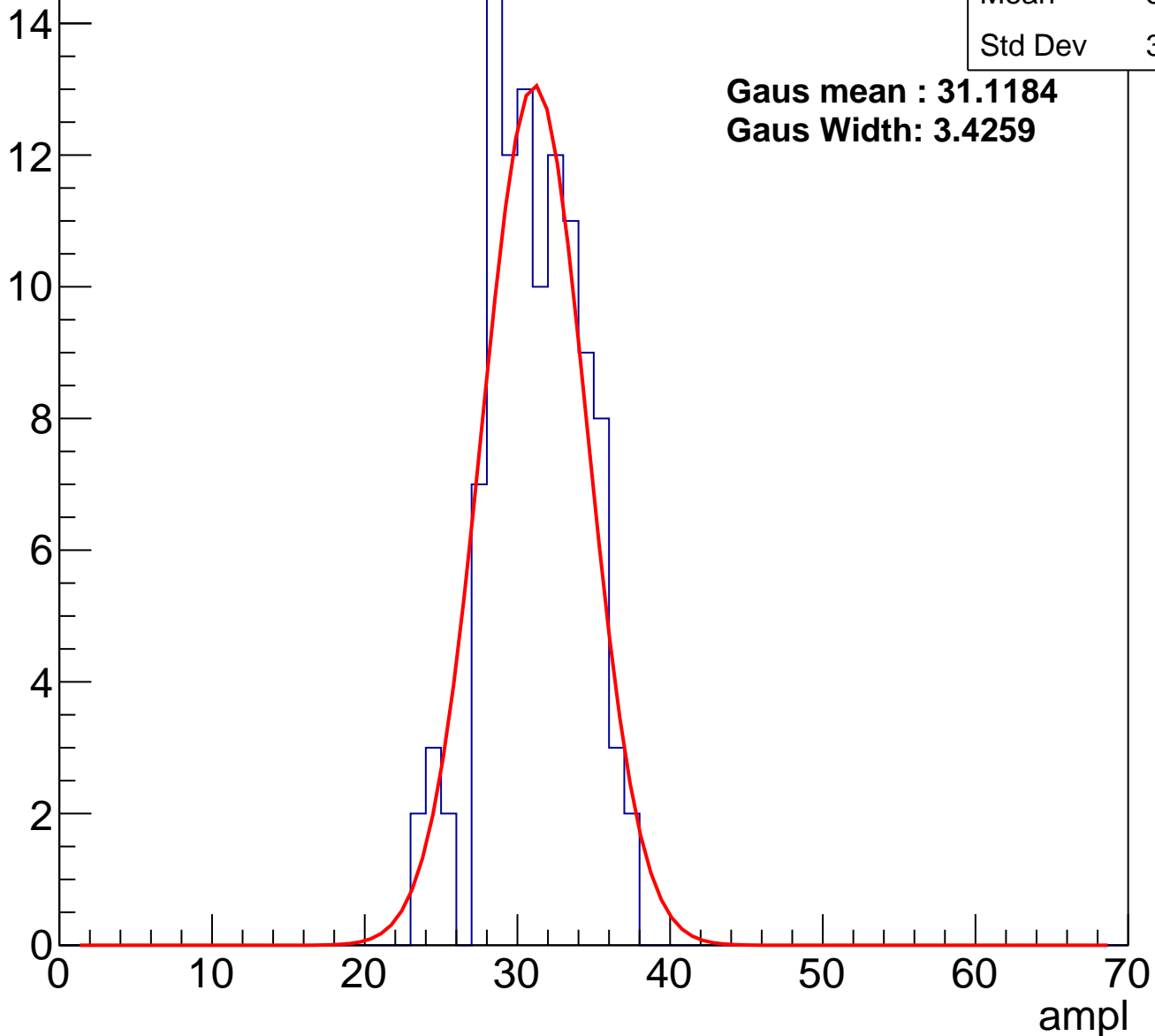
calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	30.64
Std Dev	3.108

Gaus mean : 31.1184

Gaus Width: 3.4259

Entry



B1L001S, U20-ch78, adc1

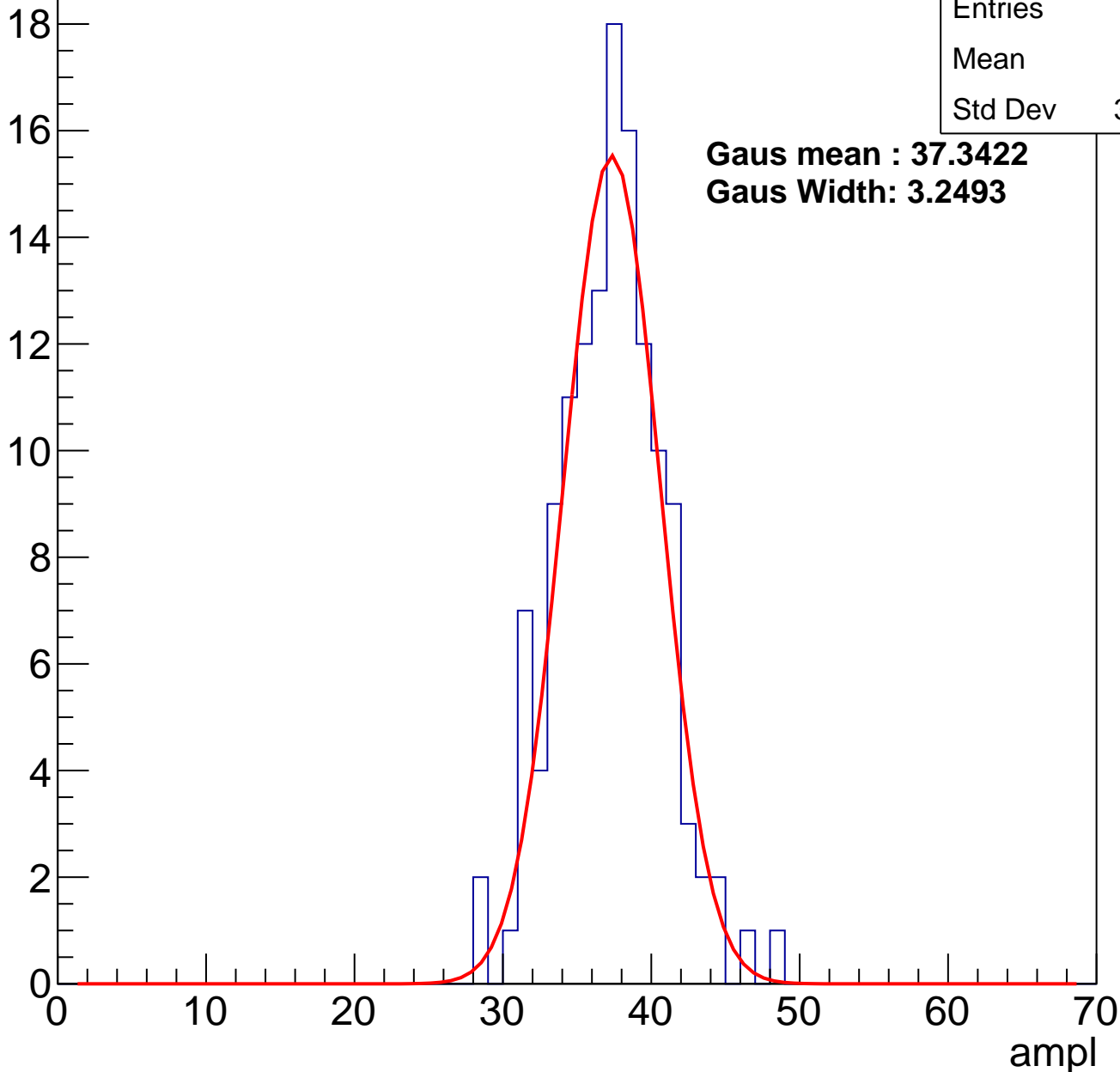
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.8
Std Dev	3.476

Gaus mean : 37.3422

Gaus Width: 3.2493

Entry



B1L001S, U20-ch78, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

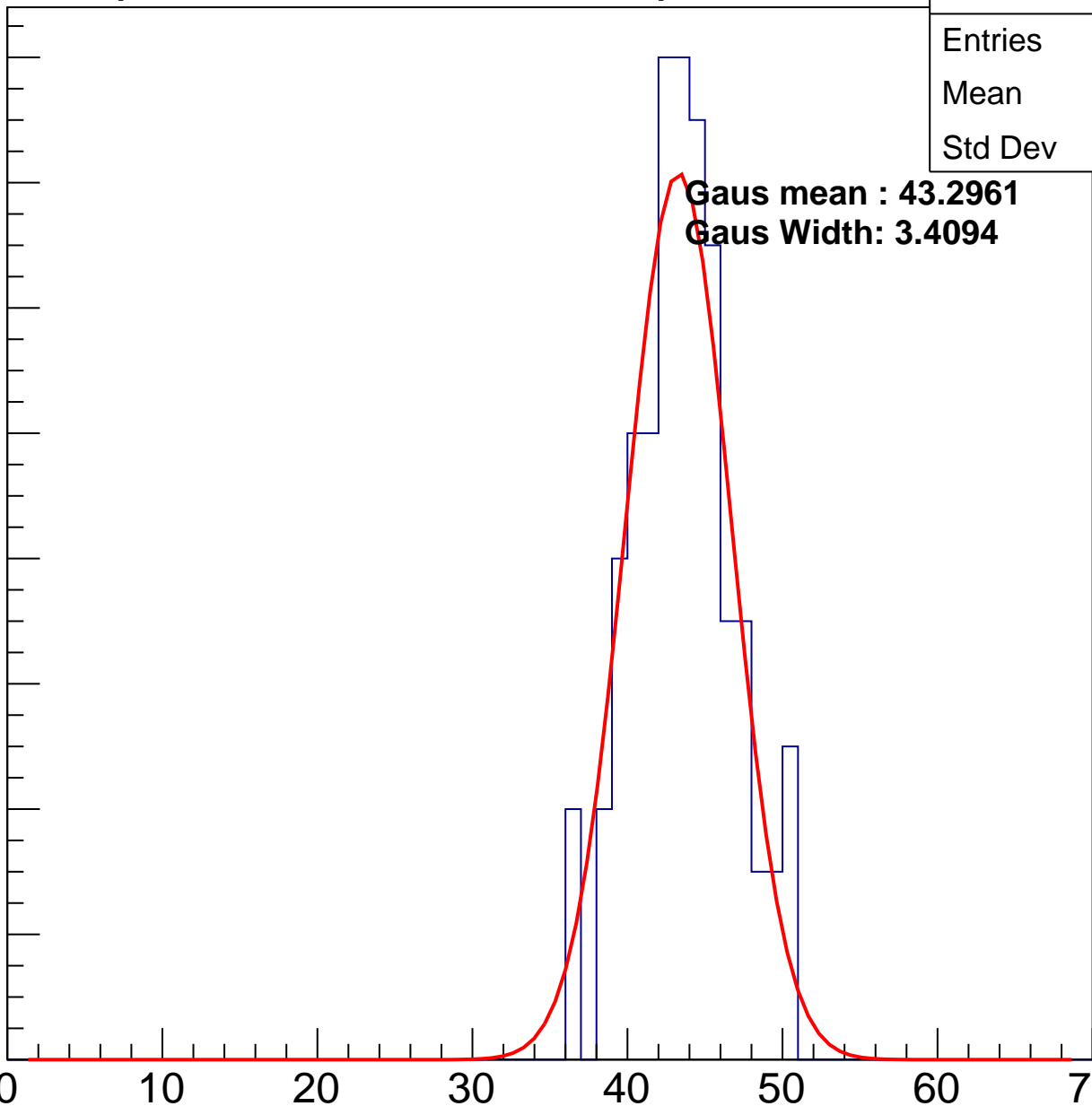
16
14
12
10
8
6
4
2
0

Entries	121
Mean	43.1
Std Dev	3.2

Gaus mean : 43.2961

Gaus Width: 3.4094

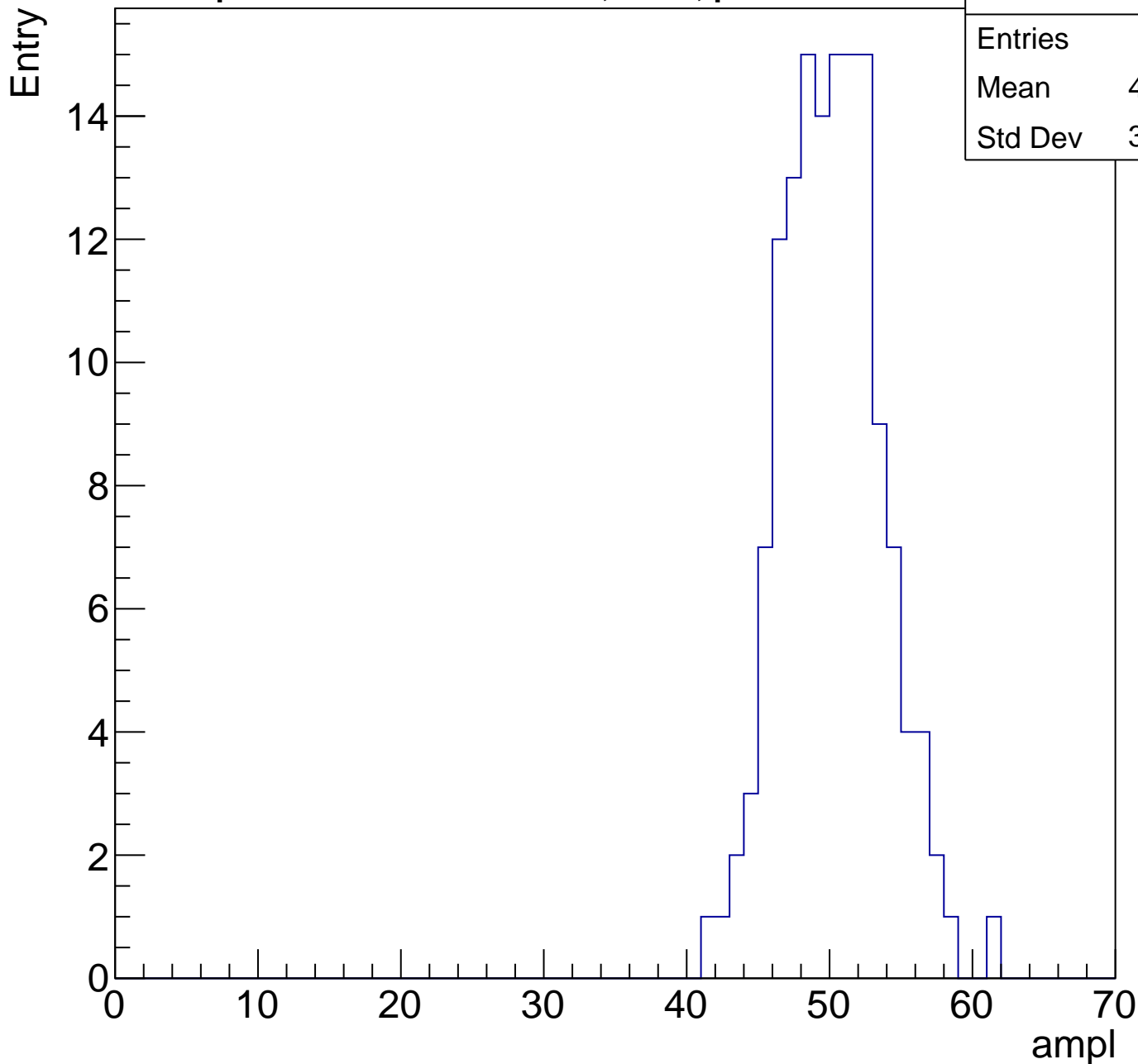
ampl



B1L001S, U20-ch78, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	49.73
Std Dev	3.466



B1L001S, U20-ch78, adc4

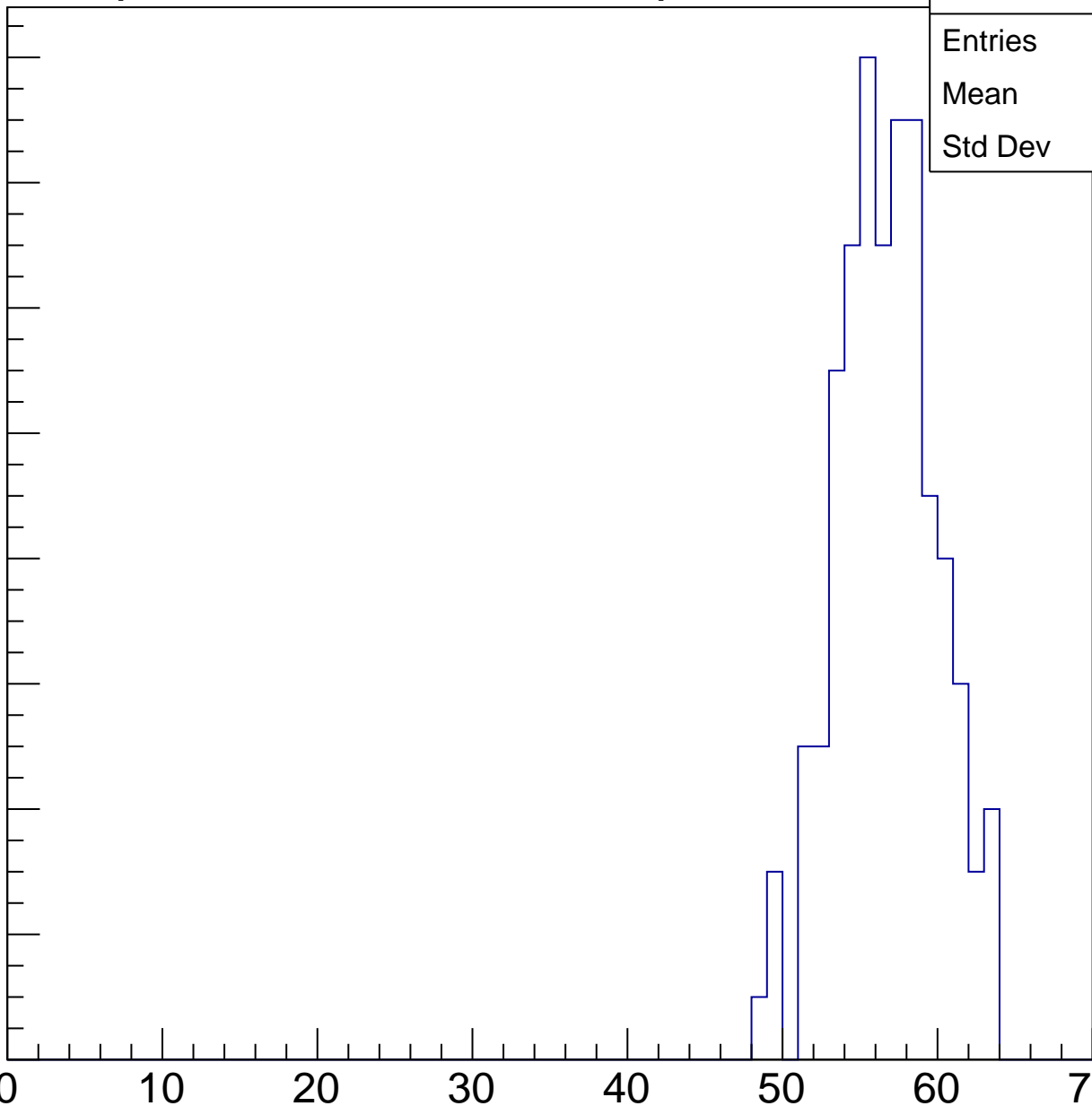
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	127
Mean	56.24
Std Dev	3.216

ampl



B1L001S, U20-ch78, adc5

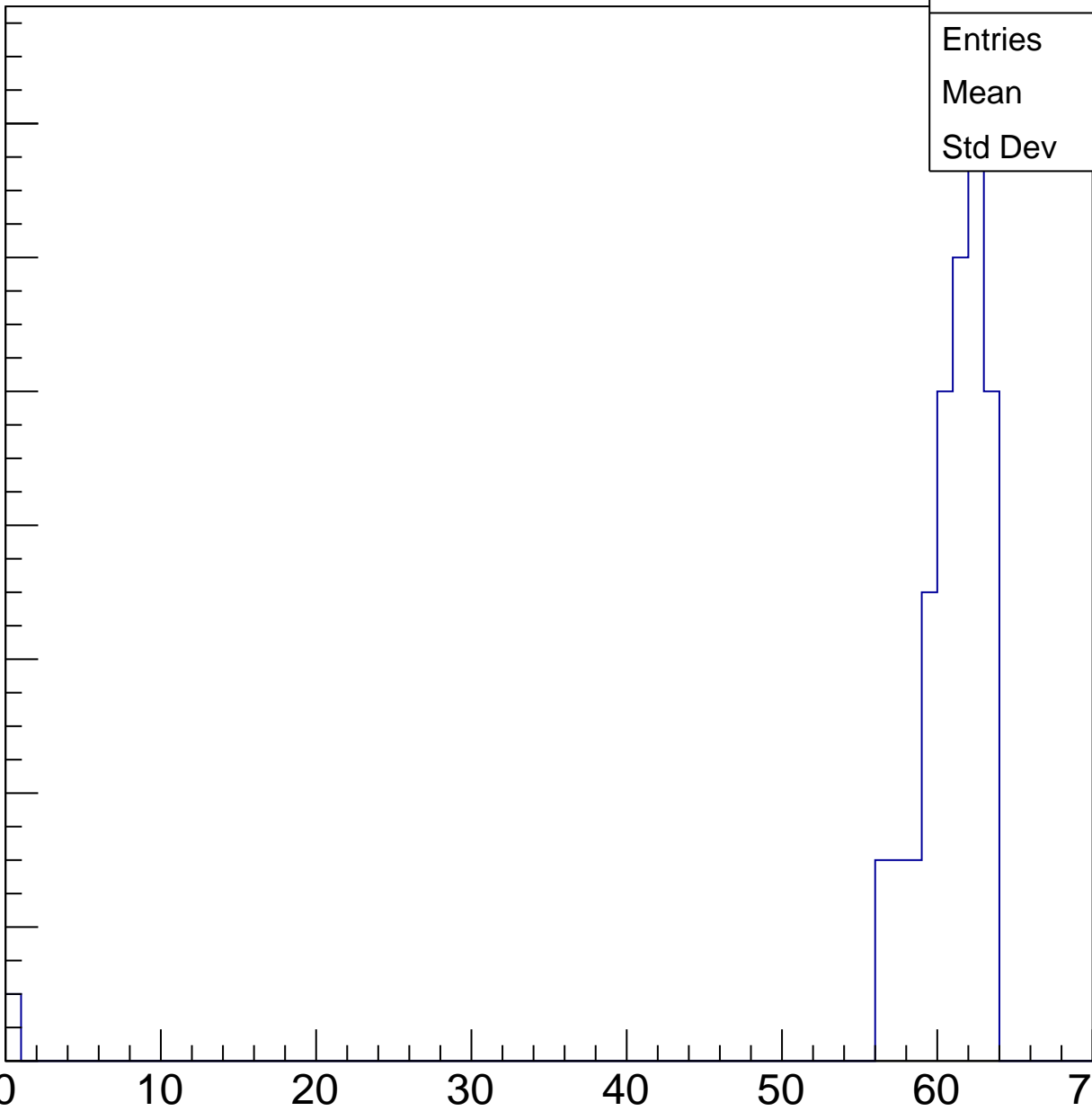
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	64
Mean	59.66
Std Dev	7.754

ampl

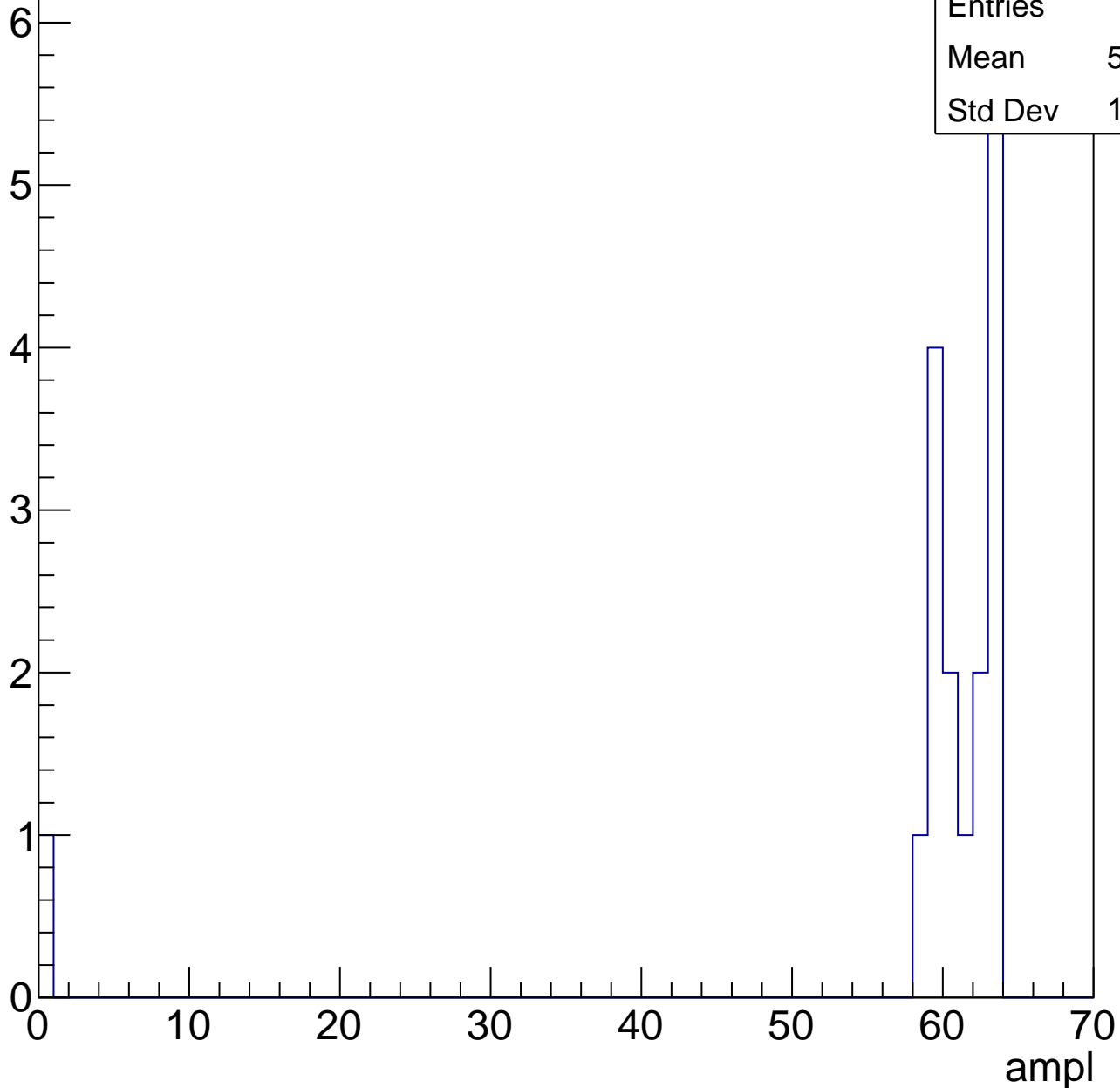


B1L001S, U20-ch78, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	57.47
Std Dev	14.48



B1L001S, U20-ch78, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U20-ch79, adc0

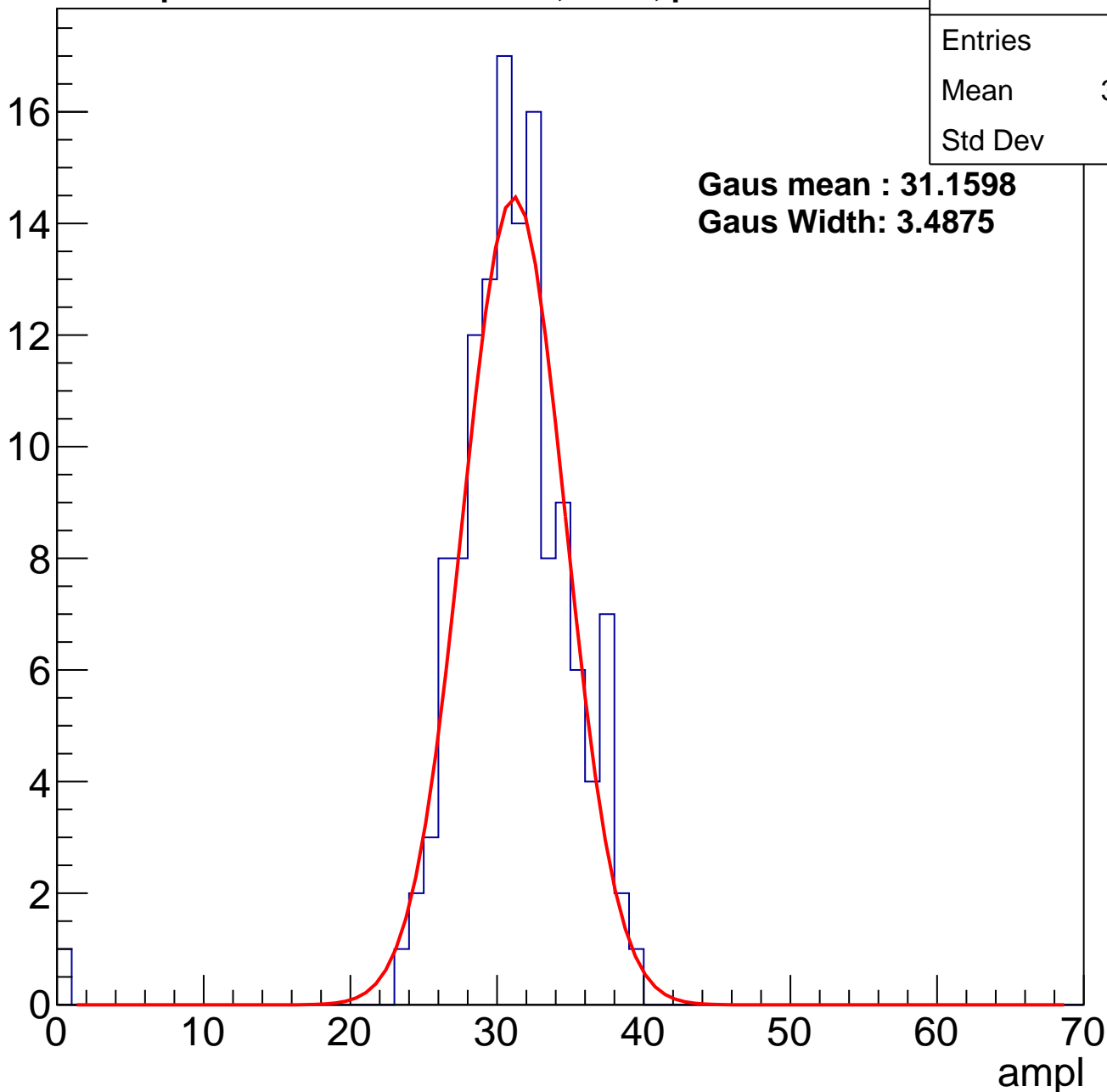
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	30.58
Std Dev	4.31

Gaus mean : 31.1598

Gaus Width: 3.4875

Entry



B1L001S, U20-ch79, adc1

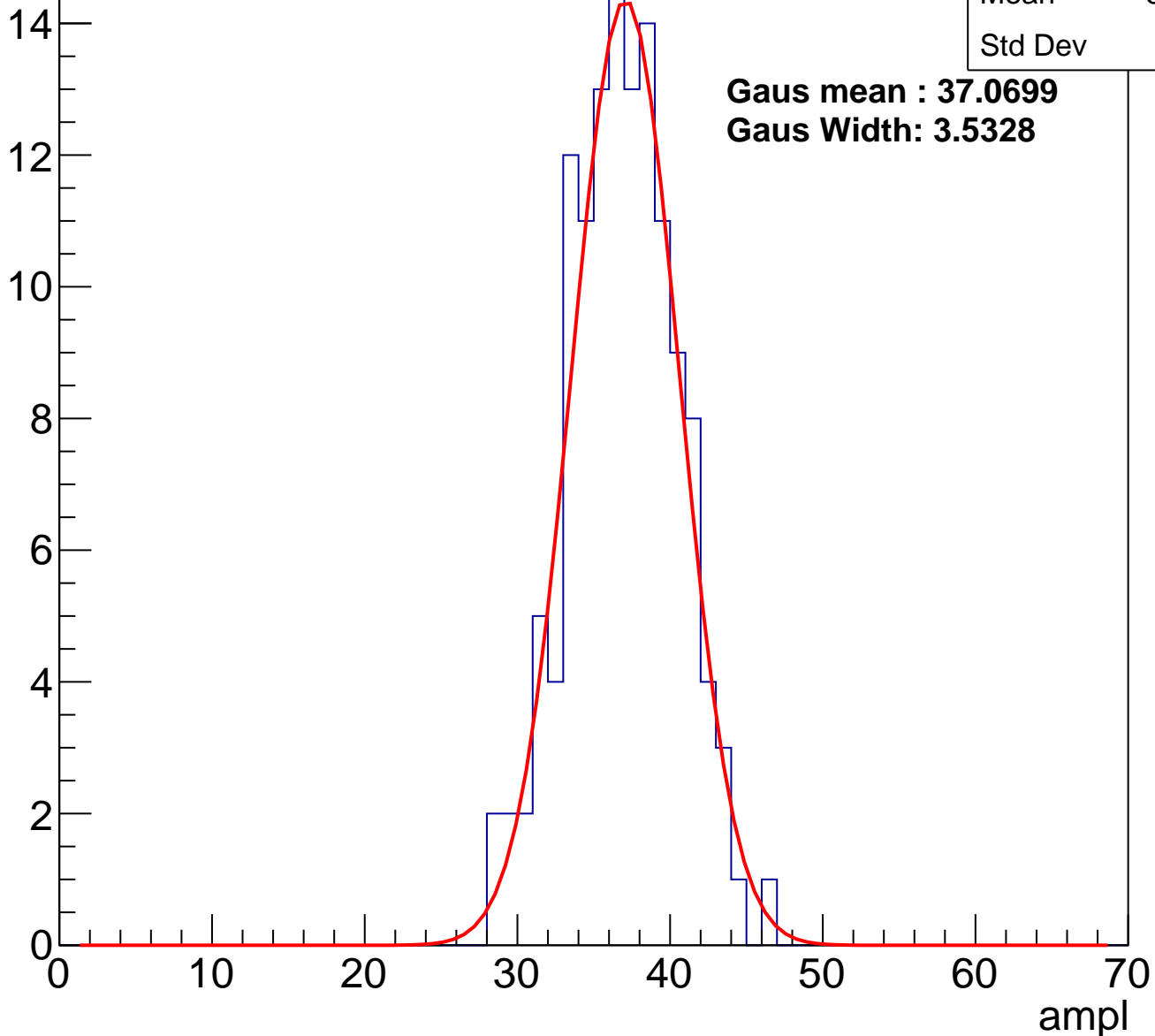
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	36.45
Std Dev	3.5

Gaus mean : 37.0699

Gaus Width: 3.5328

Entry



B1L001S, U20-ch79, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	43.08
Std Dev	3.069

Gaus mean : 43.2000

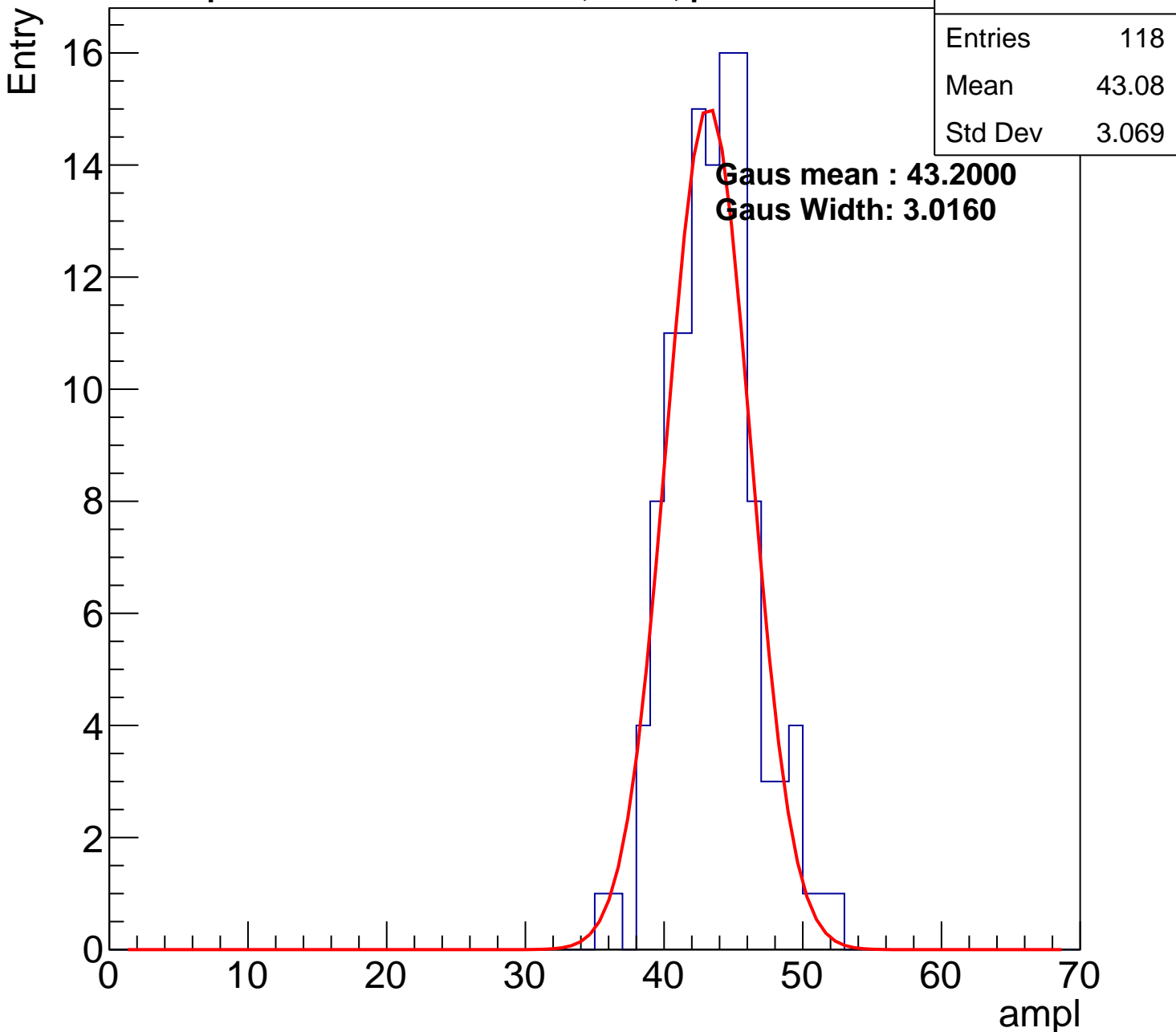
Gaus Width: 3.0160

Entry

16
14
12
10
8
6
4
2
0

ampl

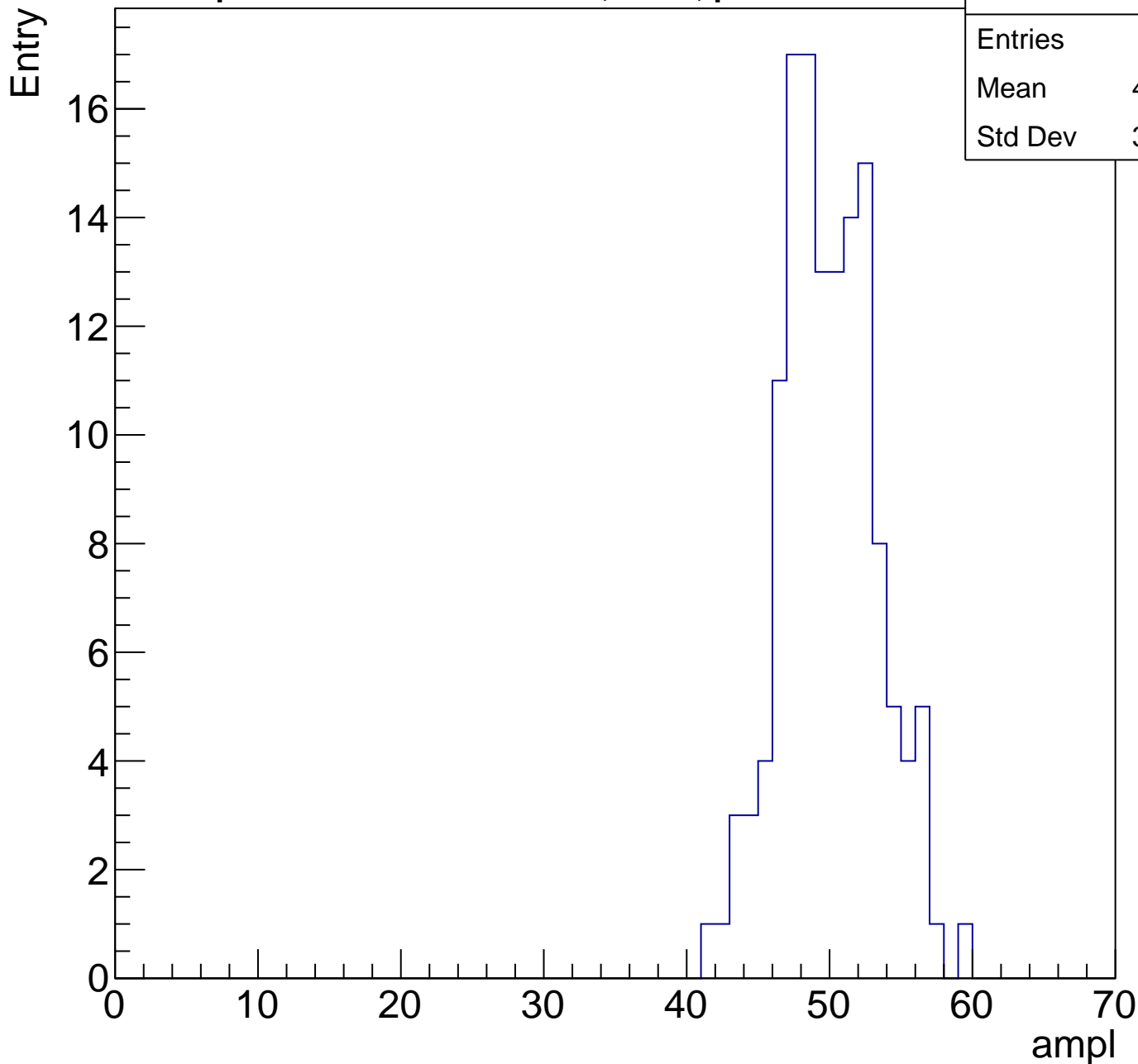
0 10 20 30 40 50 60 70



B1L001S, U20-ch79, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	49.53
Std Dev	3.339



B1L001S, U20-ch79, adc4

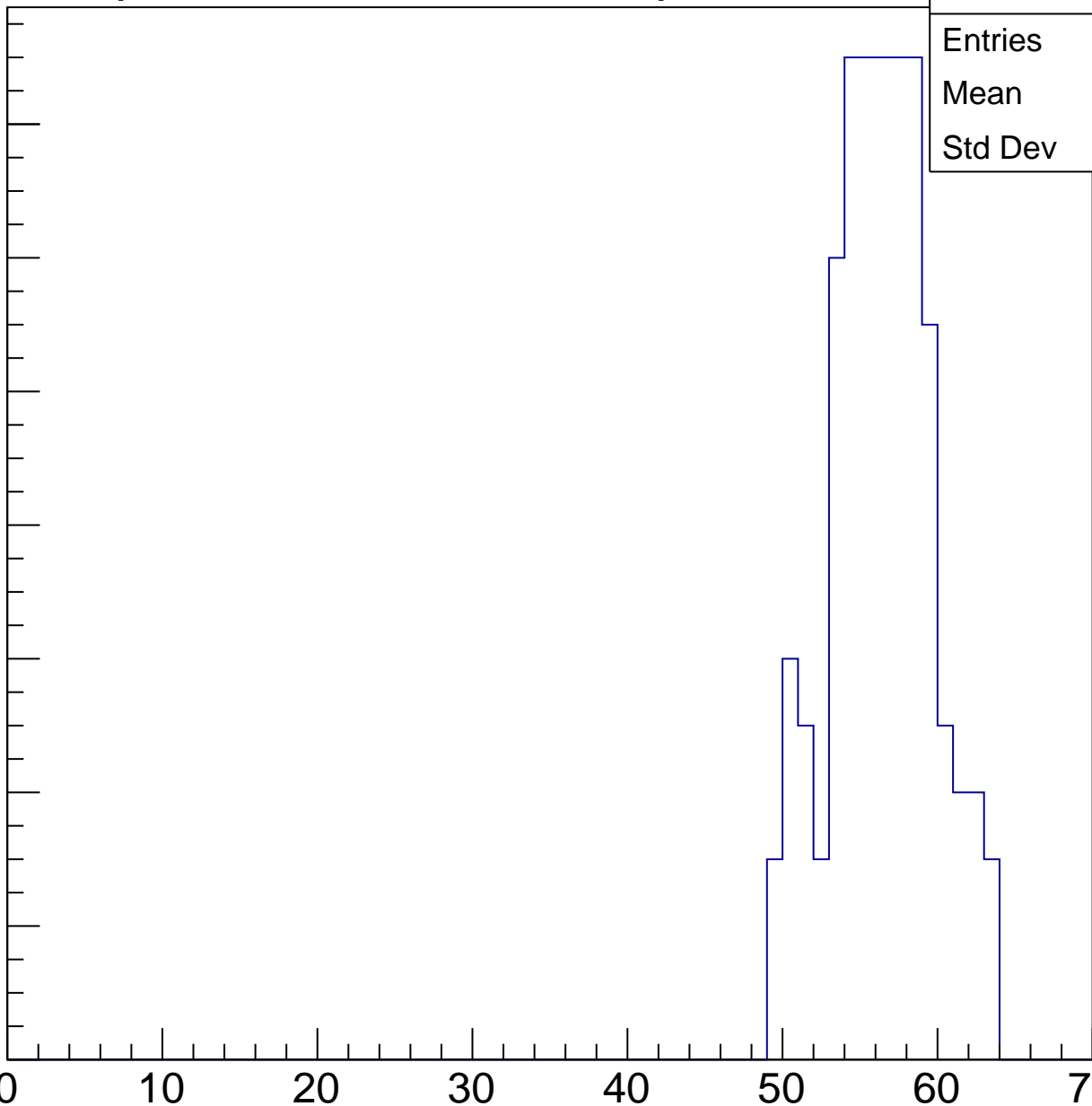
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	131
Mean	55.91
Std Dev	3.225

ampl



B1L001S, U20-ch79, adc5

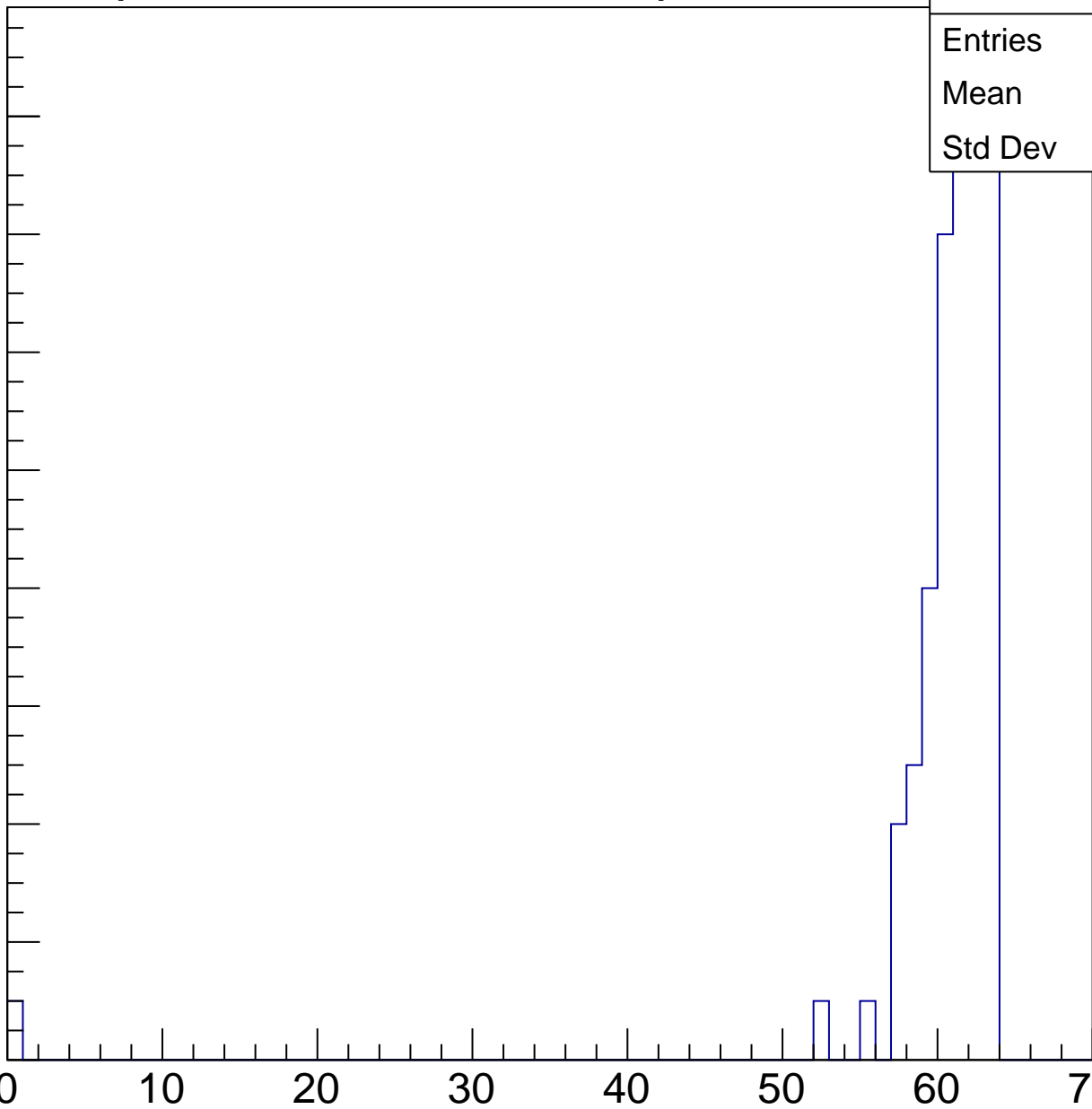
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59.95
Std Dev	6.93

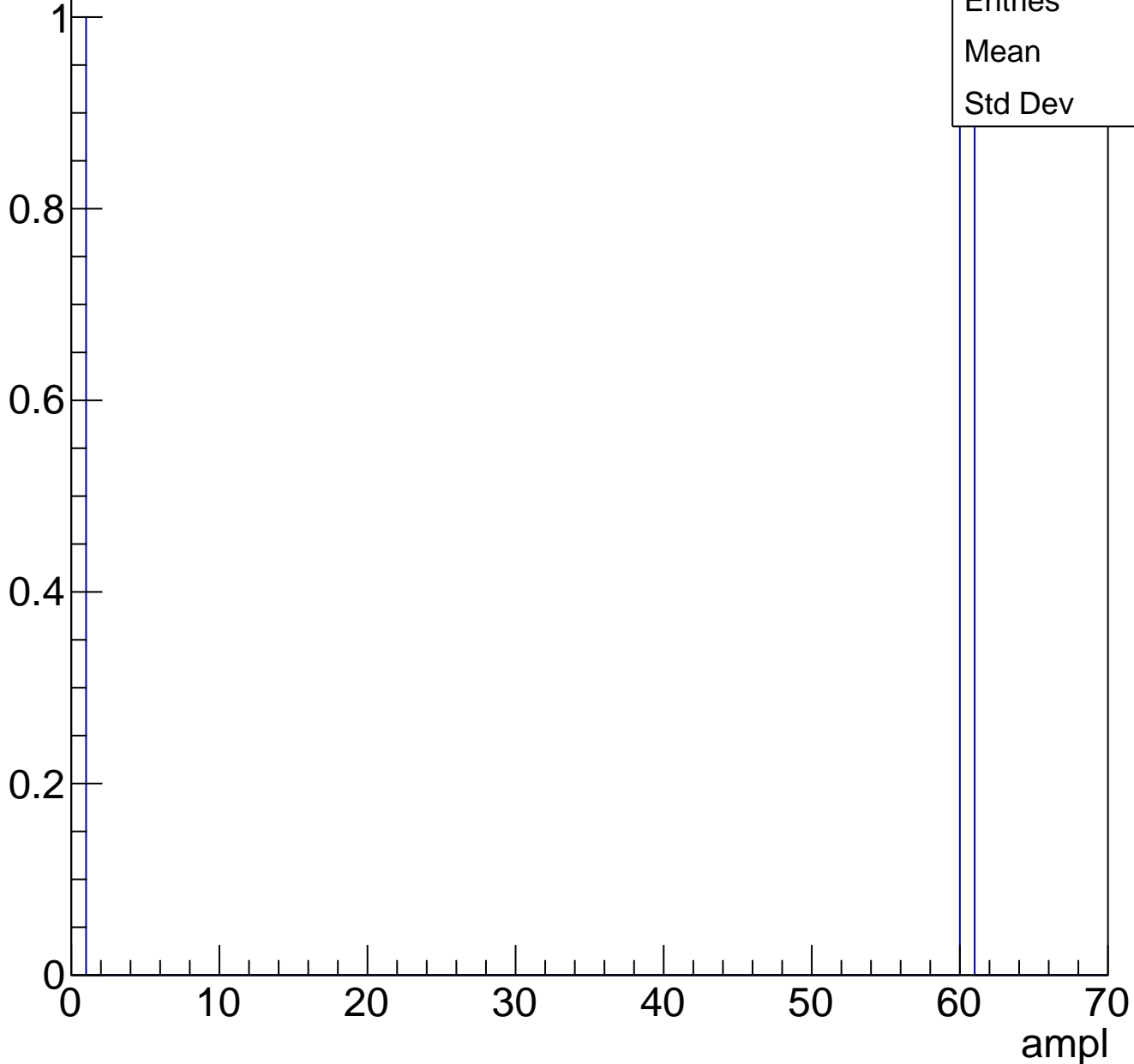
ampl



B1L001S, U20-ch79, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch79, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch80, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	28.72
Std Dev	3.277

Gaus mean : 29.6045

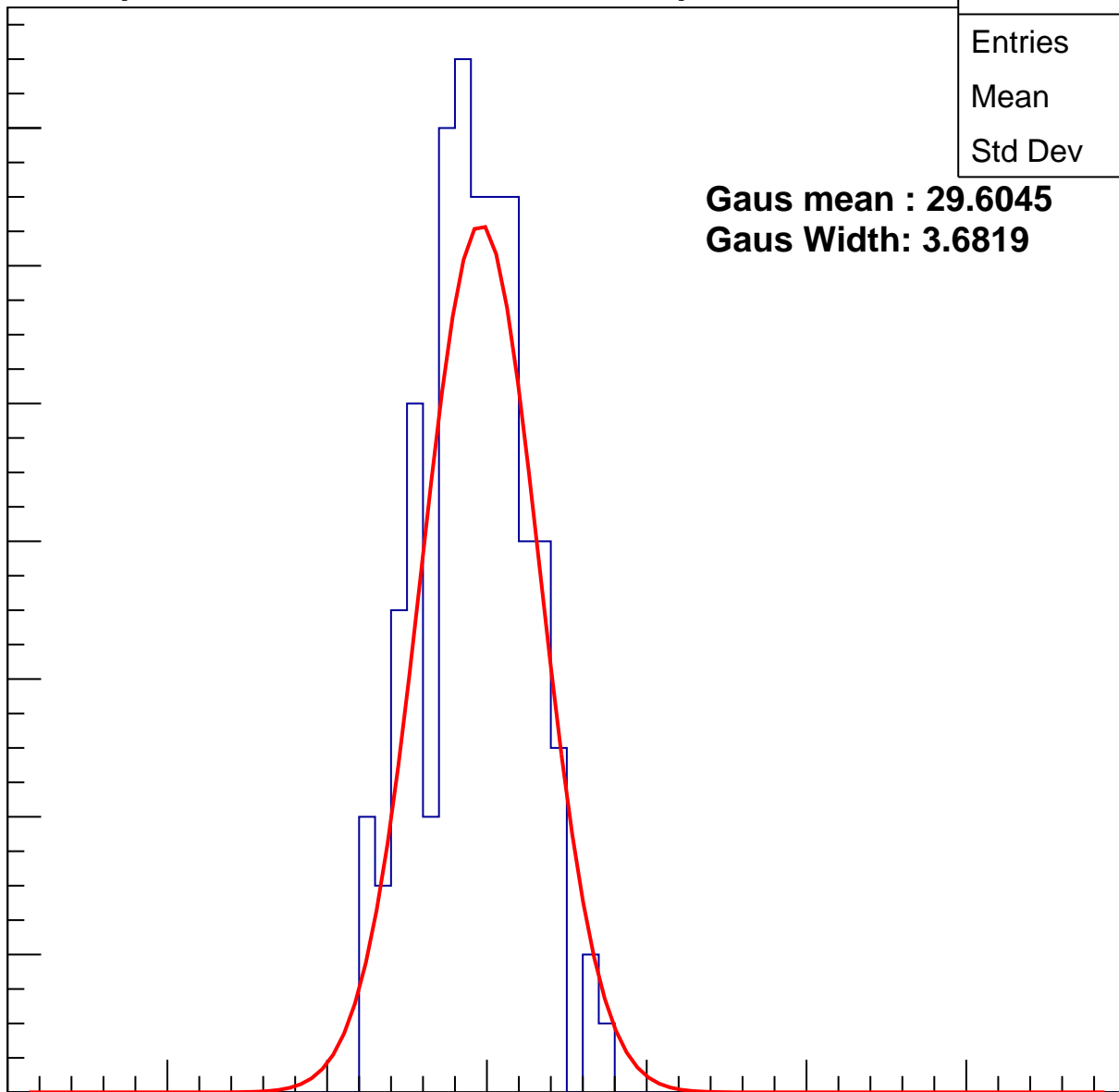
Gaus Width: 3.6819

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch80, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	34.99
Std Dev	3.341

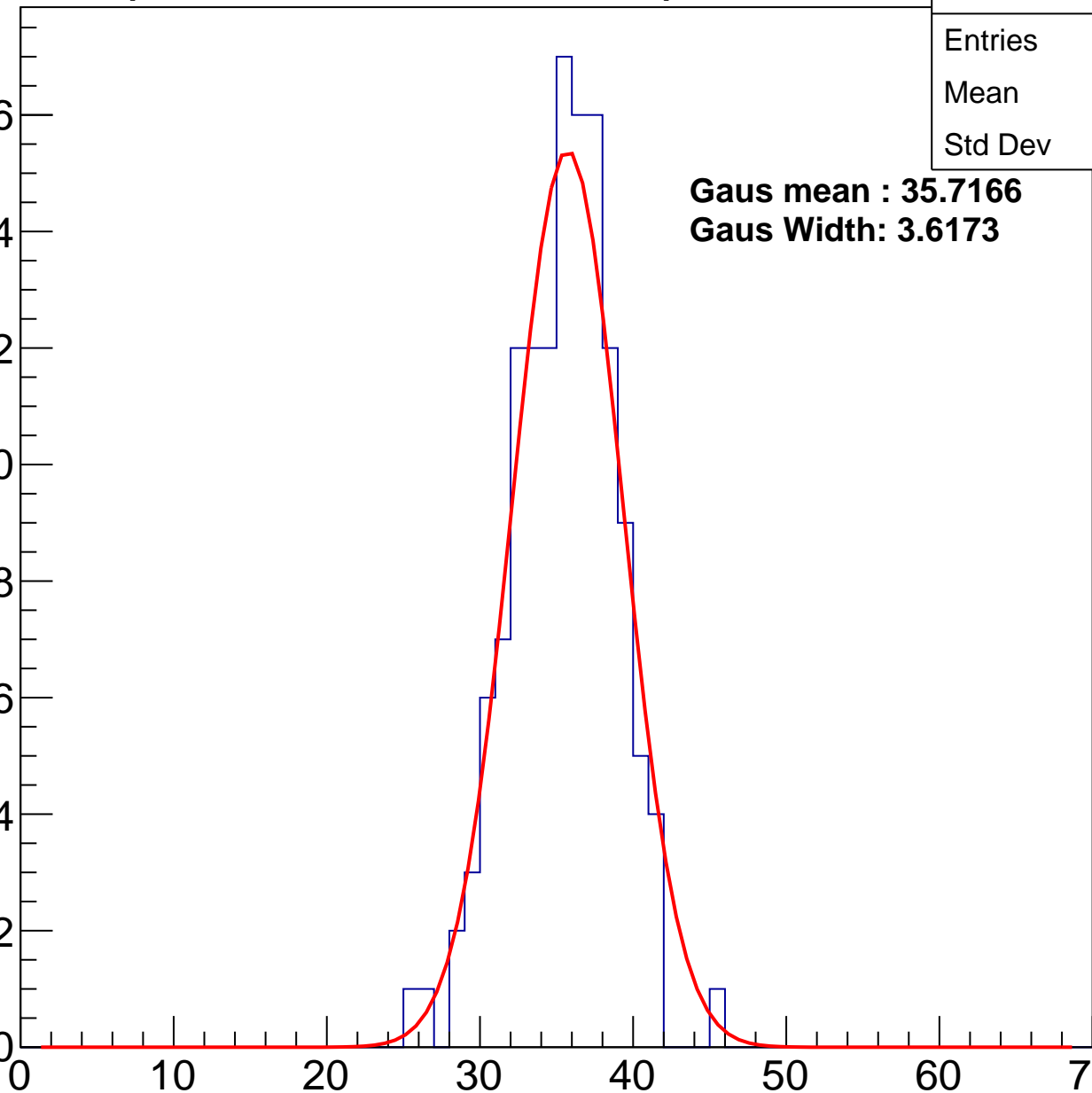
Gaus mean : 35.7166

Gaus Width: 3.6173

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch80, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

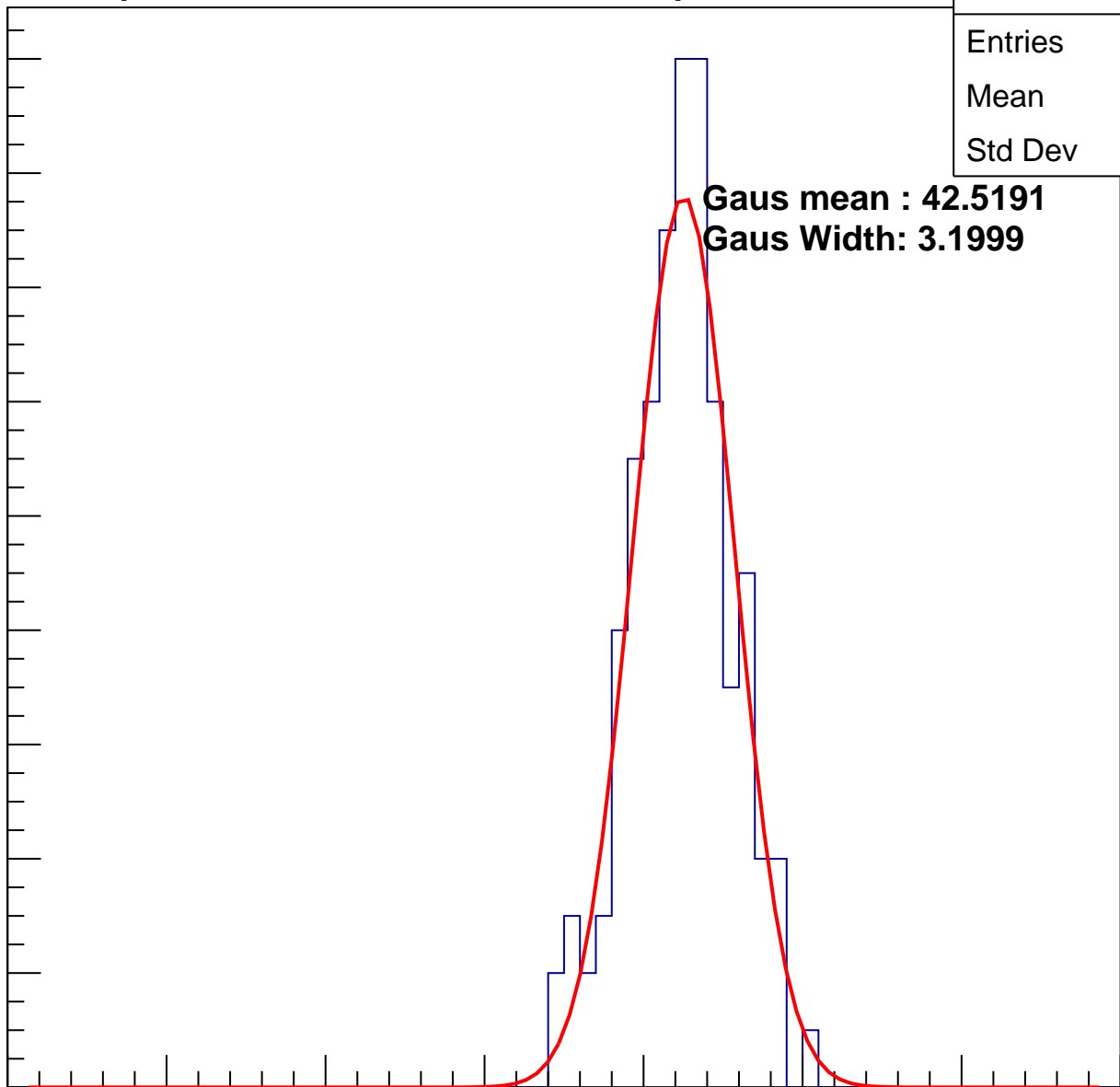
Entries	129
Mean	41.87
Std Dev	3.163

Gaus mean : 42.5191

Gaus Width: 3.1999

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch80, adc3

calib_packv5_042523_0143.root, FC#2, port C2

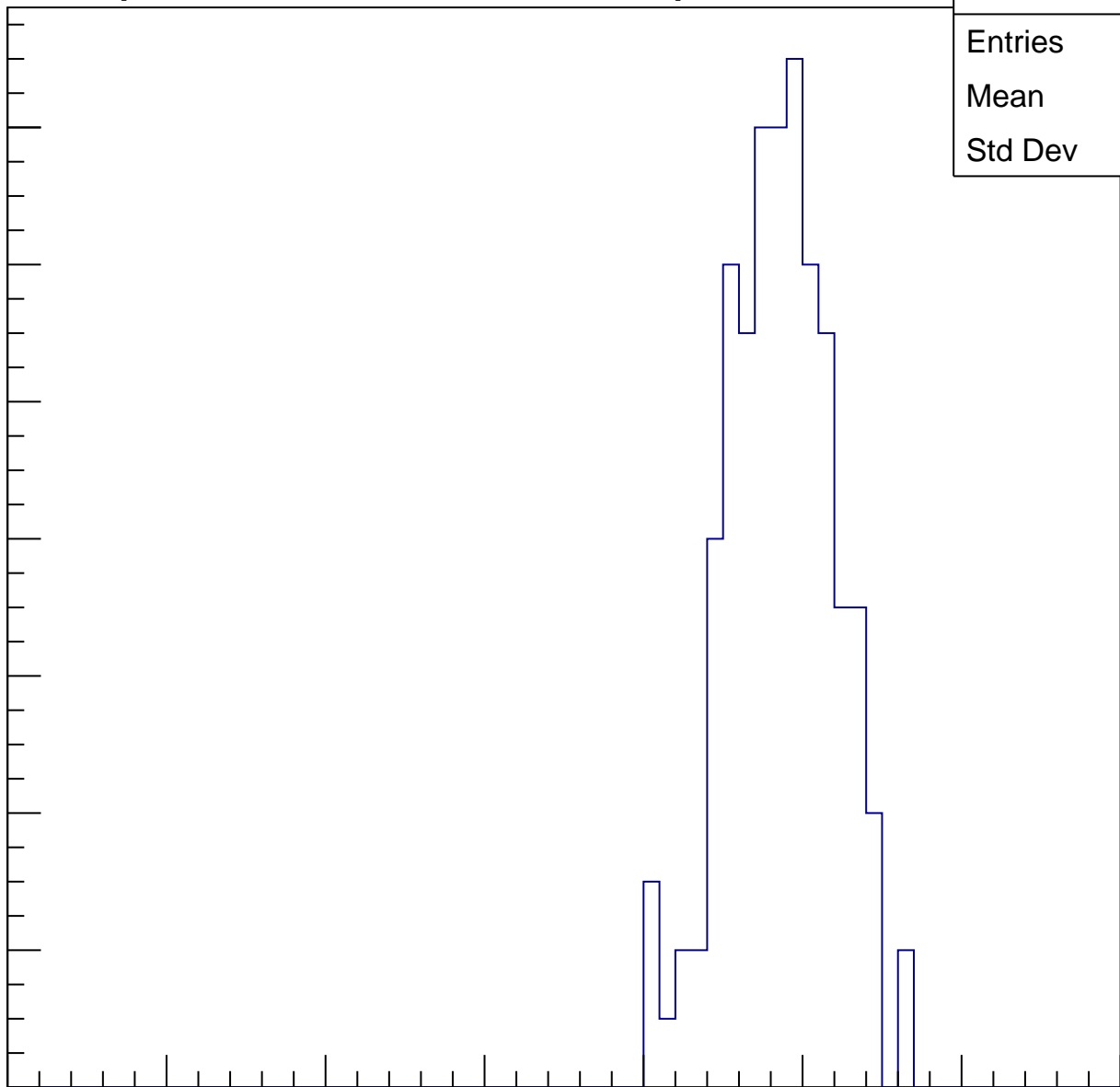
Entries	125
Mean	48.14
Std Dev	3.317

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

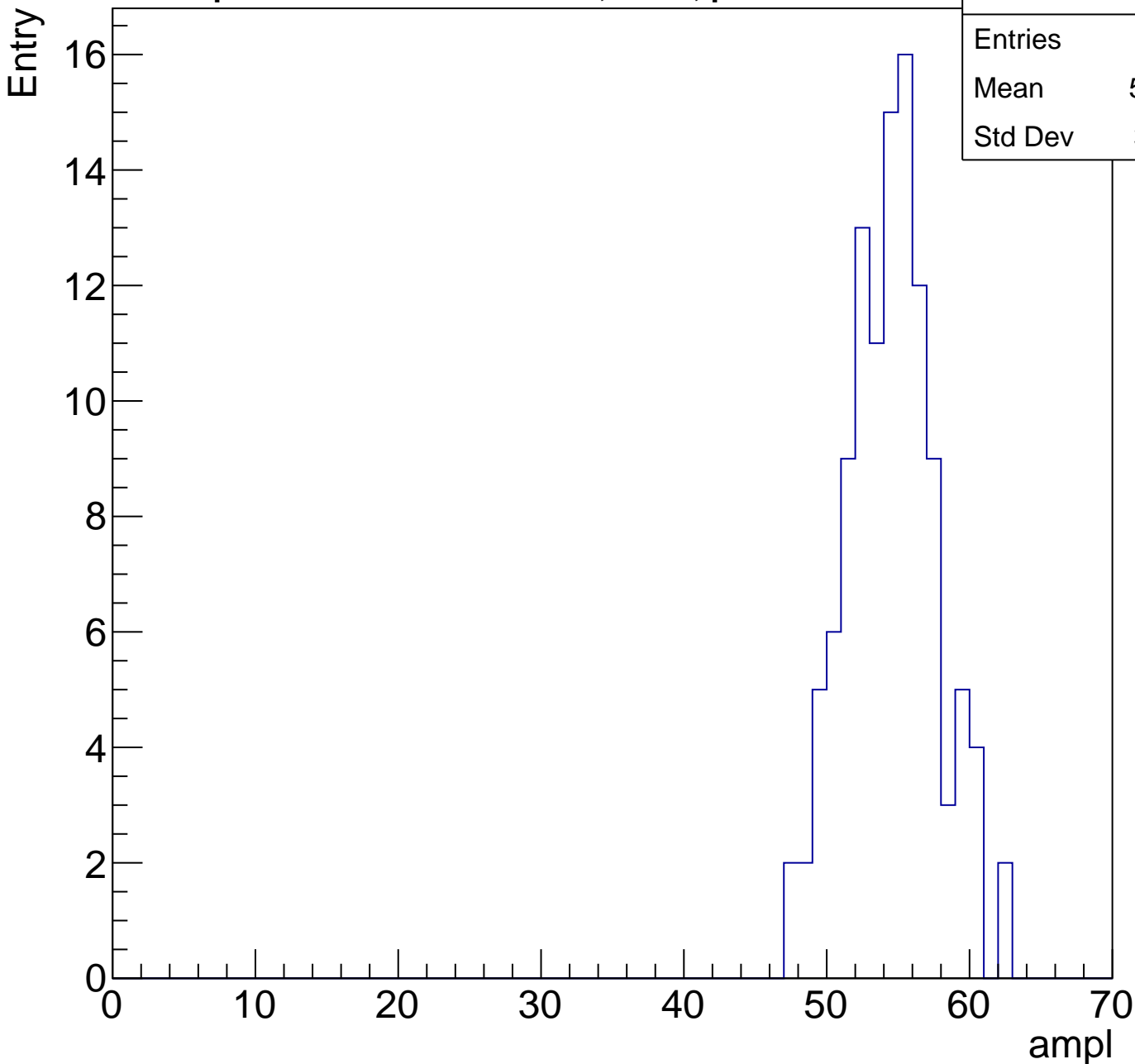
ampl



B1L001S, U20-ch80, adc4

calib_packv5_042523_0143.root, FC#2, port C2

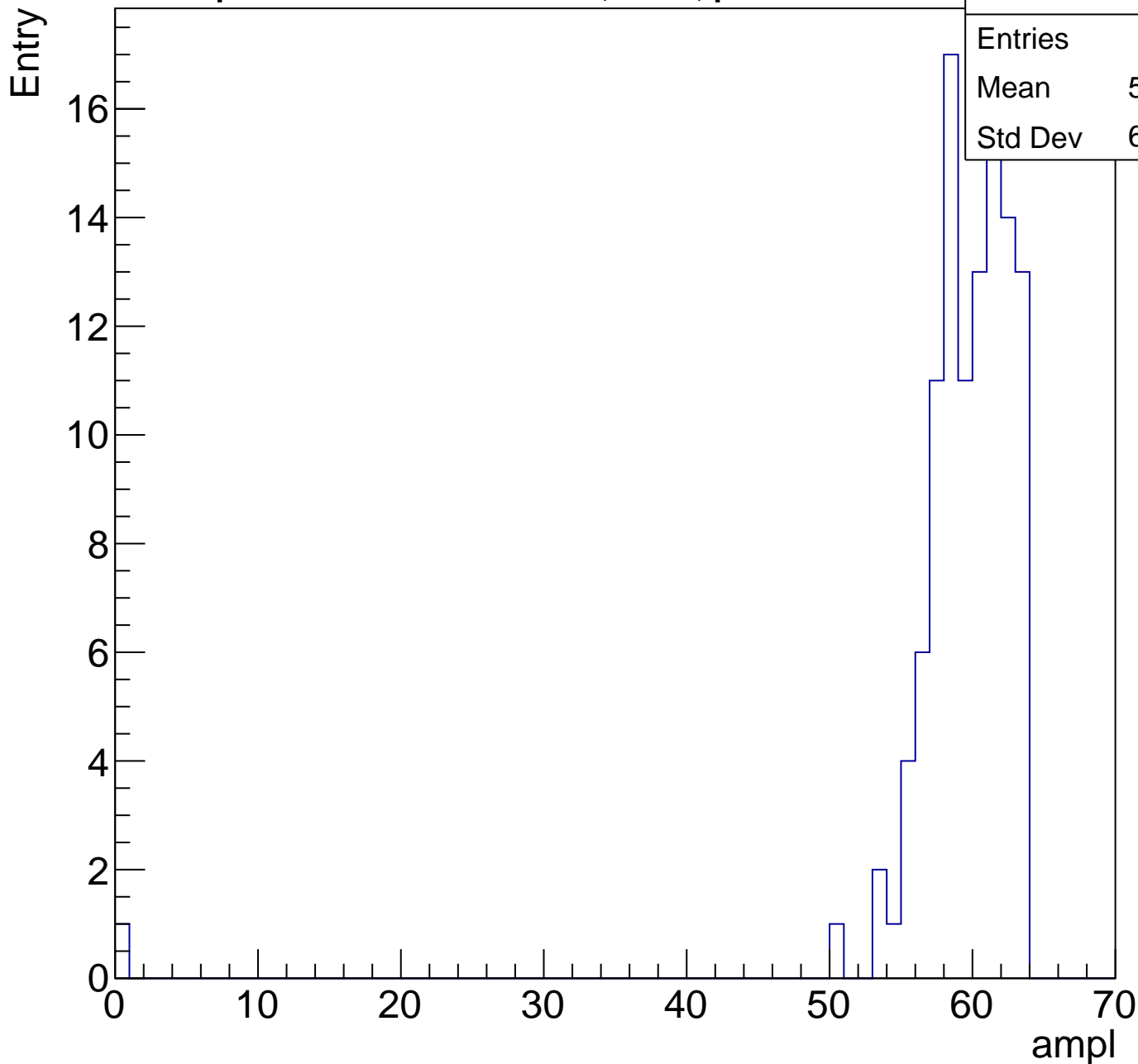
Entries	114
Mean	54.04
Std Dev	3.141



B1L001S, U20-ch80, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	58.85
Std Dev	6.188

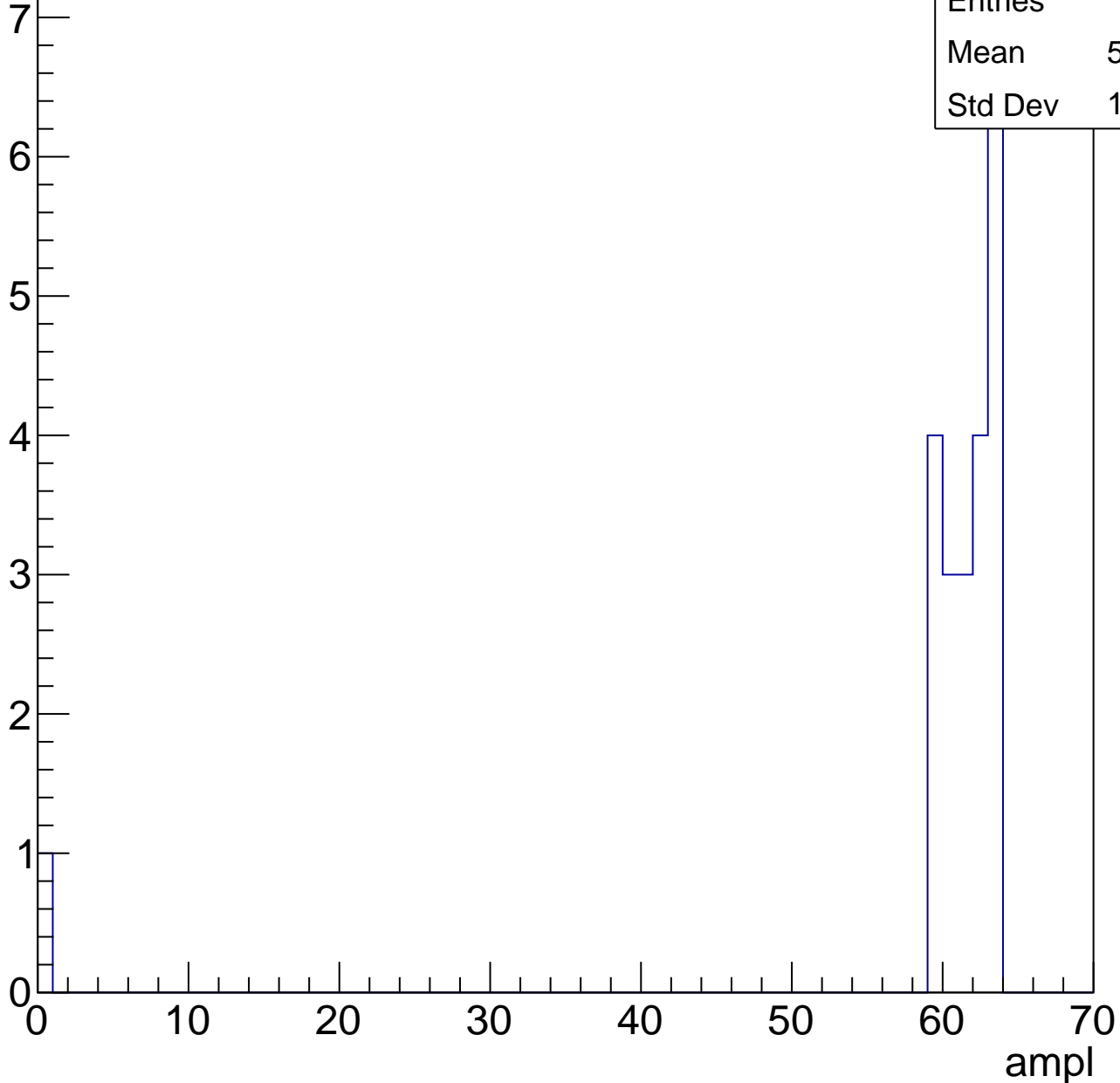


B1L001S, U20-ch80, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	22
Mean	58.55
Std Dev	12.86



B1L001S, U20-ch80, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch81, adc0

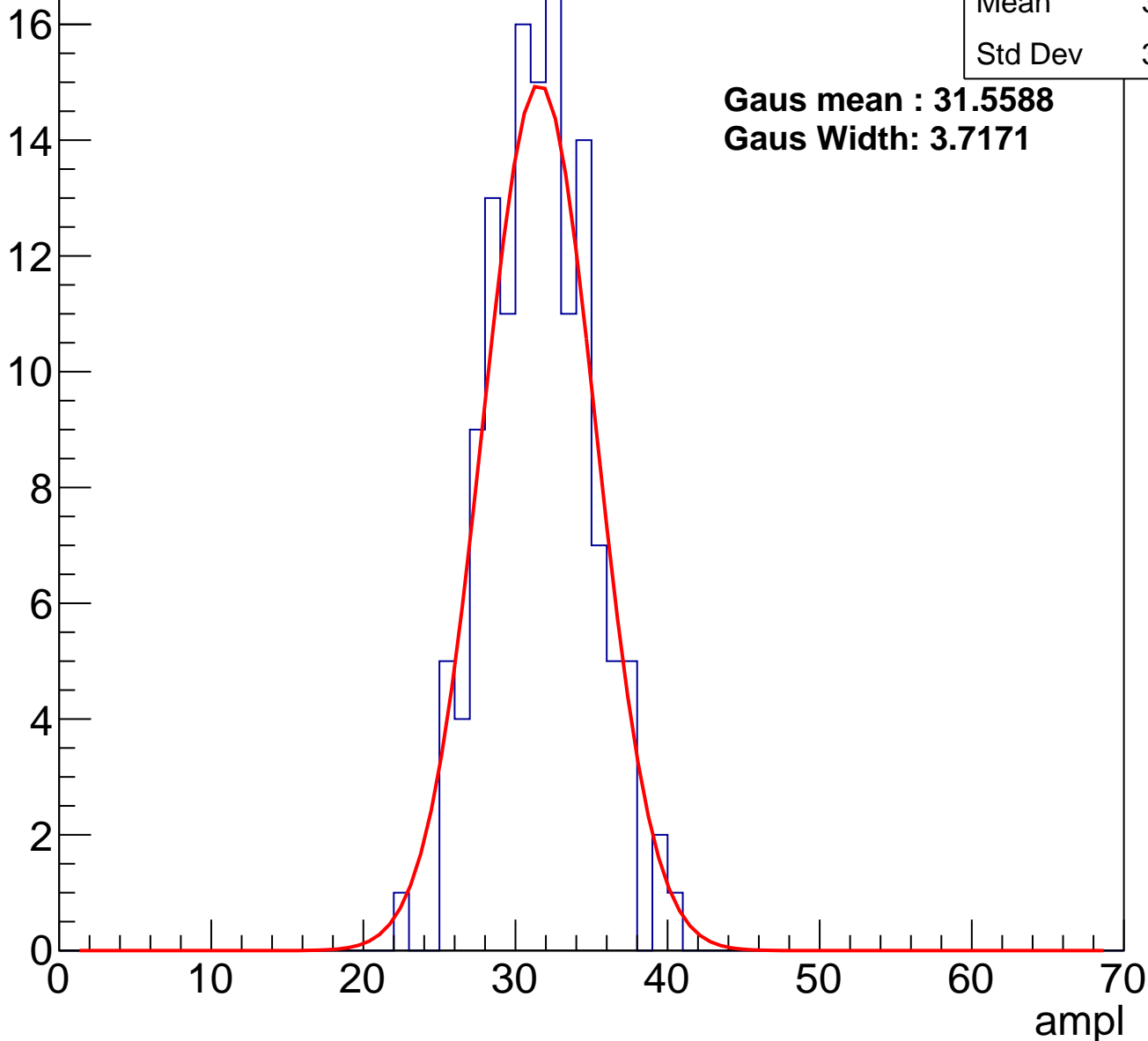
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	136
Mean	31.12
Std Dev	3.313

Gaus mean : 31.5588

Gaus Width: 3.7171



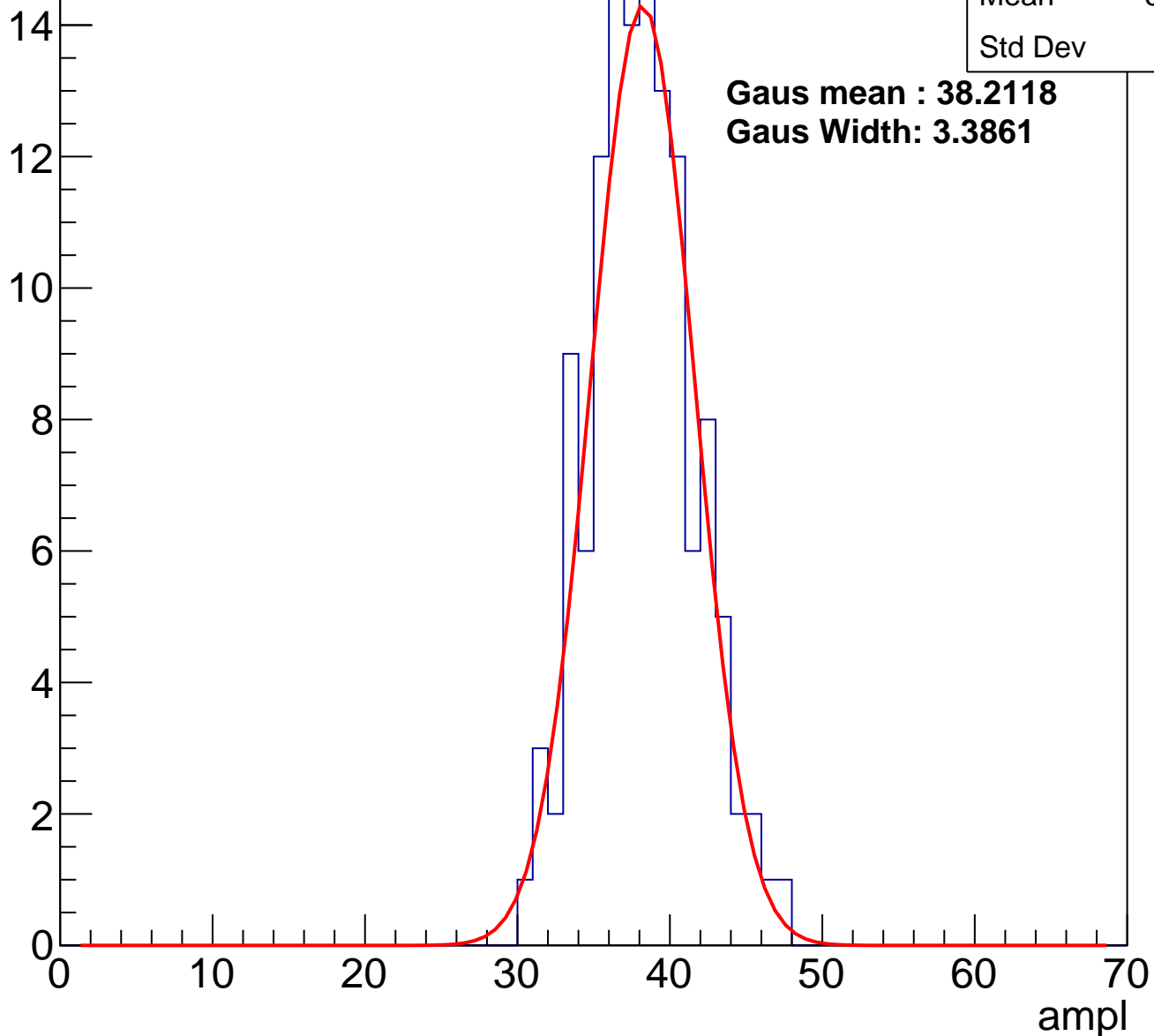
B1L001S, U20-ch81, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	37.72
Std Dev	3.39

Gaus mean : 38.2118
Gaus Width: 3.3861

Entry



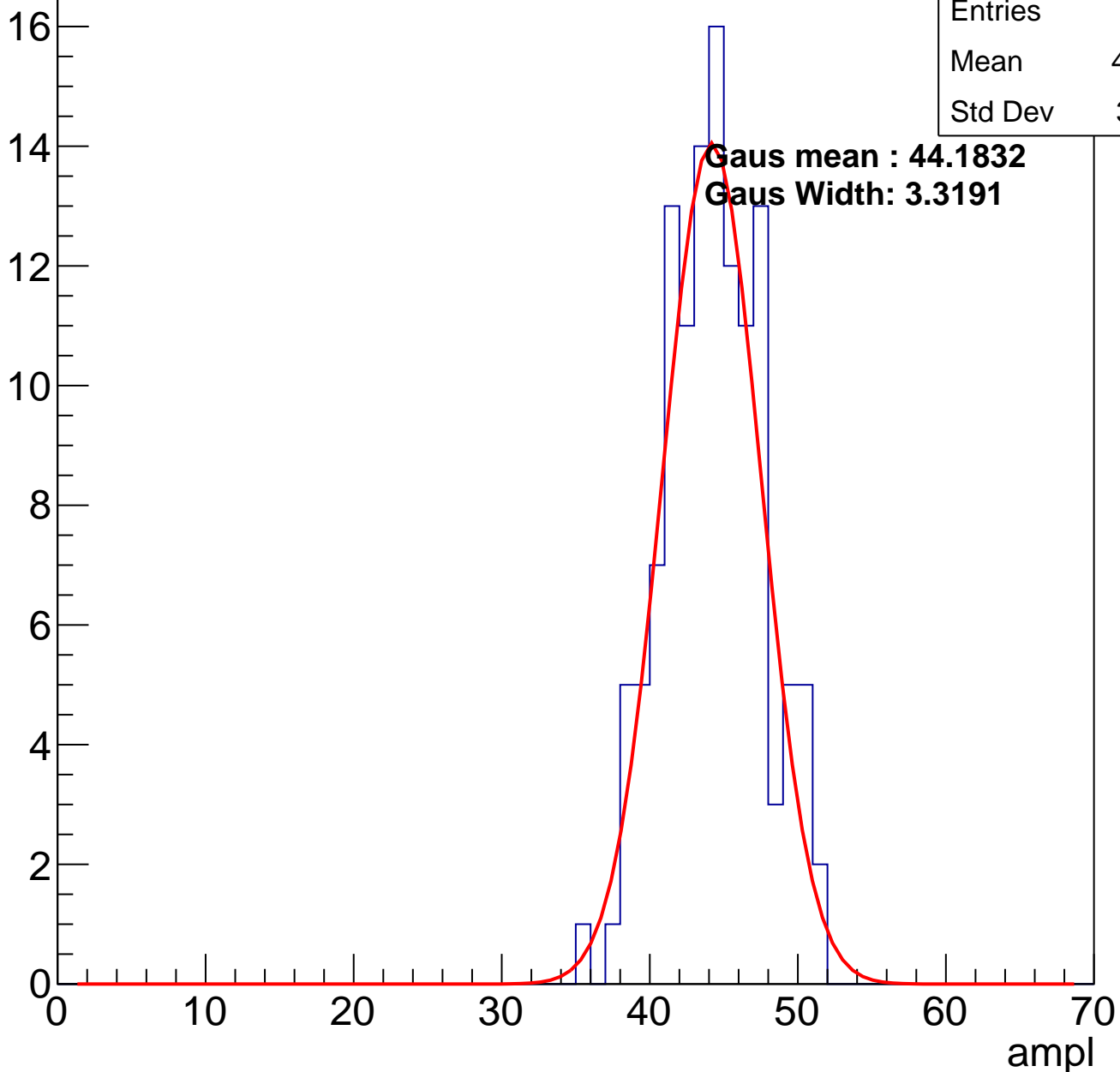
B1L001S, U20-ch81, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	43.84
Std Dev	3.291

Gaus mean : 44.1832
Gaus Width: 3.3191

Entry



B1L001S, U20-ch81, adc3

calib_packv5_042523_0143.root, FC#2, port C2

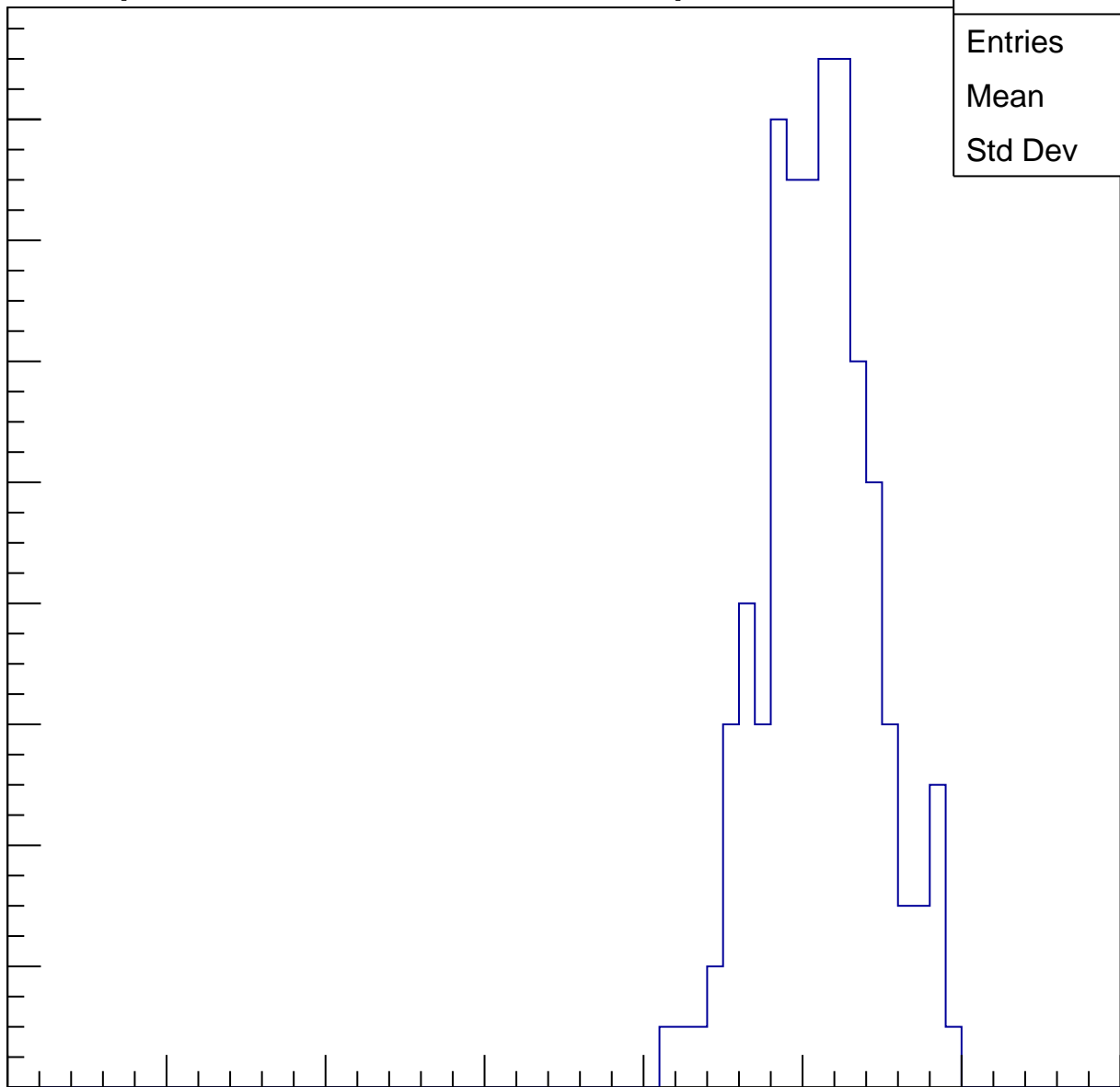
Entries	145
Mean	50.57
Std Dev	3.513

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch81, adc4

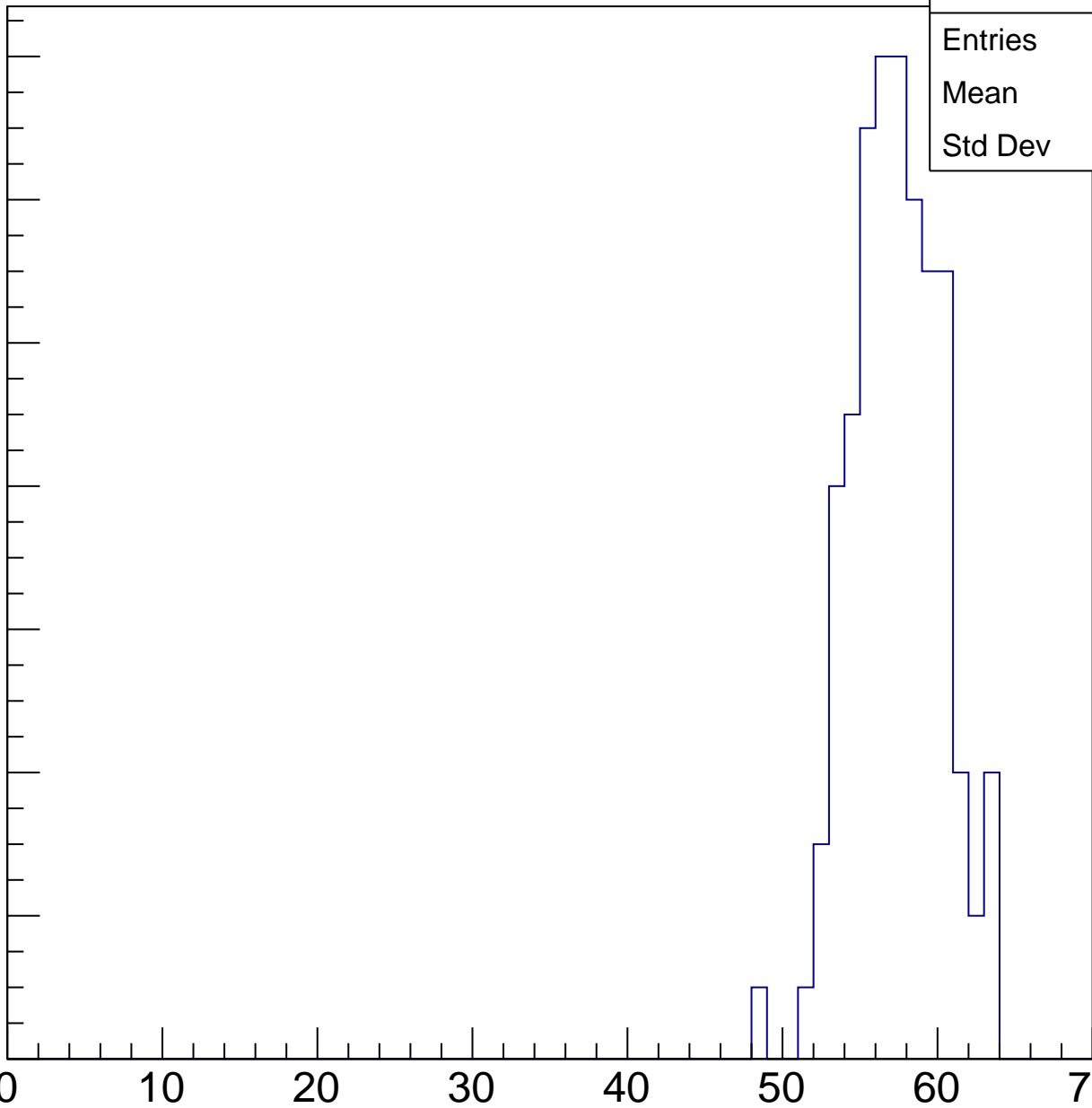
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	56.89
Std Dev	2.866

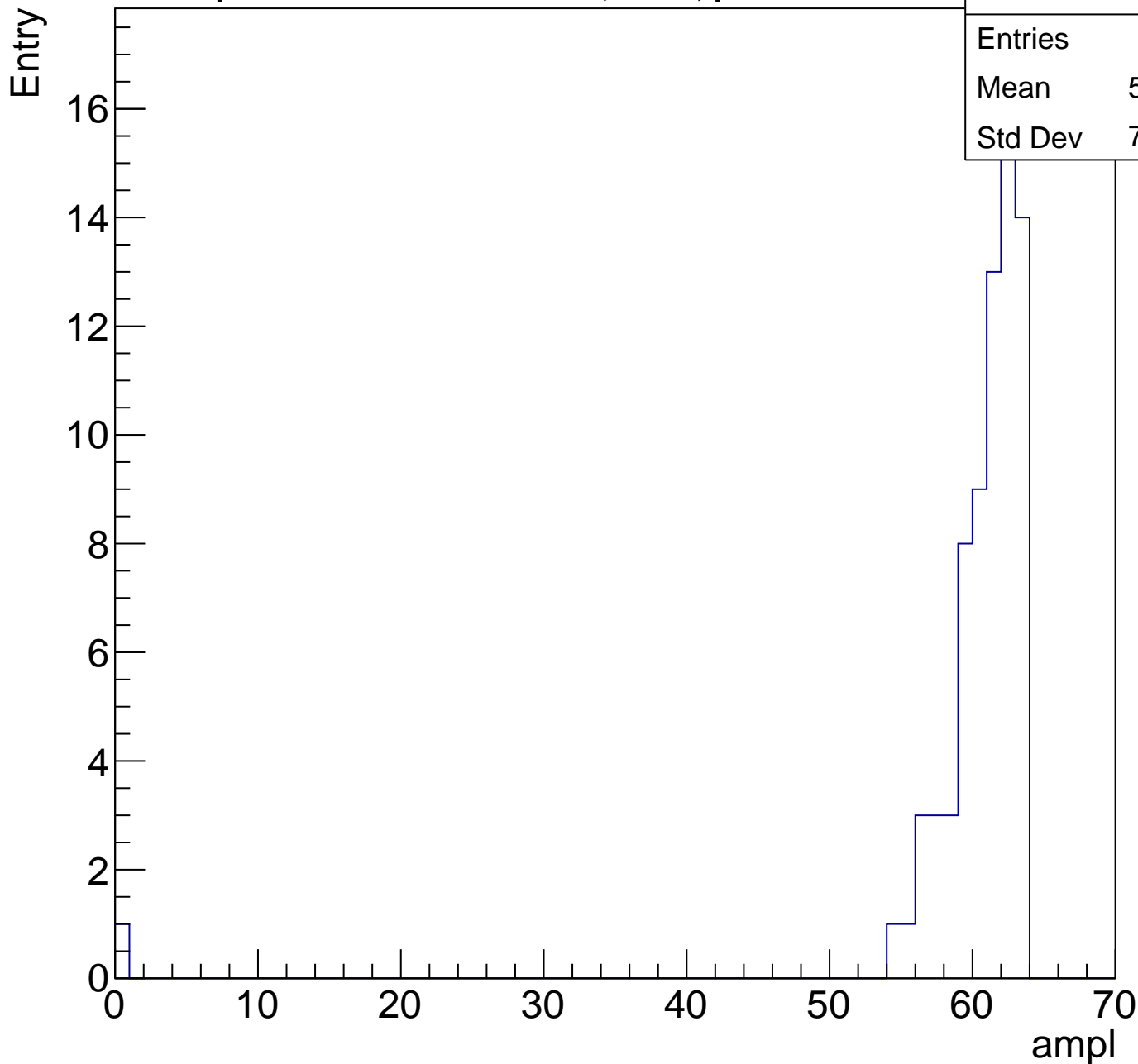
ampl



B1L001S, U20-ch81, adc5

calib_packv5_042523_0143.root, FC#2, port C2

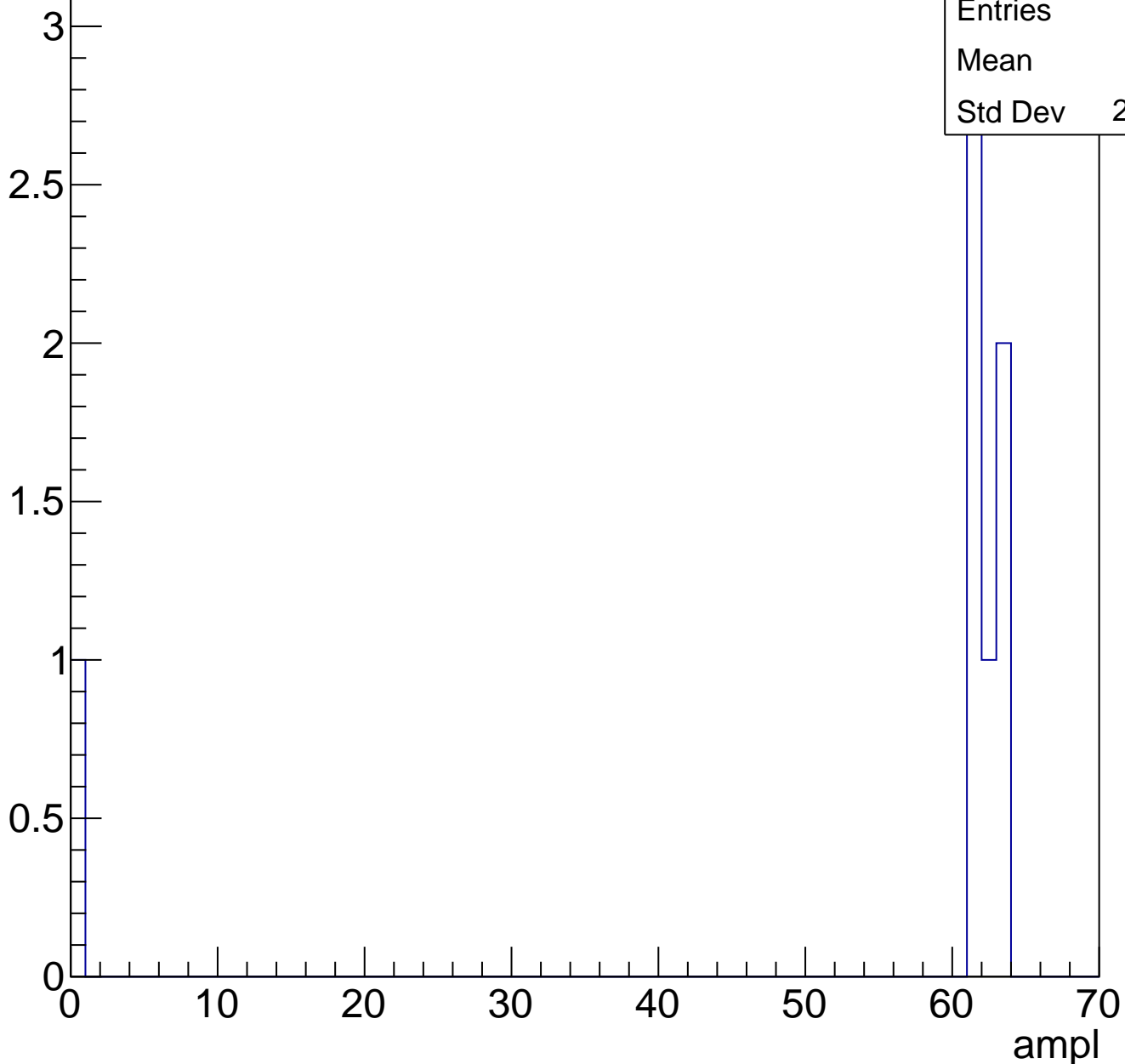
Entries	73
Mean	59.77
Std Dev	7.364



B1L001S, U20-ch81, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	7
Mean	53
Std Dev	21.65

B1L001S, U20-ch81, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L001S, U20-ch82, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	31.59
Std Dev	3.271

Gaus mean : 32.2897

Gaus Width: 3.1740

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

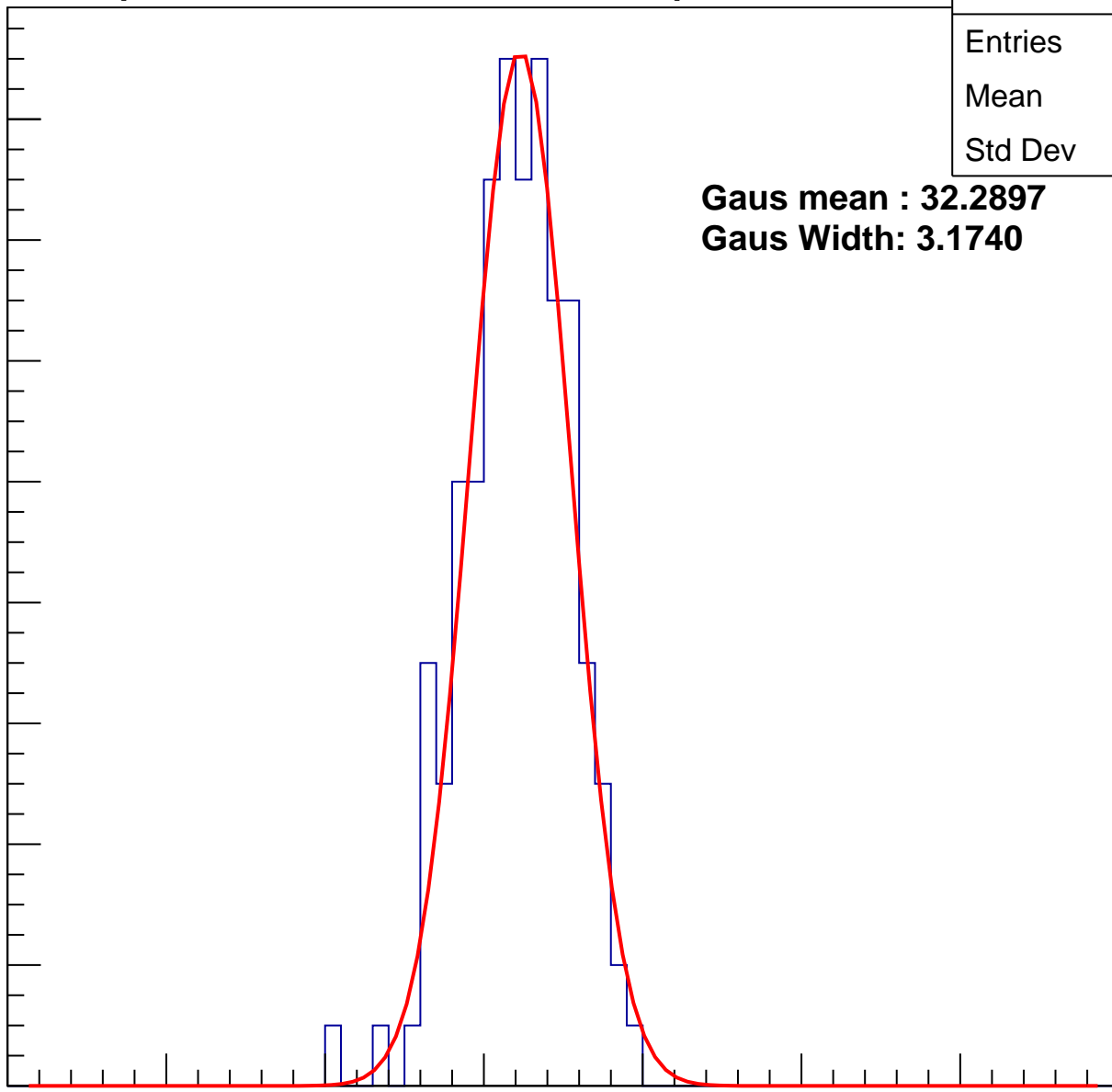
40

50

60

70

ampl

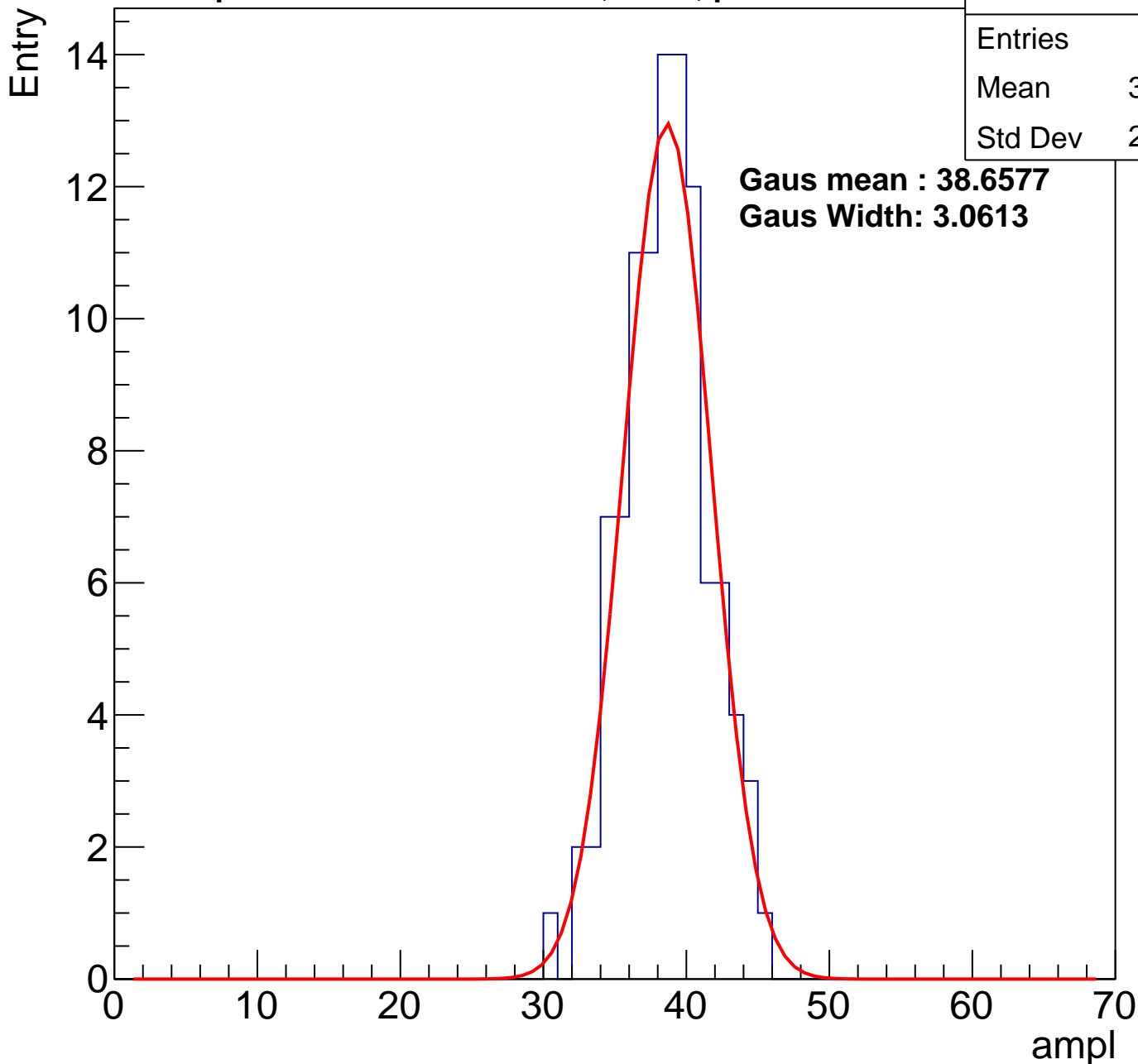


B1L001S, U20-ch82, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	38.13
Std Dev	2.957

Gaus mean : 38.6577
Gaus Width: 3.0613



B1L001S, U20-ch82, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

162

Mean

44.31

Std Dev

3.568

Gaus mean : 44.8063

Gaus Width: 3.7286

0

10

20

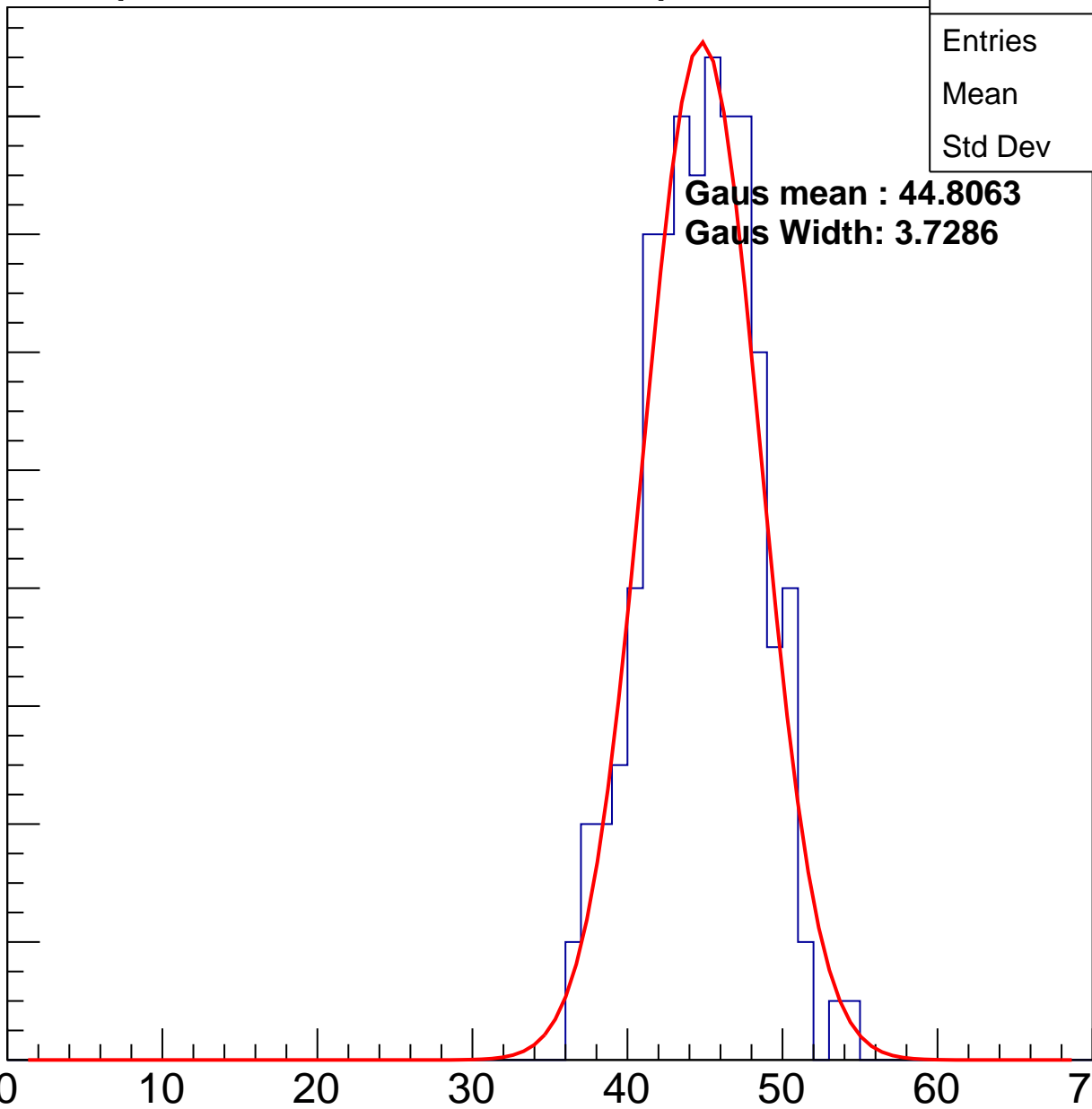
30

40

50

60

ampl



B1L001S, U20-ch82, adc3

calib_packv5_042523_0143.root, FC#2, port C2

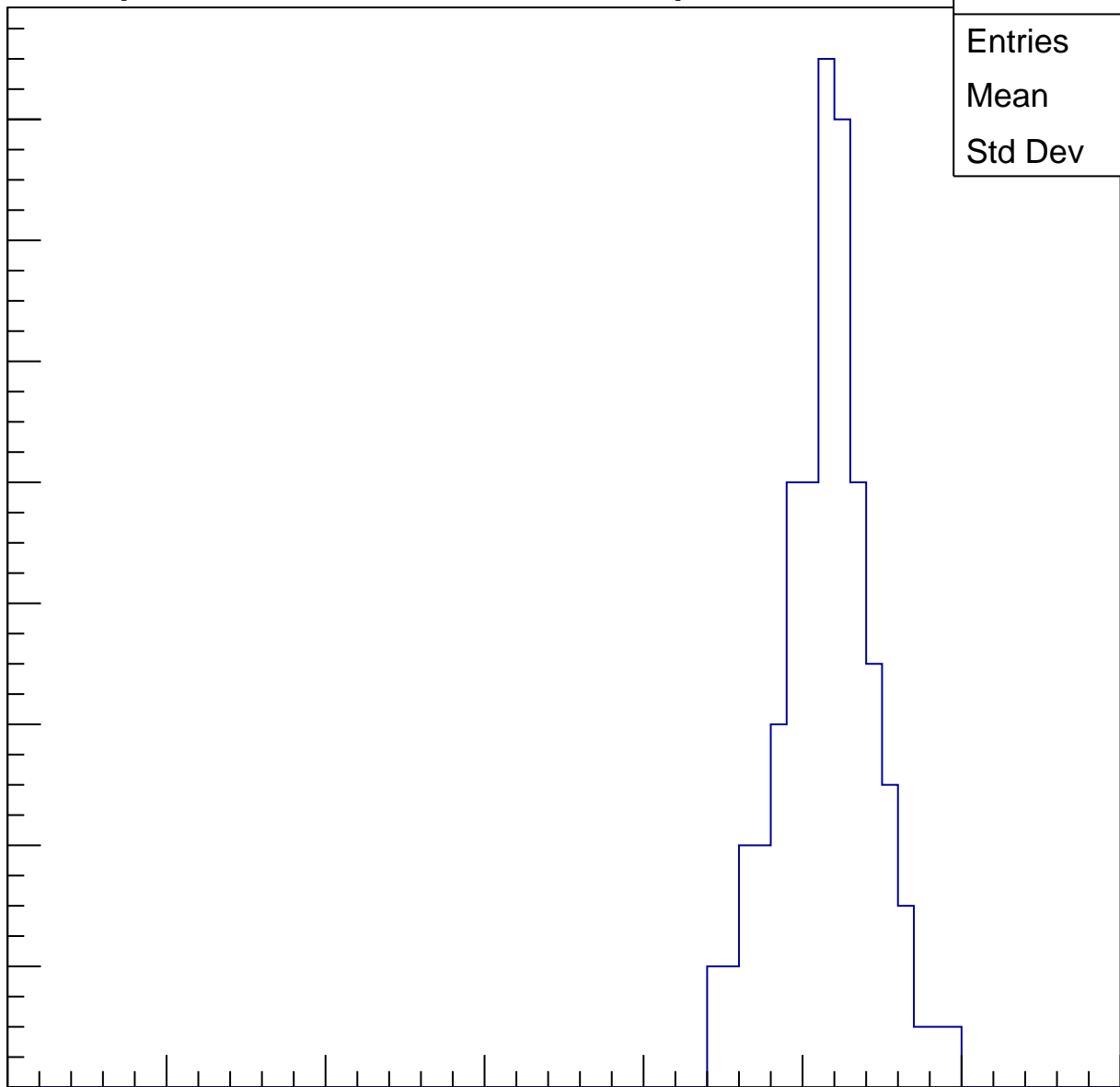
Entries	99
Mean	51.03
Std Dev	2.949

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

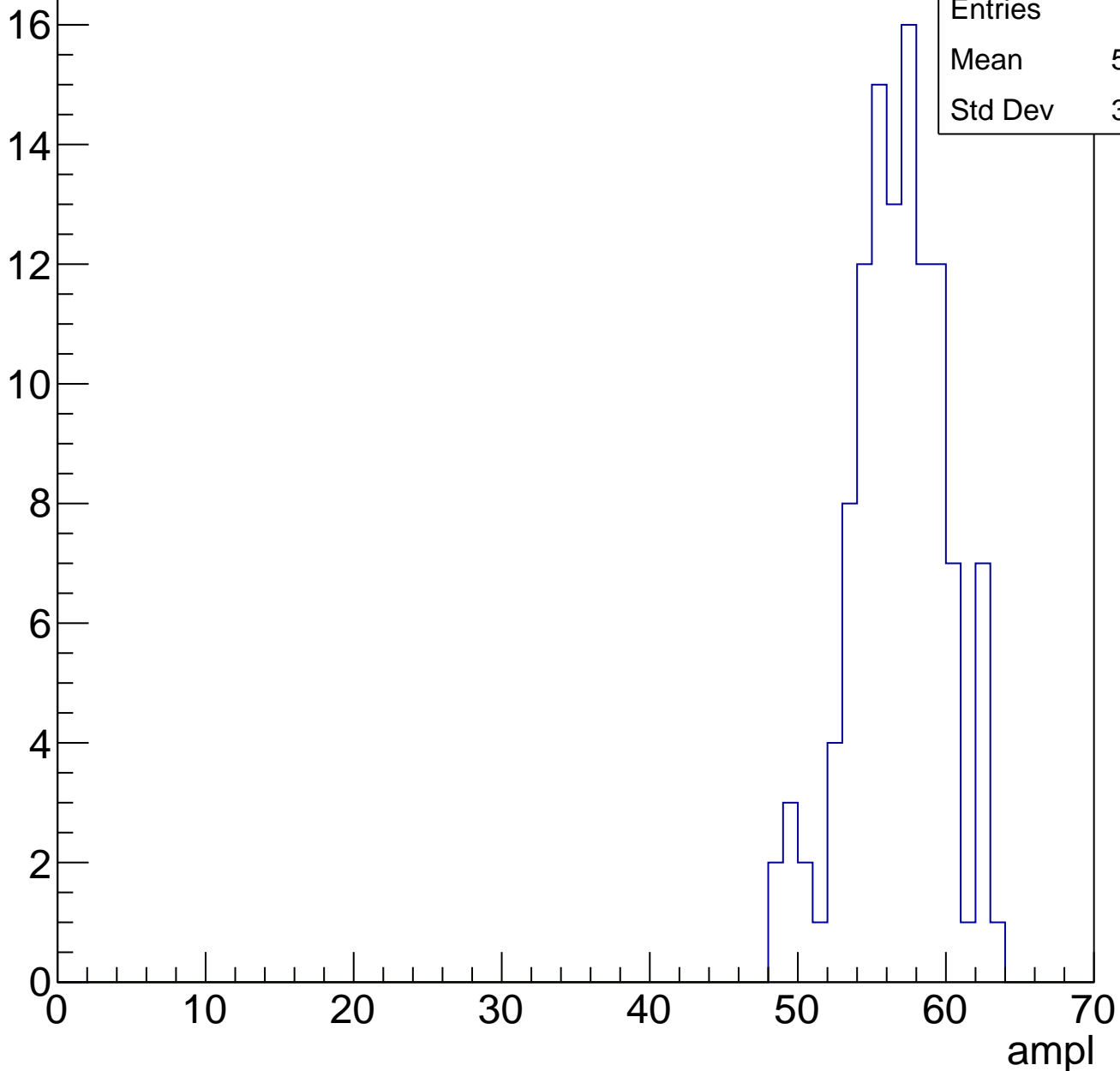
ampl



B1L001S, U20-ch82, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

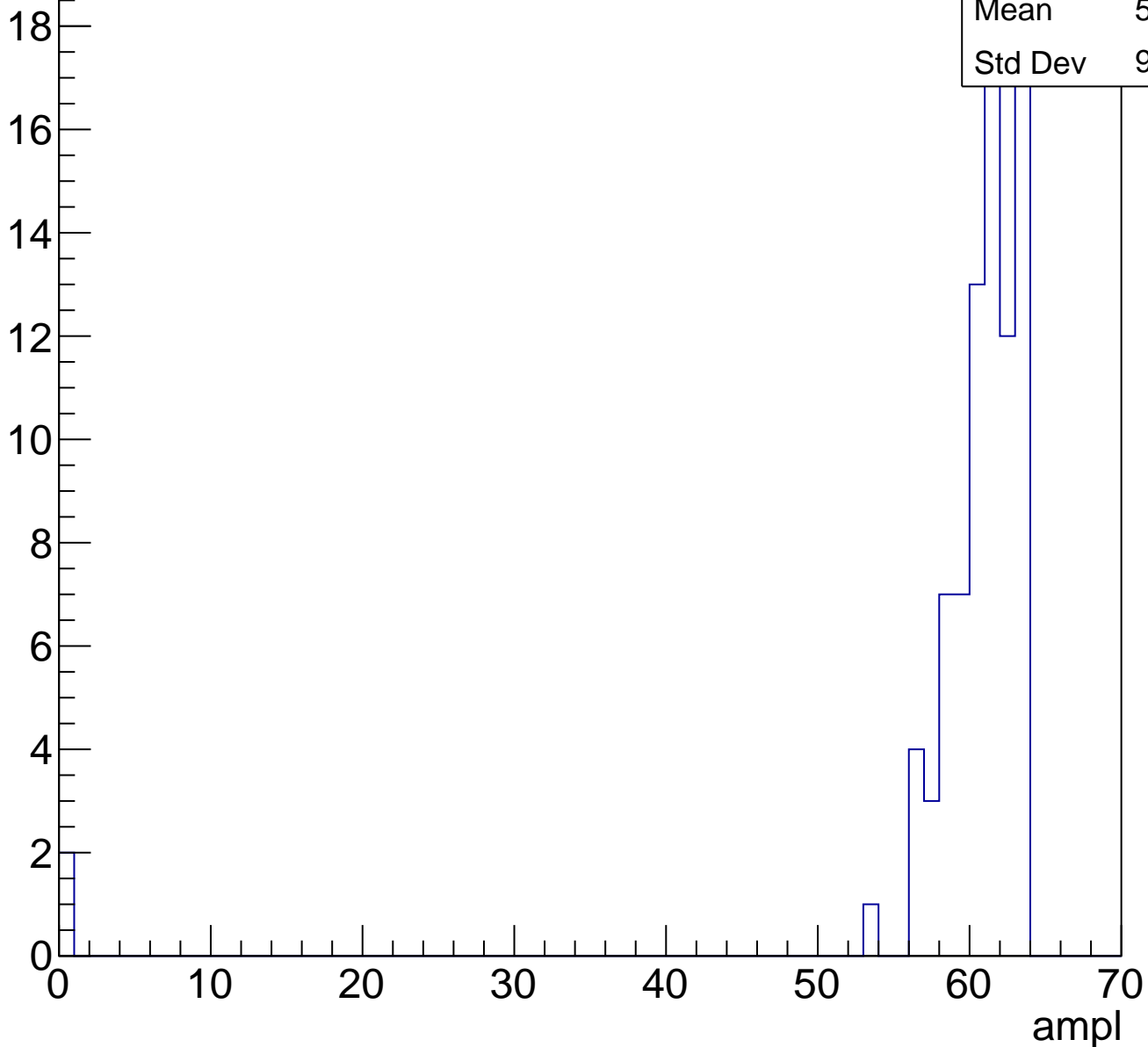


Entries	116
Mean	56.22
Std Dev	3.183

B1L001S, U20-ch82, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch82, adc6

calib_packv5_042523_0143.root, FC#2, port C2

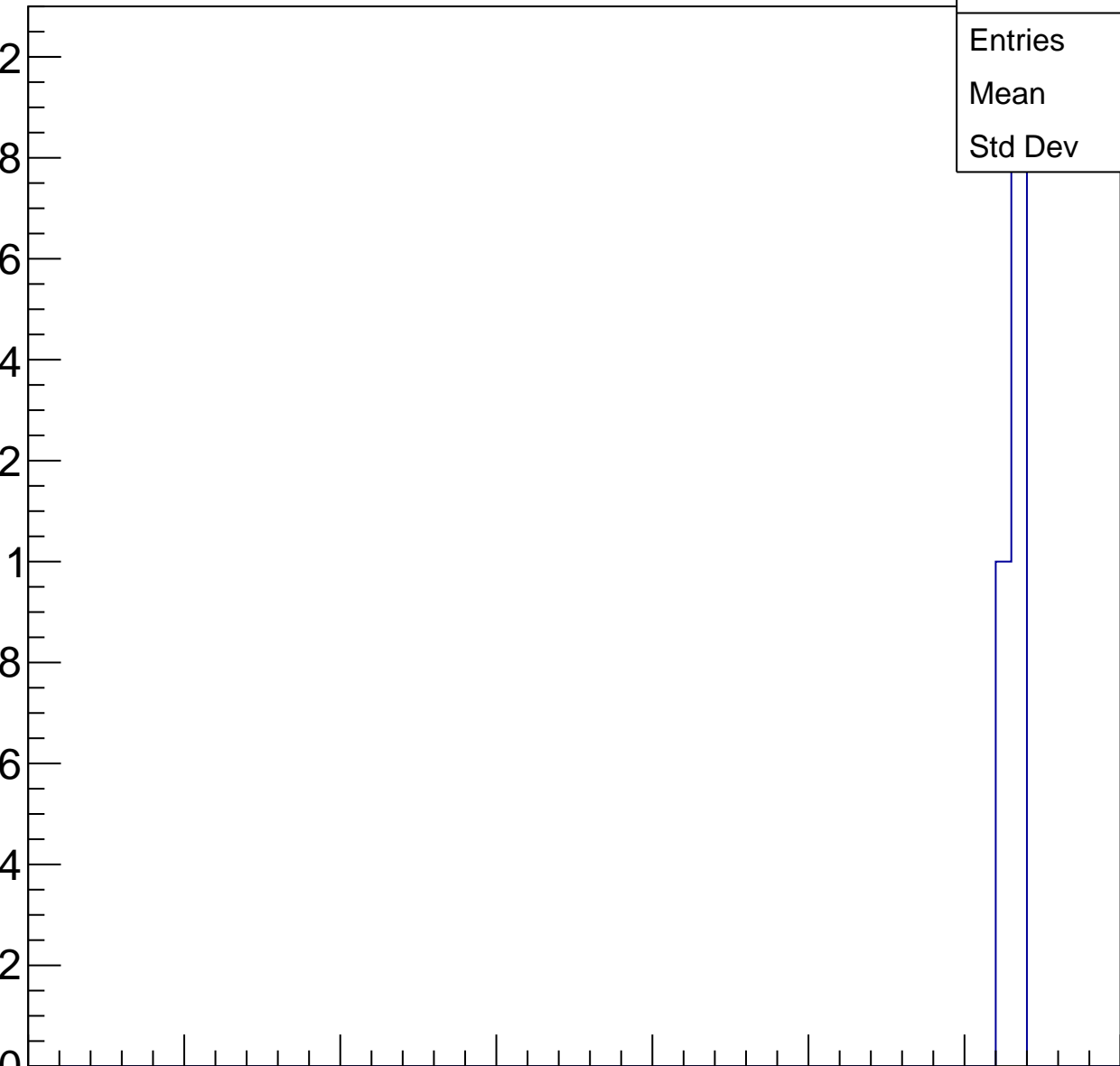
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch82, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch83, adc0

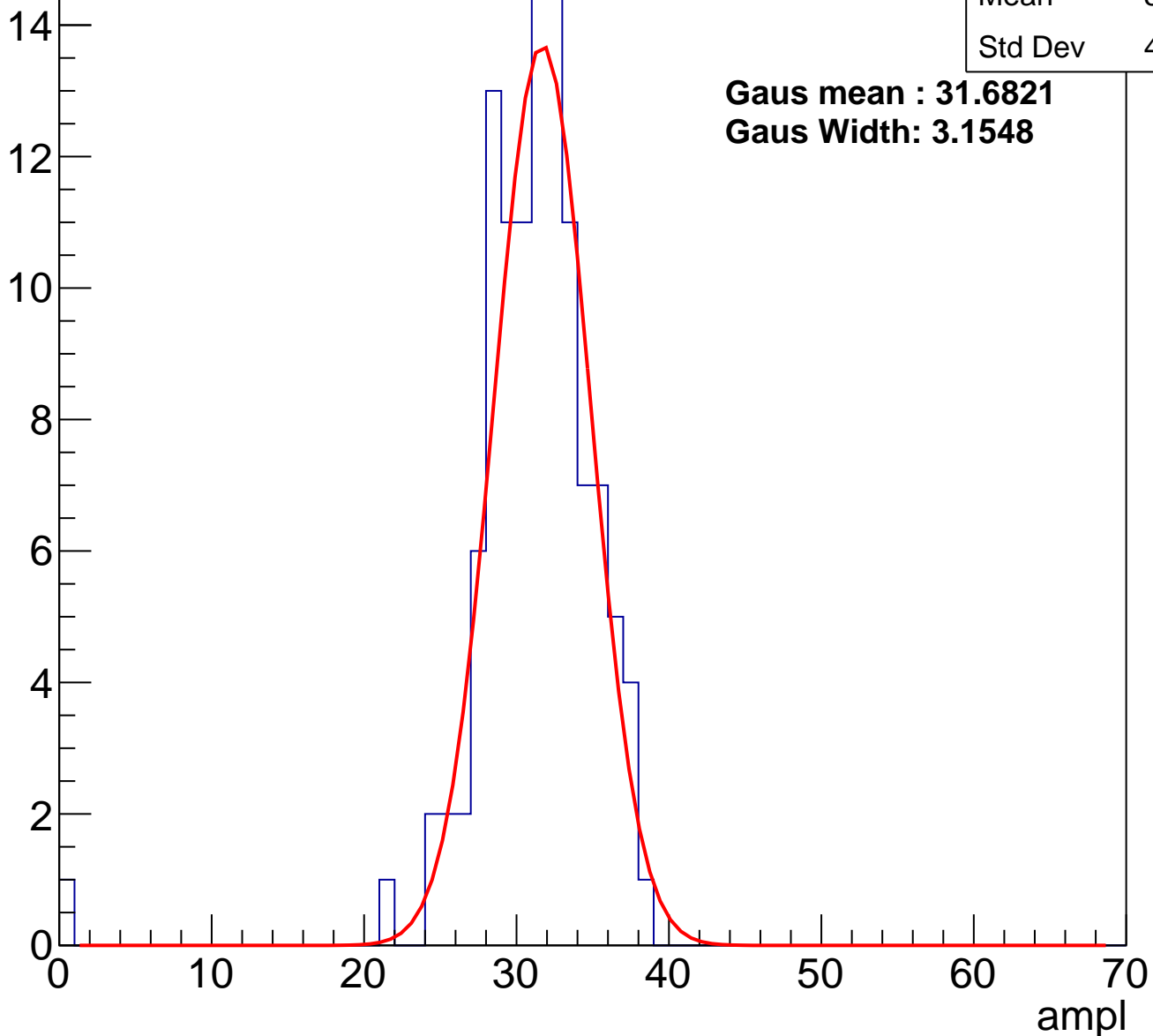
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	30.73
Std Dev	4.284

Gaus mean : 31.6821

Gaus Width: 3.1548

Entry



B1L001S, U20-ch83, adc1

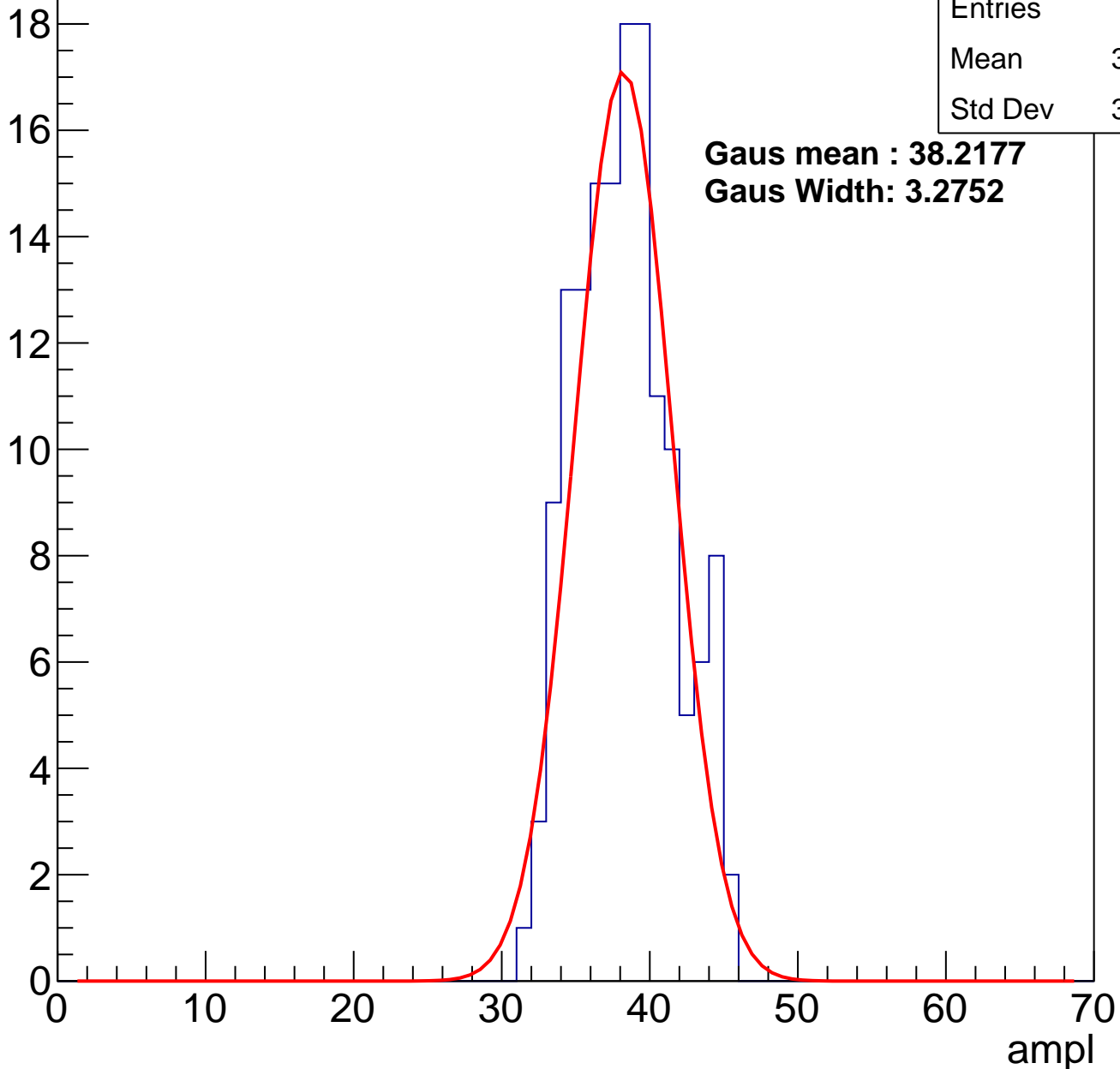
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	37.84
Std Dev	3.243

Gaus mean : 38.2177

Gaus Width: 3.2752

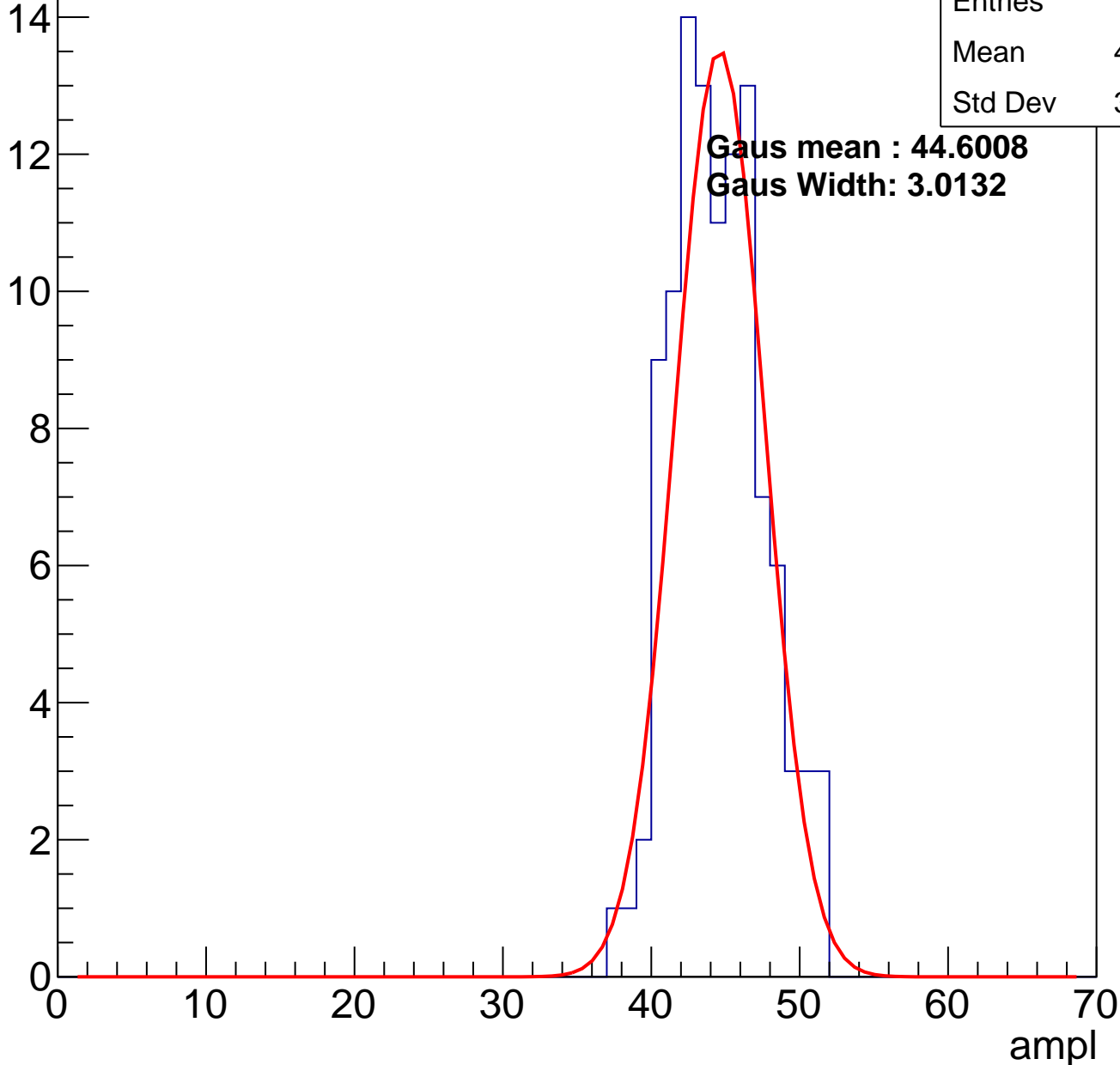
Entry



B1L001S, U20-ch83, adc2

calib_packv5_042523_0143.root, FC#2, port C2

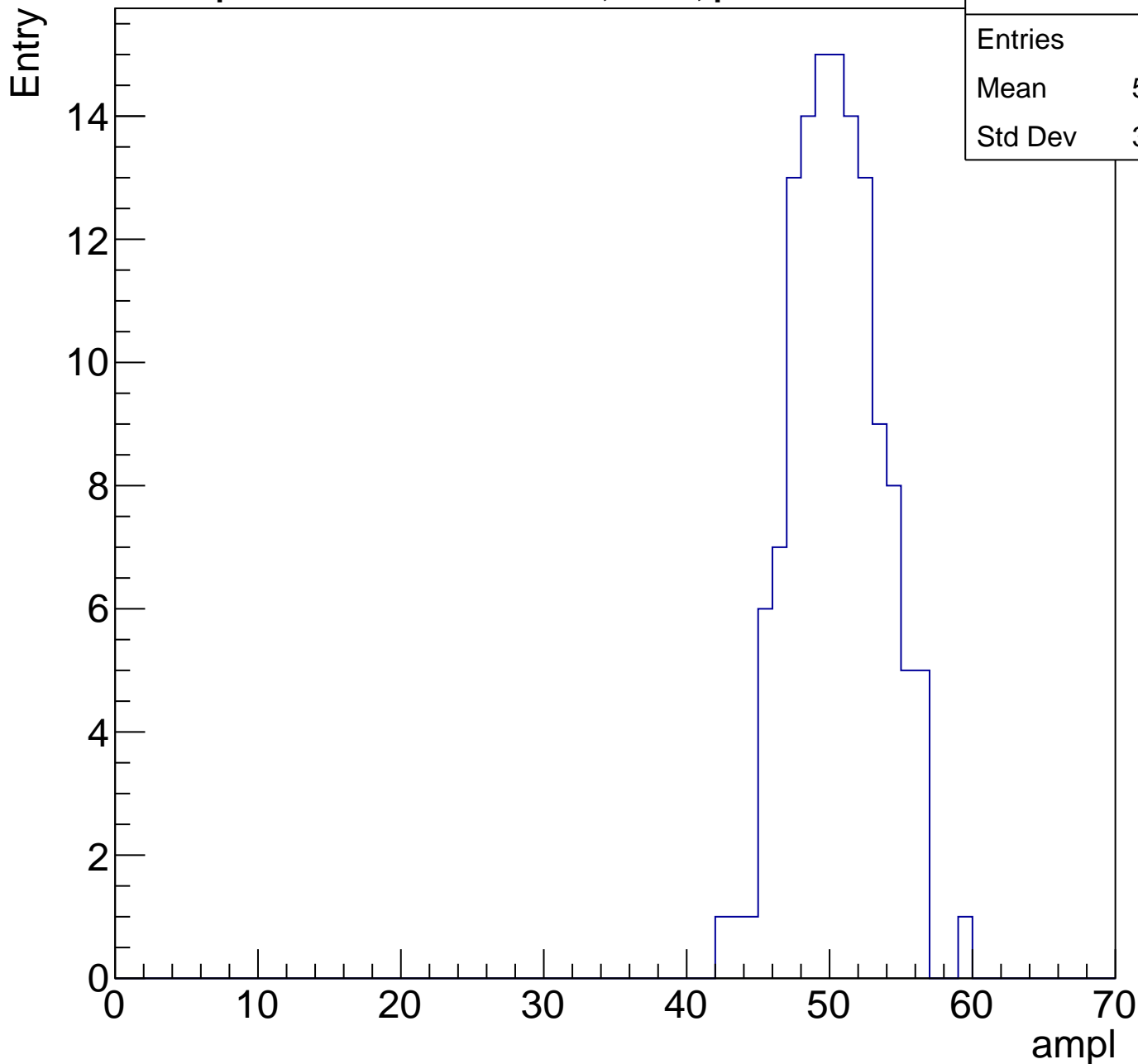
Entry



B1L001S, U20-ch83, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	50.02
Std Dev	3.137



B1L001S, U20-ch83, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

133

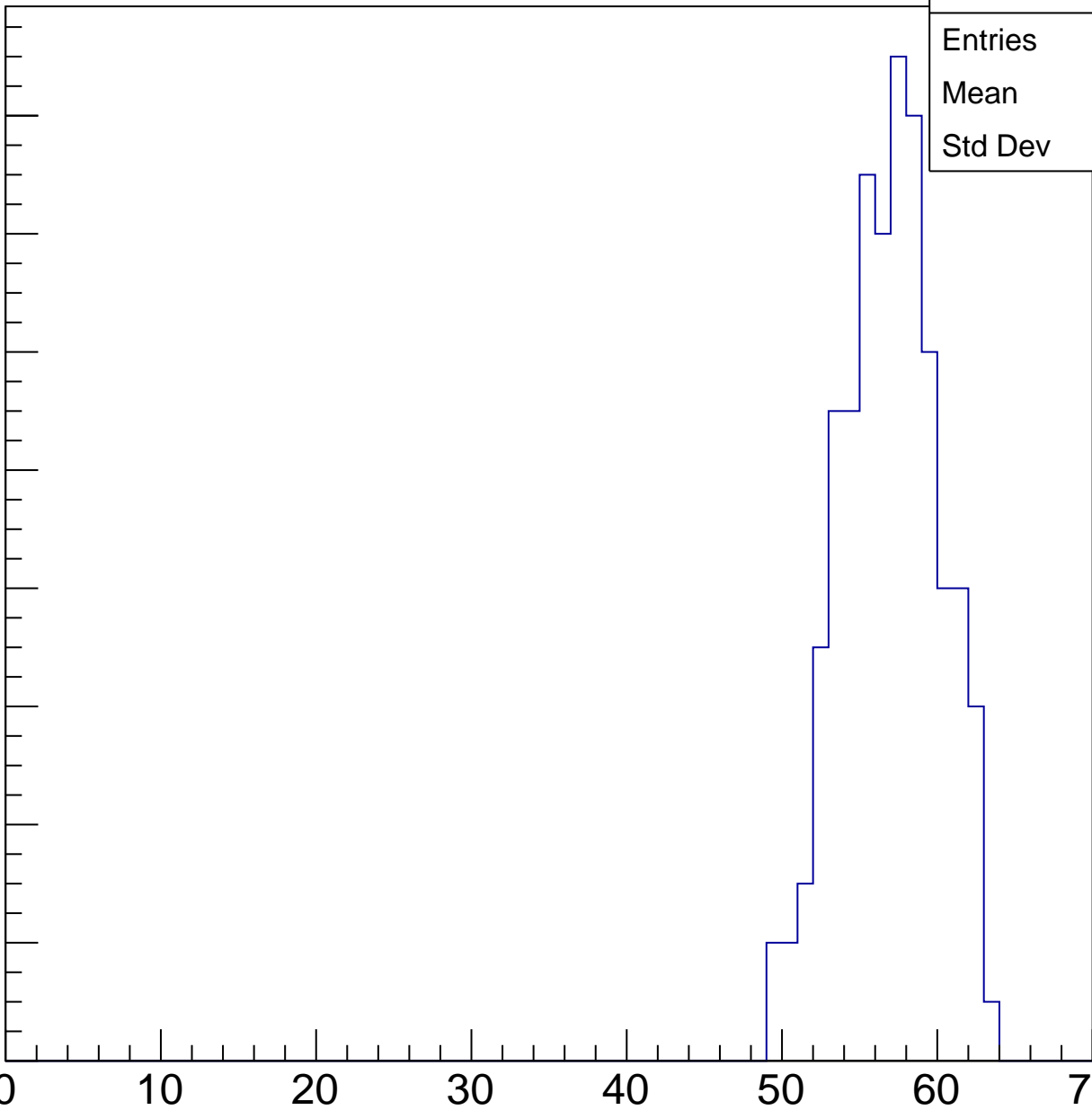
Mean

56.46

Std Dev

3.089

ampl



B1L001S, U20-ch83, adc5

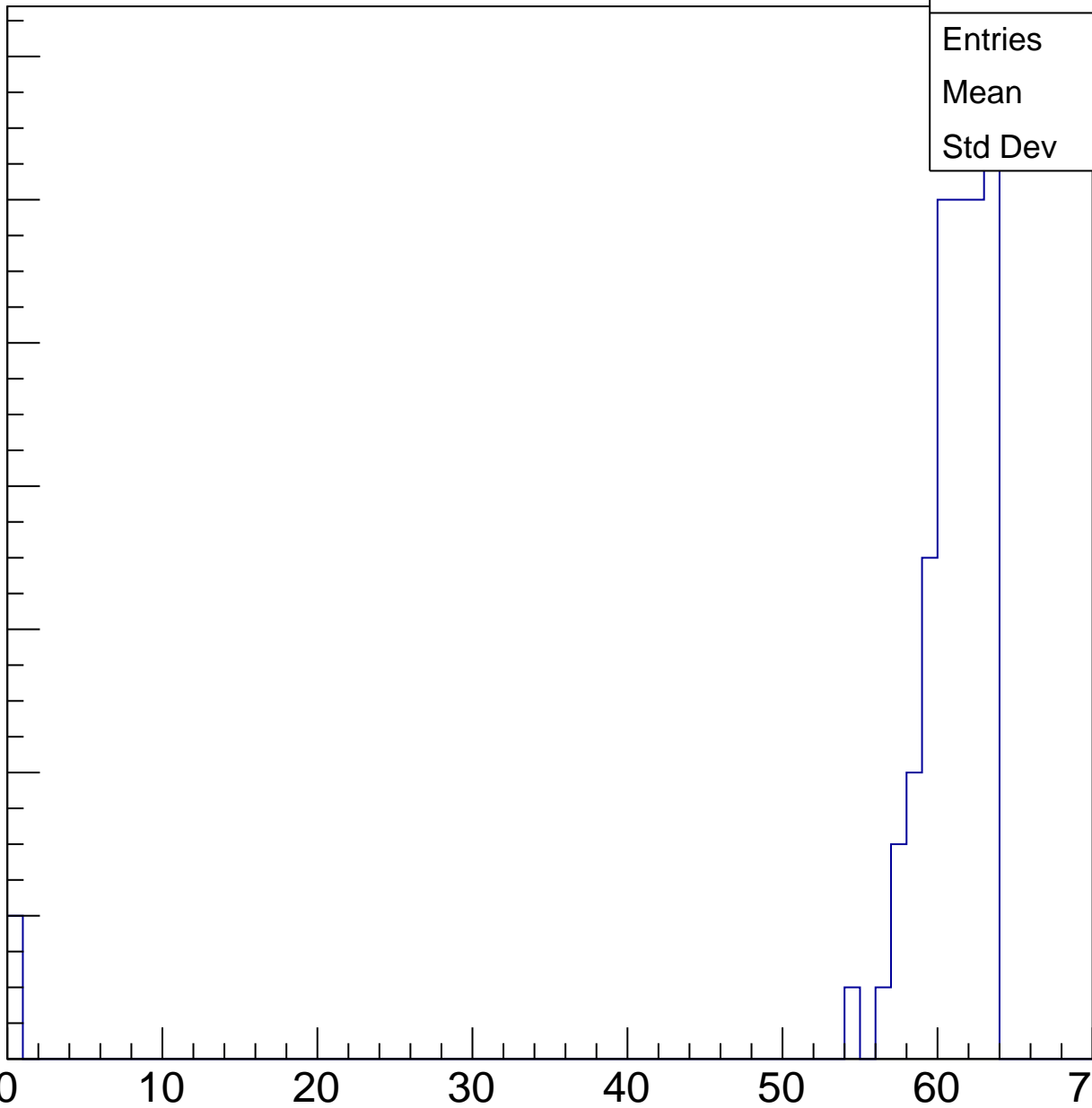
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	68
Mean	58.88
Std Dev	10.43

ampl

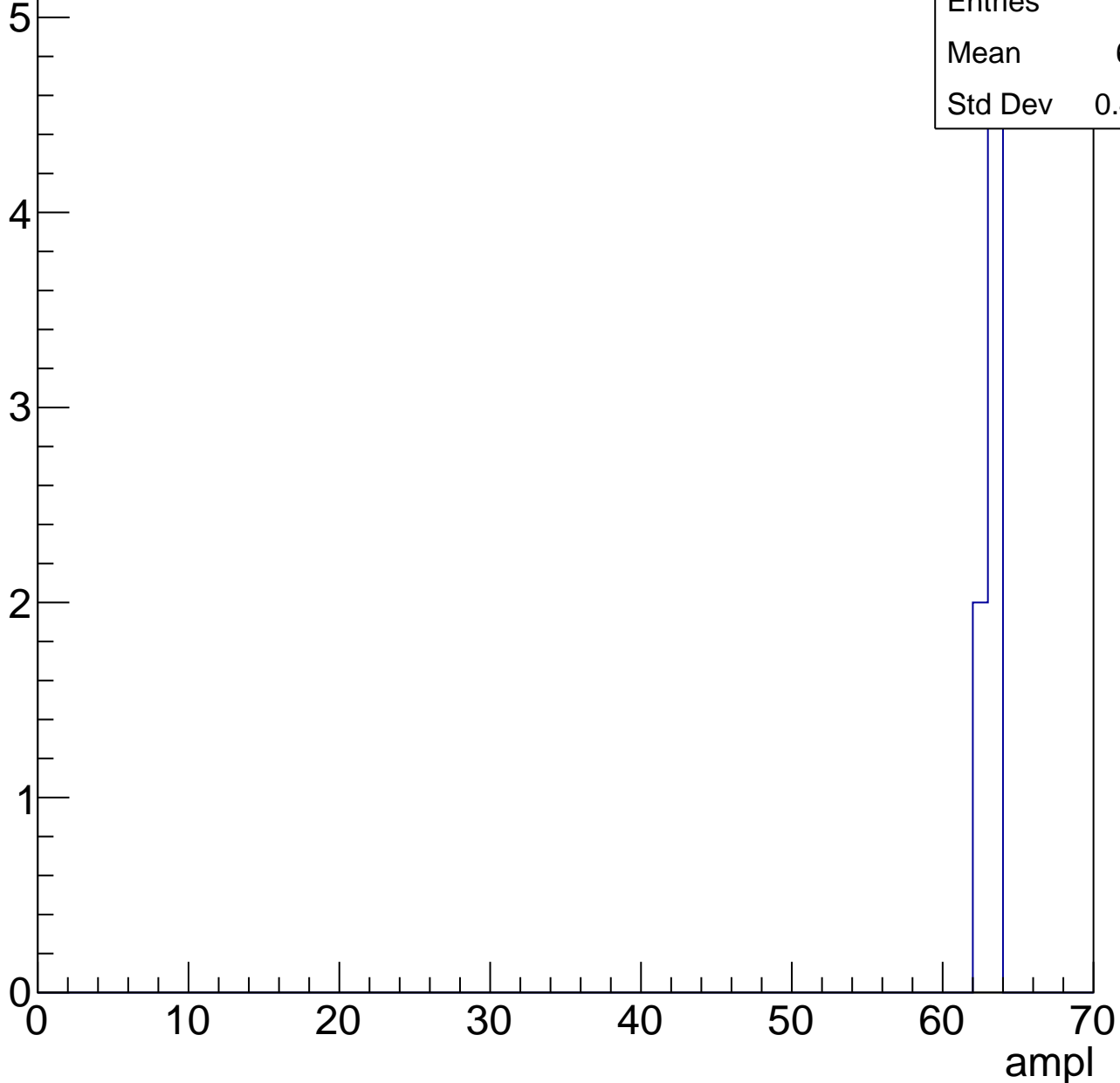


B1L001S, U20-ch83, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	7
Mean	62.71
Std Dev	0.4518



B1L001S, U20-ch83, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B1L001S, U20-ch84, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

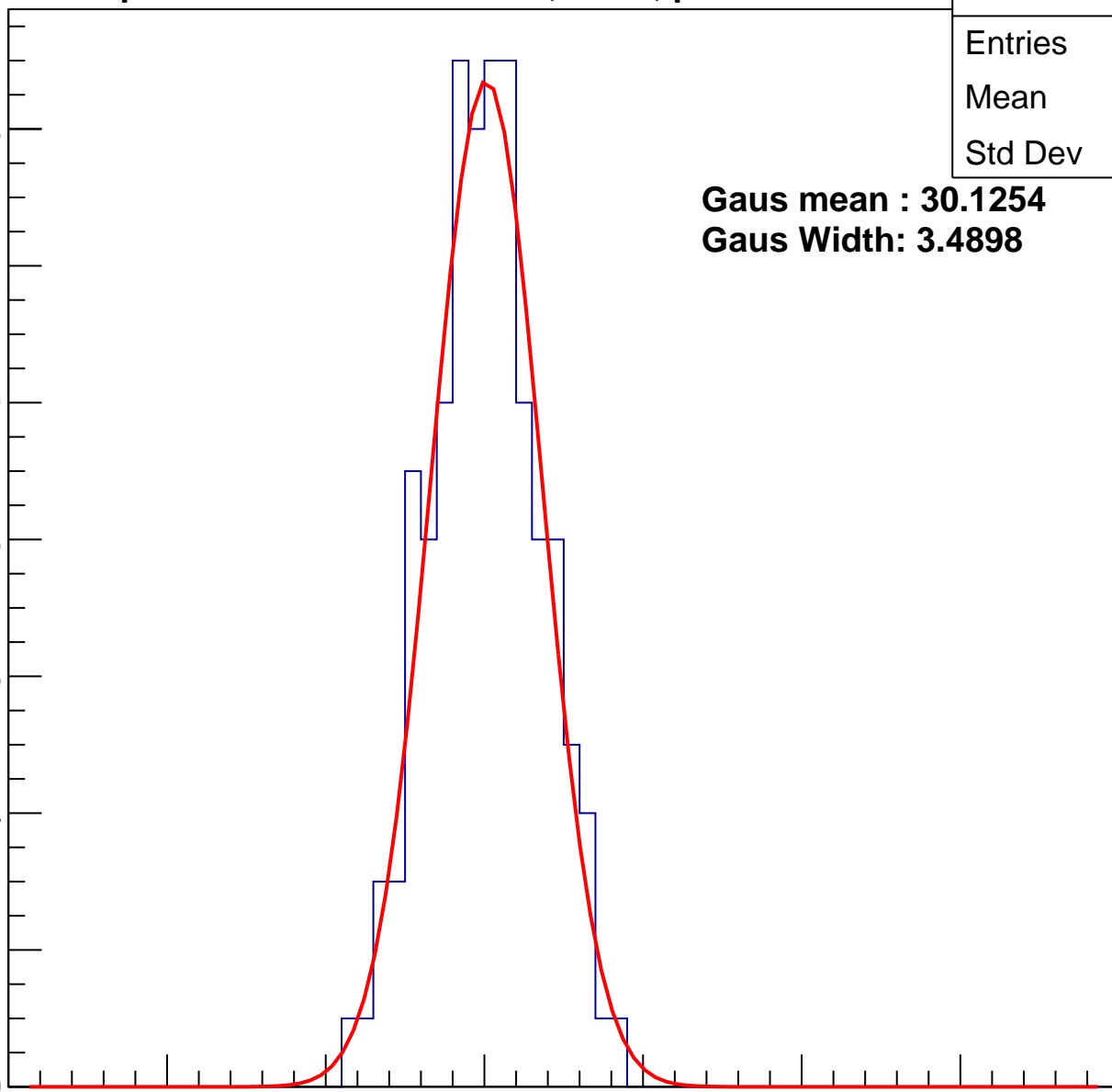
70

ampl

Entries	131
Mean	29.6
Std Dev	3.421

Gaus mean : 30.1254

Gaus Width: 3.4898



B1L001S, U20-ch84, adc1

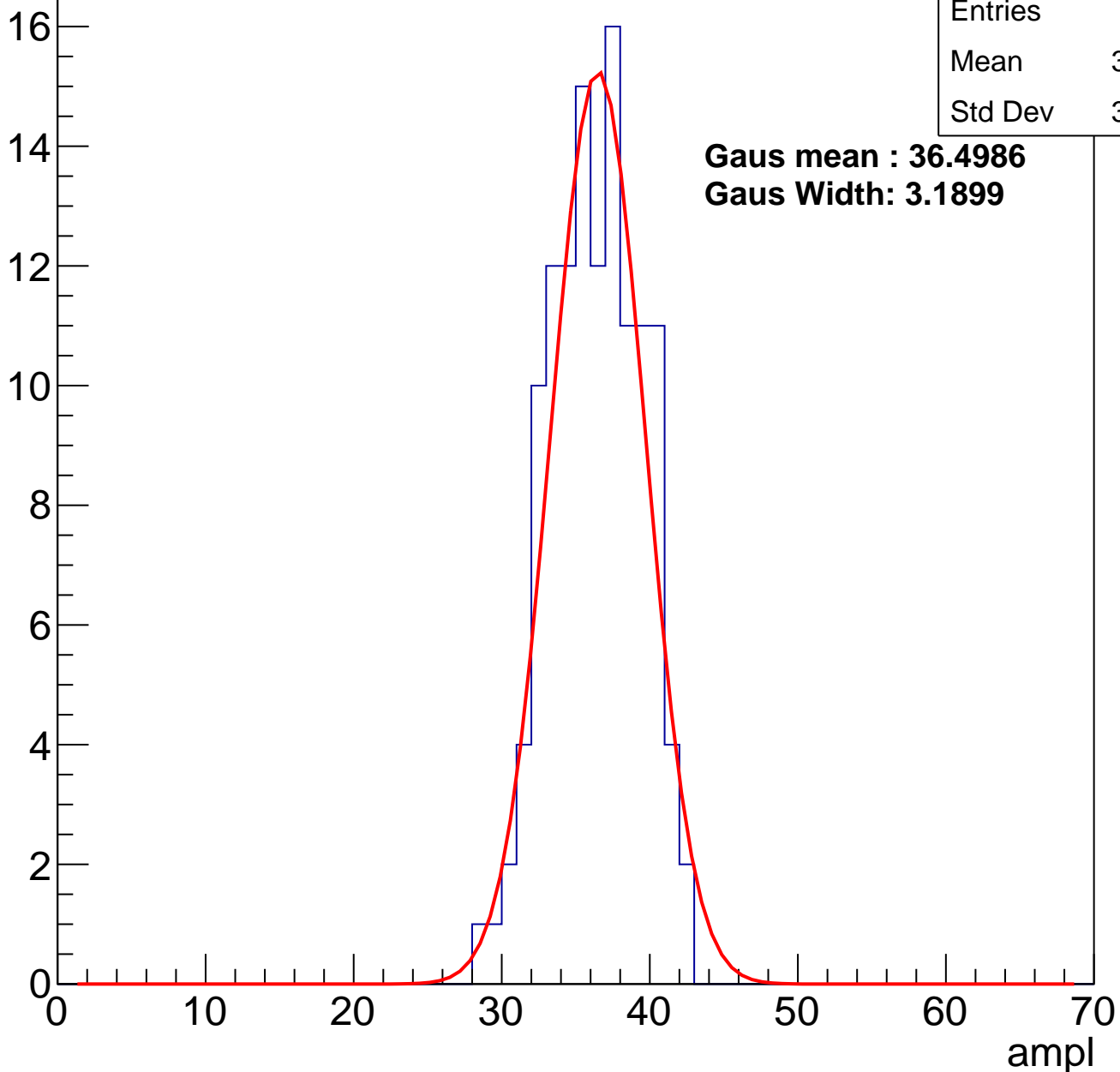
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	35.88
Std Dev	3.007

Gaus mean : 36.4986

Gaus Width: 3.1899

Entry



B1L001S, U20-ch84, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

131

Mean

42.64

Std Dev

3.236

Gaus mean : 43.0399

Gaus Width: 3.4078

0

10

20

30

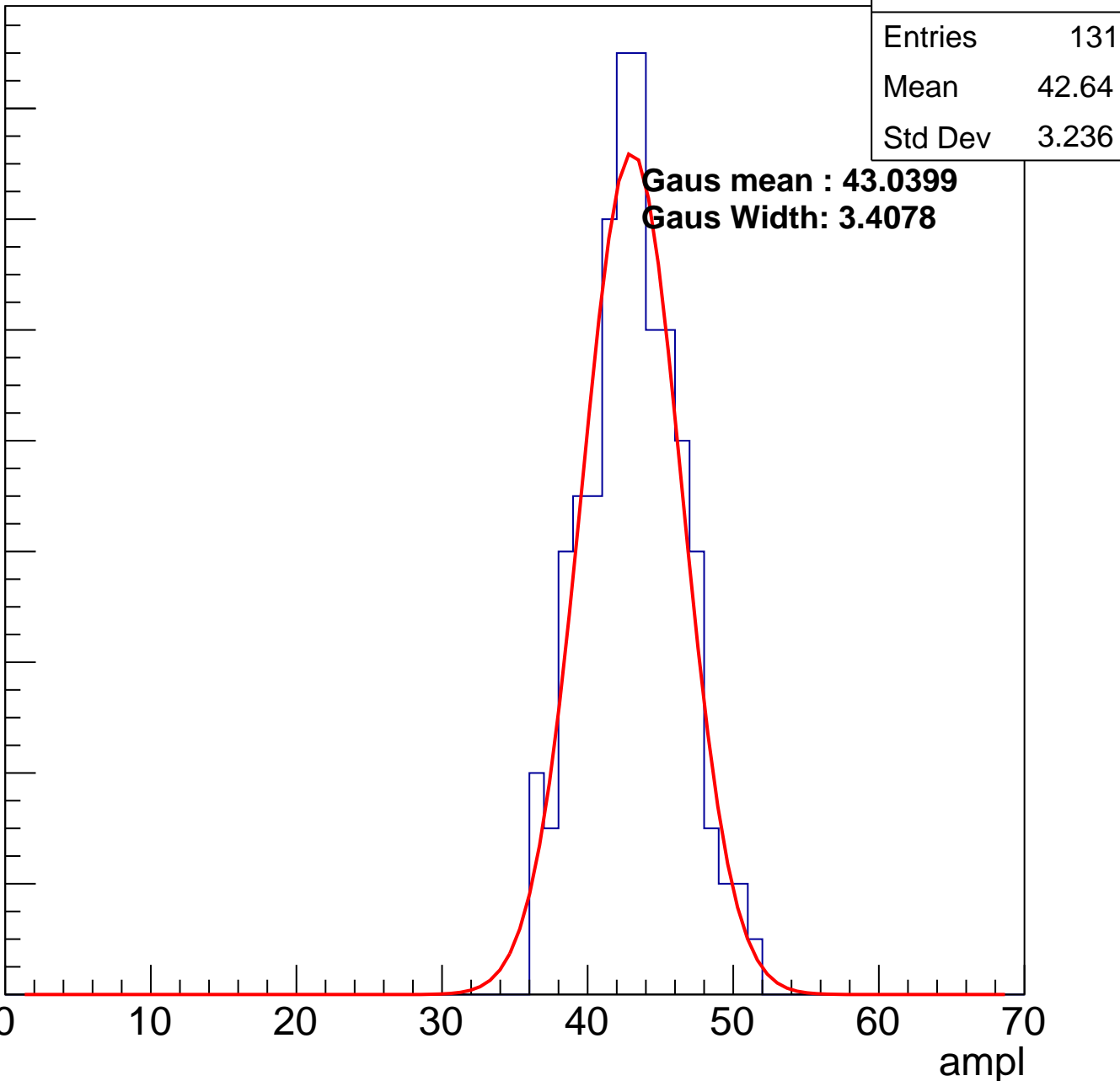
40

50

60

70

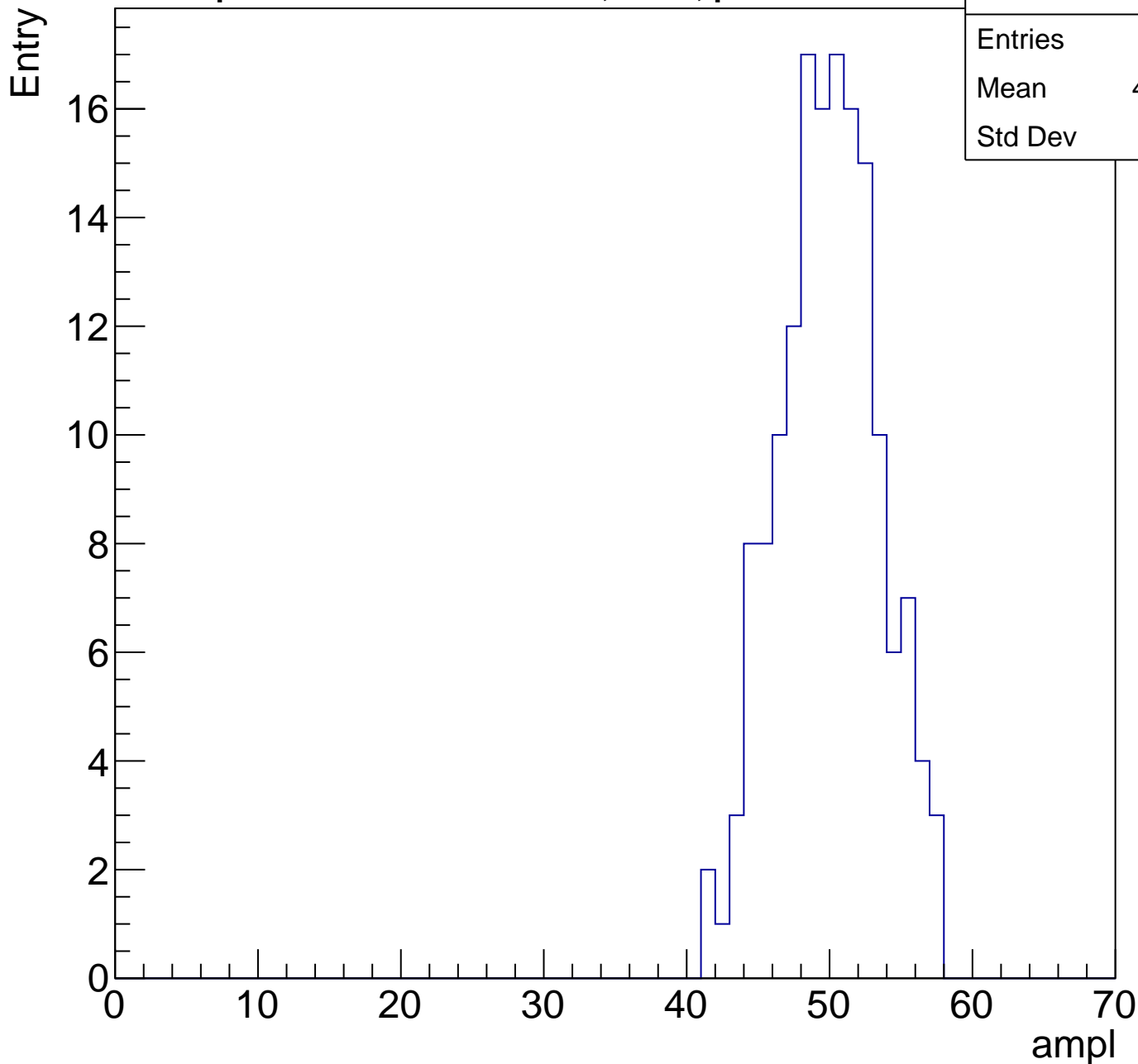
ampl



B1L001S, U20-ch84, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	49.48
Std Dev	3.5



B1L001S, U20-ch84, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

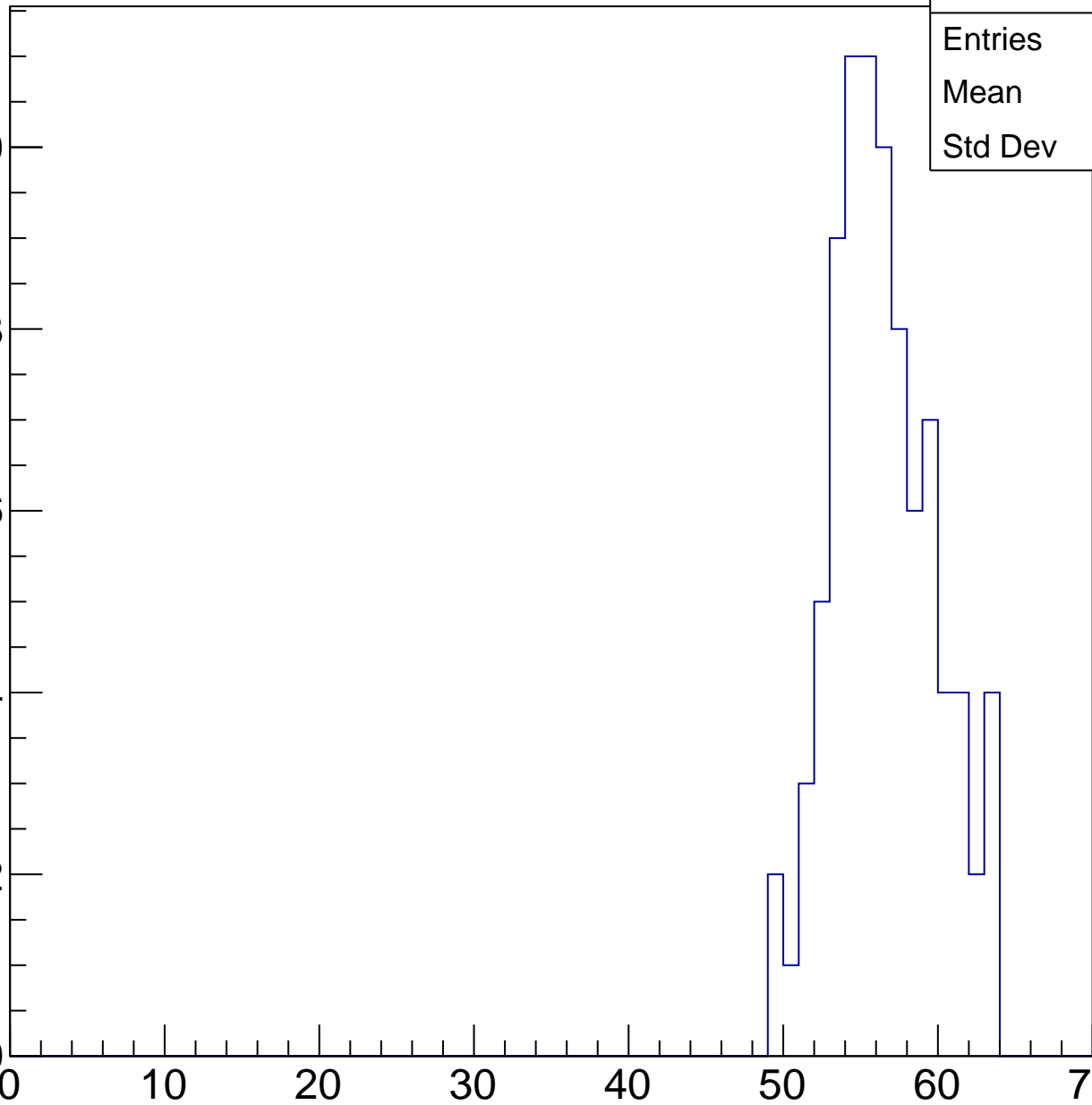
40

50

60

ampl

Entries	87
Mean	56.02
Std Dev	3.308



B1L001S, U20-ch84, adc5

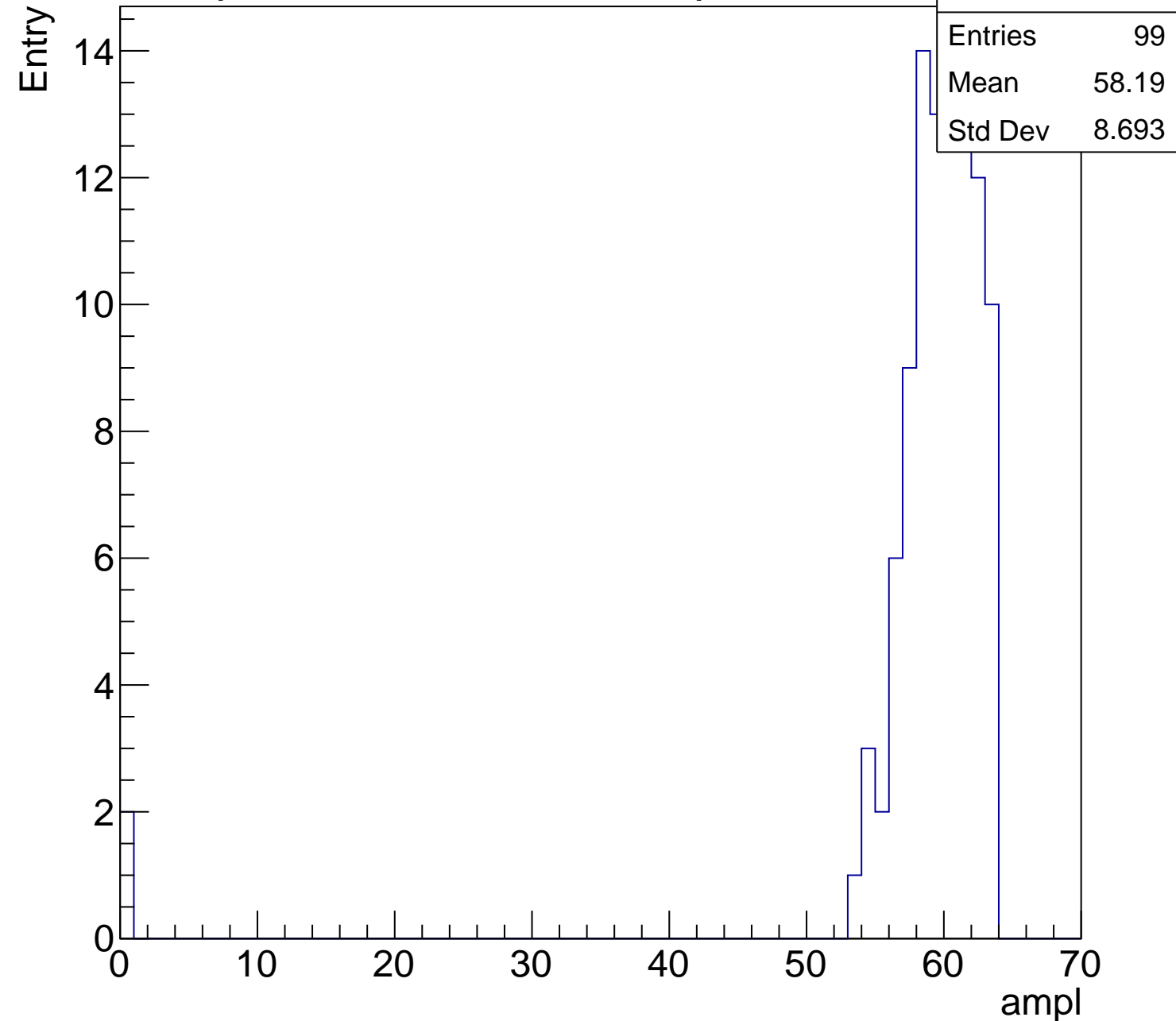
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	58.19
Std Dev	8.693

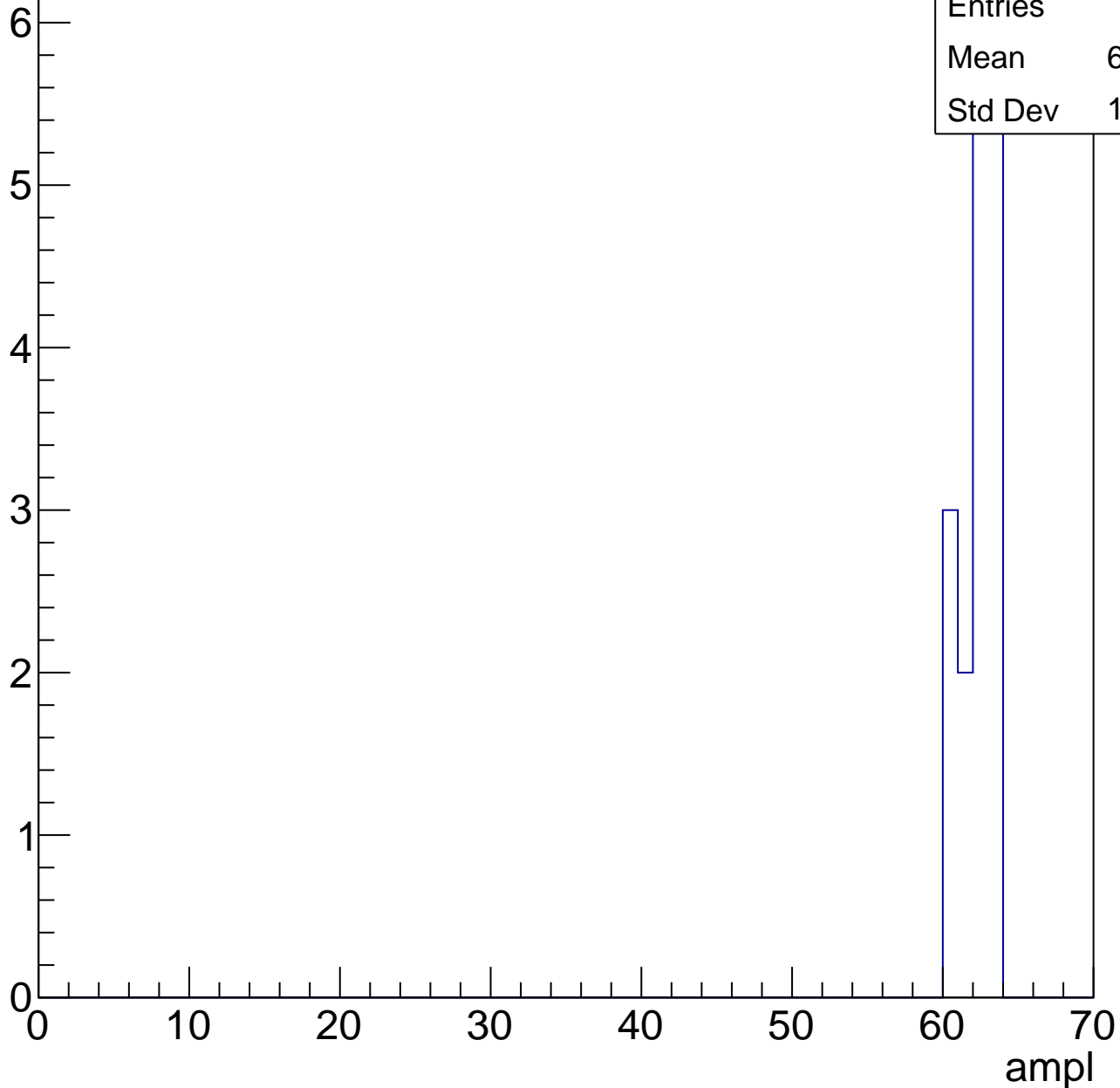
ampl



B1L001S, U20-ch84, adc6

calib_packv5_042523_0143.root, FC#2, port C2

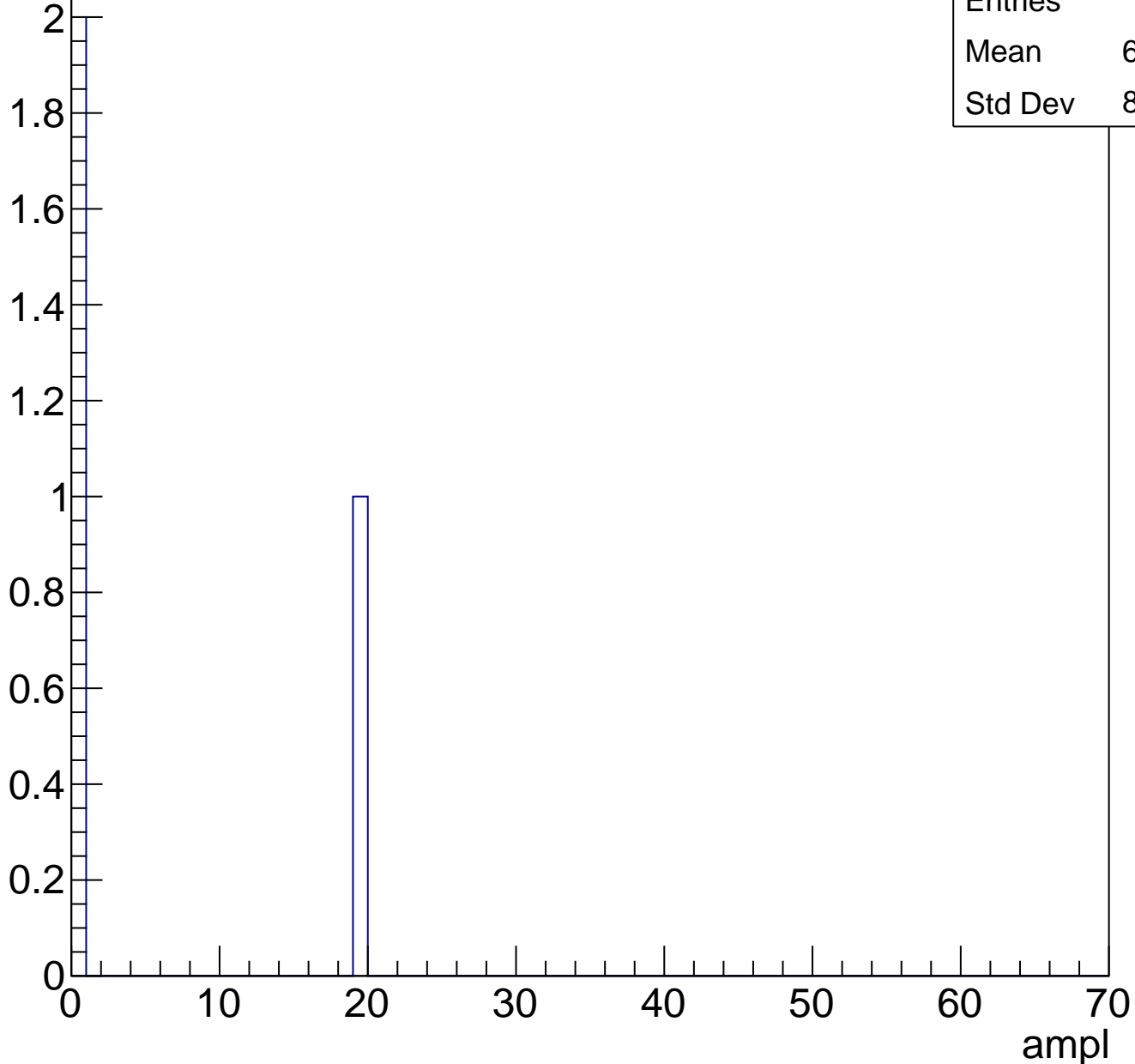
Entry



B1L001S, U20-ch84, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L001S, U20-ch85, adc0

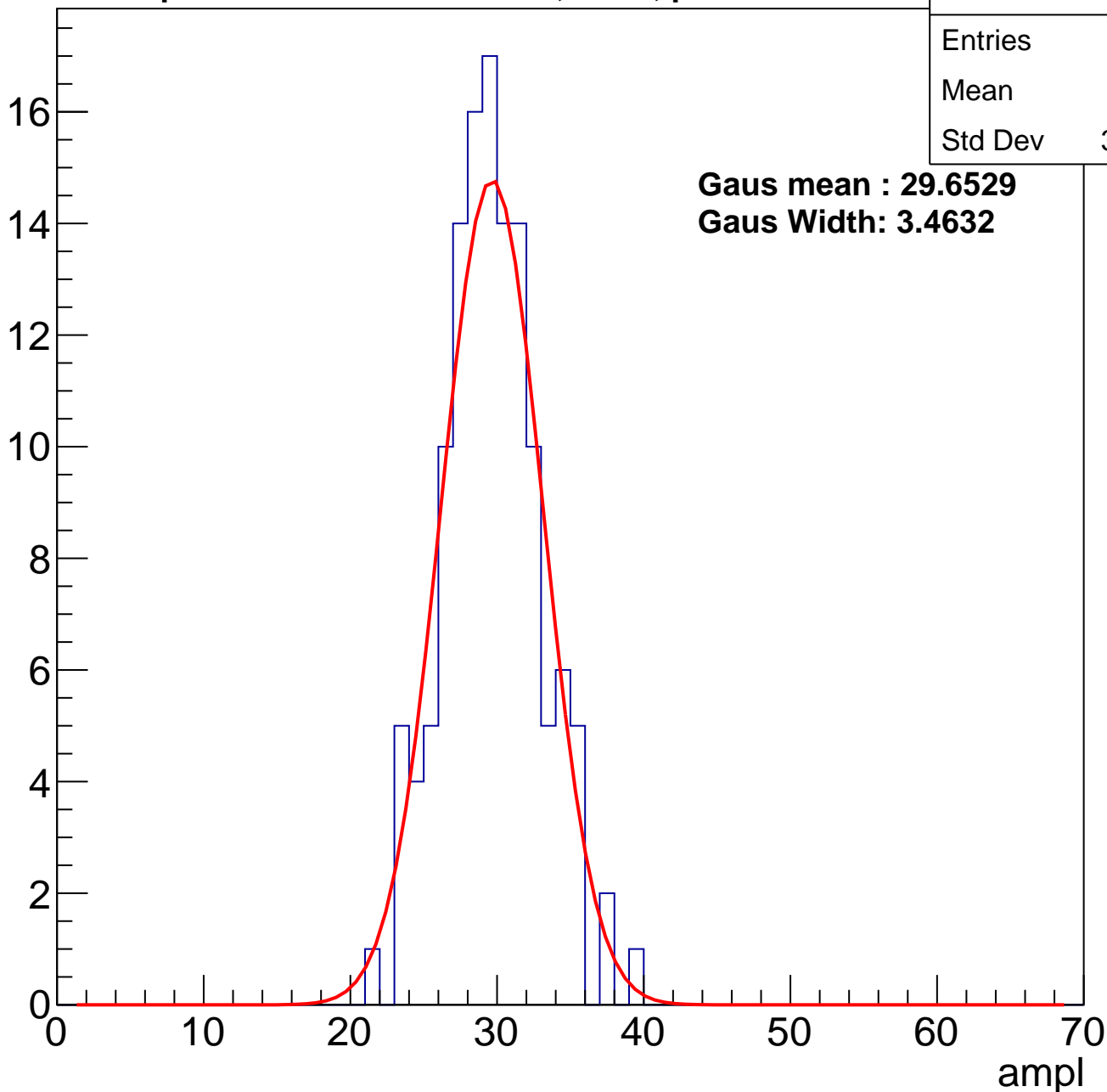
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	29.2
Std Dev	3.269

Gaus mean : 29.6529

Gaus Width: 3.4632

Entry



B1L001S, U20-ch85, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

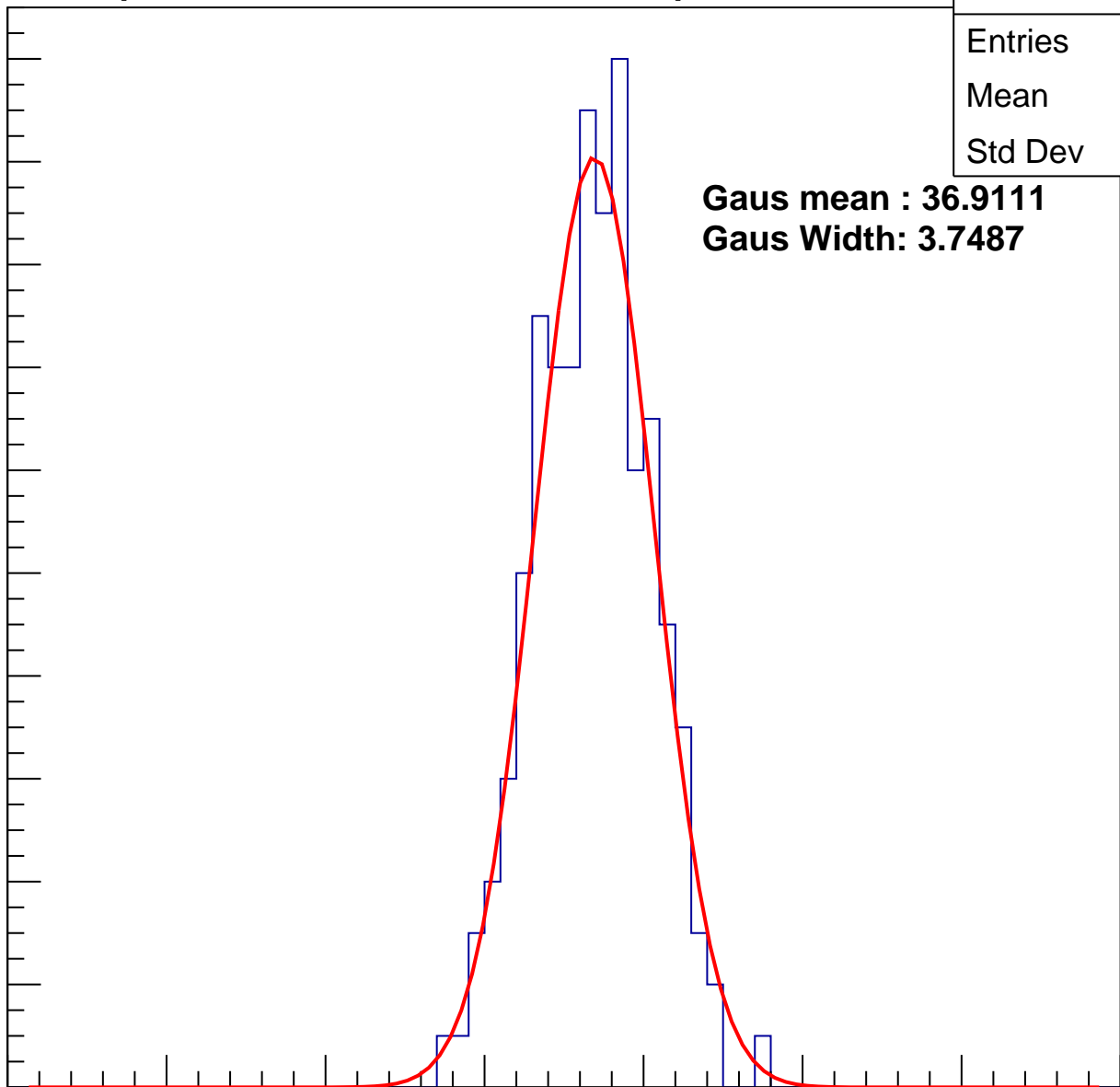
Entries	171
Mean	36.36
Std Dev	3.599

Gaus mean : 36.9111

Gaus Width: 3.7487

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch85, adc2

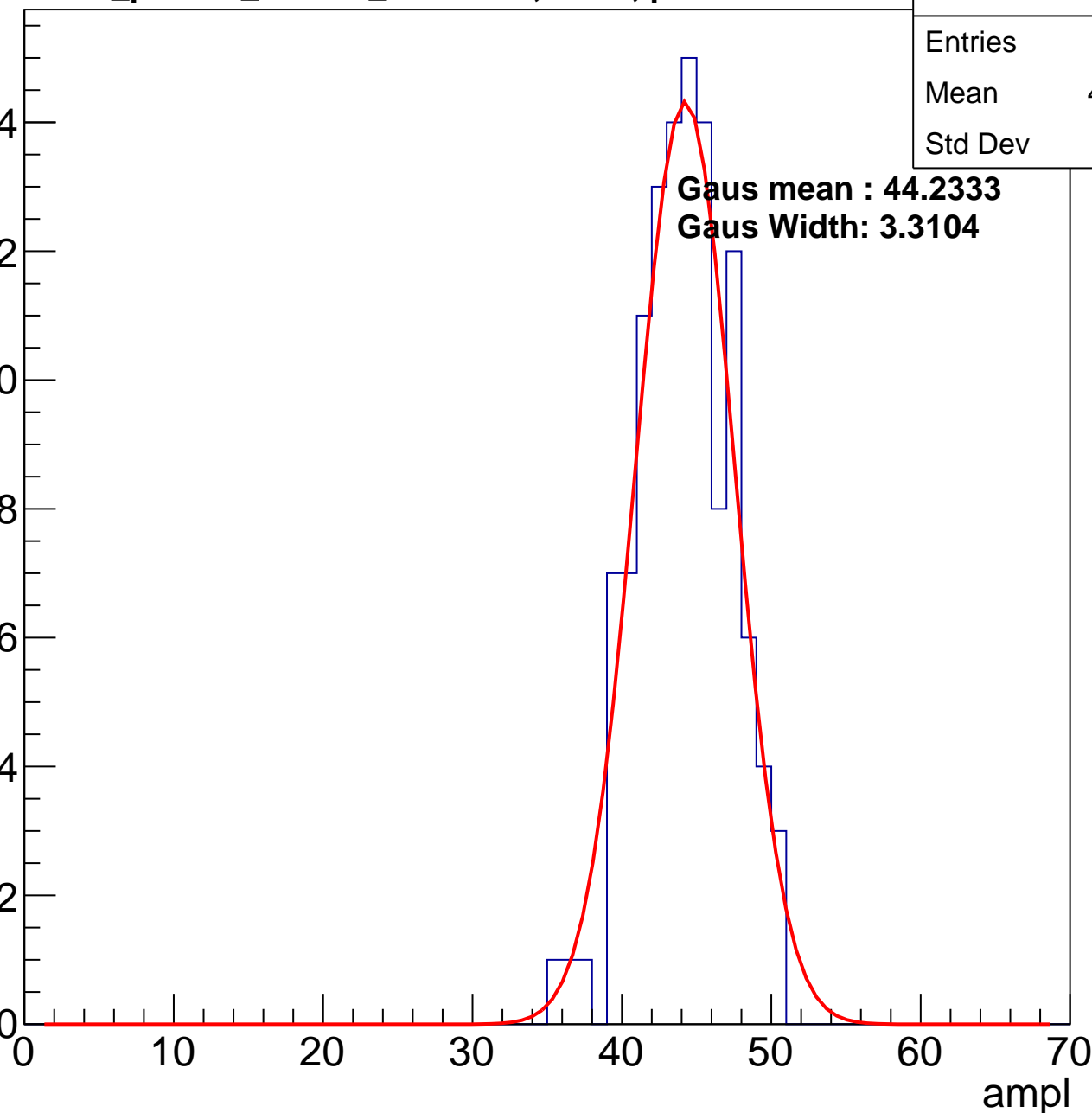
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	117
Mean	43.73
Std Dev	3.06

Gaus mean : 44.2333
Gaus Width: 3.3104



B1L001S, U20-ch85, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	49.6
Std Dev	3.099

Entry

14

12

10

8

6

4

2

0

0

10

20

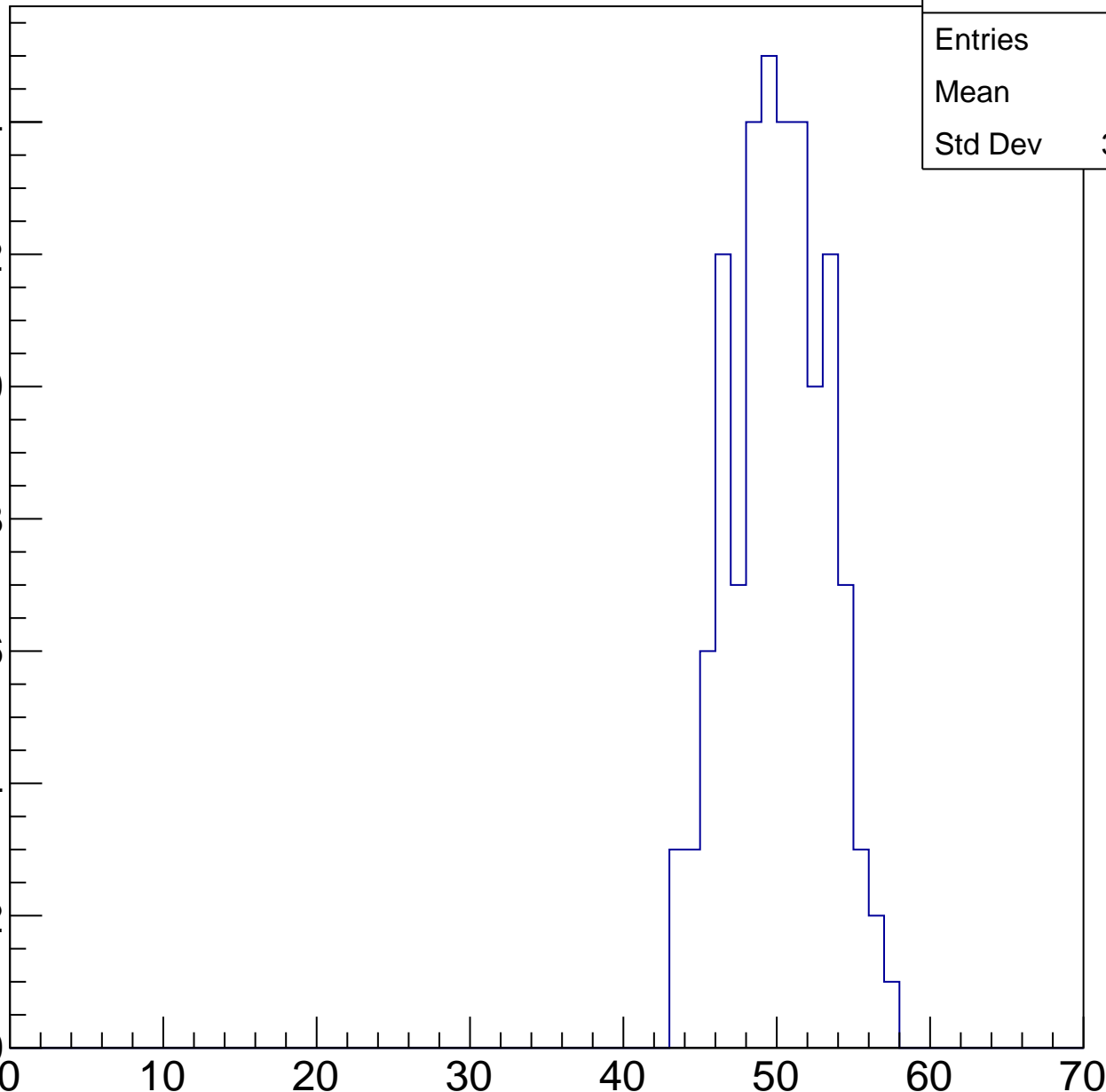
30

40

50

60

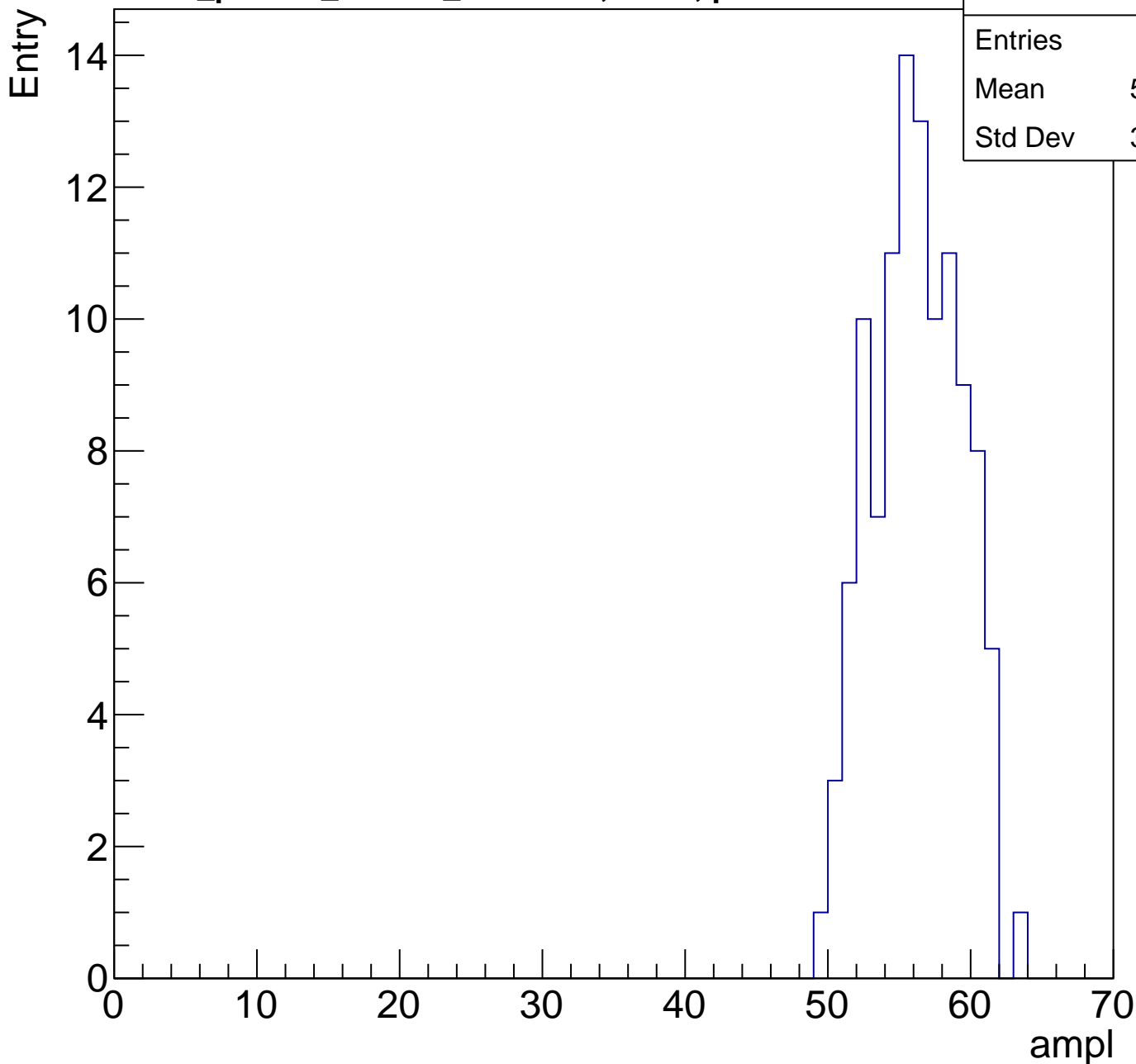
ampl



B1L001S, U20-ch85, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	55.73
Std Dev	3.055



B1L001S, U20-ch85, adc5

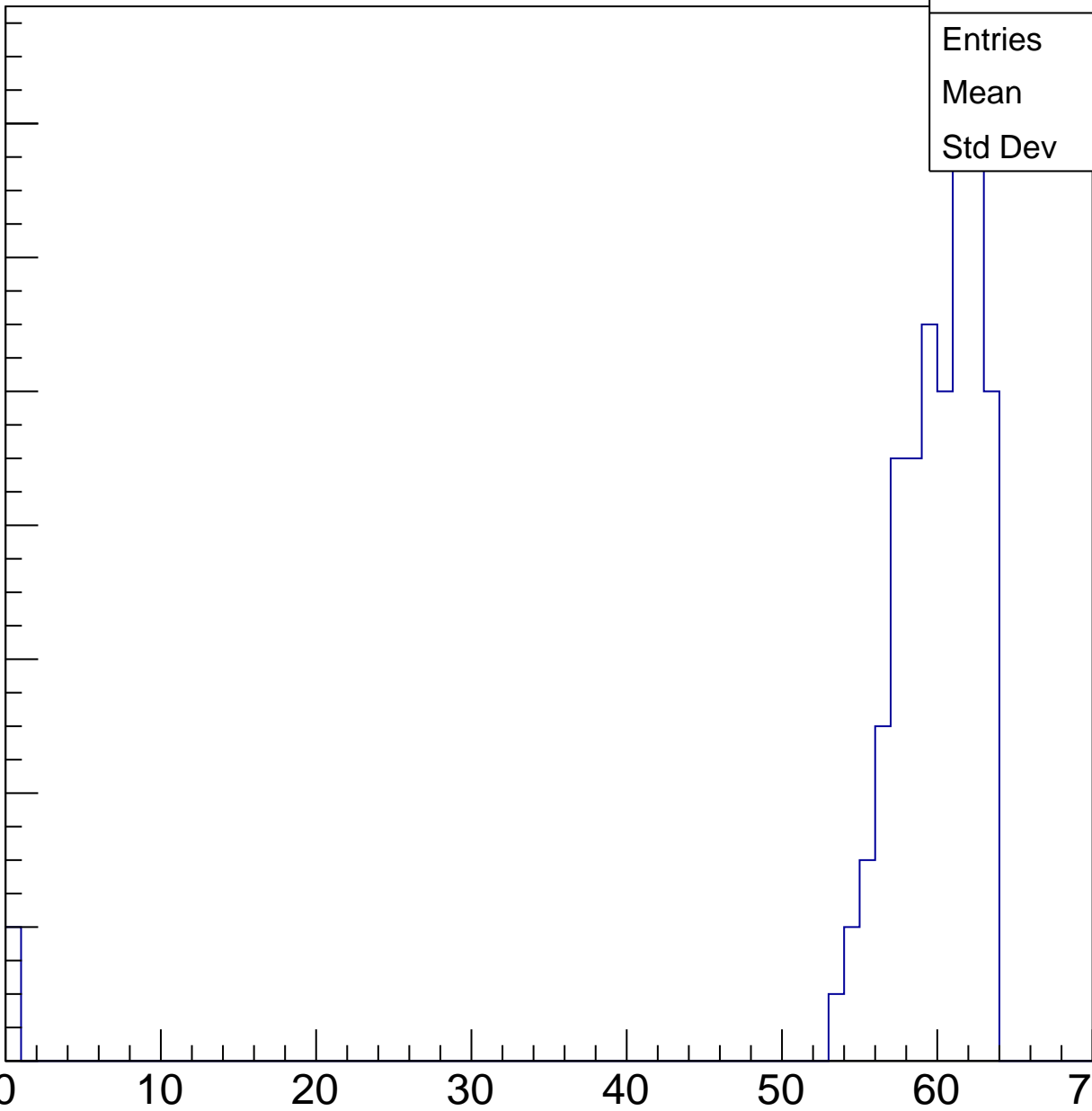
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.29
Std Dev	9.079

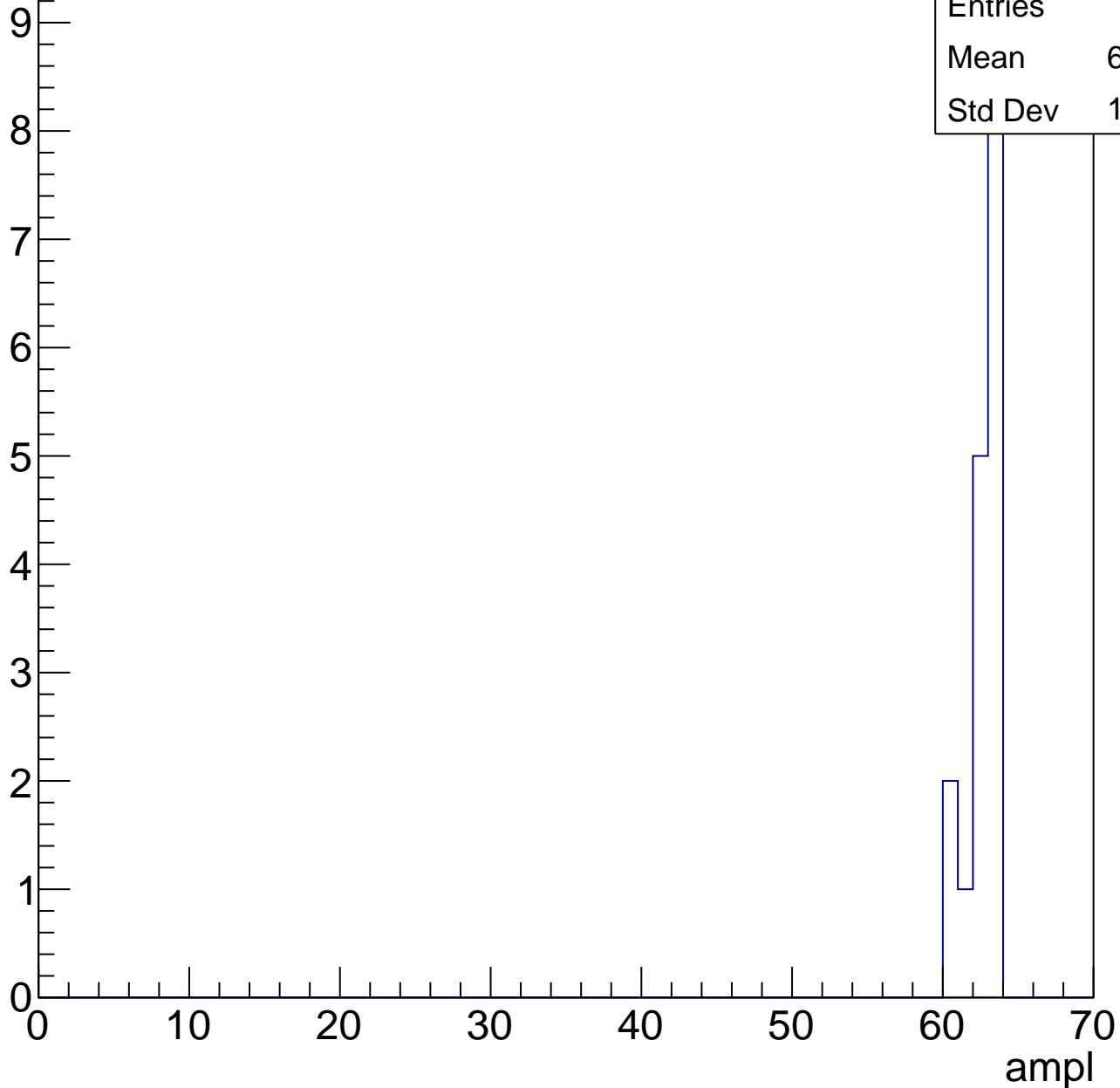
ampl



B1L001S, U20-ch85, adc6

calib_packv5_042523_0143.root, FC#2, port C2

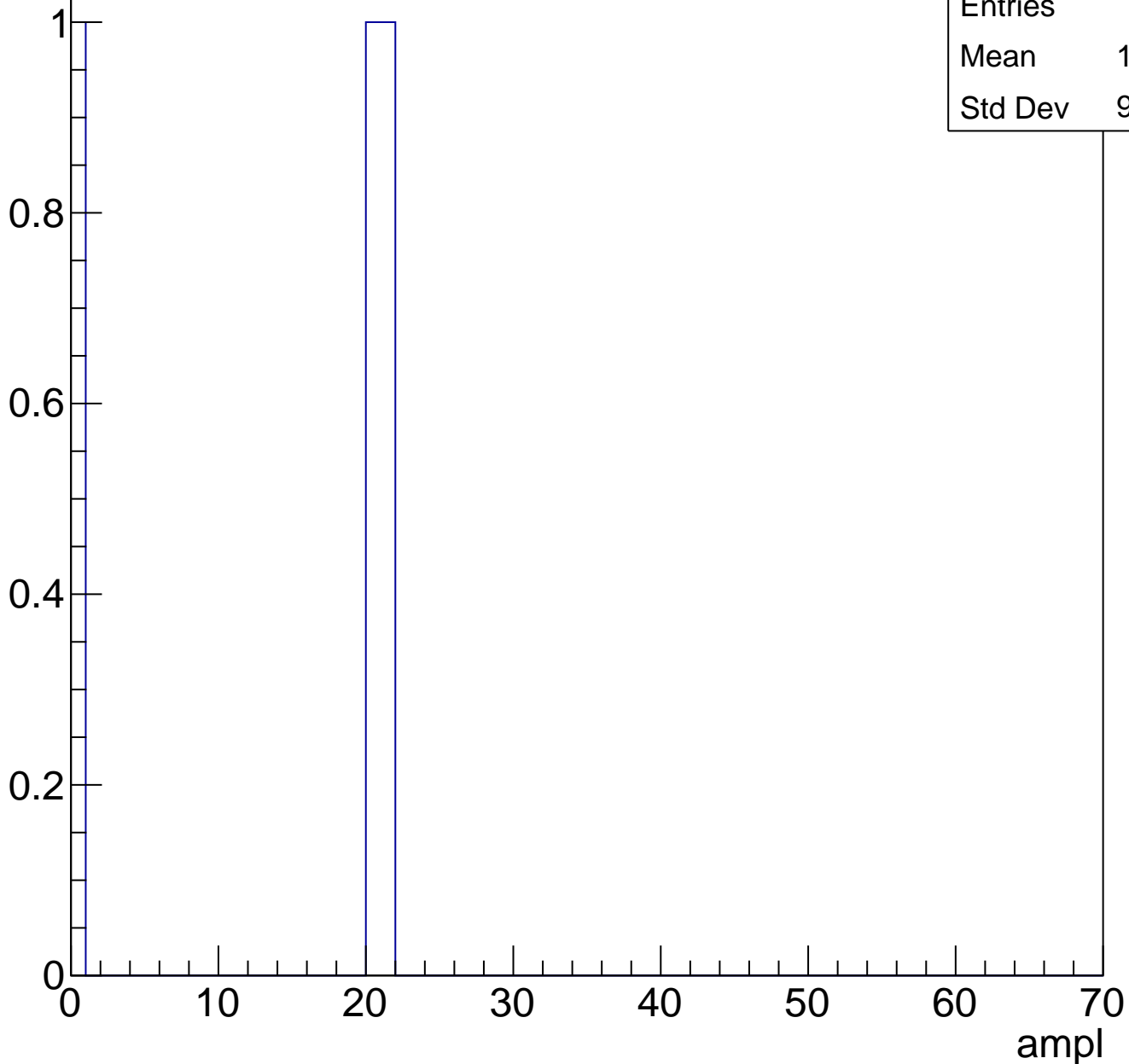
Entry



B1L001S, U20-ch85, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	13.67
Std Dev	9.672

B1L001S, U20-ch86, adc0

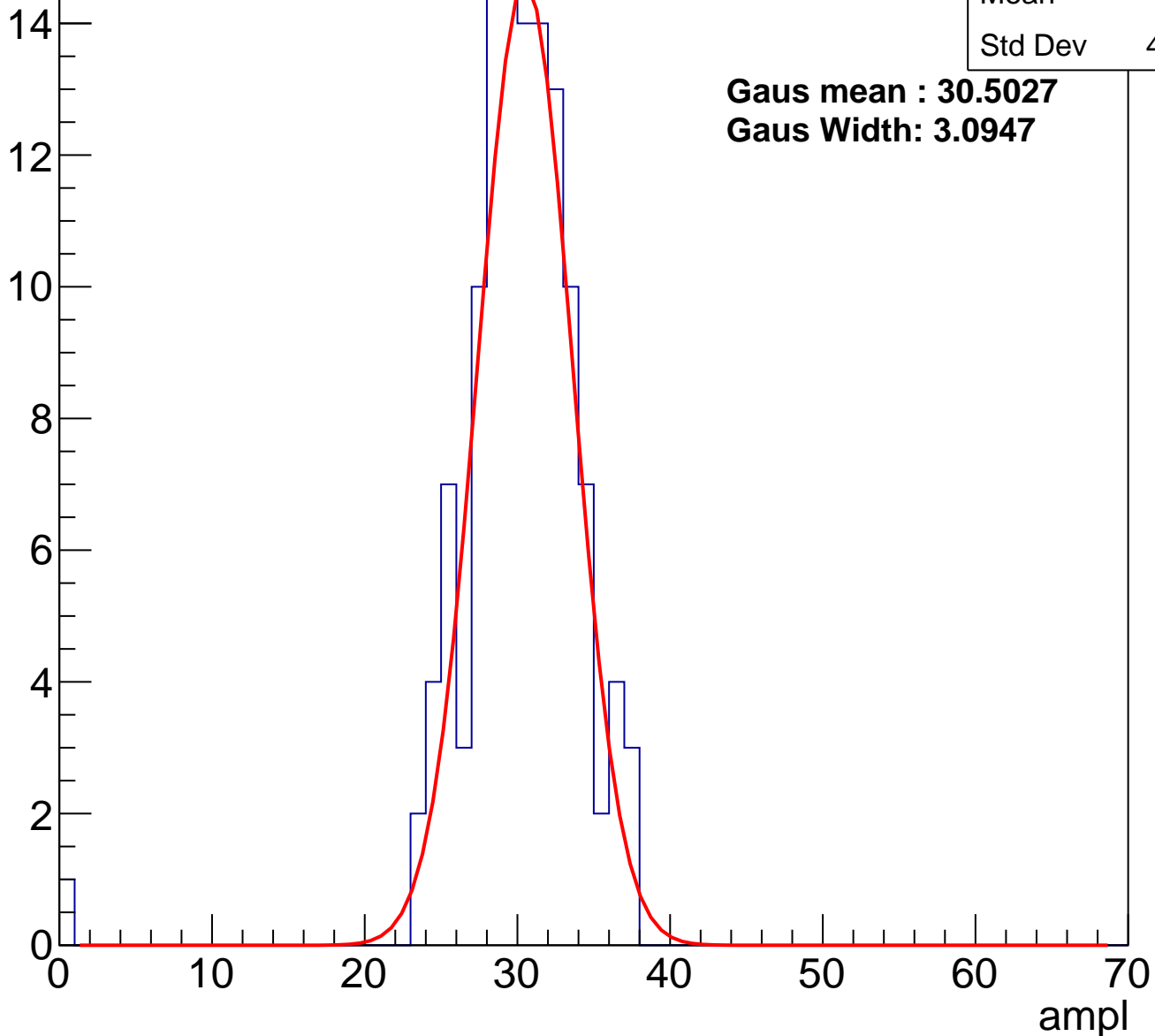
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	29.7
Std Dev	4.143

Gaus mean : 30.5027

Gaus Width: 3.0947

Entry



B1L001S, U20-ch86, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	35.77
Std Dev	3.241

Gaus mean : 36.3167

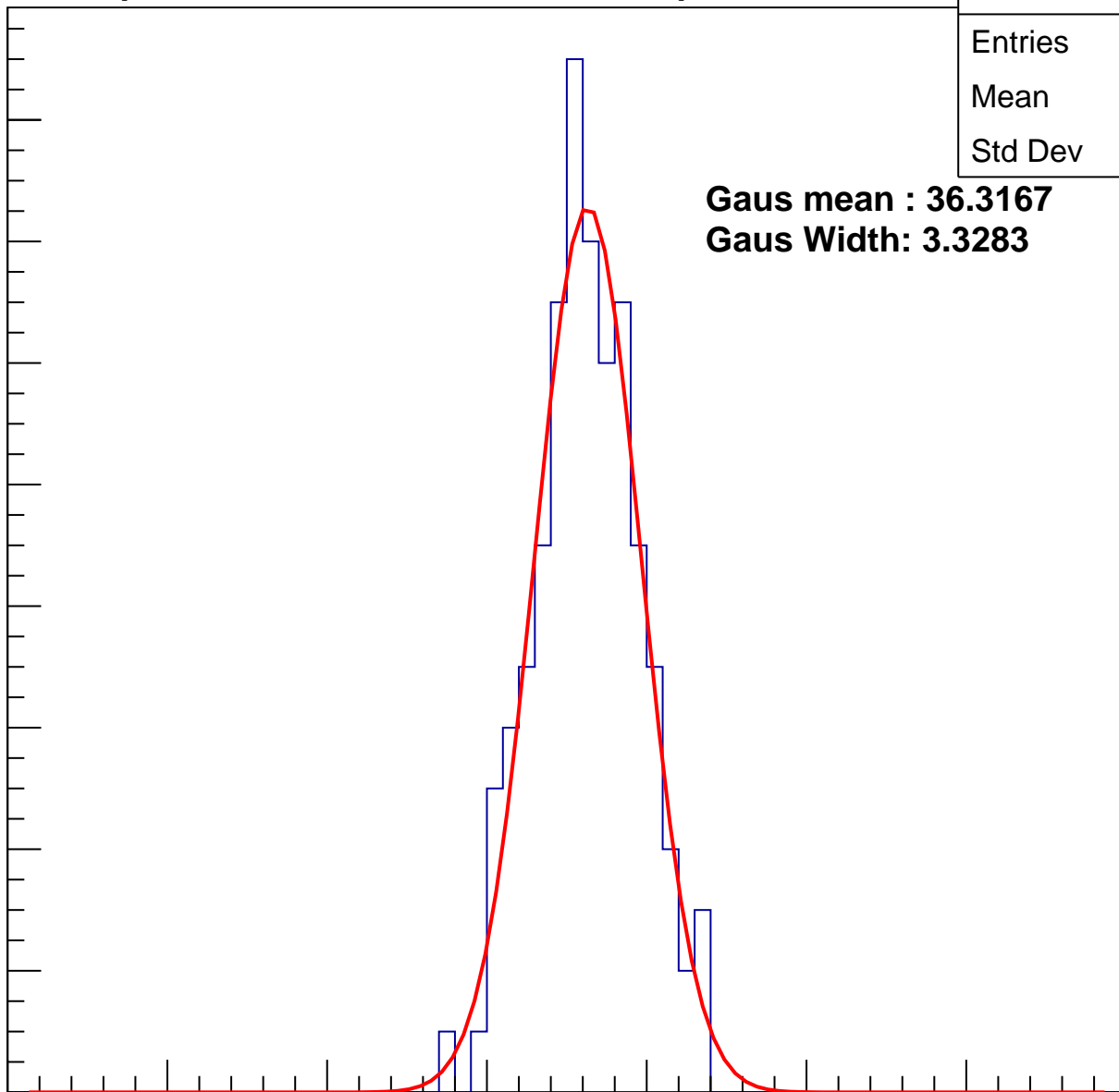
Gaus Width: 3.3283

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch86, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

138

Mean

42.61

Std Dev

3.342

Gaus mean : 42.8266

Gaus Width: 3.2692

0

10

20

30

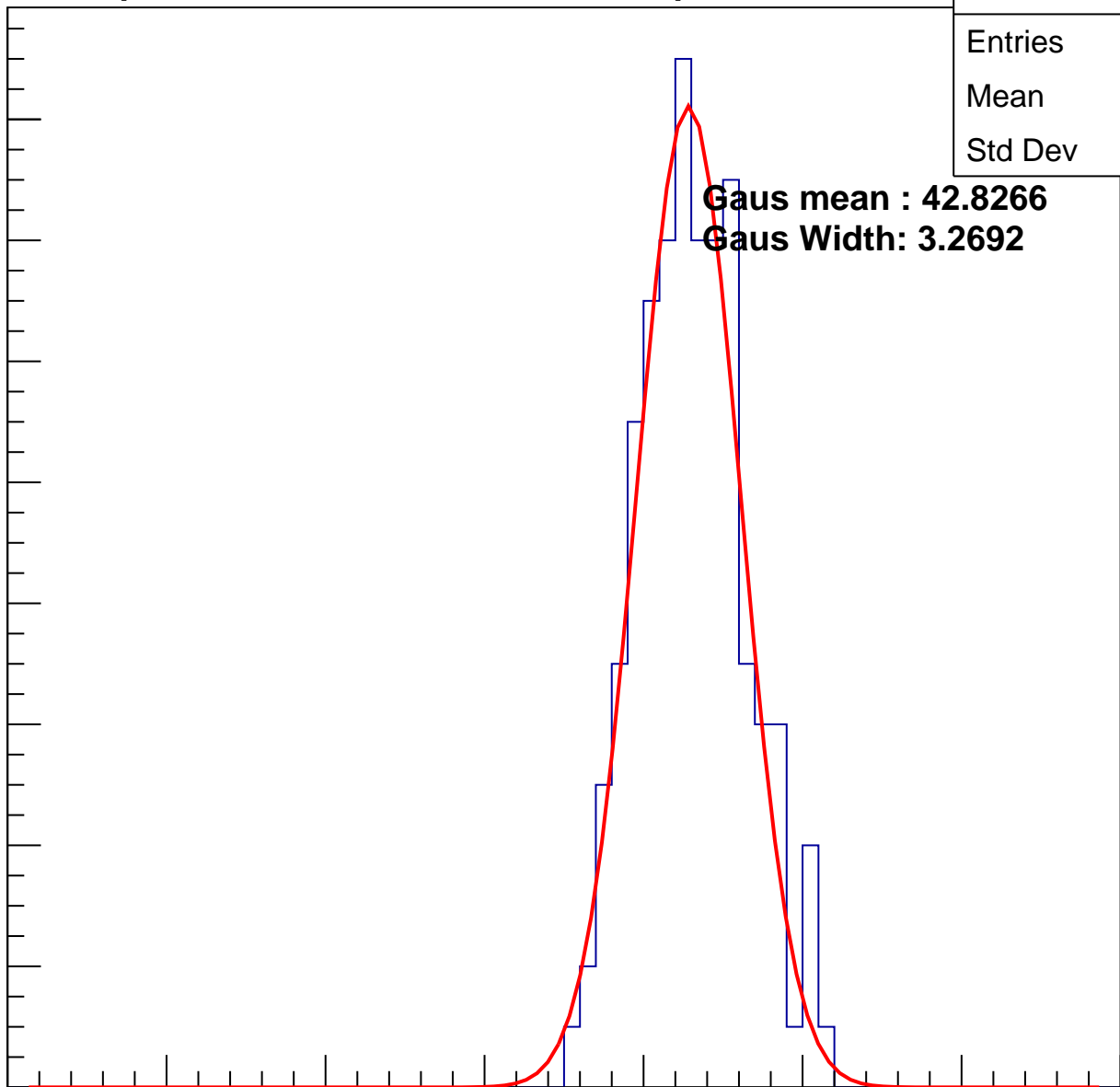
40

50

60

70

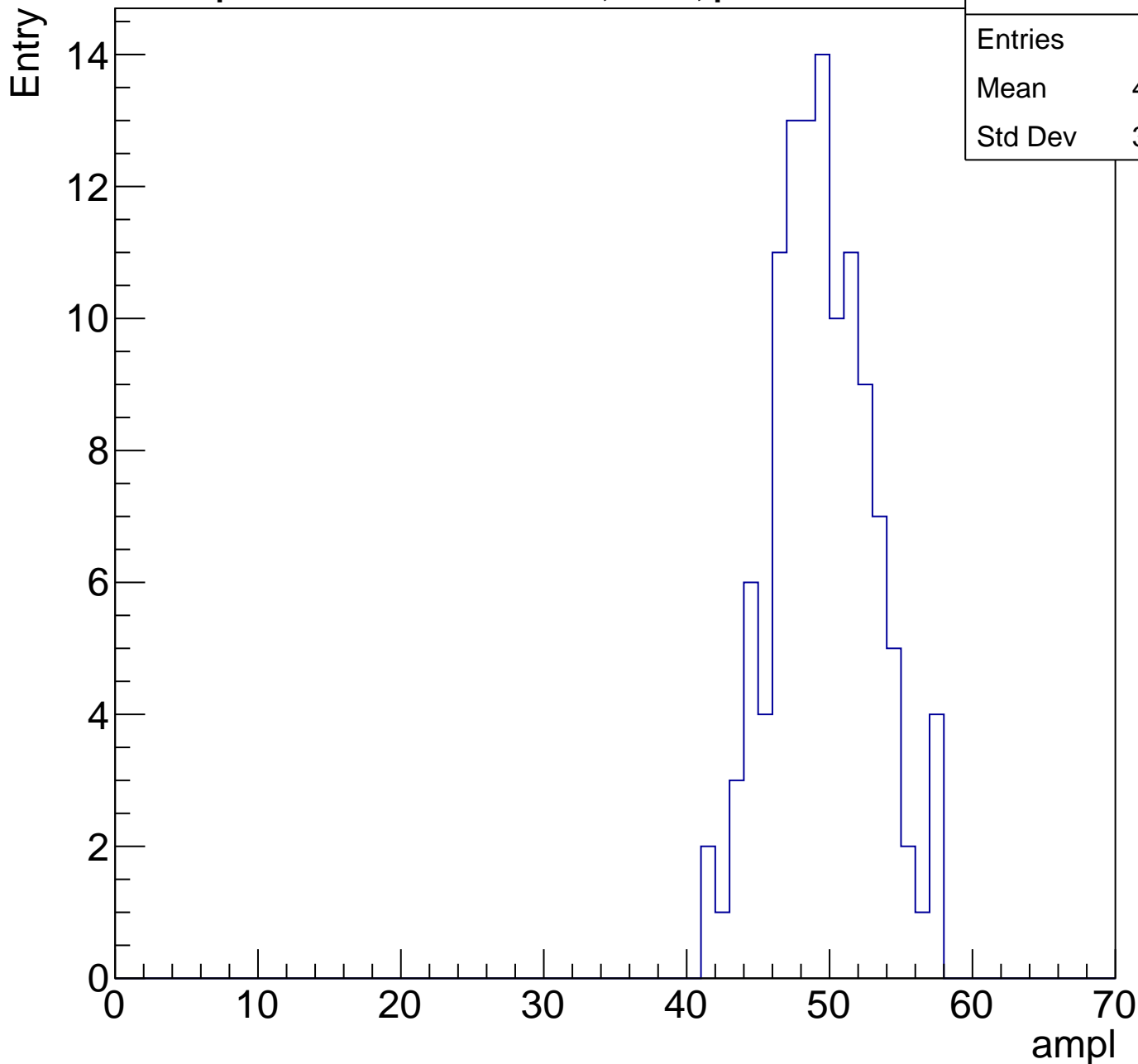
ampl



B1L001S, U20-ch86, adc3

calib_packv5_042523_0143.root, FC#2, port C2

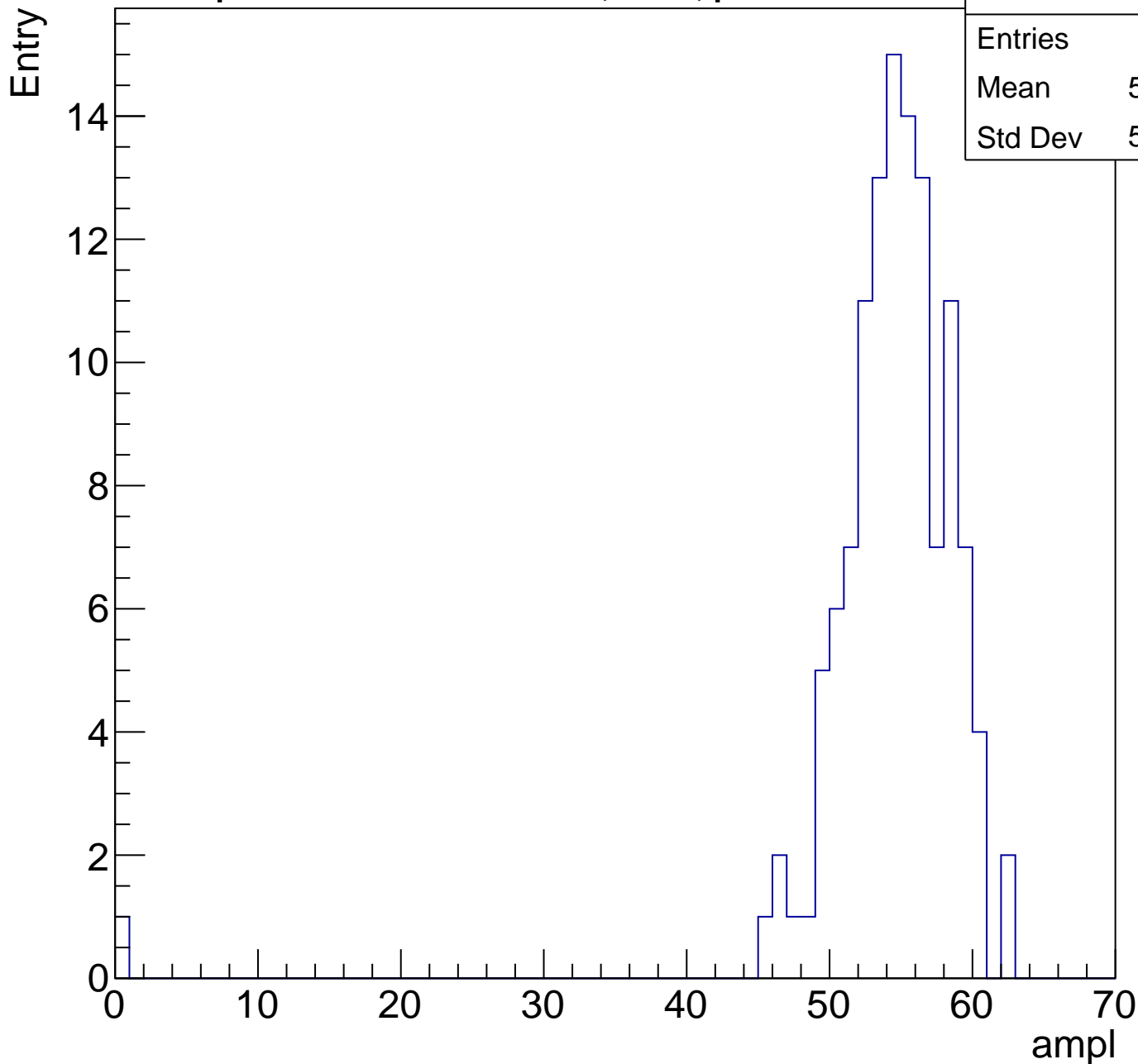
Entries	116
Mean	49.03
Std Dev	3.489



B1L001S, U20-ch86, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	53.86
Std Dev	5.962



B1L001S, U20-ch86, adc5

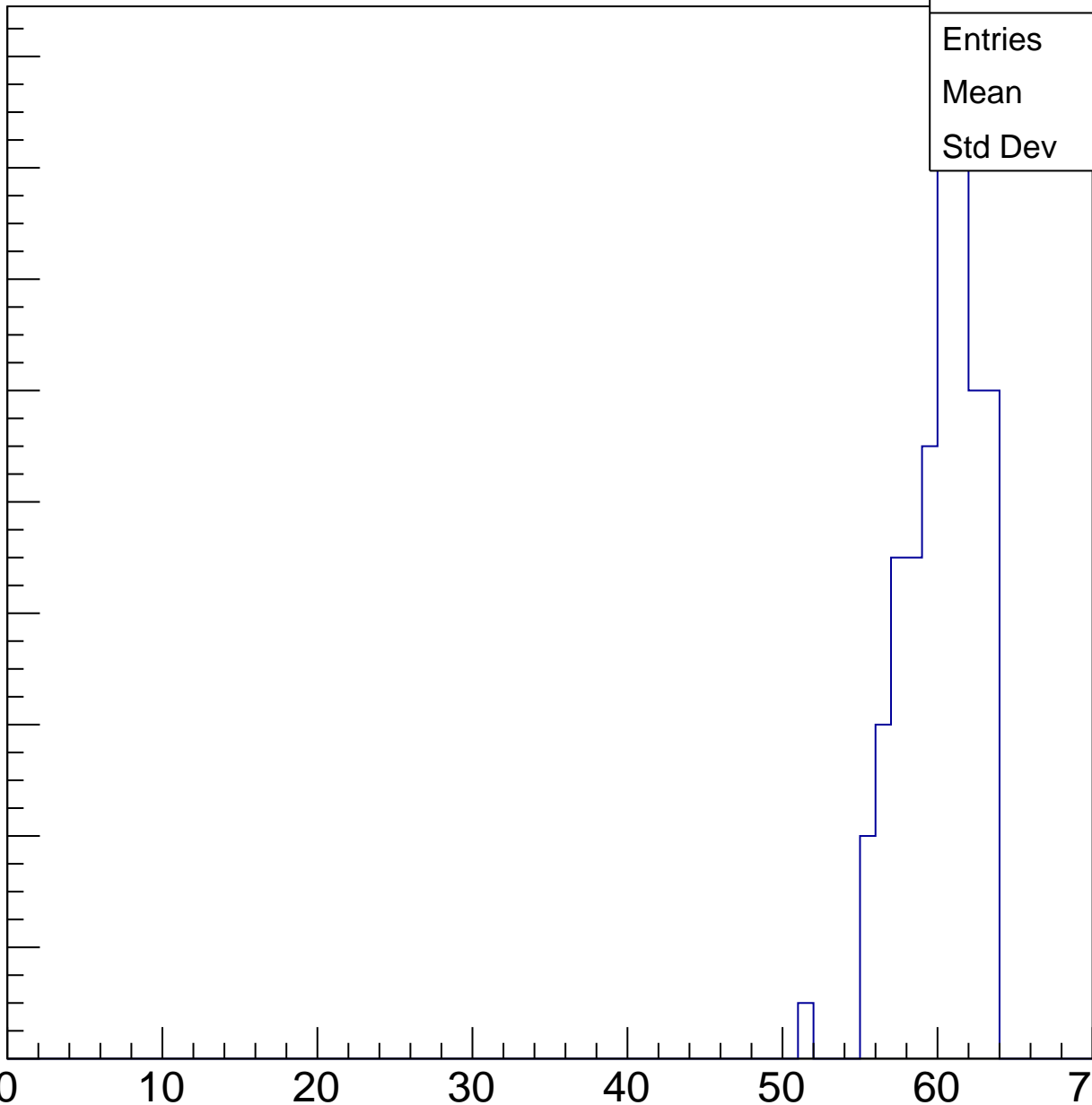
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.68
Std Dev	2.41

ampl

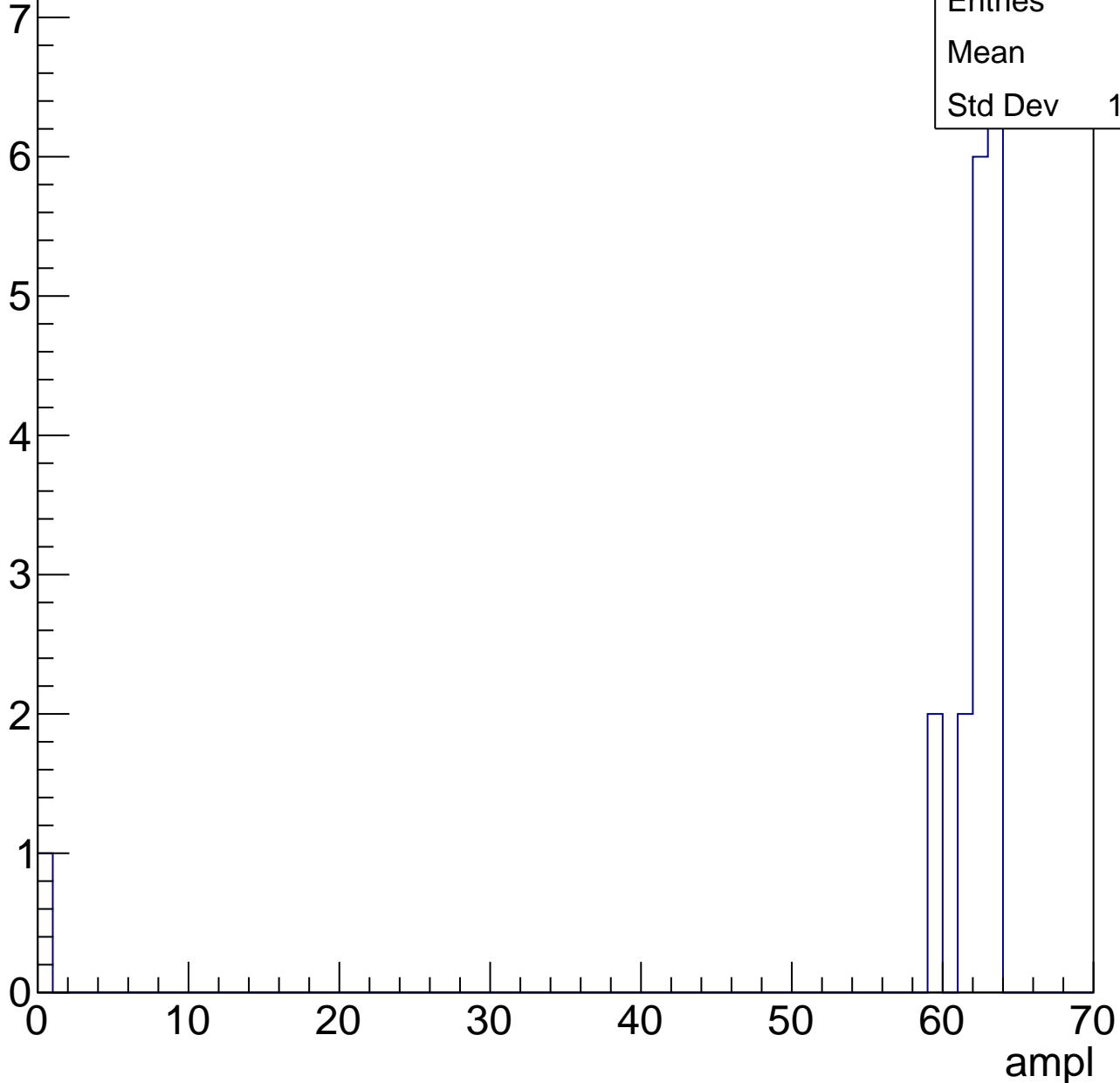


B1L001S, U20-ch86, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

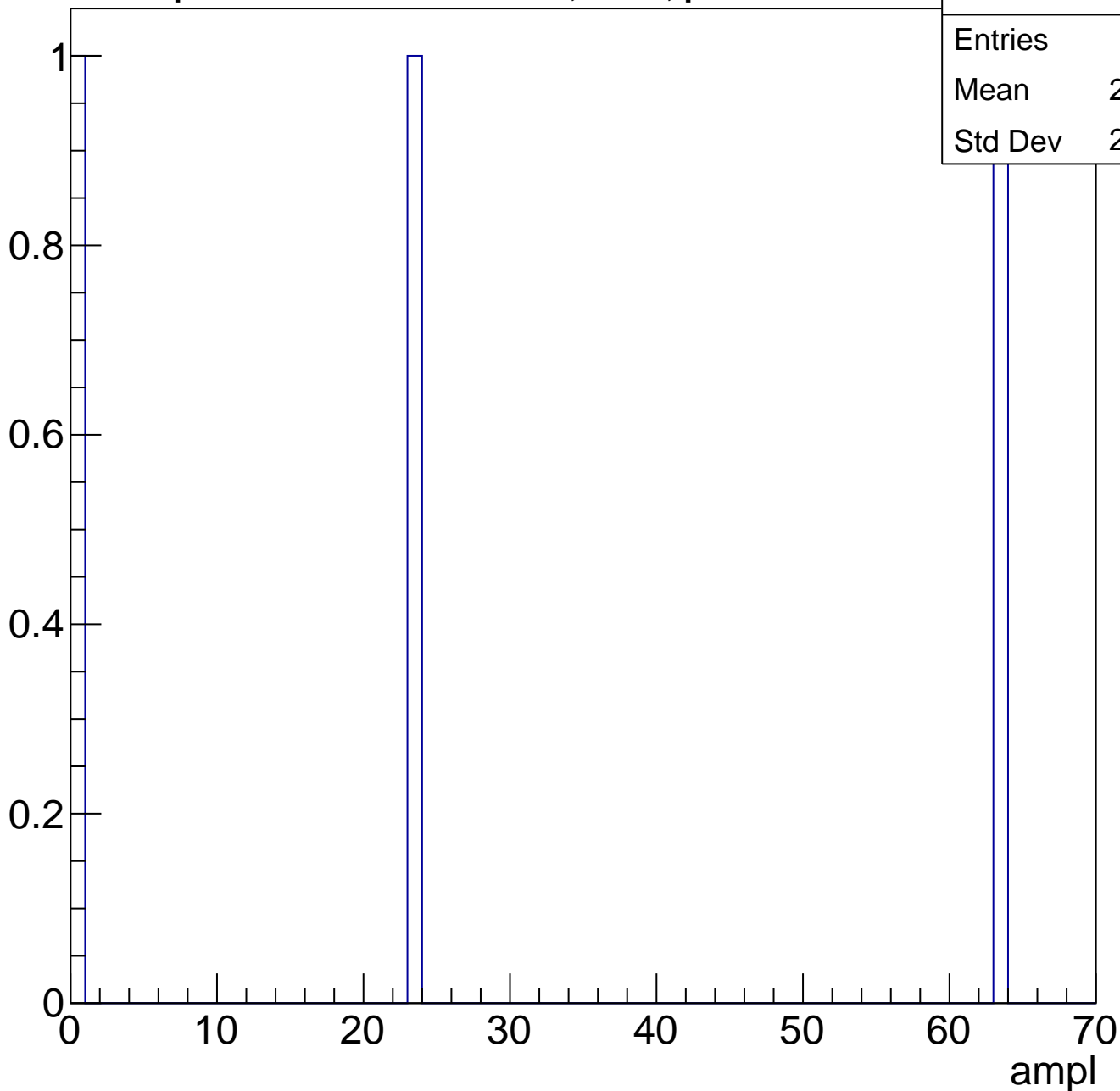
Entries	18
Mean	58.5
Std Dev	14.24



B1L001S, U20-ch86, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28.67
Std Dev	26.03

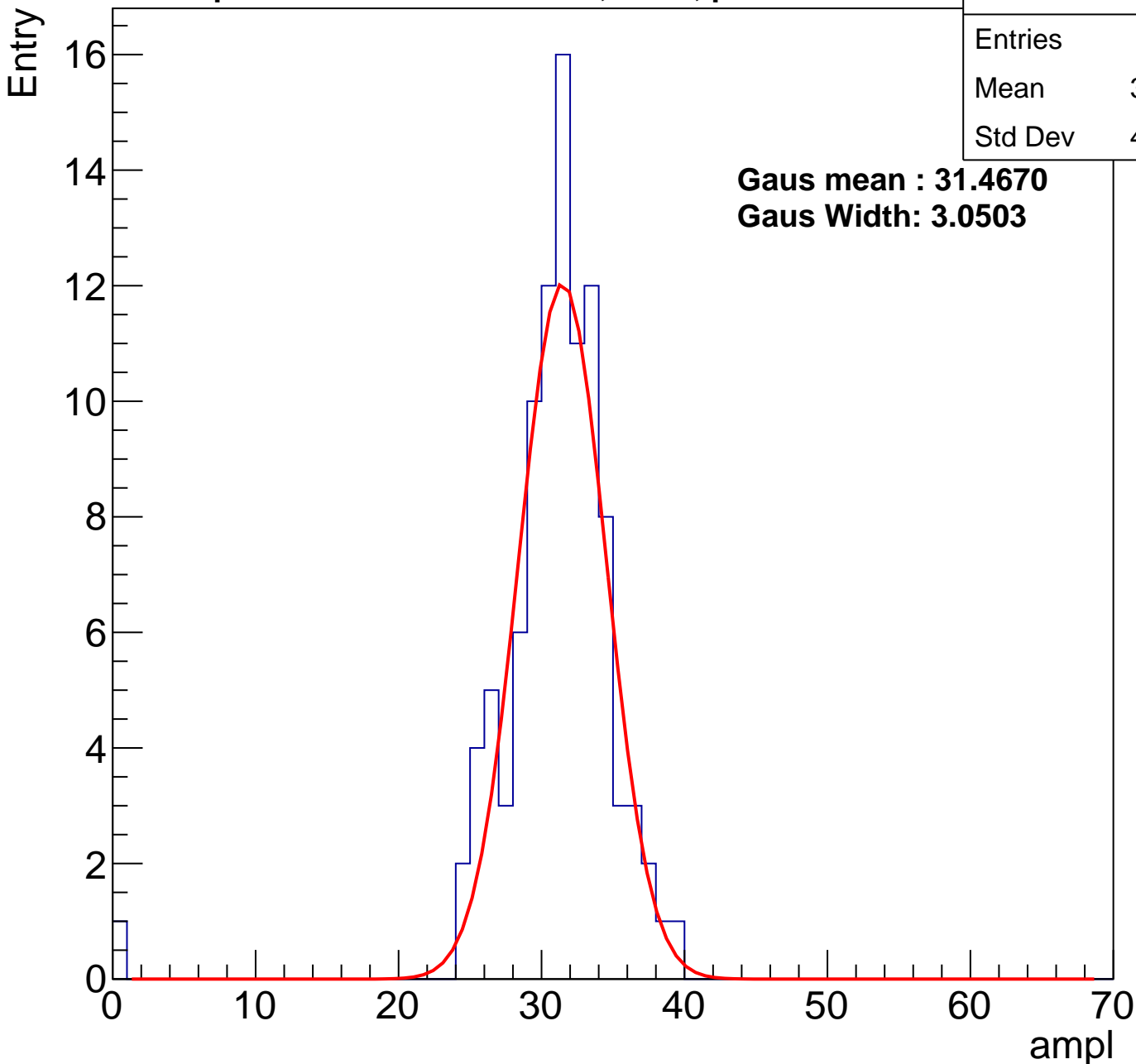
B1L001S, U20-ch87, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	30.57
Std Dev	4.362

Gaus mean : 31.4670

Gaus Width: 3.0503



B1L001S, U20-ch87, adc1

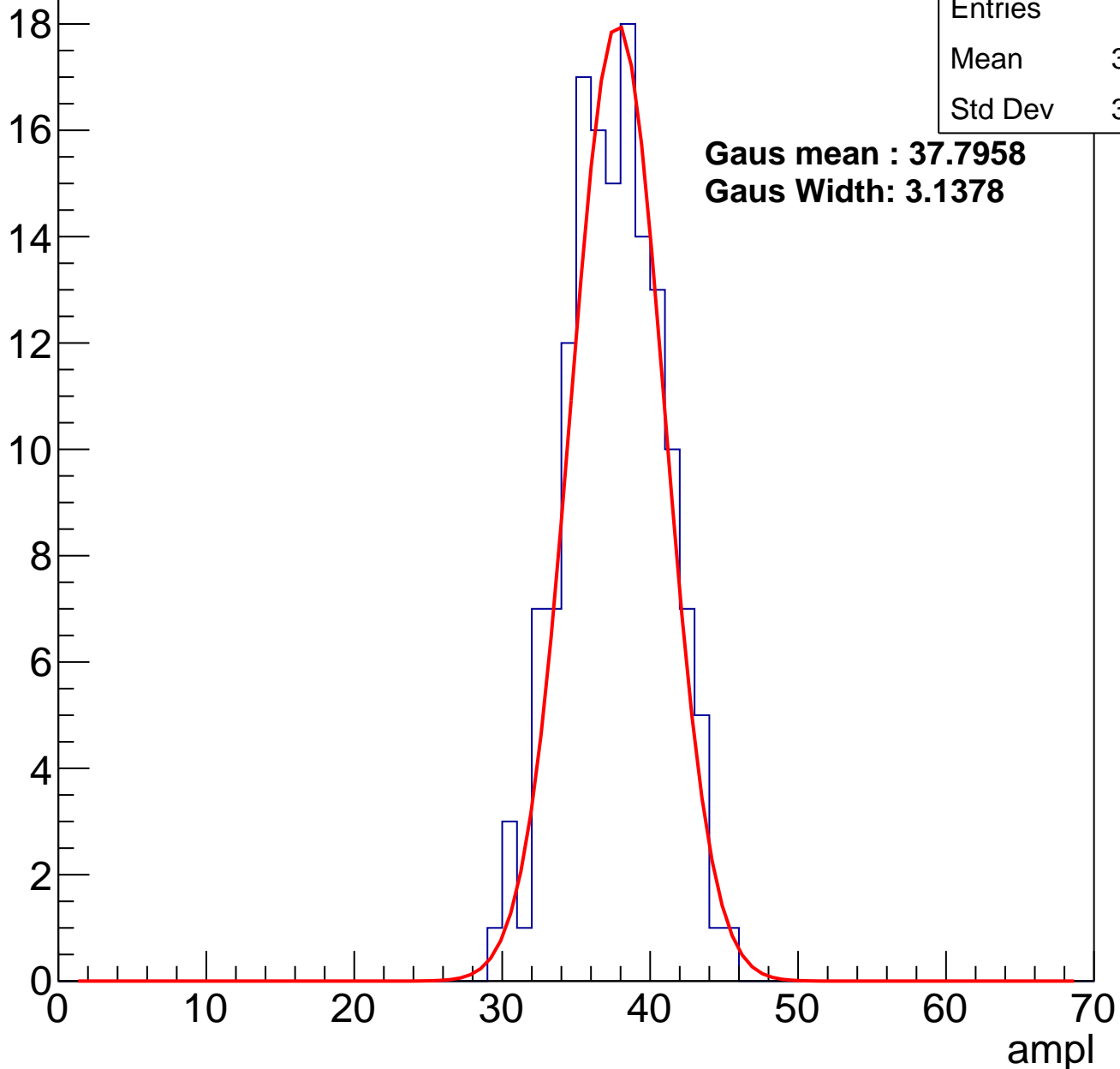
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	37.14
Std Dev	3.228

Gaus mean : 37.7958

Gaus Width: 3.1378

Entry



B1L001S, U20-ch87, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

105

Mean

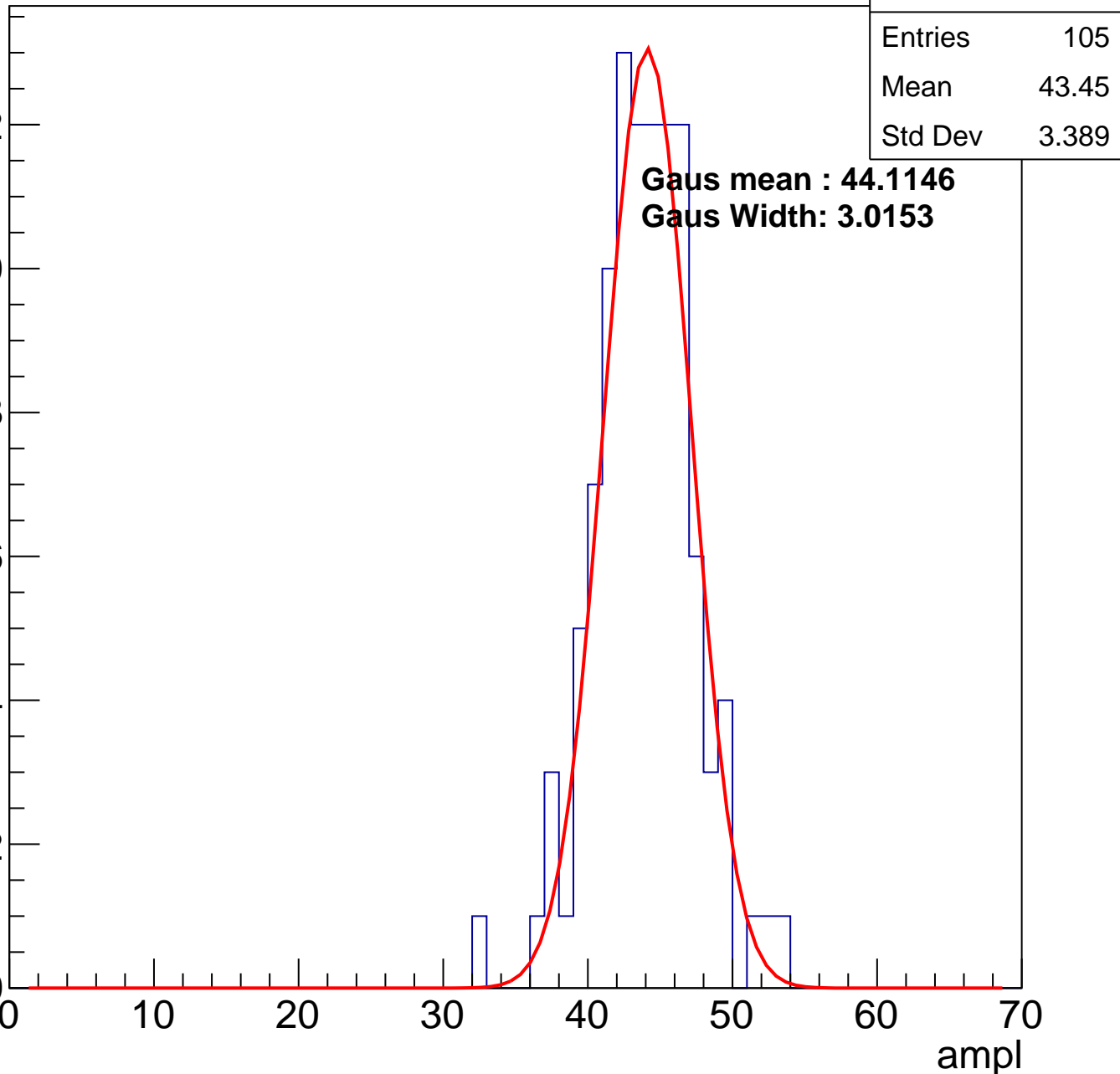
43.45

Std Dev

3.389

Gaus mean : 44.1146

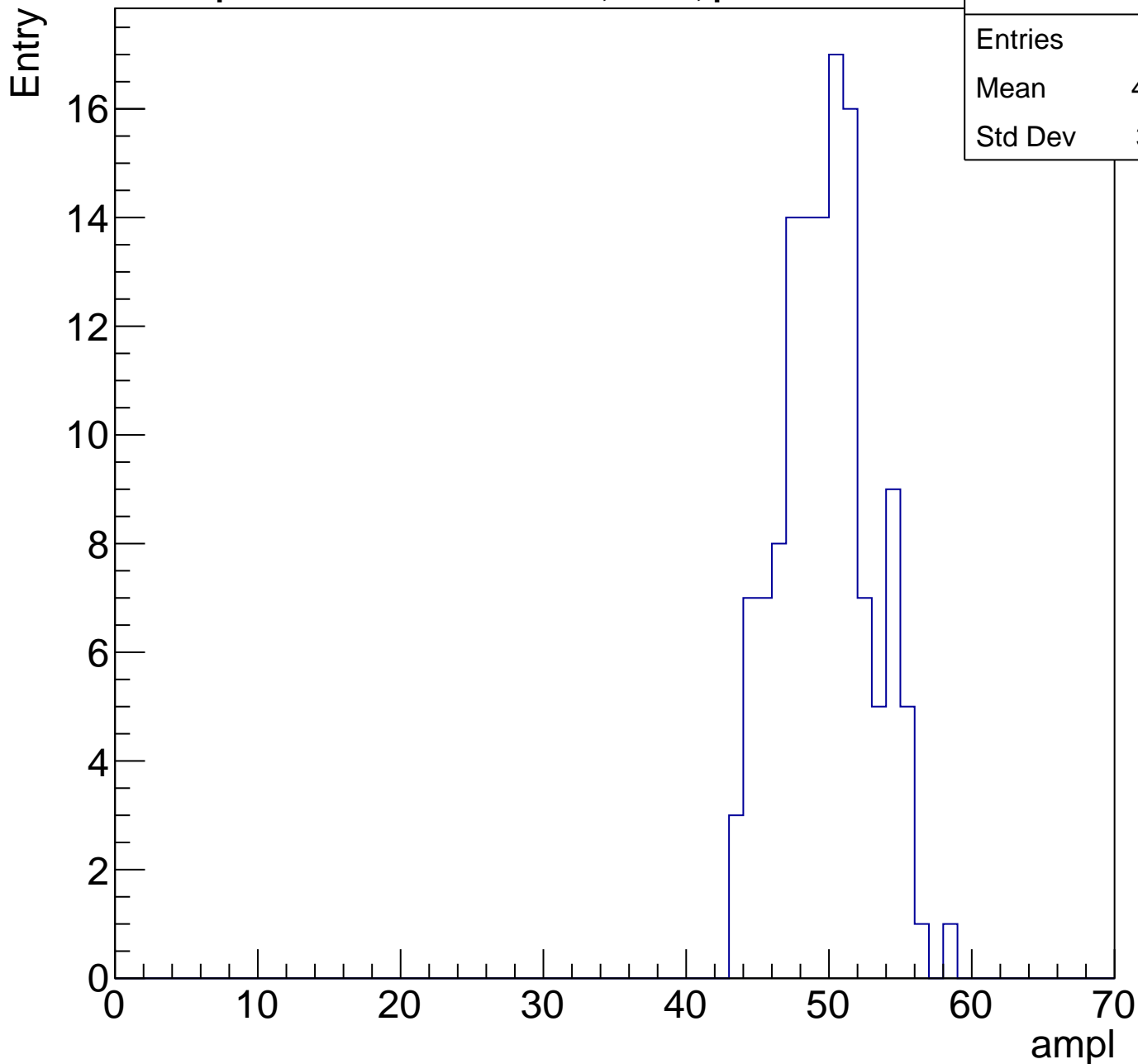
Gaus Width: 3.0153



B1L001S, U20-ch87, adc3

calib_packv5_042523_0143.root, FC#2, port C2

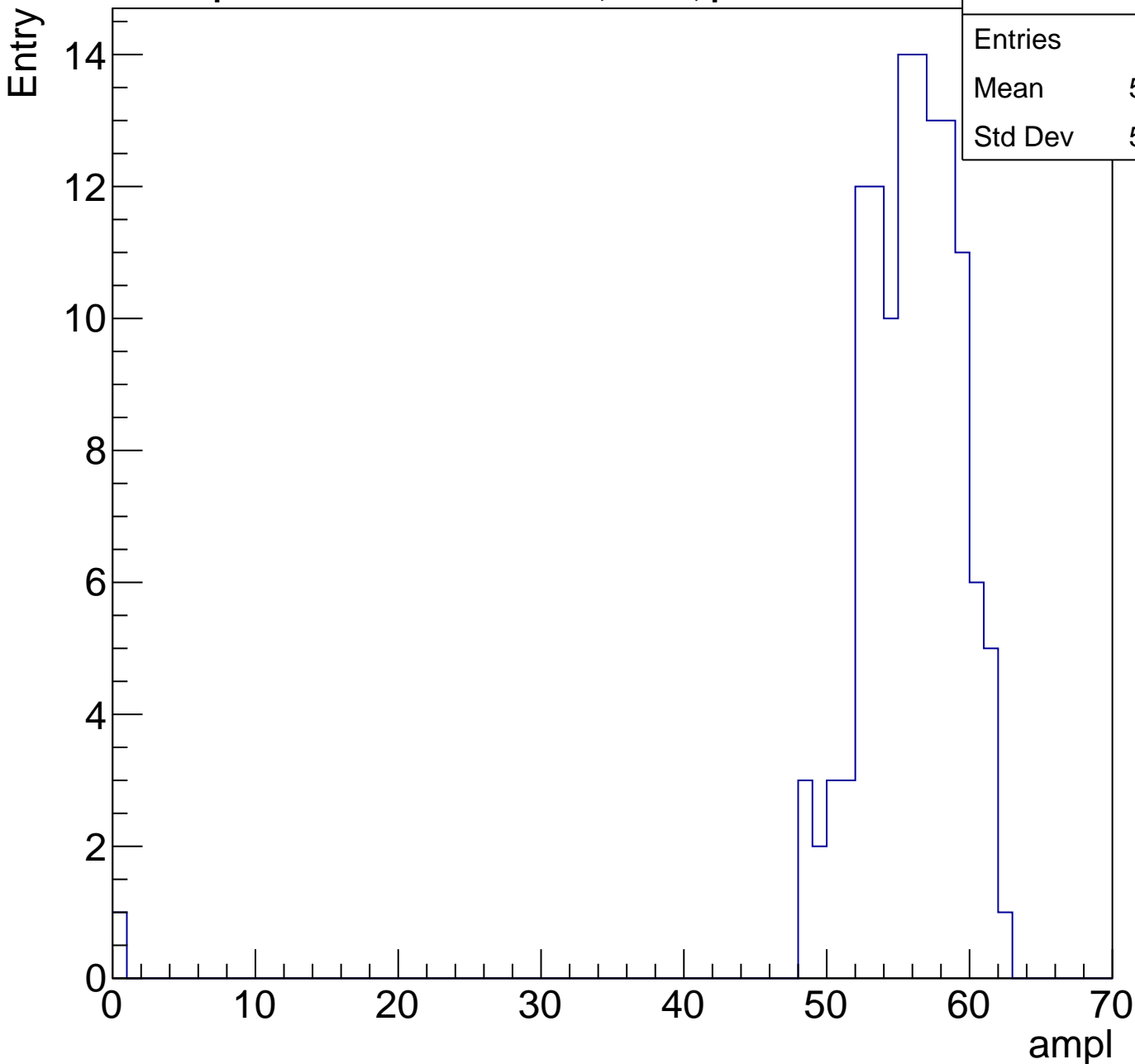
Entries	128
Mean	49.27
Std Dev	3.171



B1L001S, U20-ch87, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	55.04
Std Dev	5.893



B1L001S, U20-ch87, adc5

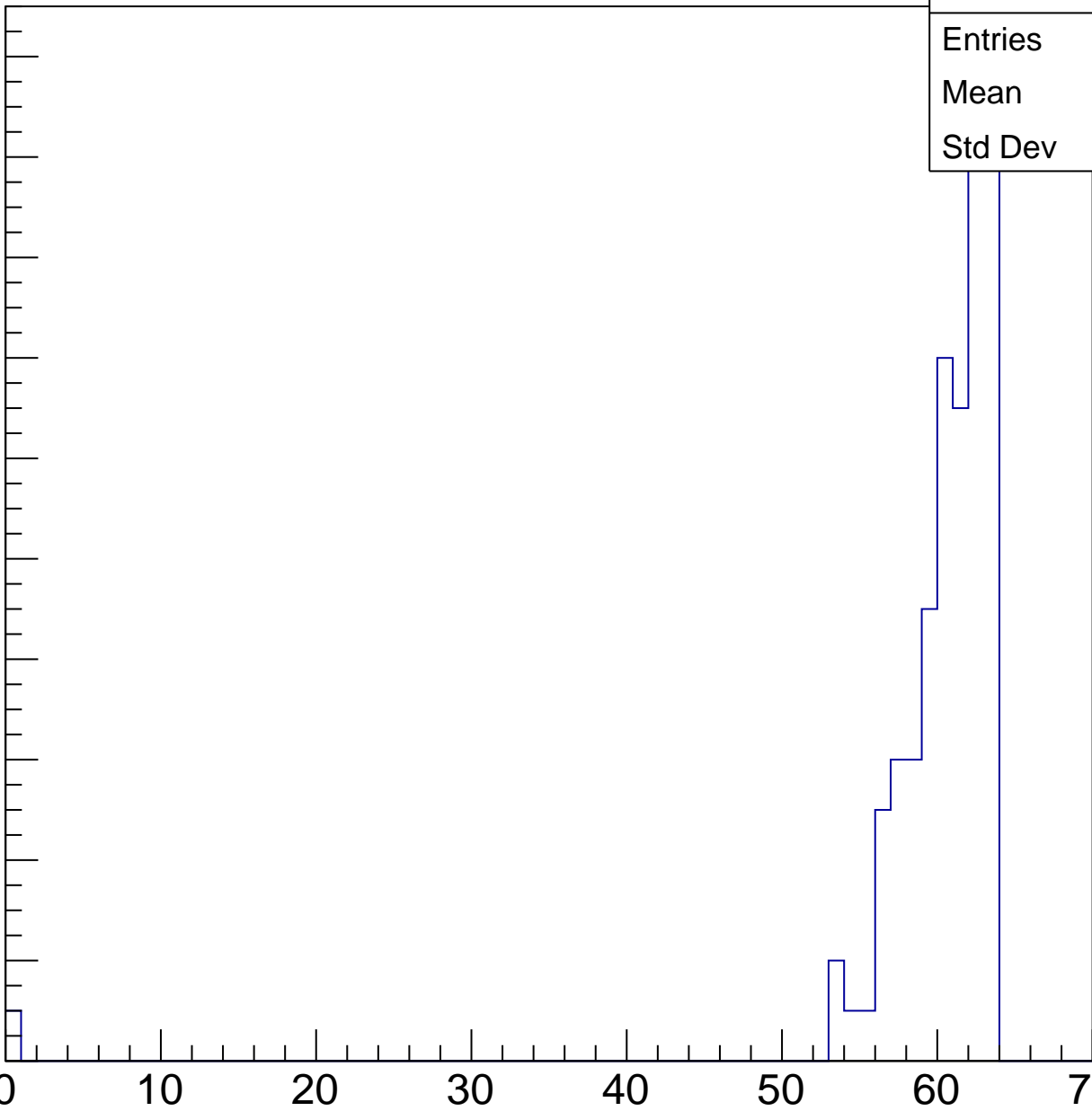
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	97
Mean	59.66
Std Dev	6.567

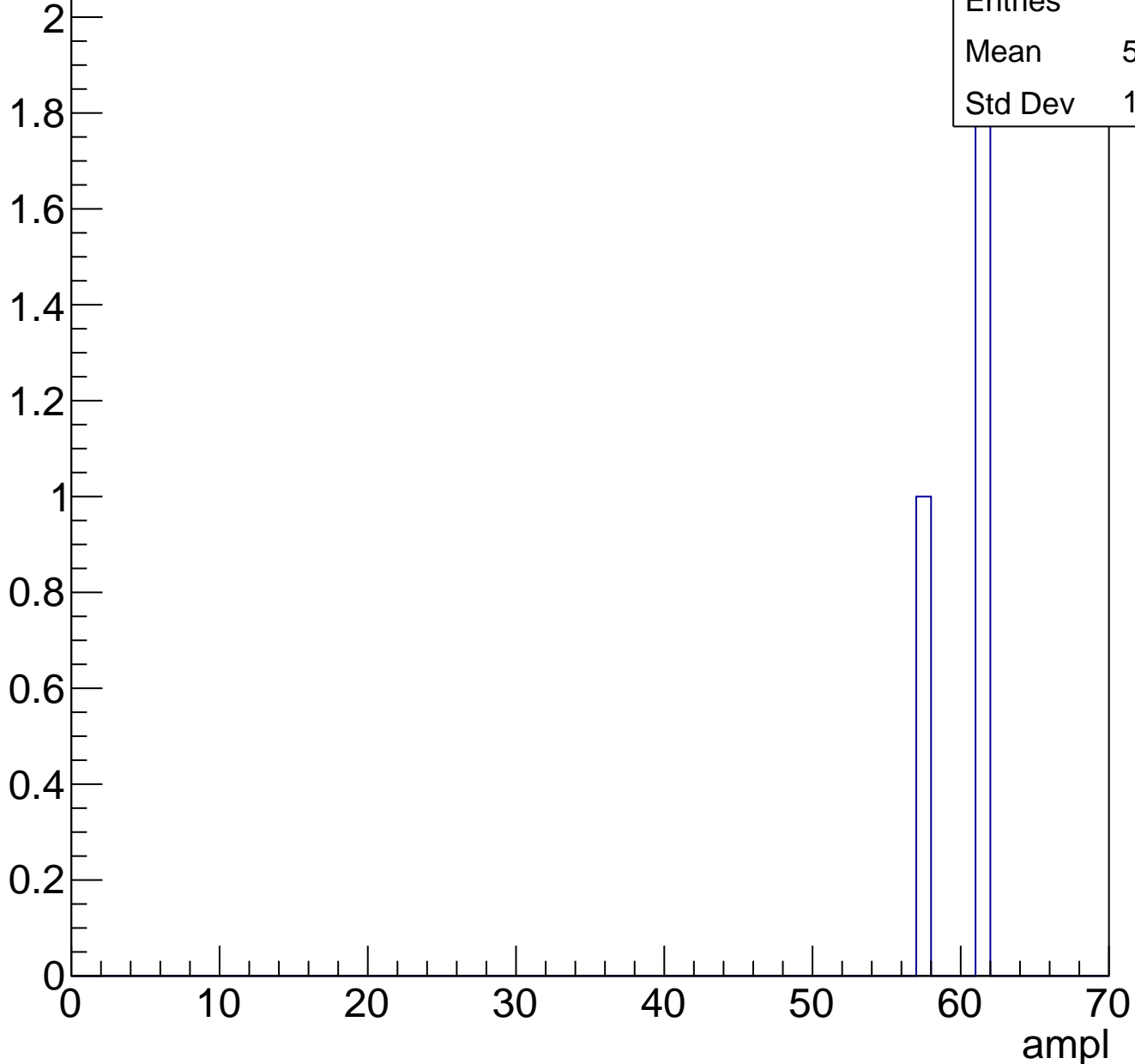
ampl



B1L001S, U20-ch87, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	59.67
Std Dev	1.886

B1L001S, U20-ch87, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L001S, U20-ch88, adc0

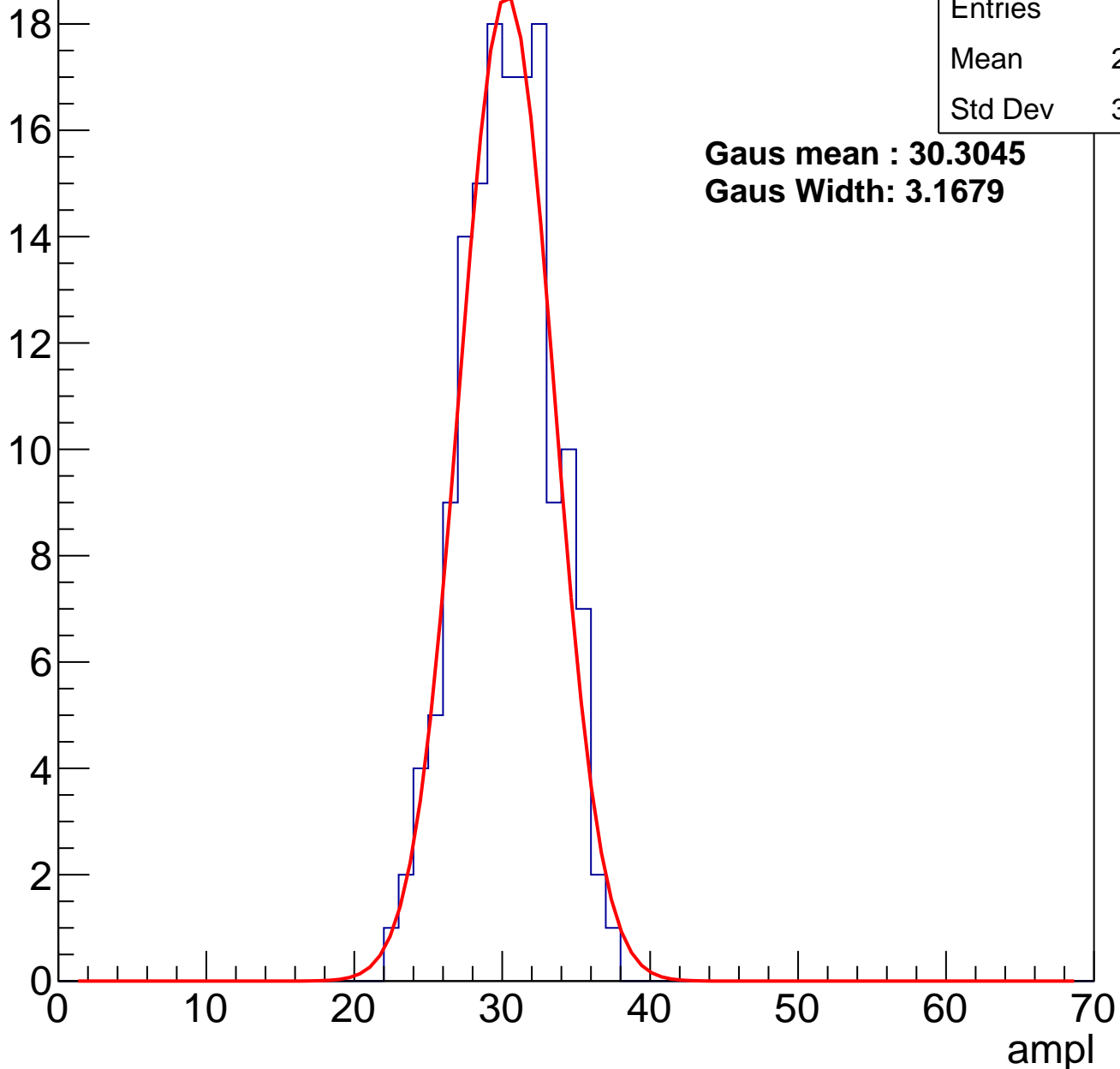
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	29.85
Std Dev	3.067

Gaus mean : 30.3045

Gaus Width: 3.1679

Entry



B1L001S, U20-ch88, adc1

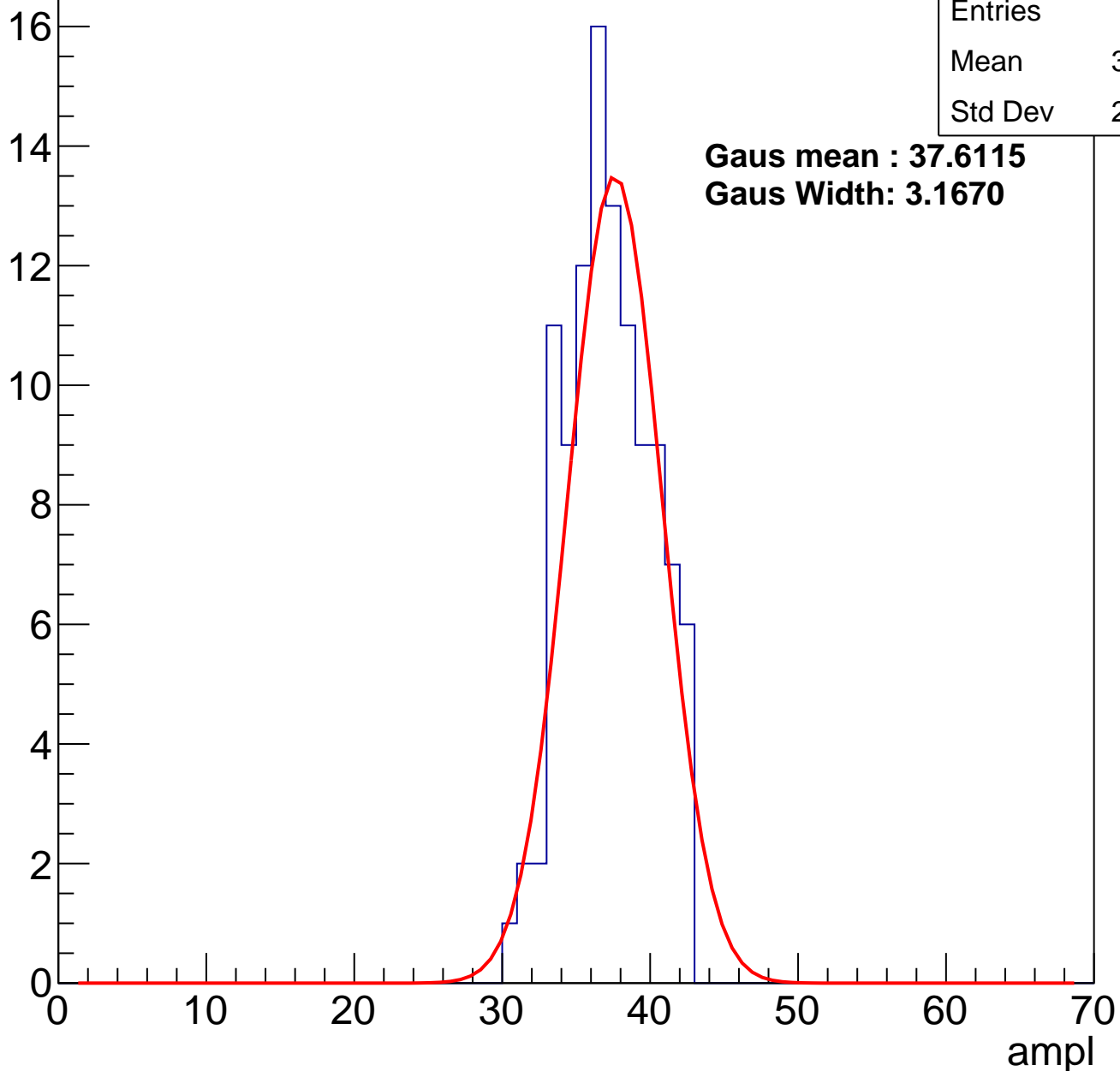
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	36.76
Std Dev	2.848

Gaus mean : 37.6115

Gaus Width: 3.1670

Entry



B1L001S, U20-ch88, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	42.14
Std Dev	3.196

Gaus mean : 42.8319

Gaus Width: 3.4020

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

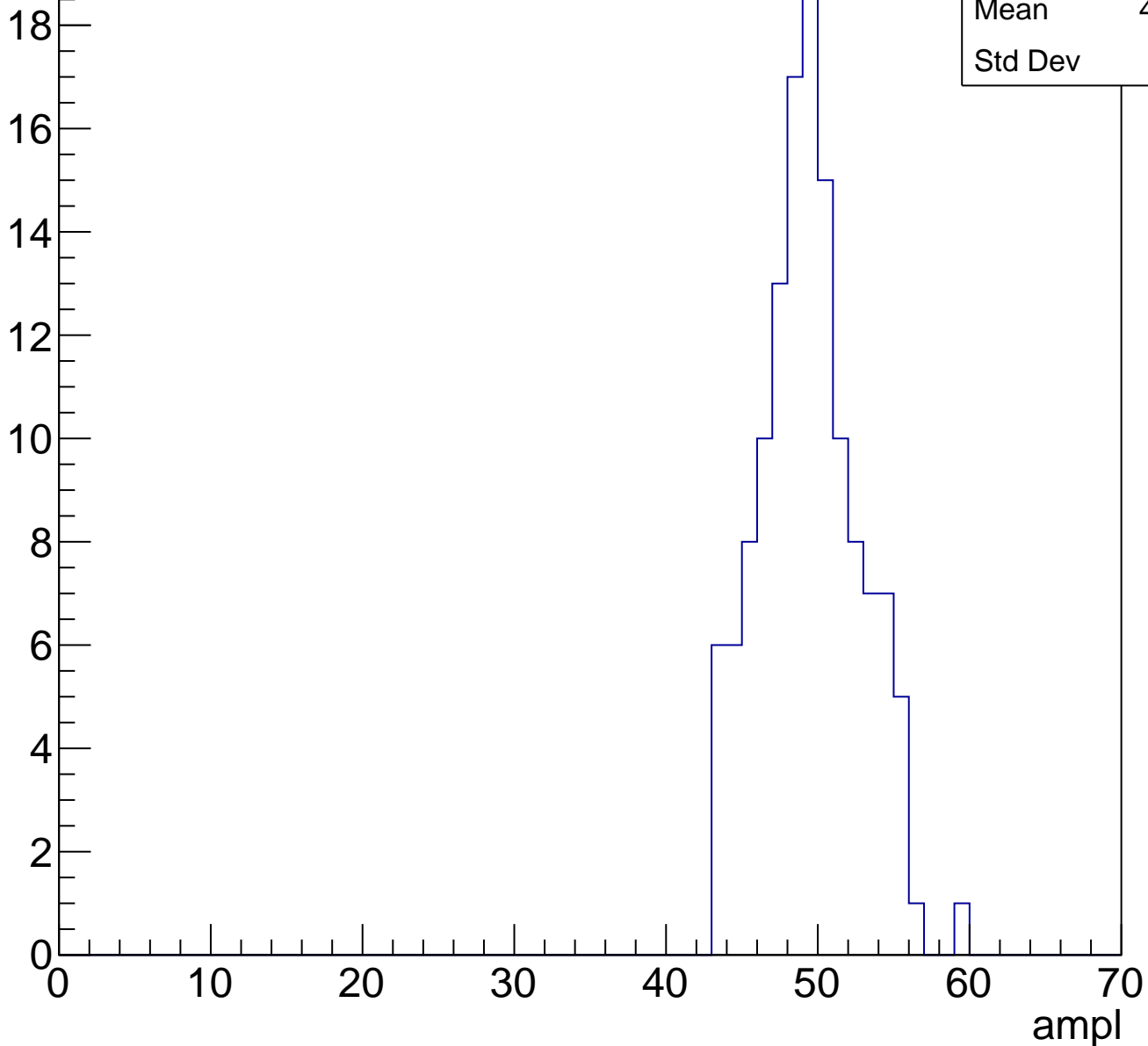
70

B1L001S, U20-ch88, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	48.98
Std Dev	3.24

Entry



B1L001S, U20-ch88, adc4

calib_packv5_042523_0143.root, FC#2, port C2

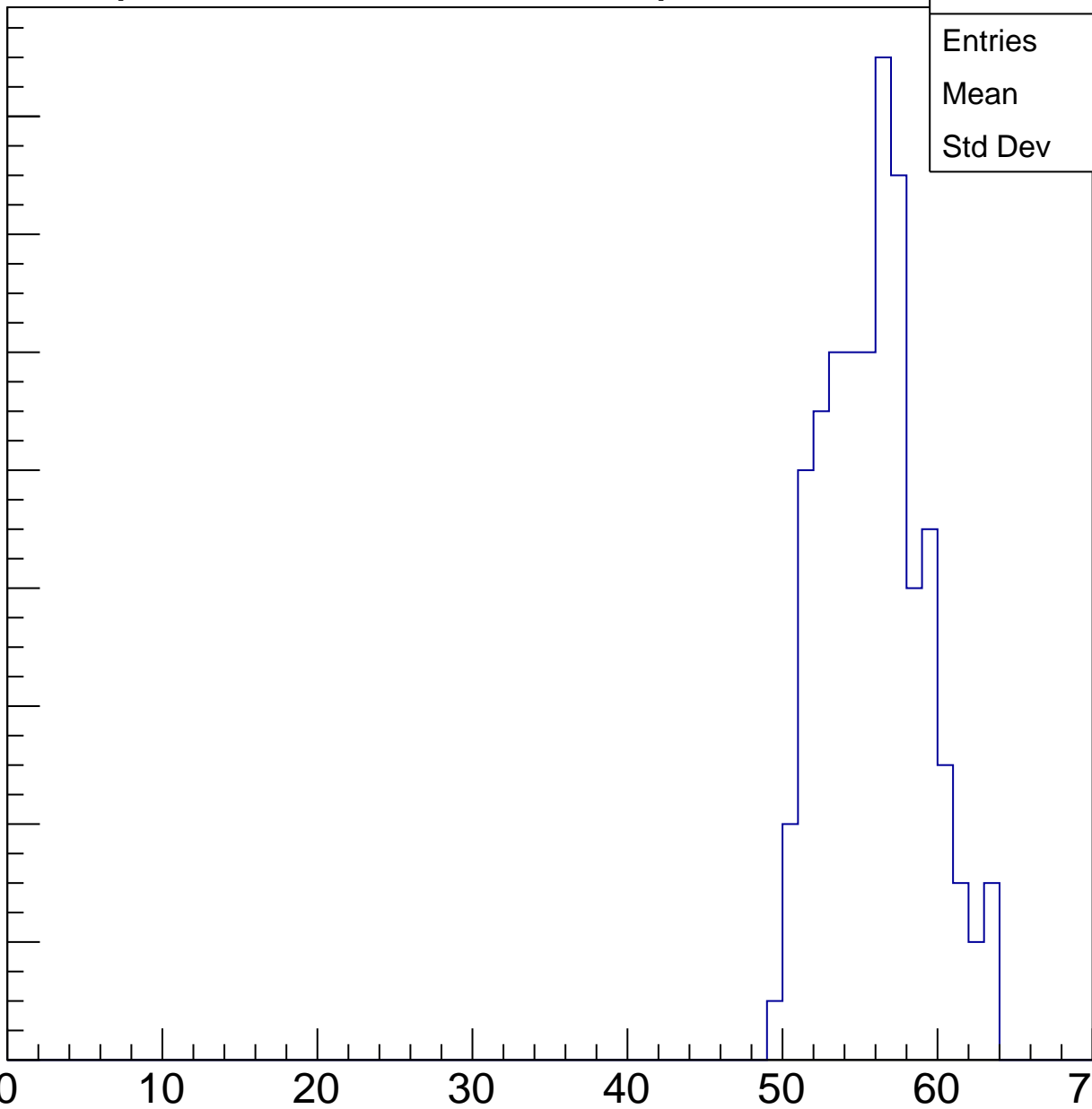
Entries	124
Mean	55.43
Std Dev	3.173

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch88, adc5

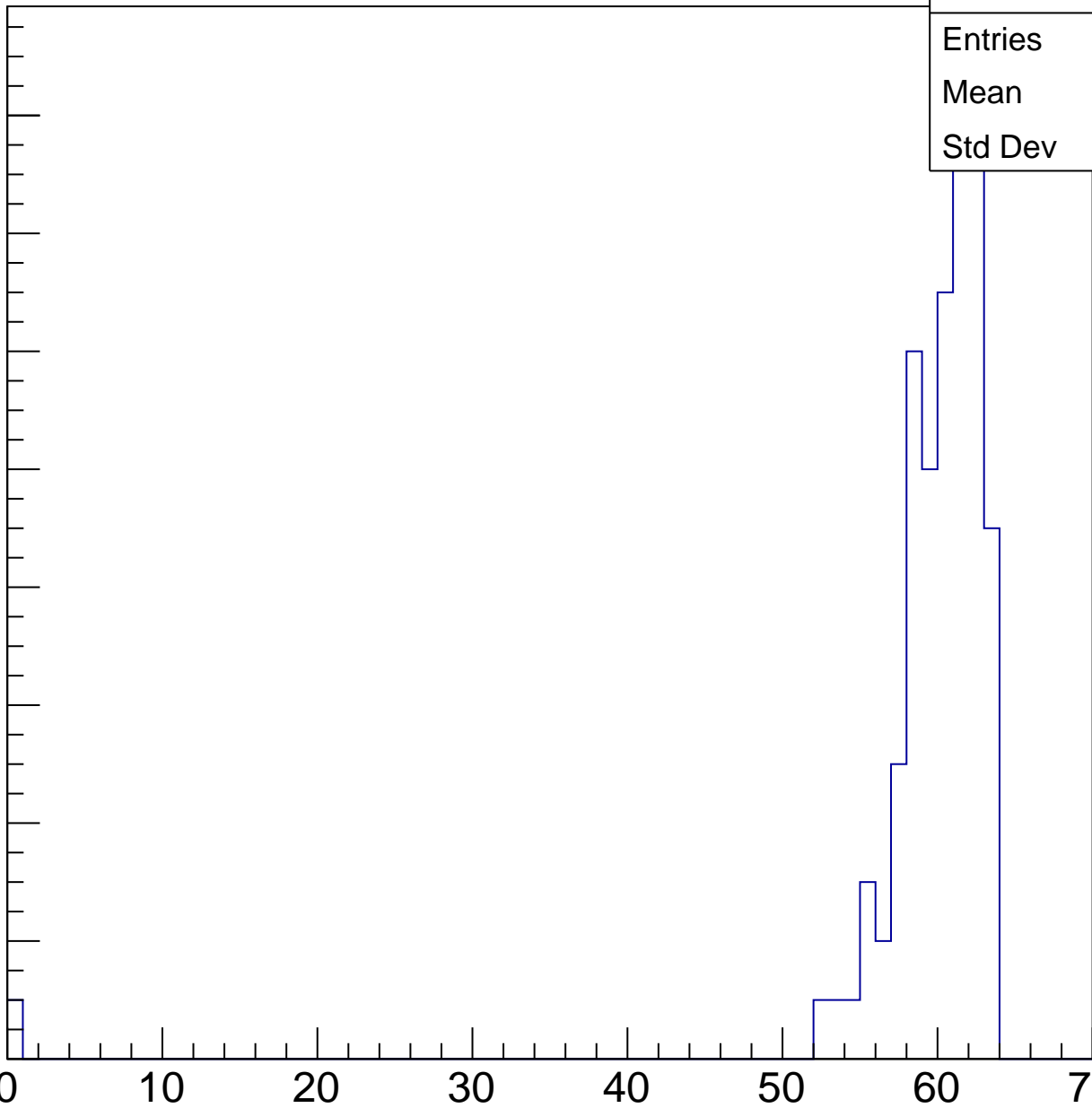
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.15
Std Dev	6.676

ampl



B1L001S, U20-ch88, adc6

calib_packv5_042523_0143.root, FC#2, port C2

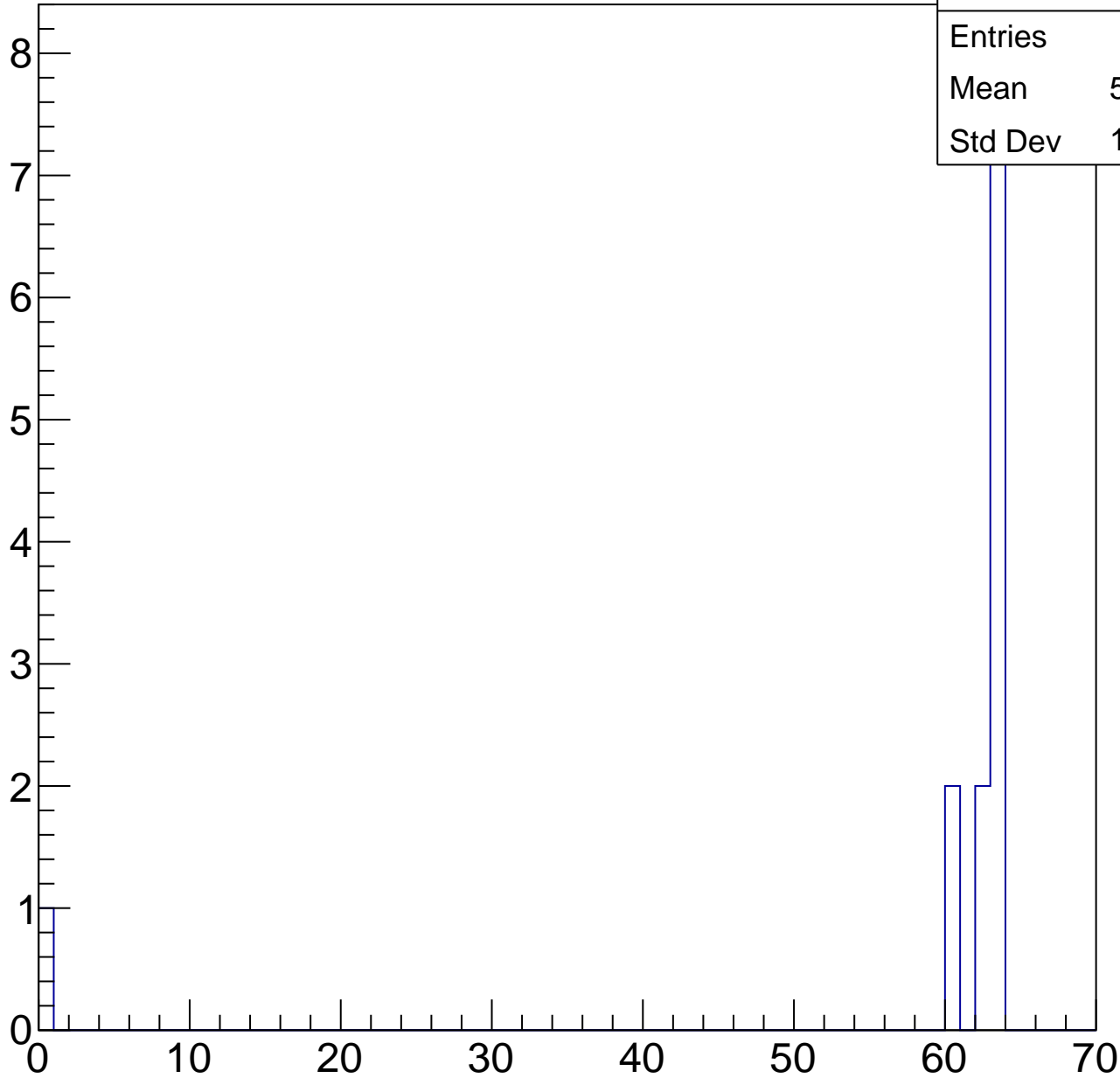
Entry

8
7
6
5
4
3
2
1
0

Entries	13
Mean	57.54
Std Dev	16.64

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch88, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U20-ch89, adc0

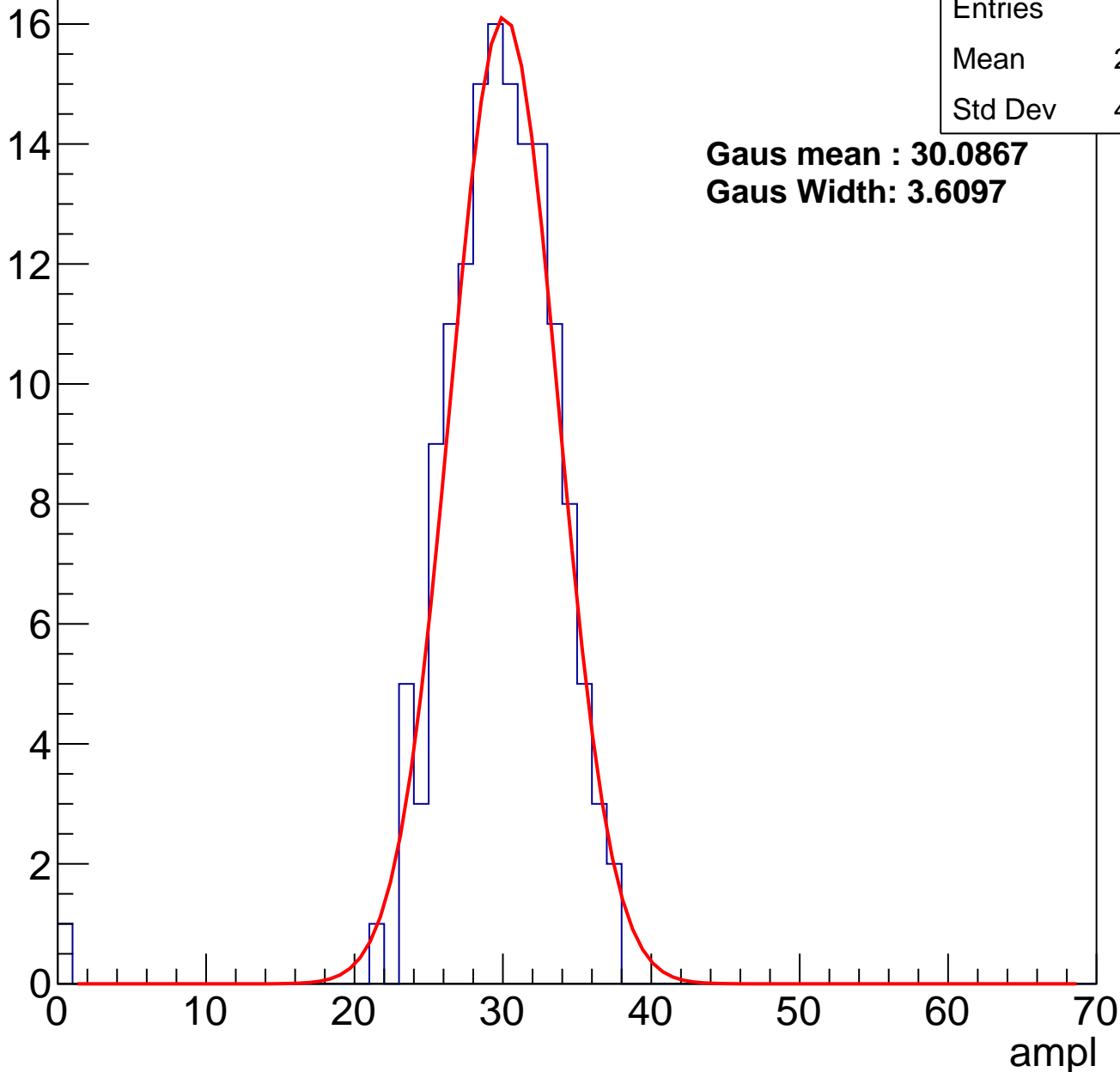
calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	29.32
Std Dev	4.135

Gaus mean : 30.0867

Gaus Width: 3.6097

Entry



B1L001S, U20-ch89, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	35.85
Std Dev	3.234

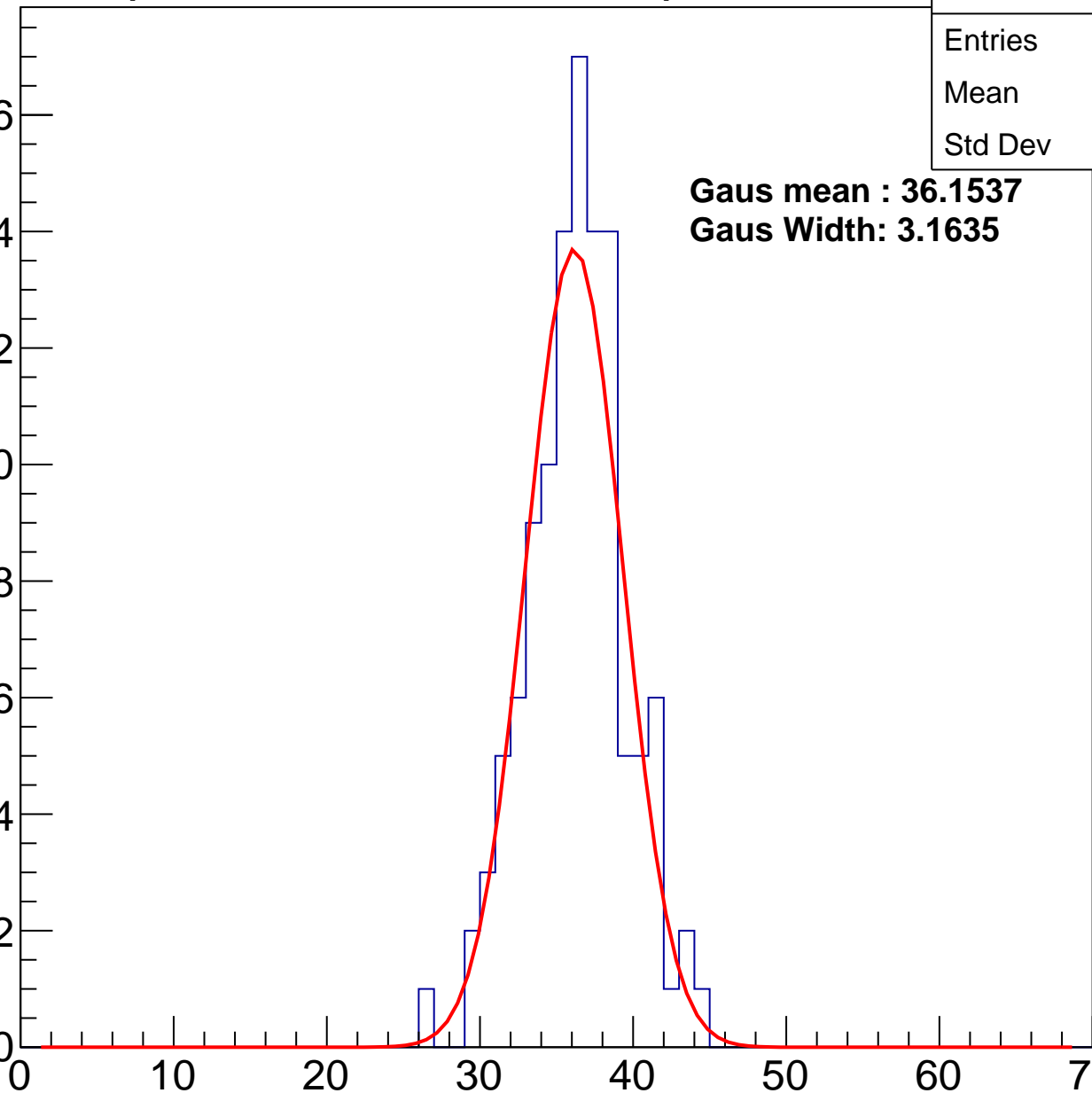
Gaus mean : 36.1537

Gaus Width: 3.1635

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch89, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

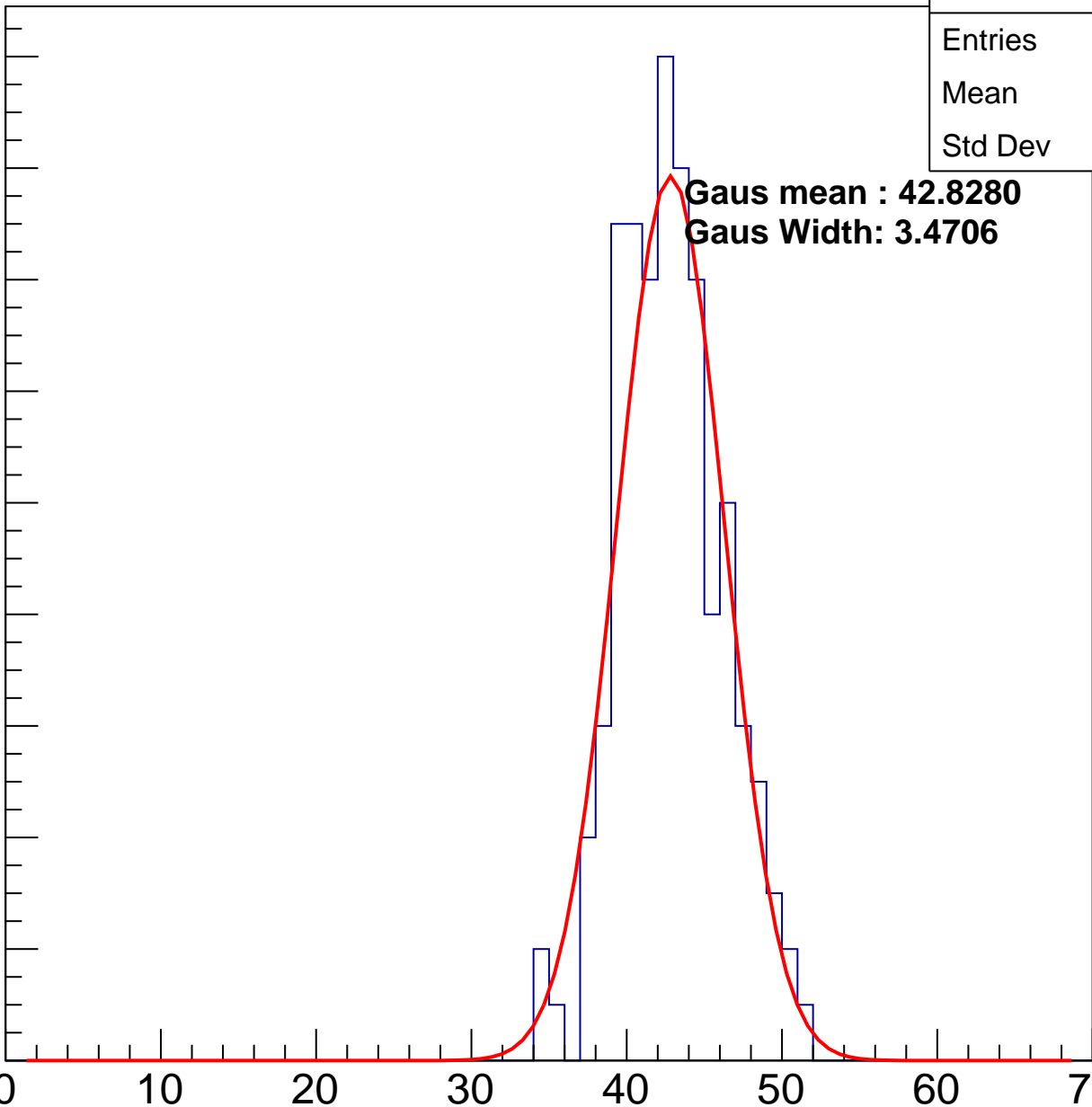
18
16
14
12
10
8
6
4
2
0

Entries	140
Mean	42.41
Std Dev	3.325

Gaus mean : 42.8280

Gaus Width: 3.4706

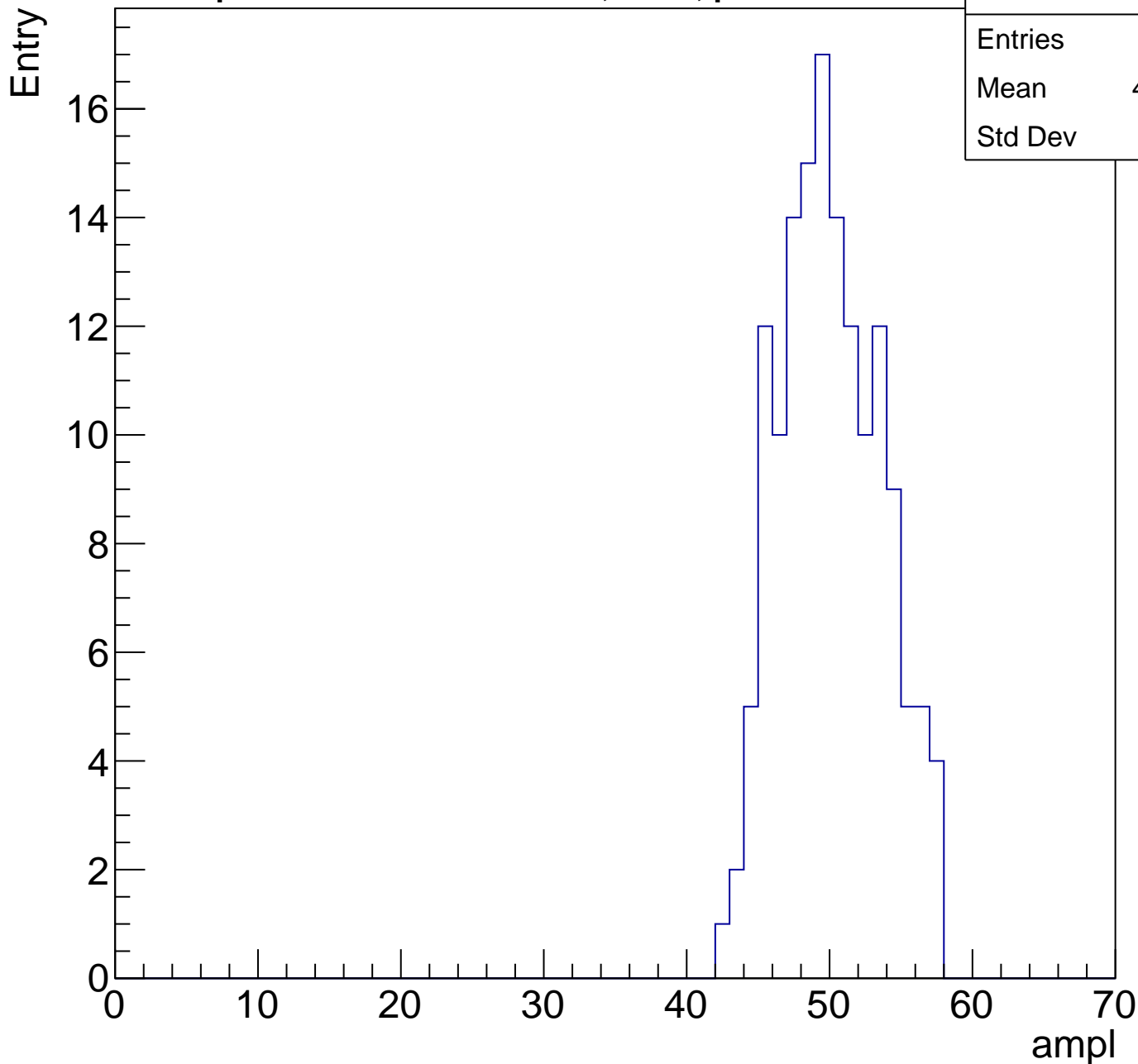
0 10 20 30 40 50 60 70
ampl



B1L001S, U20-ch89, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	49.63
Std Dev	3.49



B1L001S, U20-ch89, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

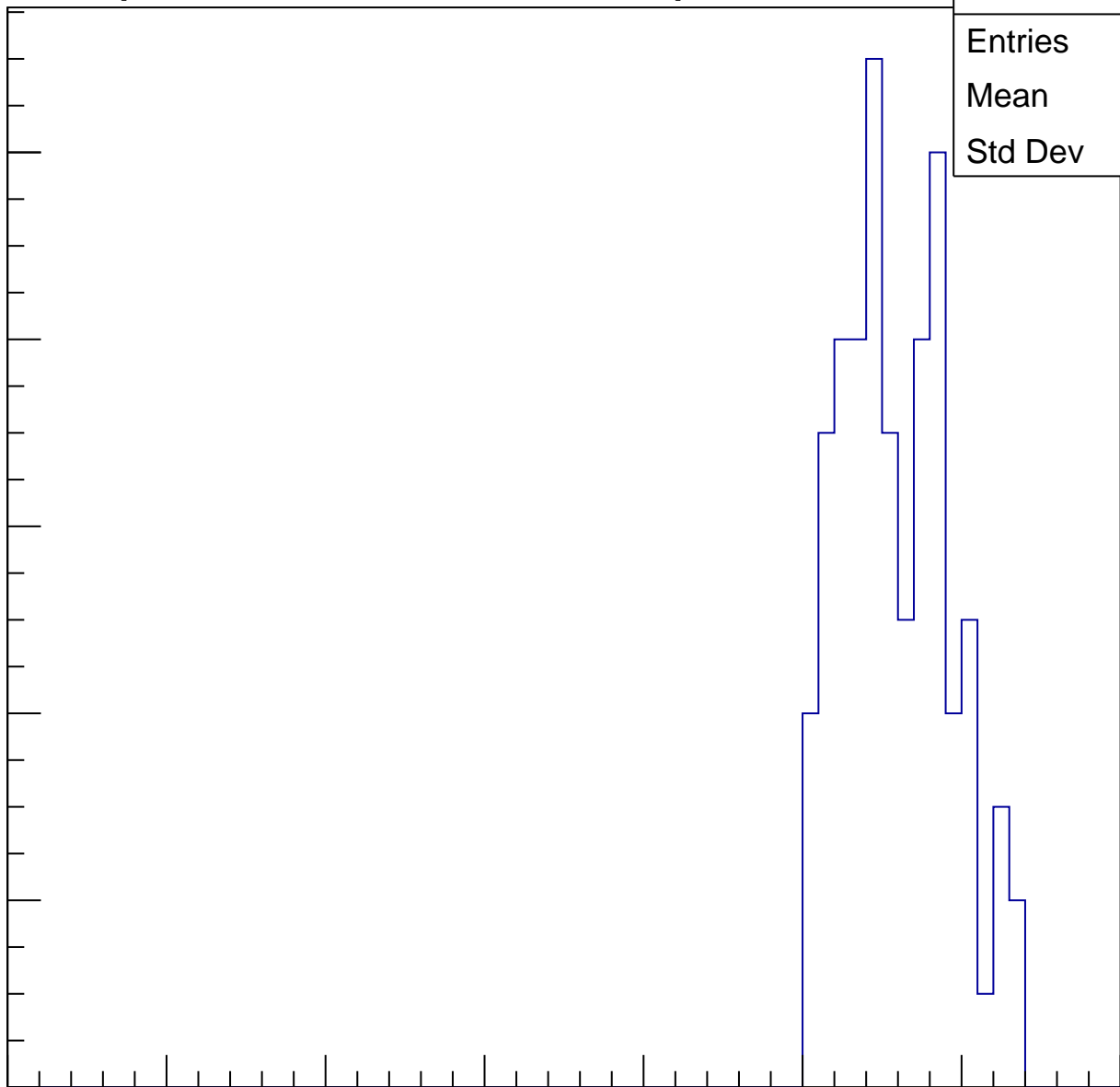
30

40

50

ampl

Entries	83
Mean	55.43
Std Dev	3.362



B1L001S, U20-ch89, adc5

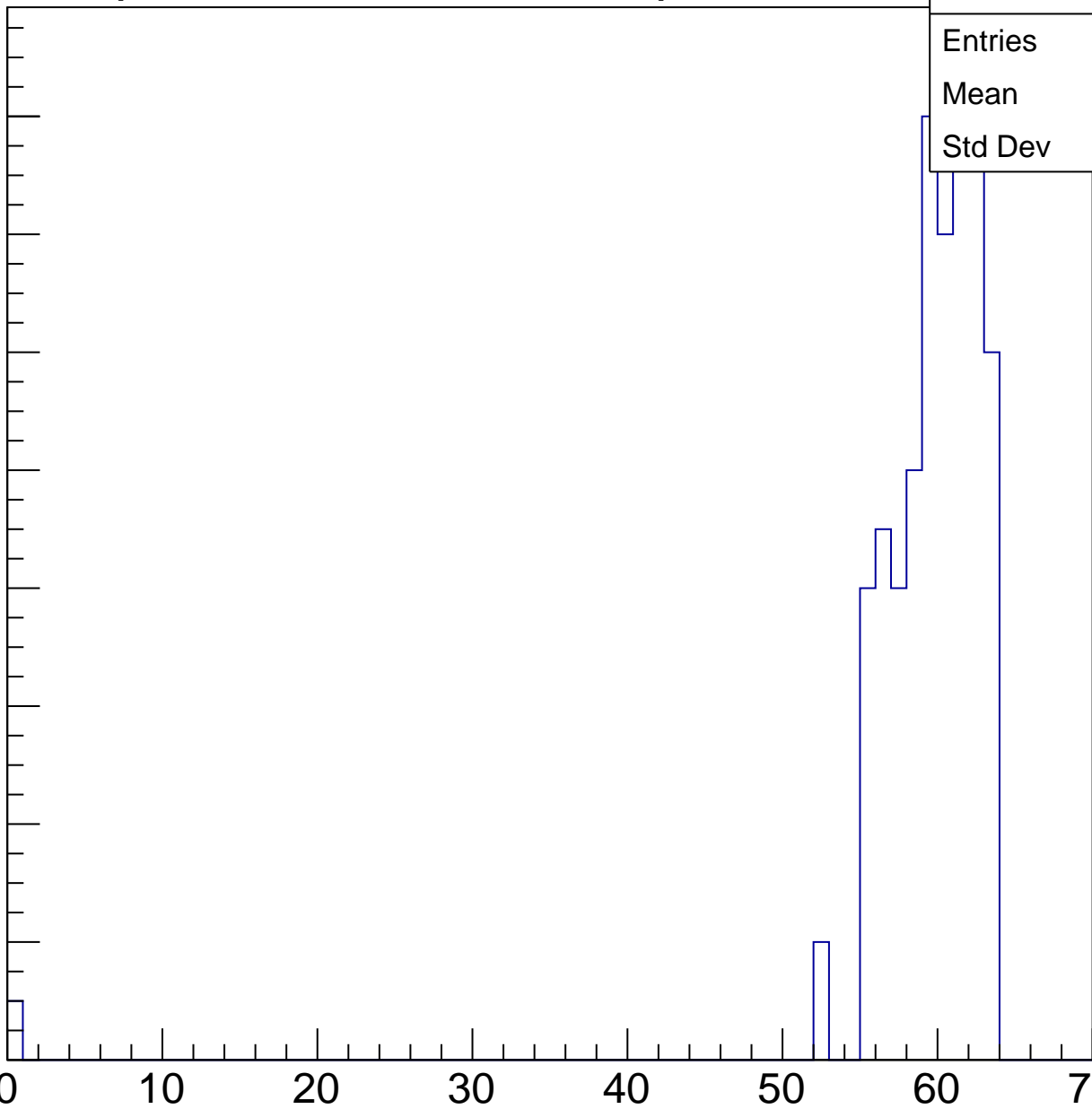
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	114
Mean	58.9
Std Dev	6.11

ampl

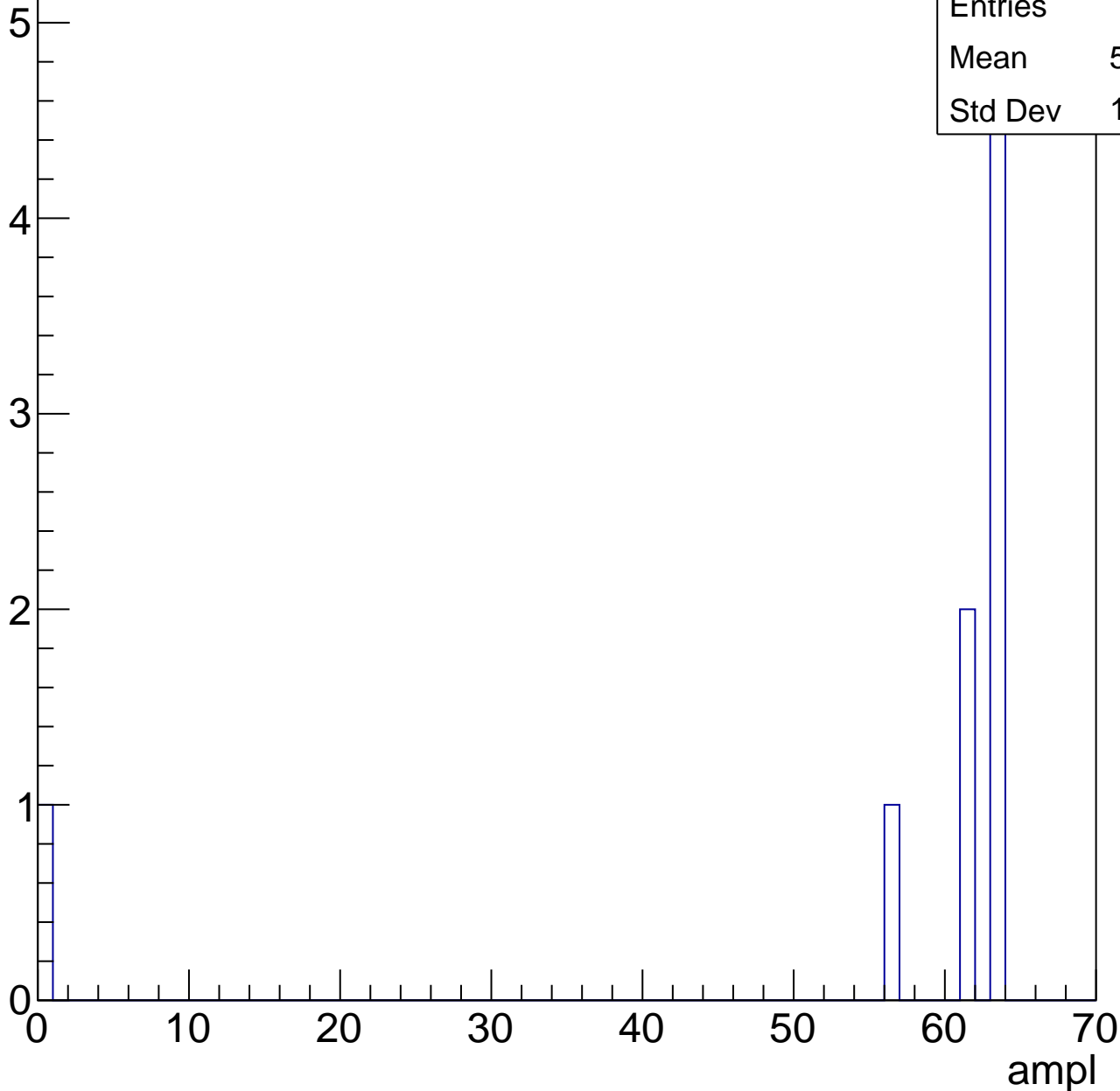


B1L001S, U20-ch89, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

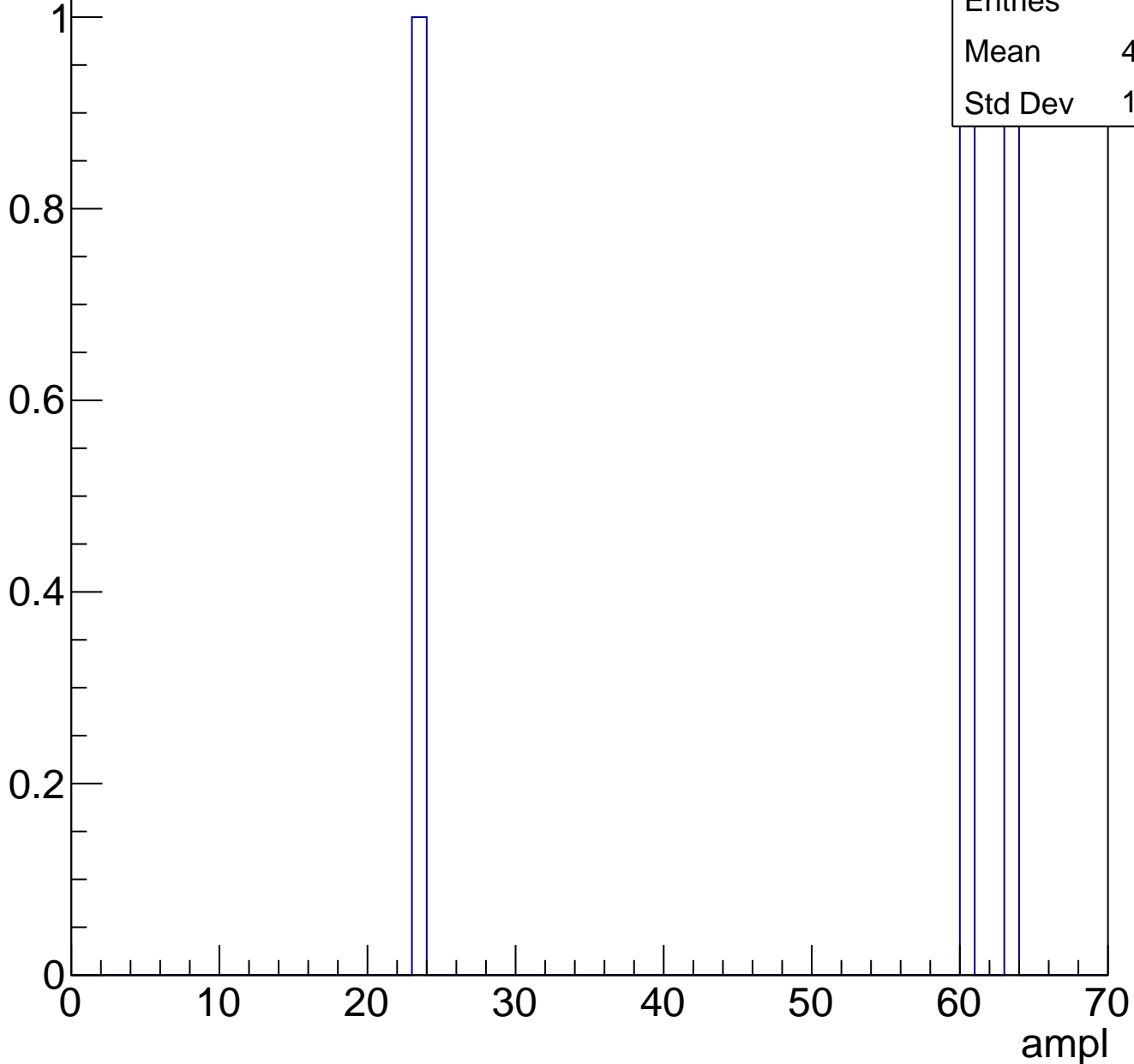
Entries	9
Mean	54.78
Std Dev	19.49



B1L001S, U20-ch89, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch90, adc0

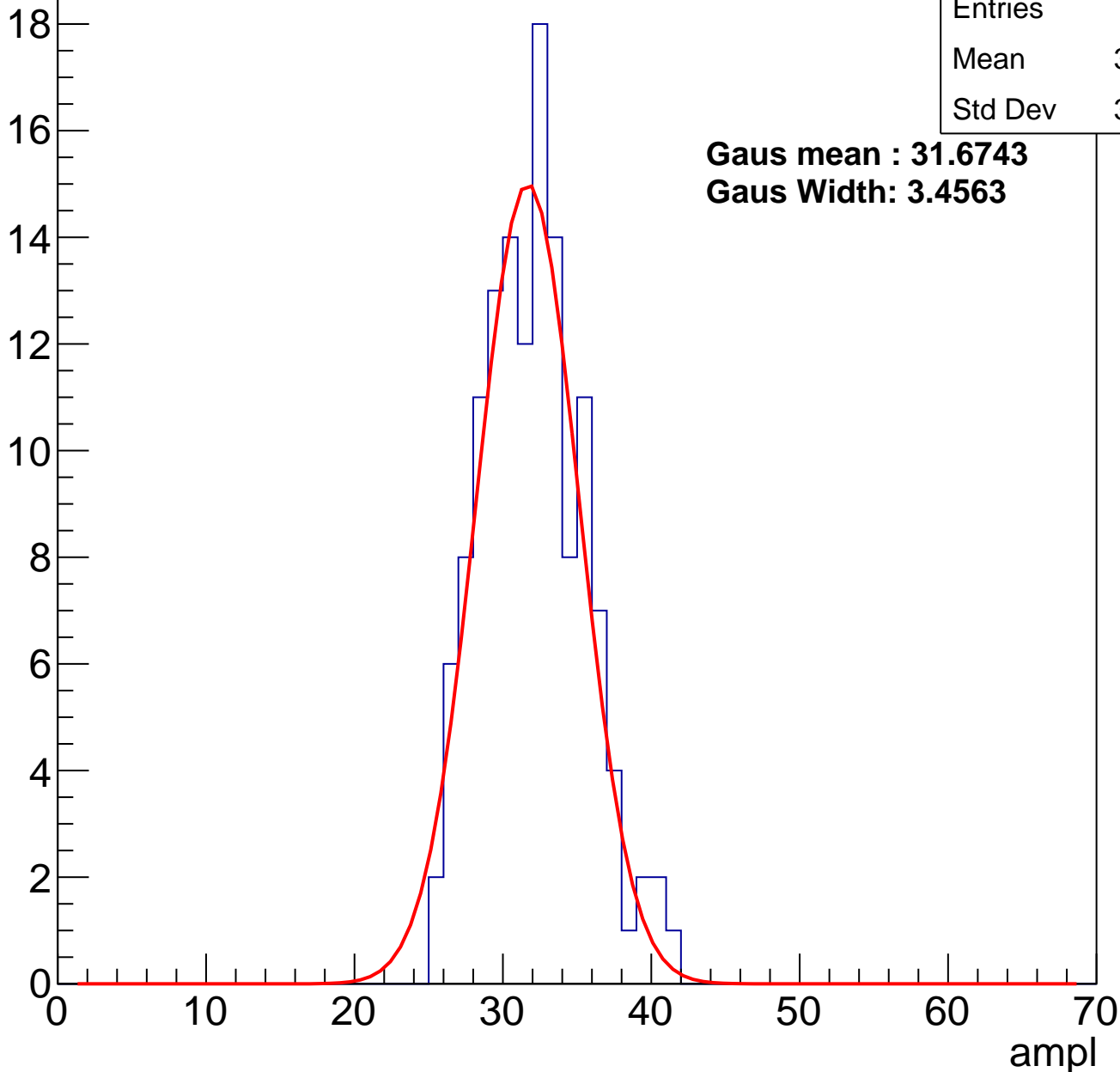
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	31.57
Std Dev	3.402

Gaus mean : 31.6743

Gaus Width: 3.4563

Entry



B1L001S, U20-ch90, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	38.19
Std Dev	3.325

Gaus mean : 38.3919

Gaus Width: 3.2283

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U20-ch90, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	44.22
Std Dev	3.076

Gaus mean : 44.8606

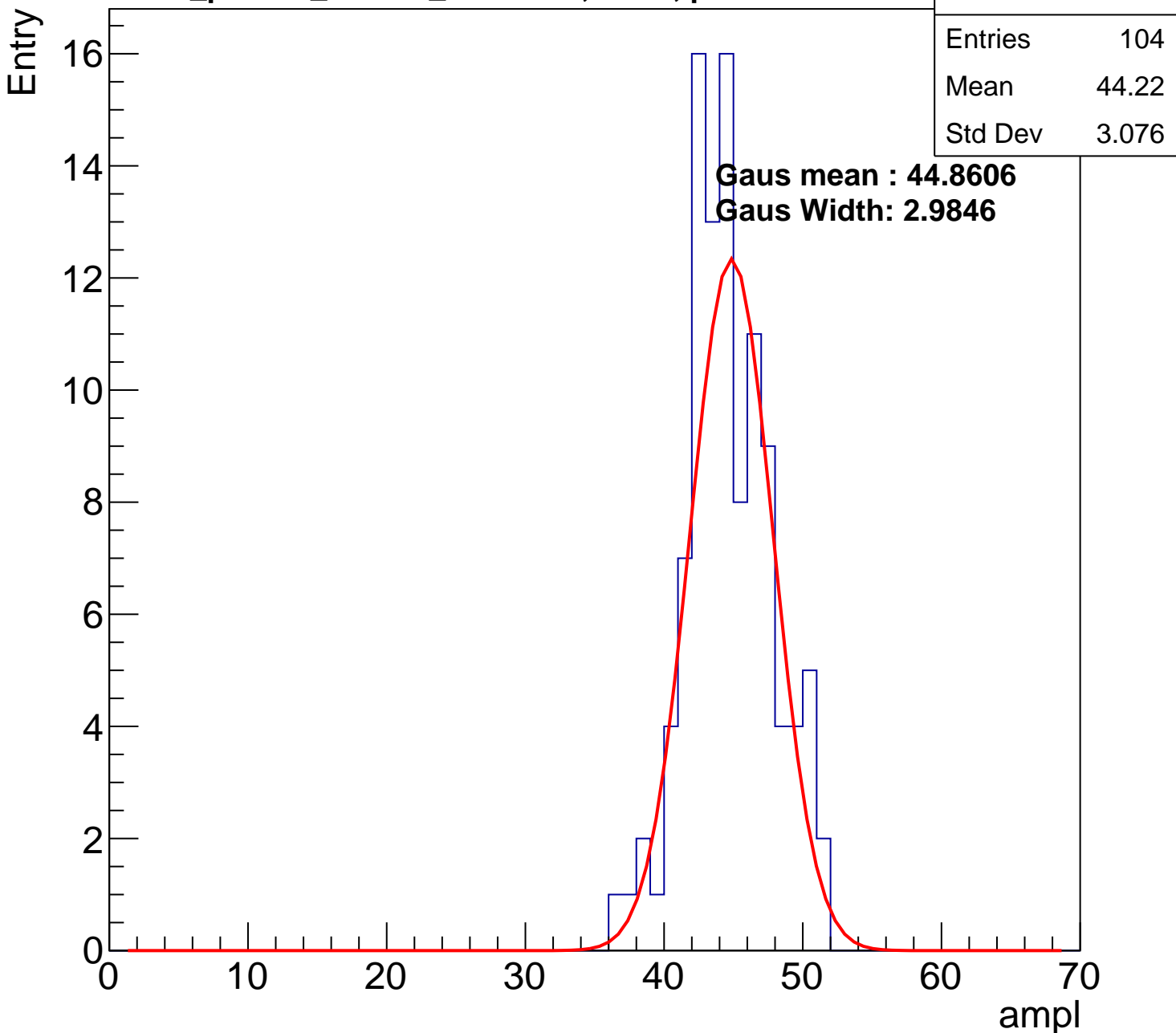
Gaus Width: 2.9846

Entry

16
14
12
10
8
6
4
2
0

ampl

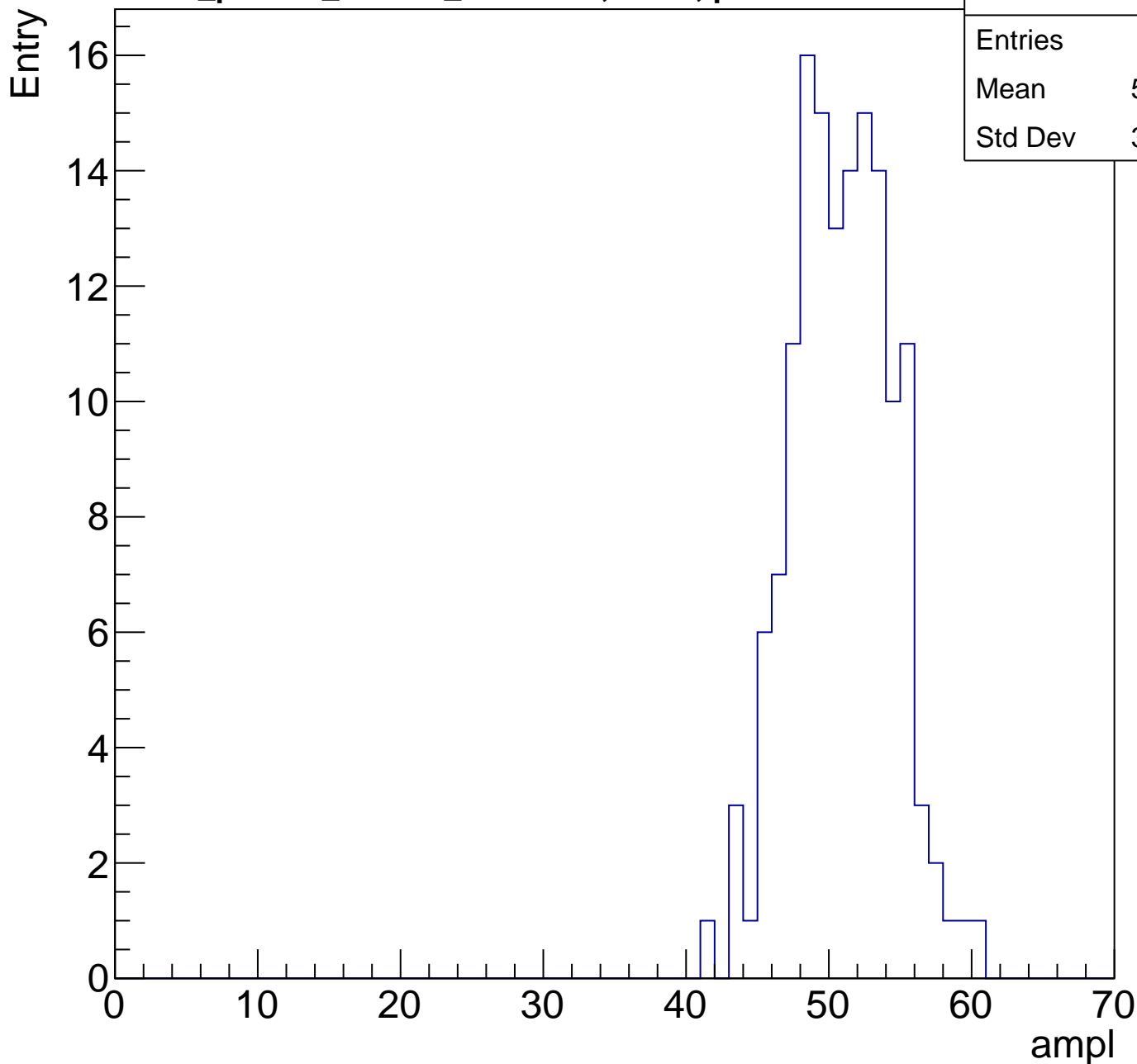
0 10 20 30 40 50 60 70



B1L001S, U20-ch90, adc3

calib_packv5_042523_0143.root, FC#2, port C2

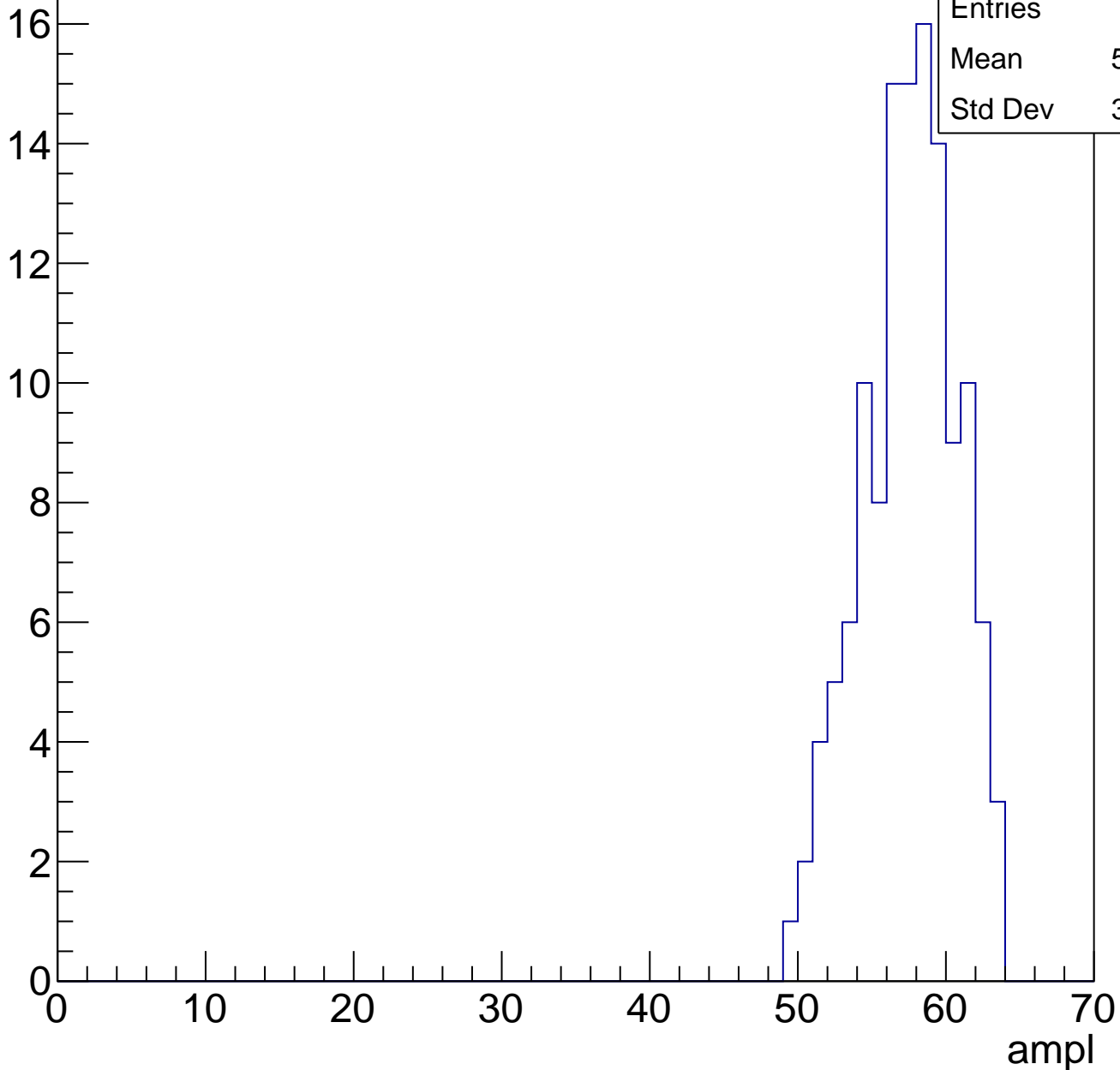
Entries	145
Mean	50.46
Std Dev	3.484



B1L001S, U20-ch90, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch90, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

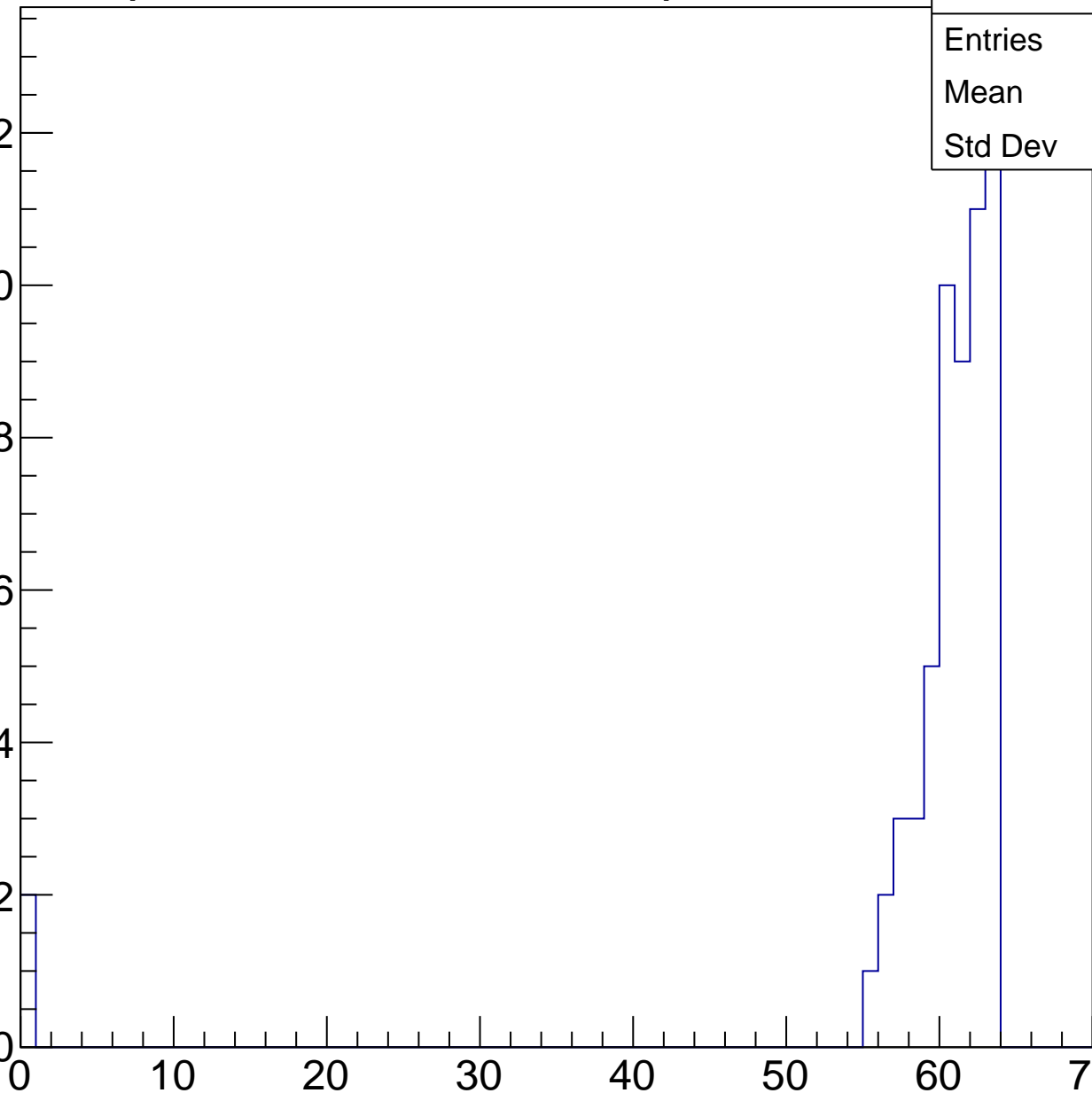
4

2

0

Entries	59
Mean	58.59
Std Dev	11.16

ampl



B1L001S, U20-ch90, adc6

calib_packv5_042523_0143.root, FC#2, port C2

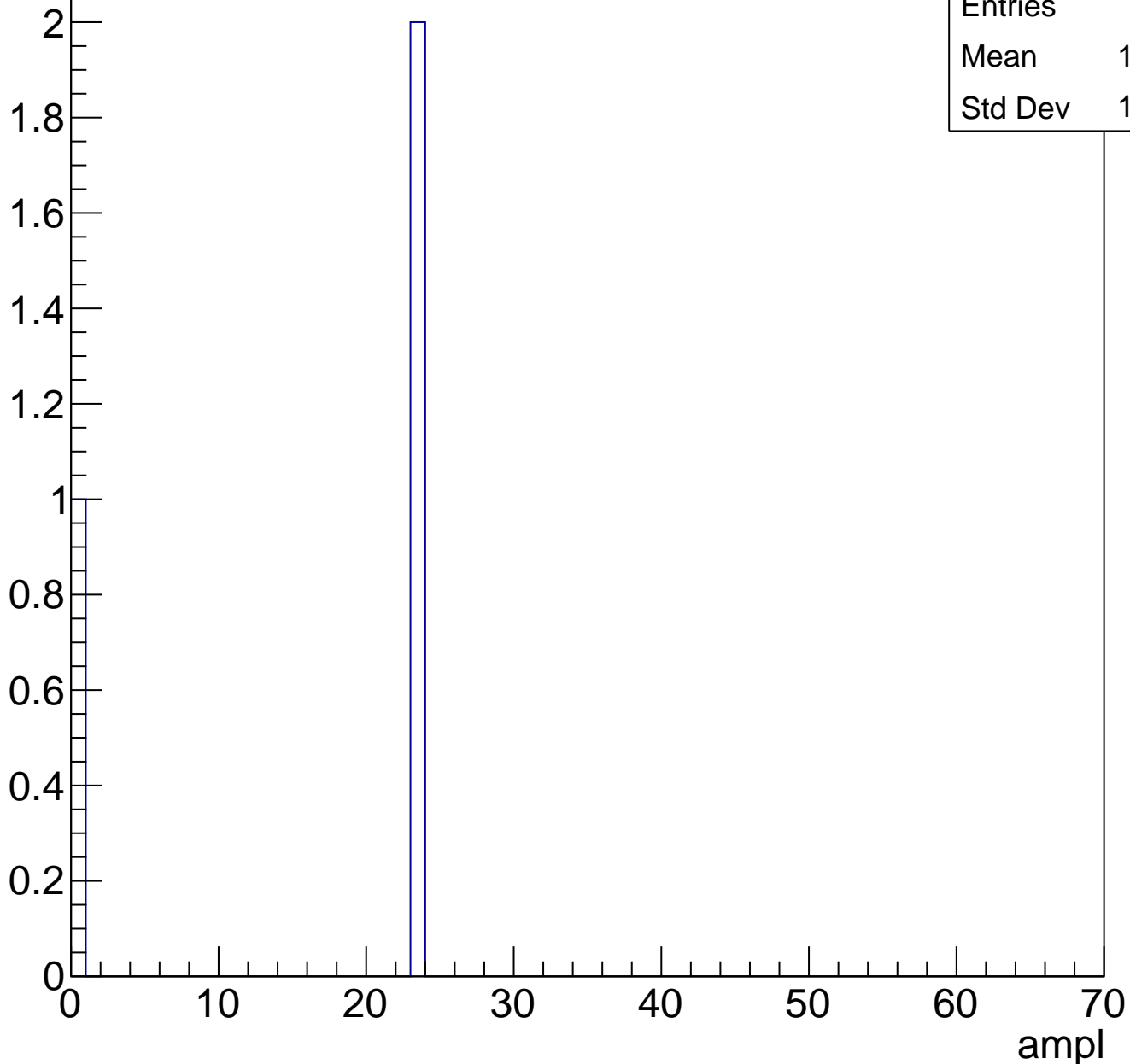
Entry



B1L001S, U20-ch90, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	15.33
Std Dev	10.84

B1L001S, U20-ch91, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	30.06
Std Dev	3.326

Gaus mean : 30.0905

Gaus Width: 3.6182

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

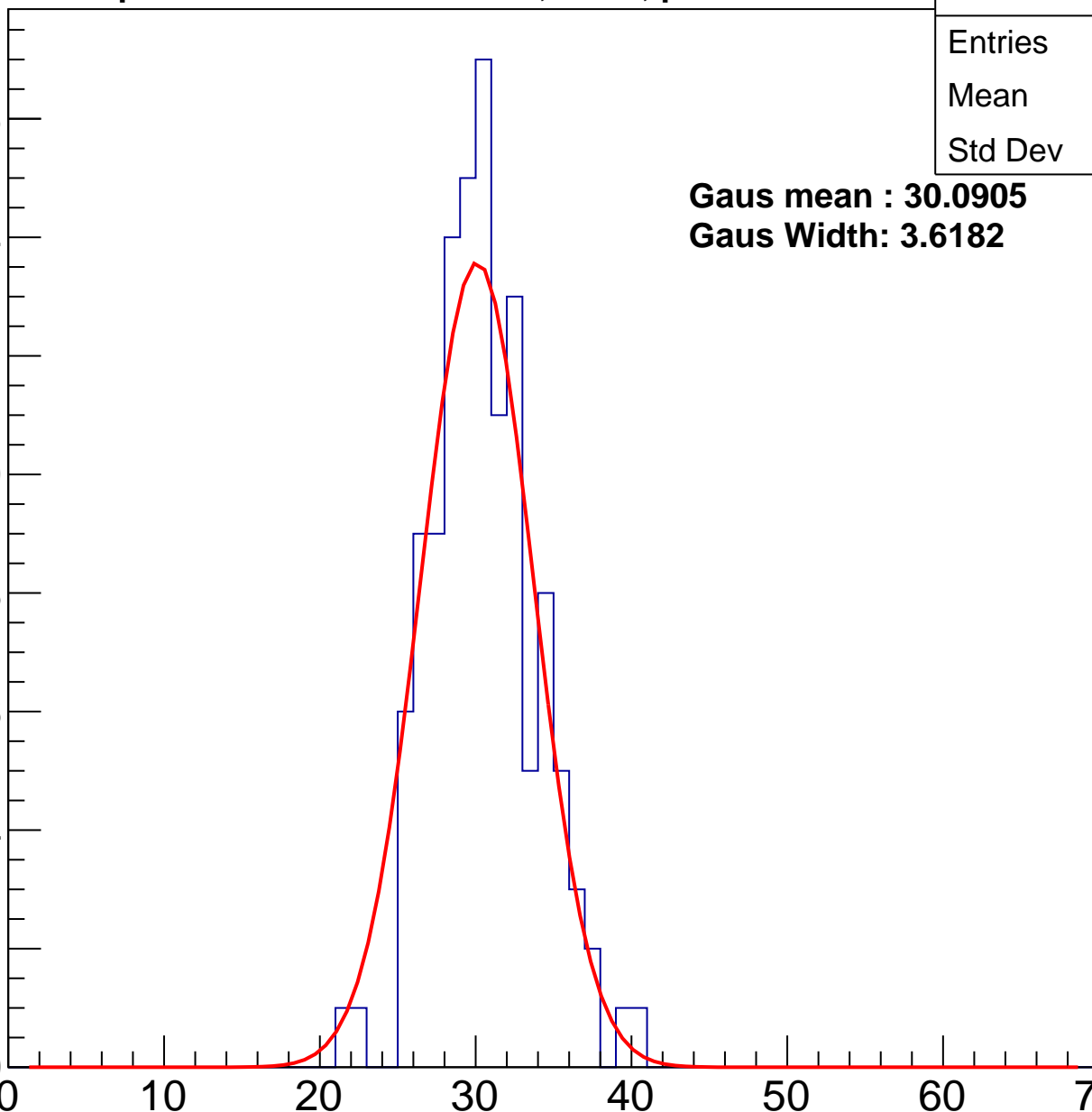
40

50

60

70

ampl



B1L001S, U20-ch91, adc1

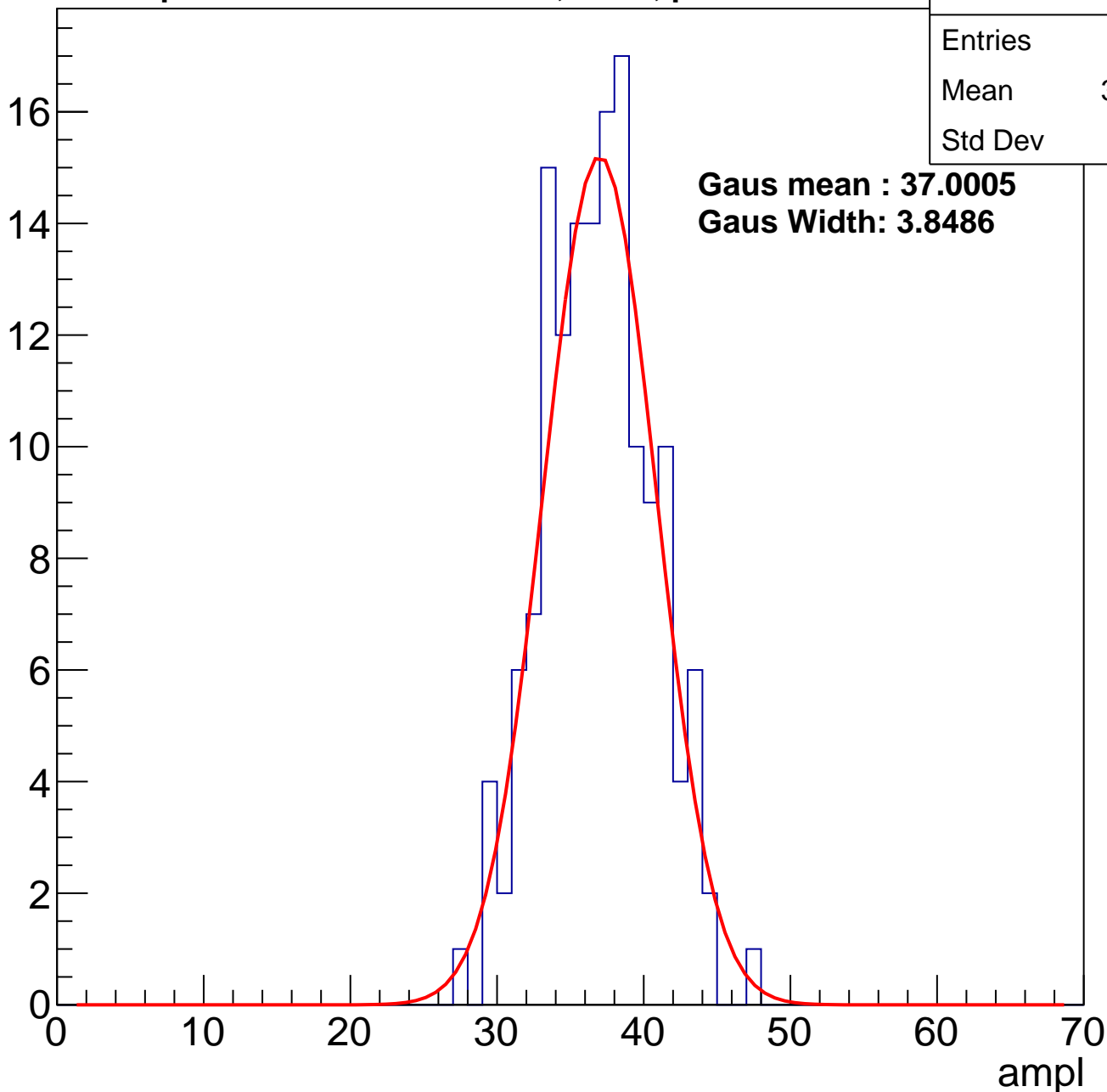
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	36.46
Std Dev	3.68

Gaus mean : 37.0005

Gaus Width: 3.8486

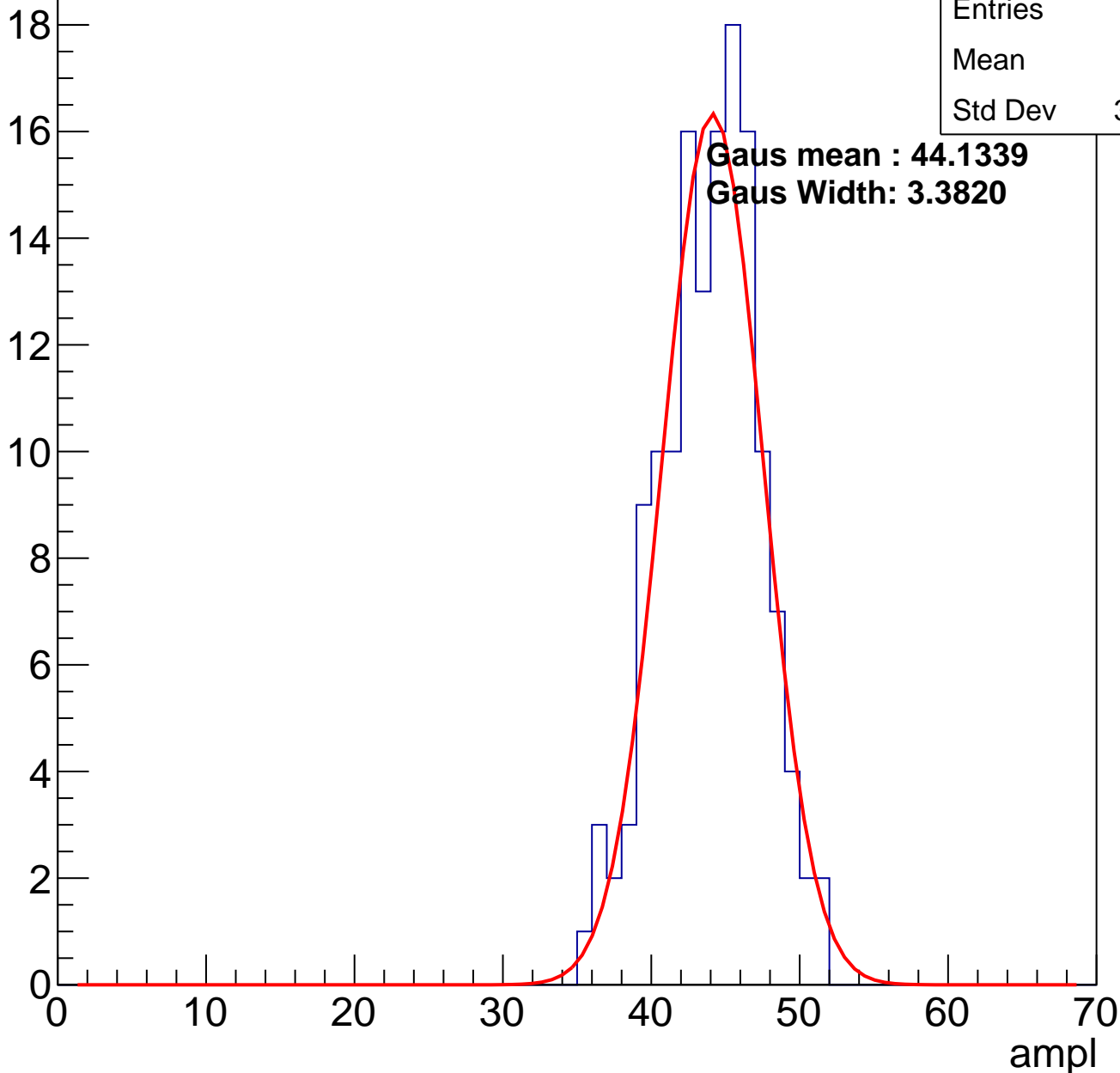
Entry



B1L001S, U20-ch91, adc2

calib_packv5_042523_0143.root, FC#2, port C2

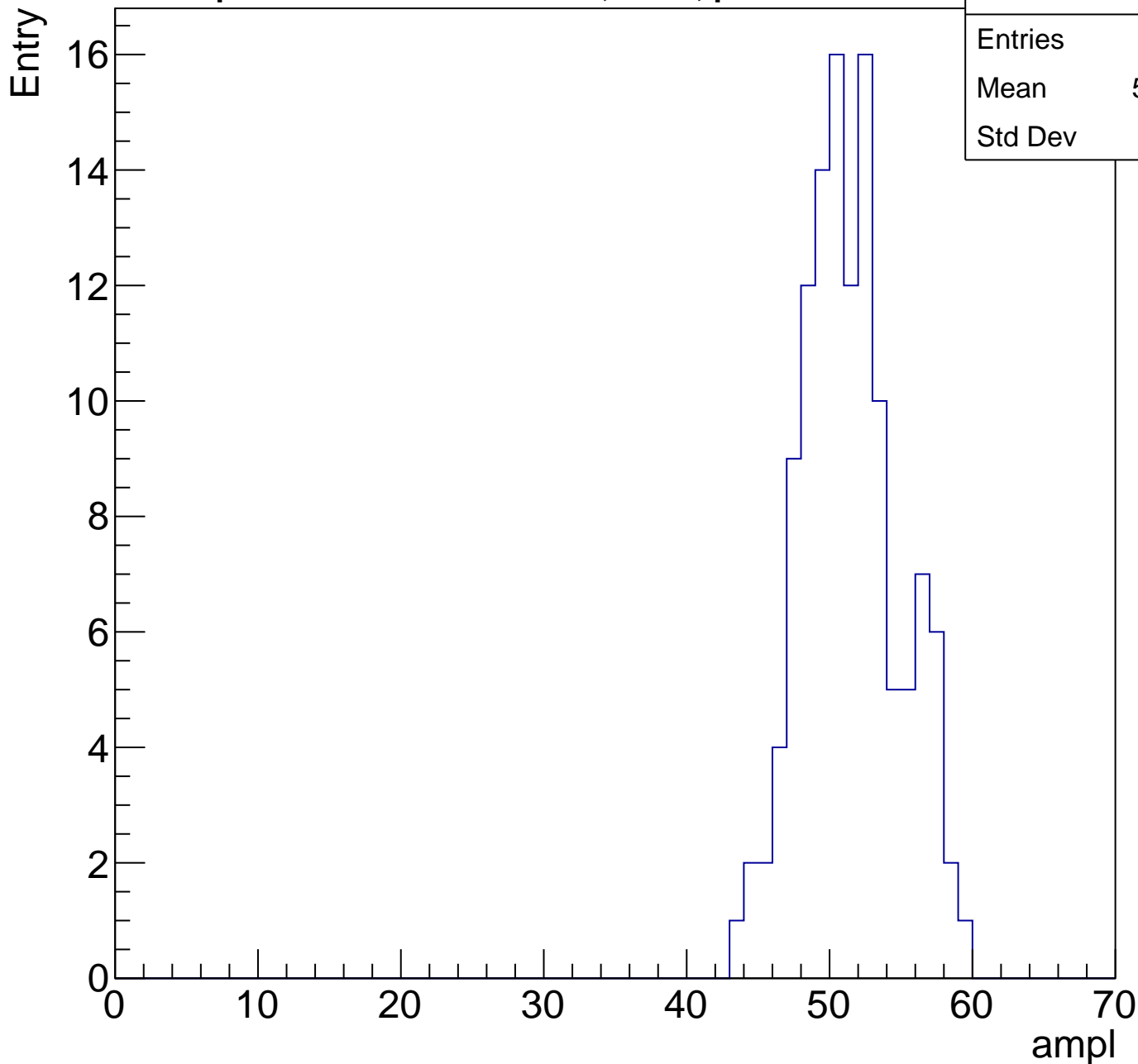
Entry



B1L001S, U20-ch91, adc3

calib_packv5_042523_0143.root, FC#2, port C2

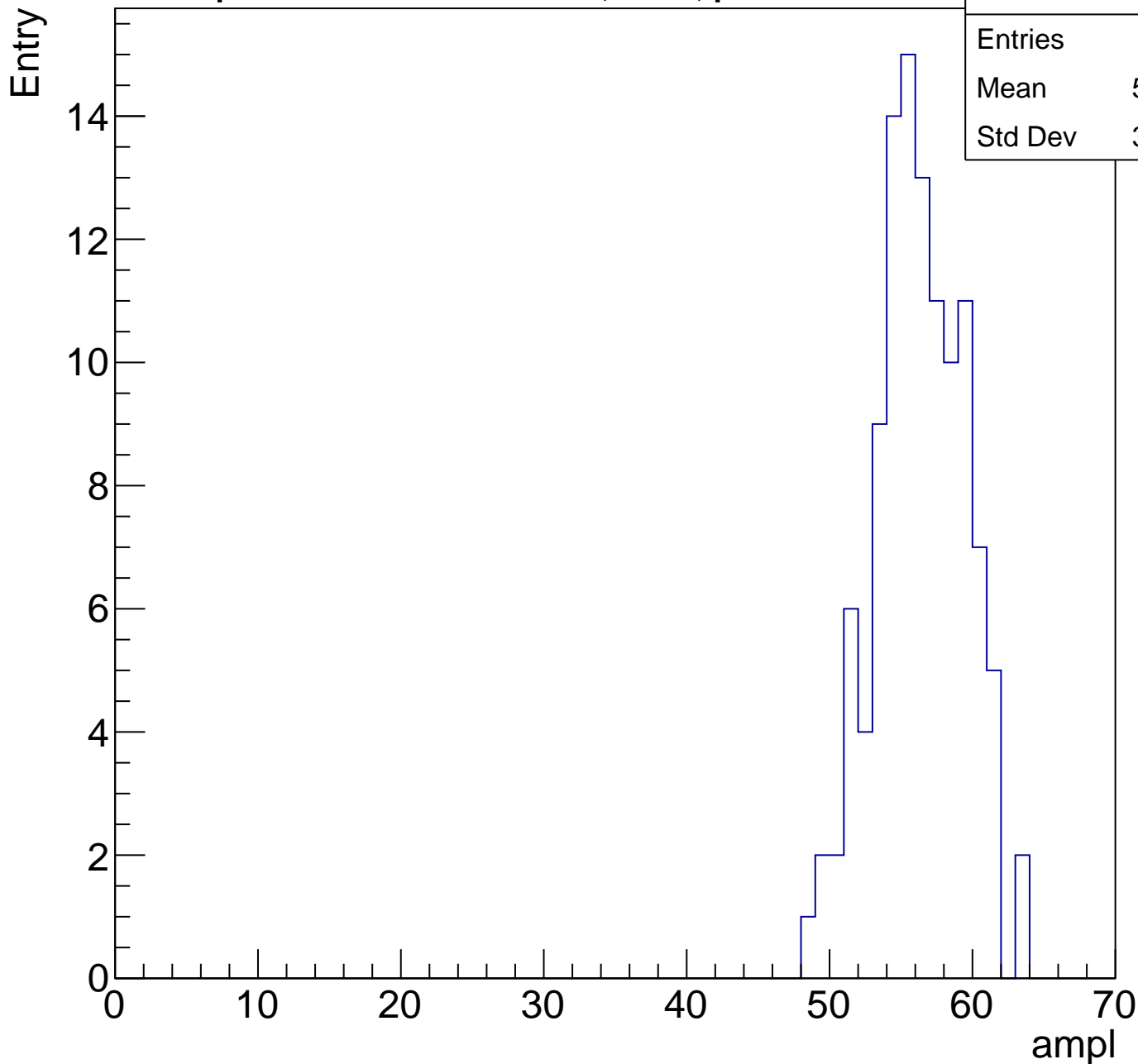
Entries	124
Mean	50.95
Std Dev	3.35



B1L001S, U20-ch91, adc4

calib_packv5_042523_0143.root, FC#2, port C2

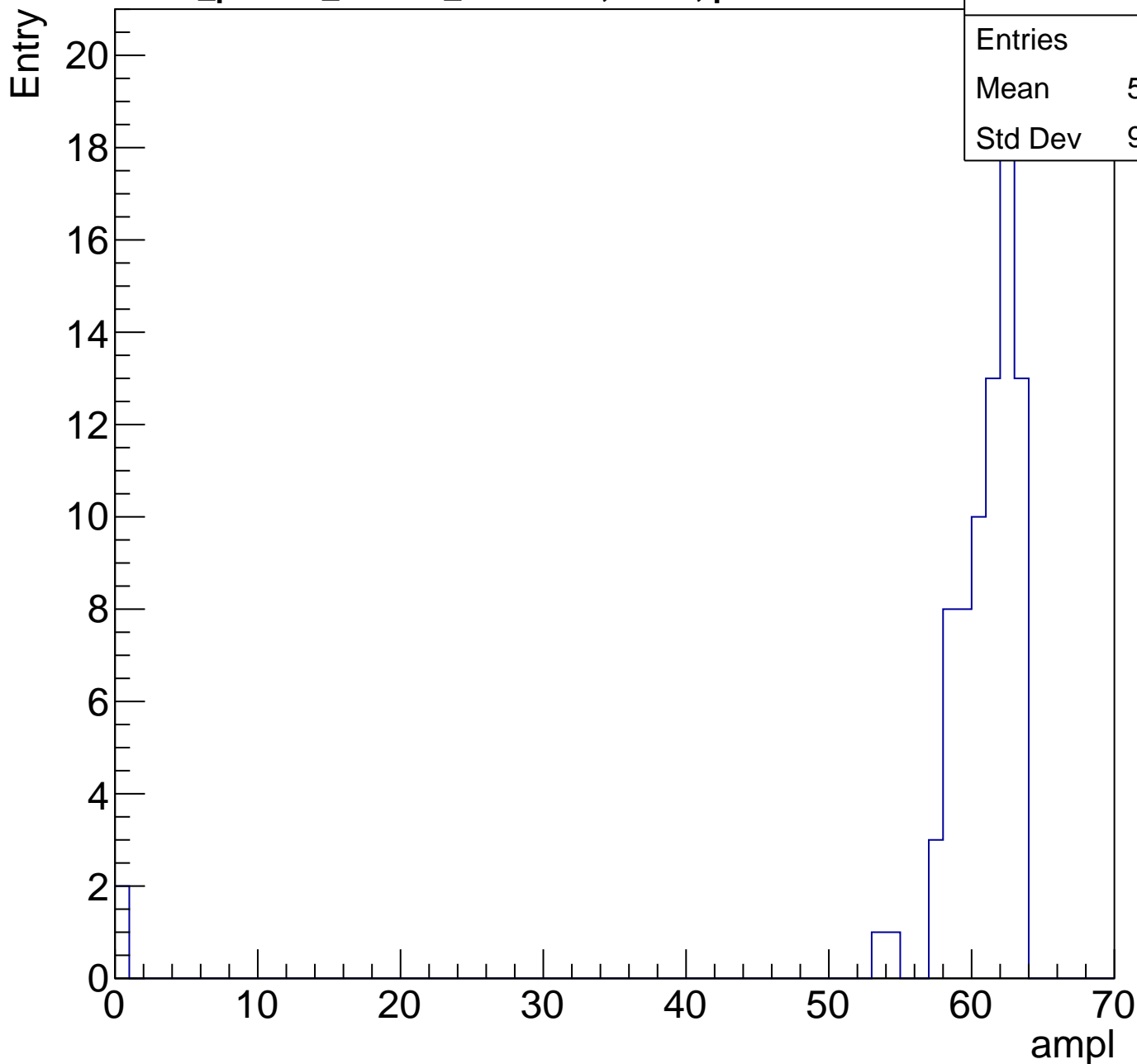
Entries	112
Mean	55.83
Std Dev	3.108



B1L001S, U20-ch91, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	79
Mean	59.06
Std Dev	9.739

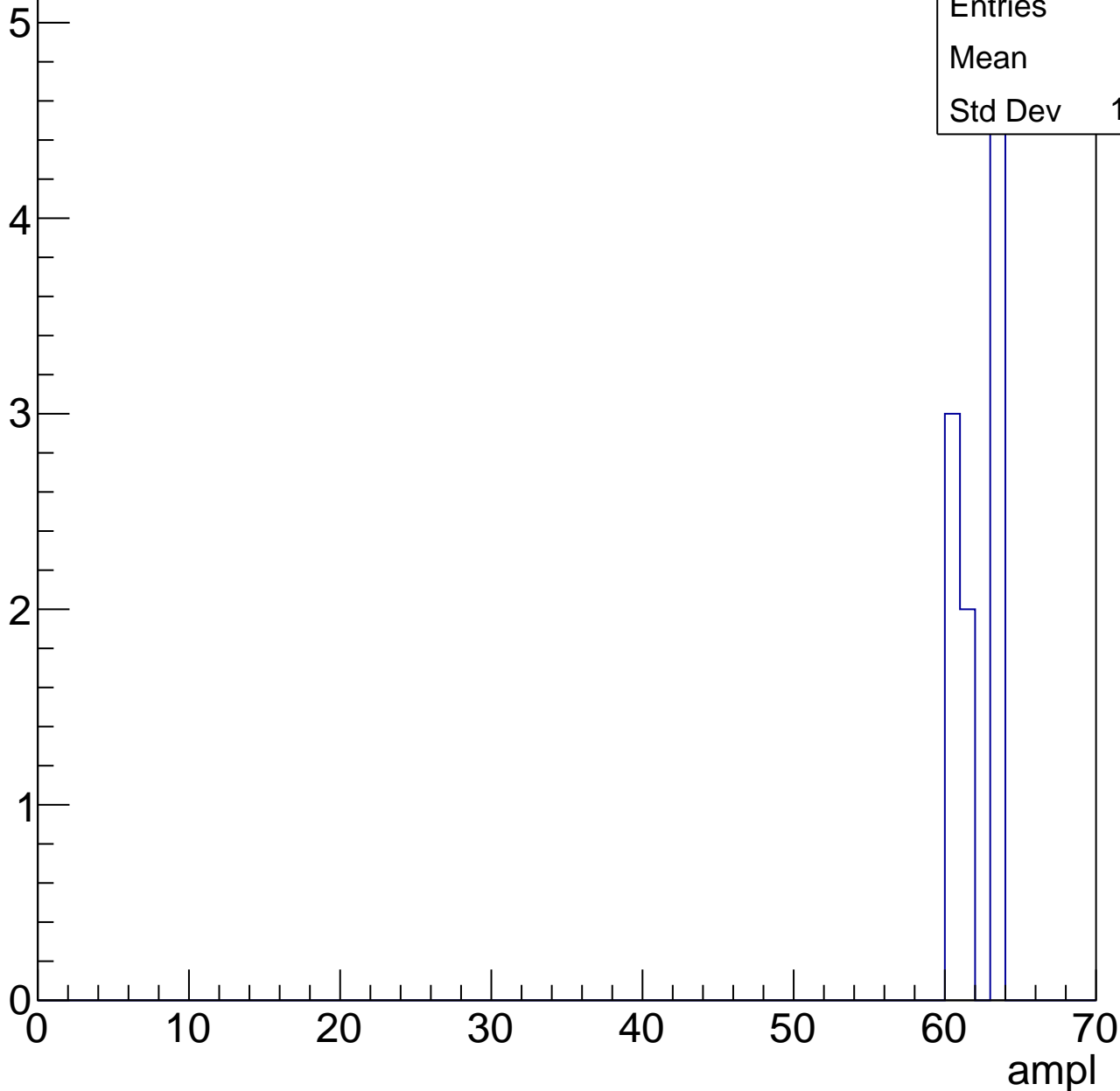


B1L001S, U20-ch91, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	61.7
Std Dev	1.345



B1L001S, U20-ch91, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U20-ch92, adc0

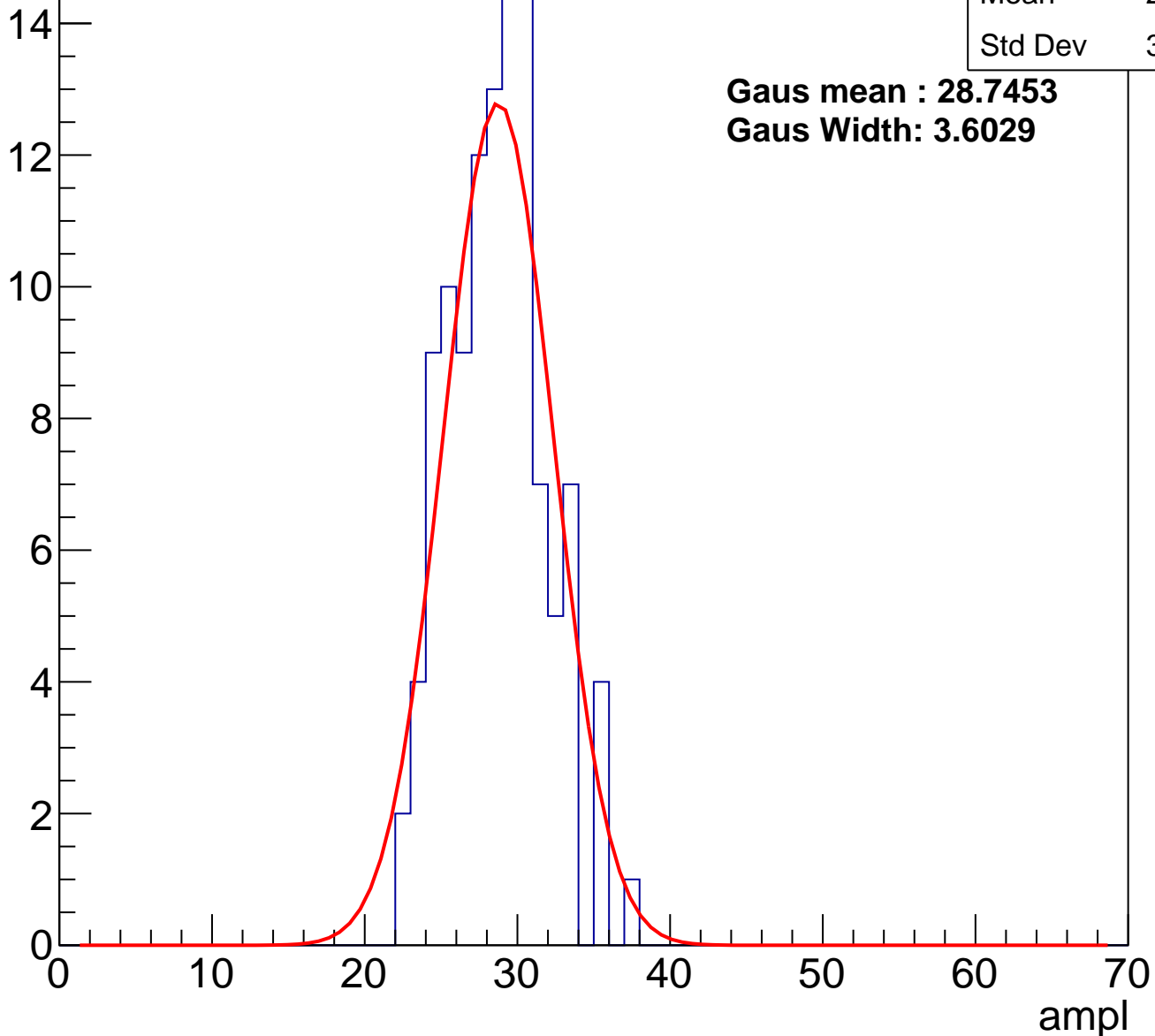
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	28.27
Std Dev	3.143

Gaus mean : 28.7453

Gaus Width: 3.6029

Entry

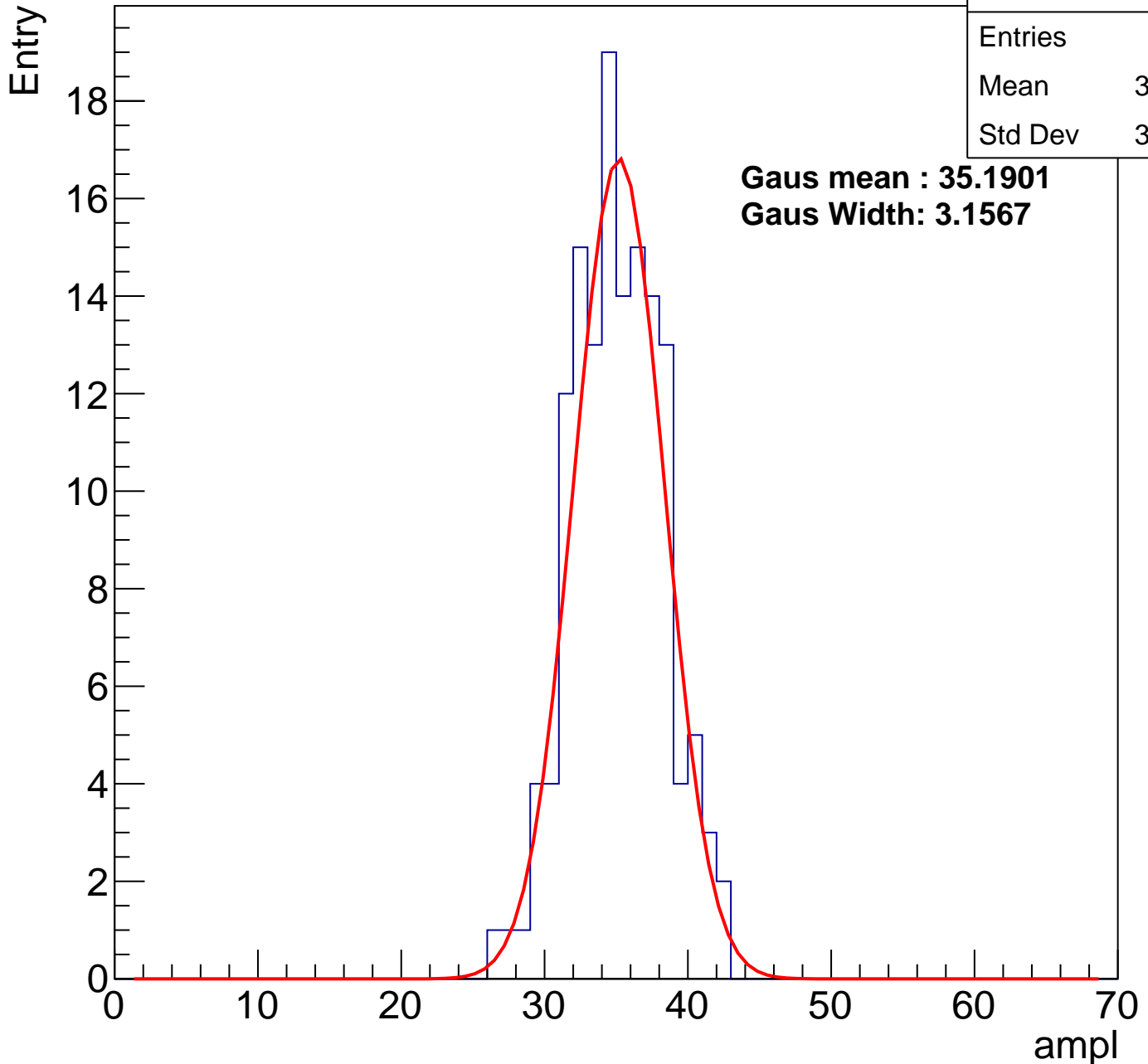


B1L001S, U20-ch92, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	34.64
Std Dev	3.163

Gaus mean : 35.1901
Gaus Width: 3.1567



B1L001S, U20-ch92, adc2

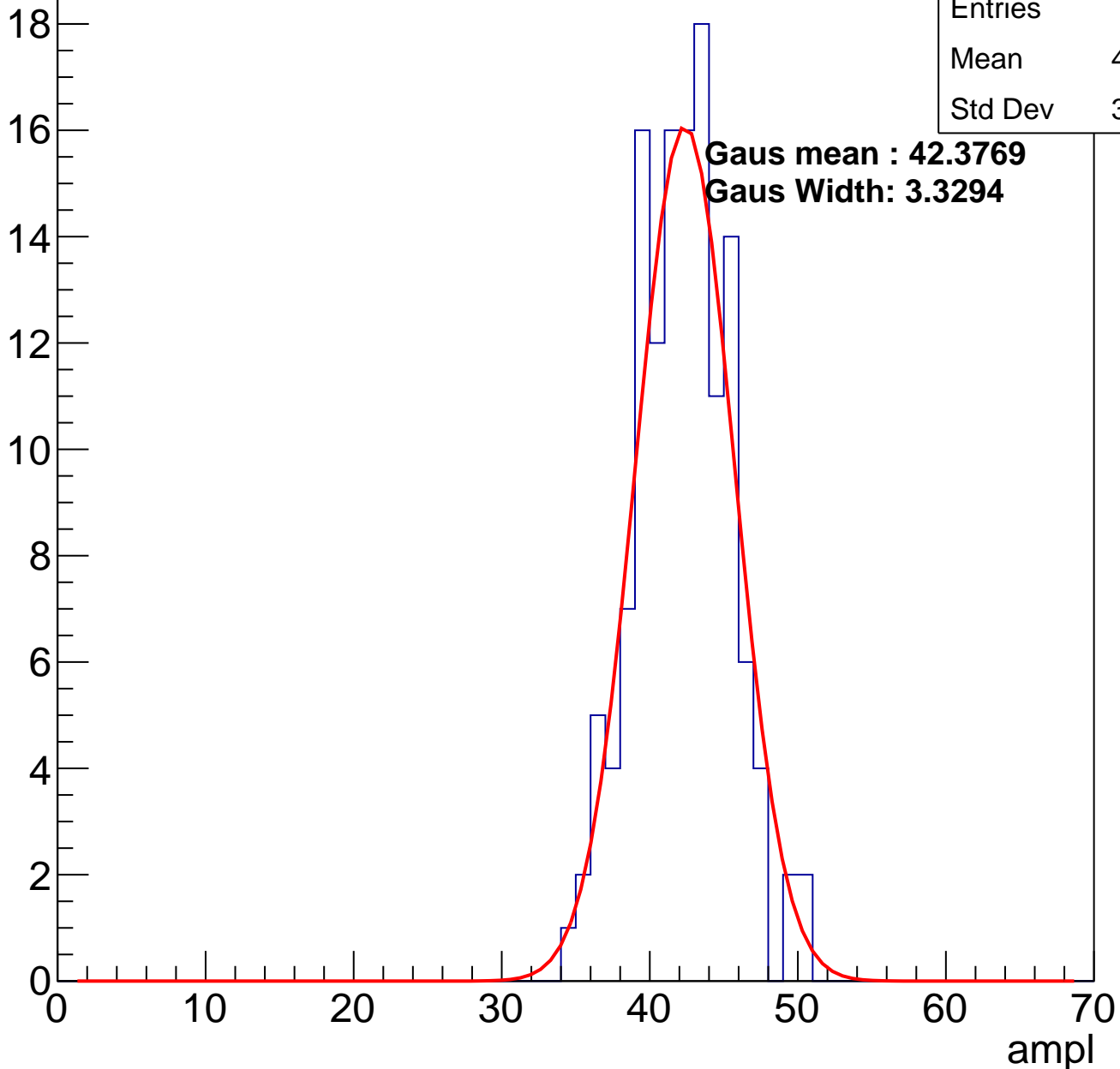
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	41.76
Std Dev	3.167

Gaus mean : 42.3769

Gaus Width: 3.3294

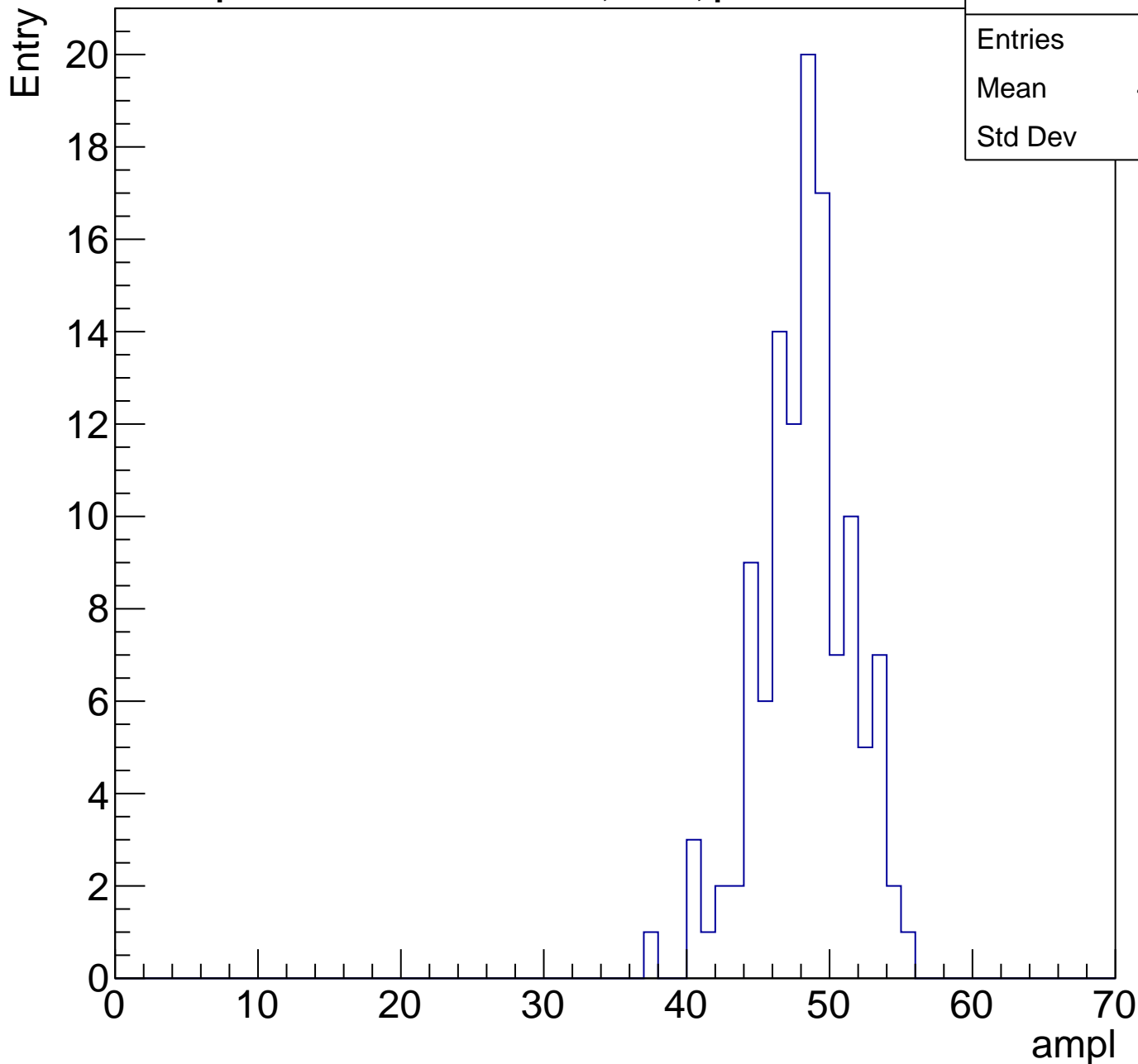
Entry



B1L001S, U20-ch92, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	47.81
Std Dev	3.25



B1L001S, U20-ch92, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	53.7
Std Dev	2.958

Entry

14
12
10
8
6
4
2
0

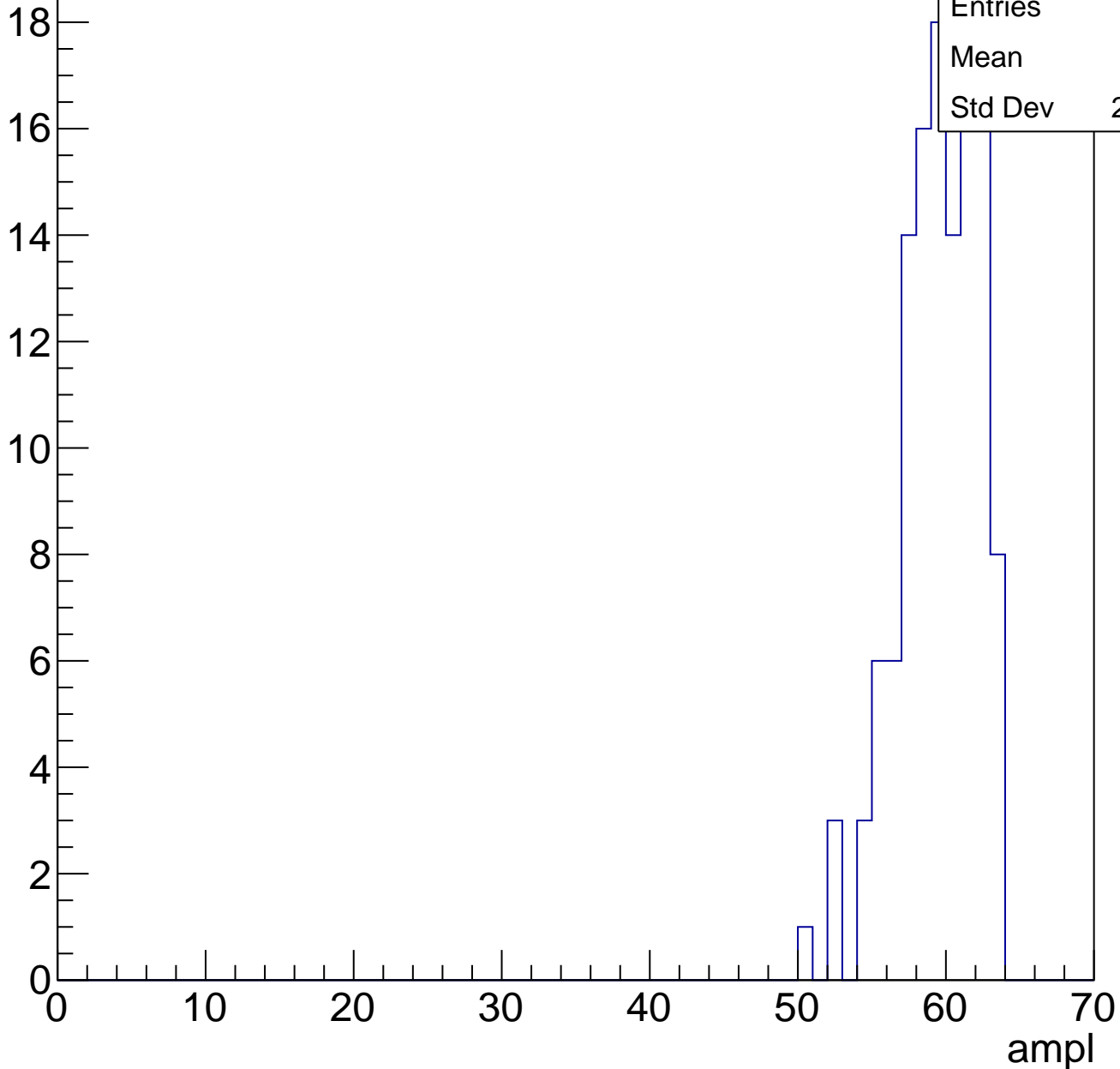
ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch92, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch92, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

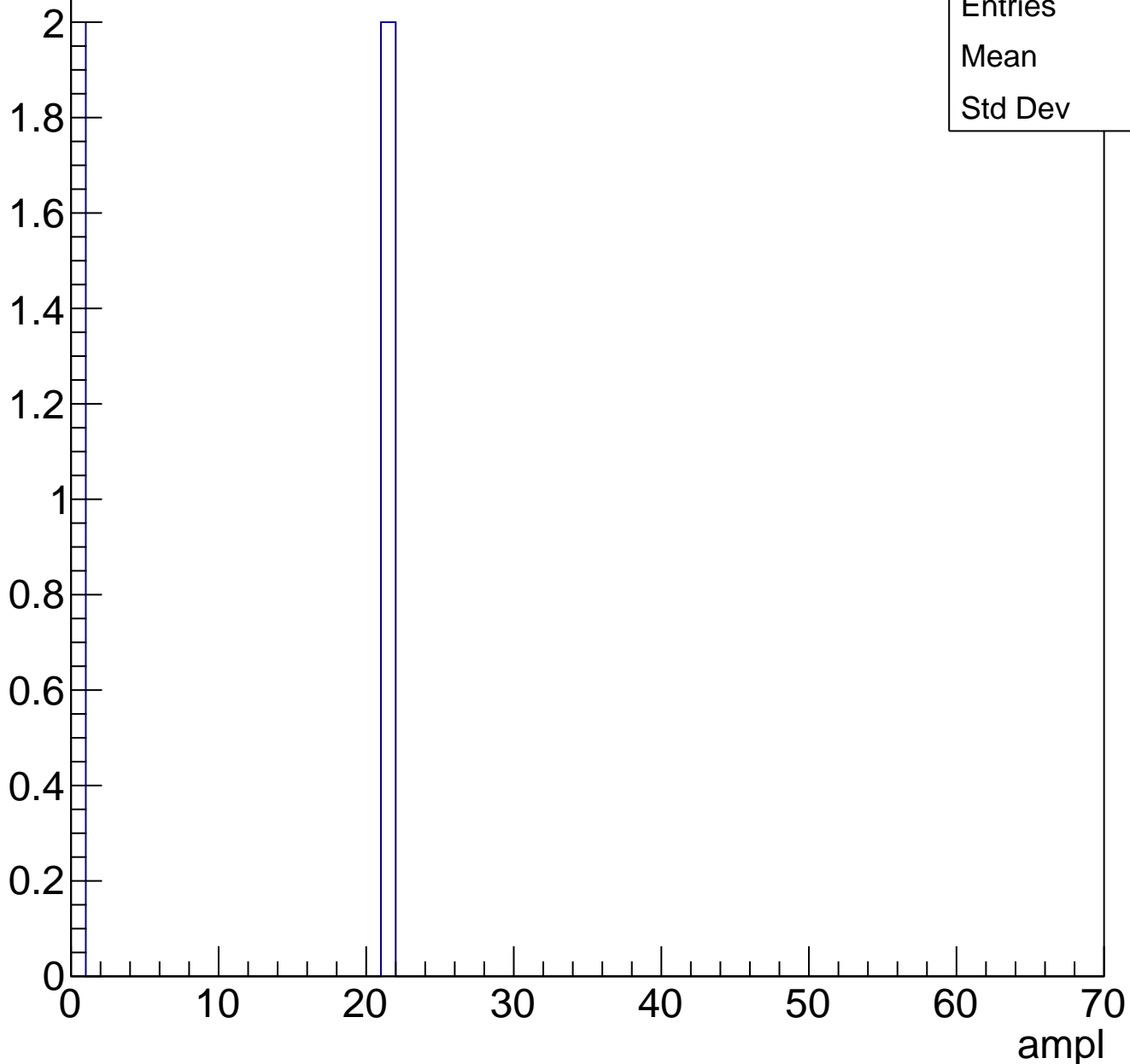
ampl

Entries	23
Mean	56.52
Std Dev	17.49

B1L001S, U20-ch92, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch93, adc0

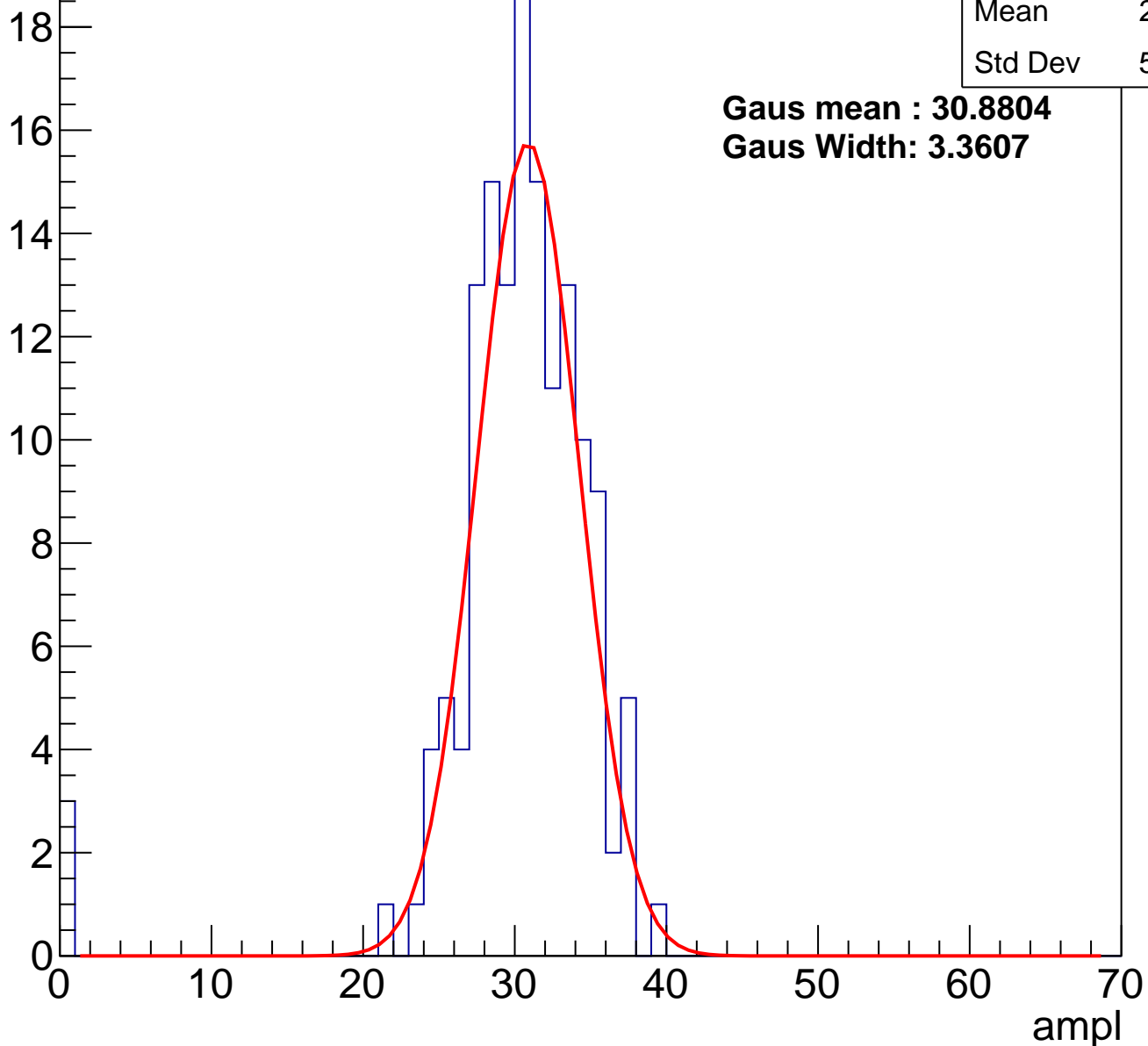
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	29.75
Std Dev	5.466

Gaus mean : 30.8804

Gaus Width: 3.3607

Entry



B1L001S, U20-ch93, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	37.47
Std Dev	3.113

Gaus mean : 37.8477

Gaus Width: 3.2456

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

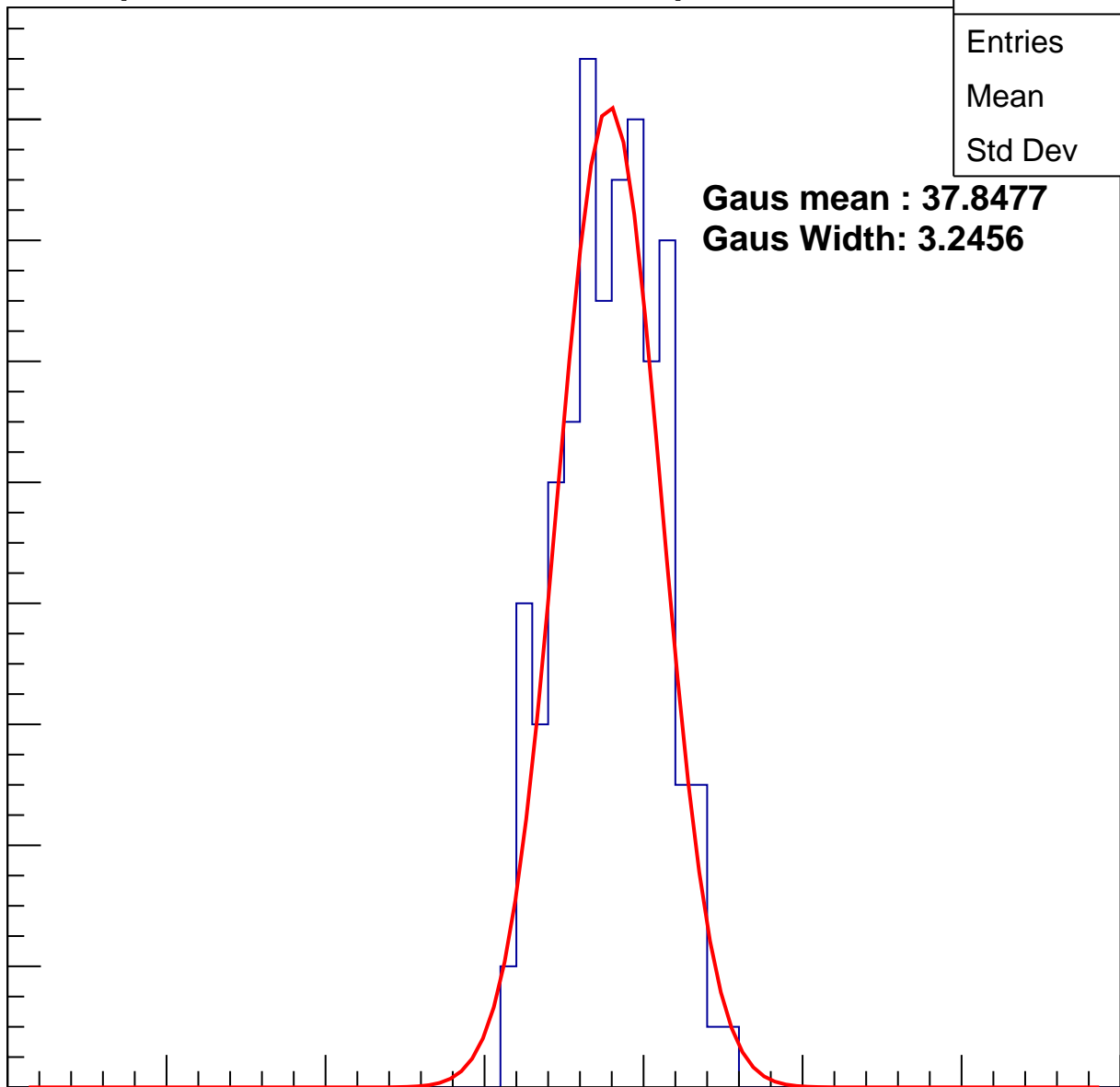
40

50

60

70

ampl



B1L001S, U20-ch93, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	44.67
Std Dev	3.406

Gaus mean : 45.2696

Gaus Width: 3.3691

Entry

16

14

12

10

8

6

4

2

0

0

10

20

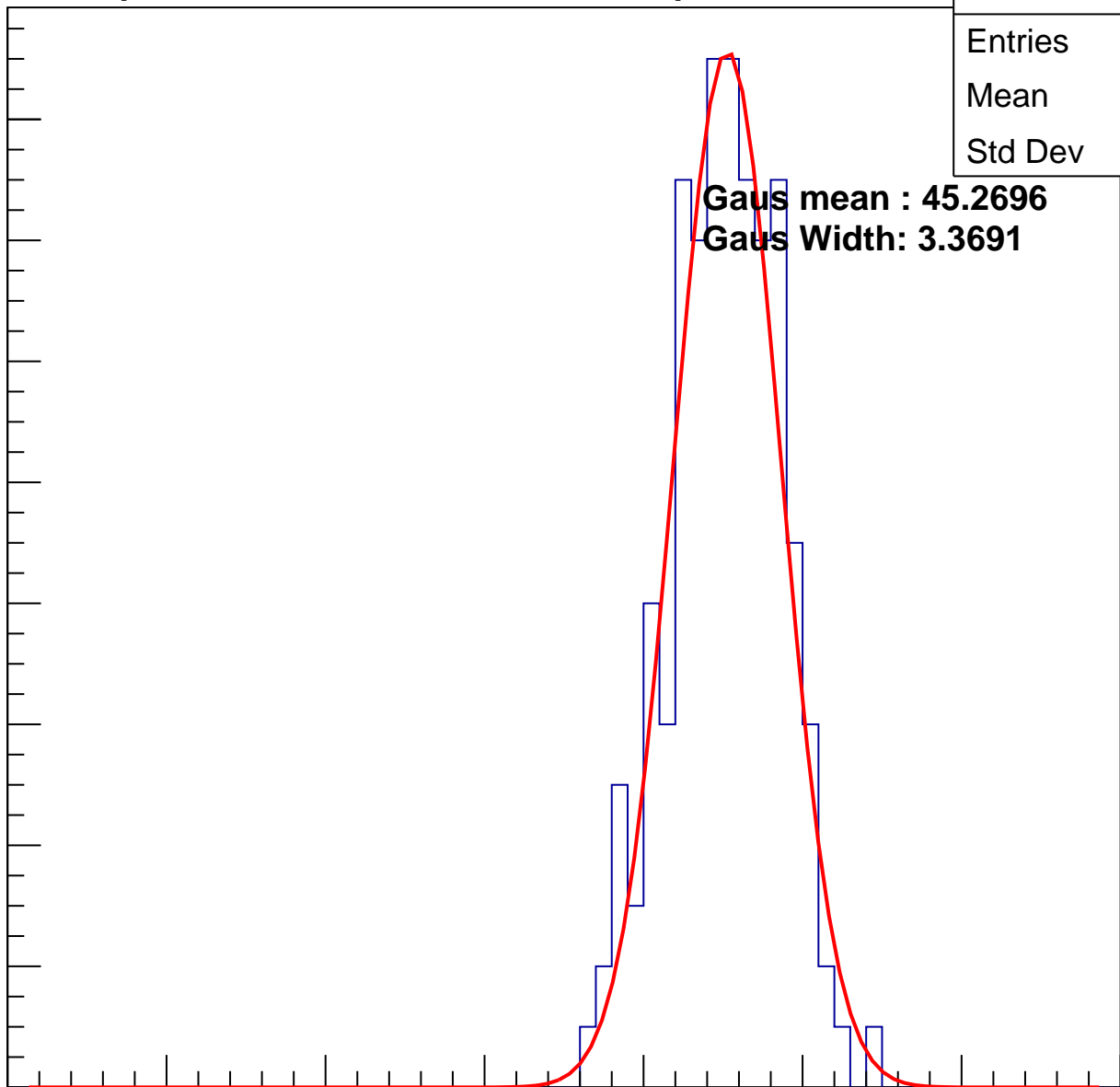
30

40

50

60

ampl



B1L001S, U20-ch93, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

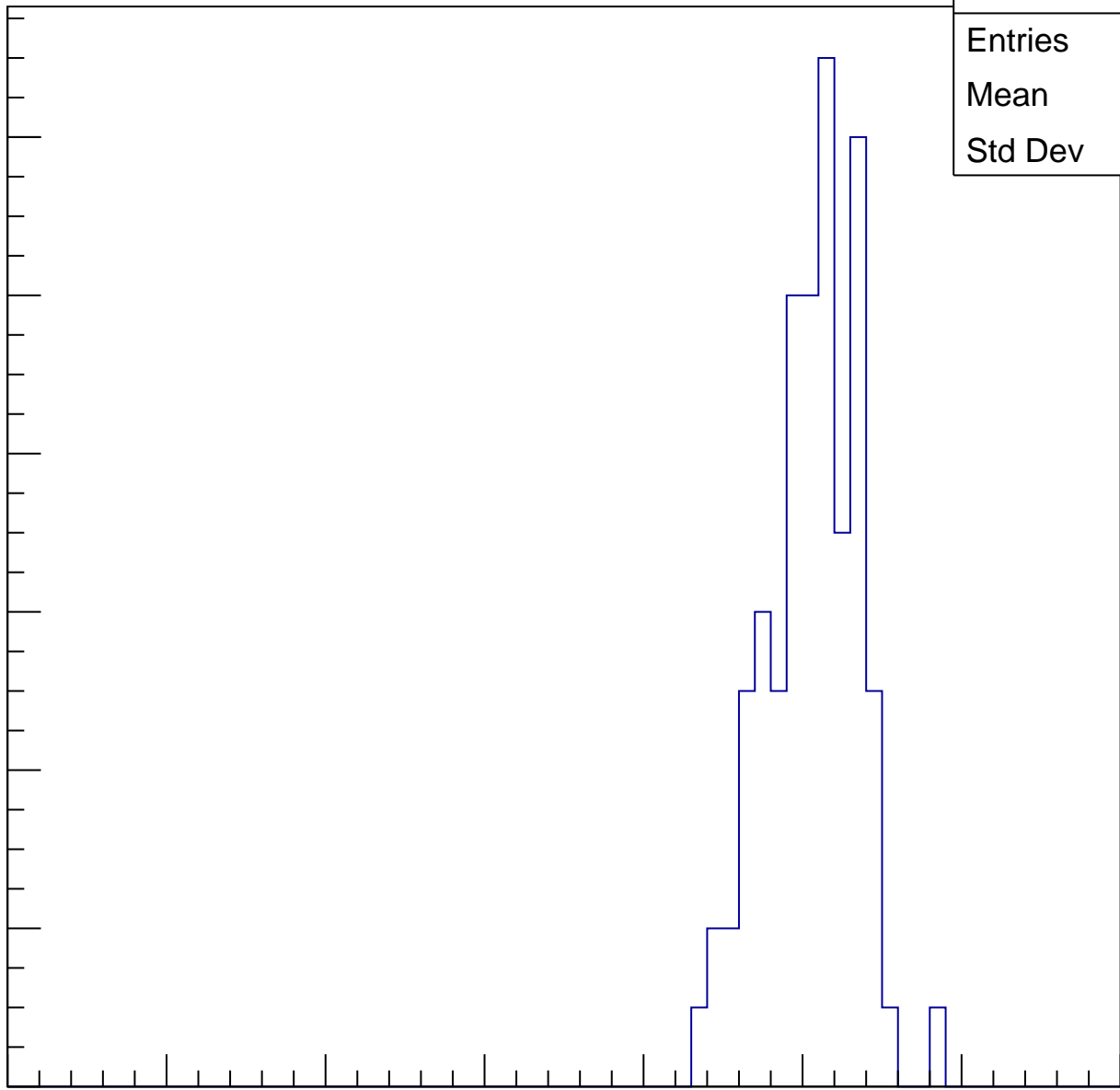
2

0

Entries	80
Mean	50.11
Std Dev	2.855

ampl

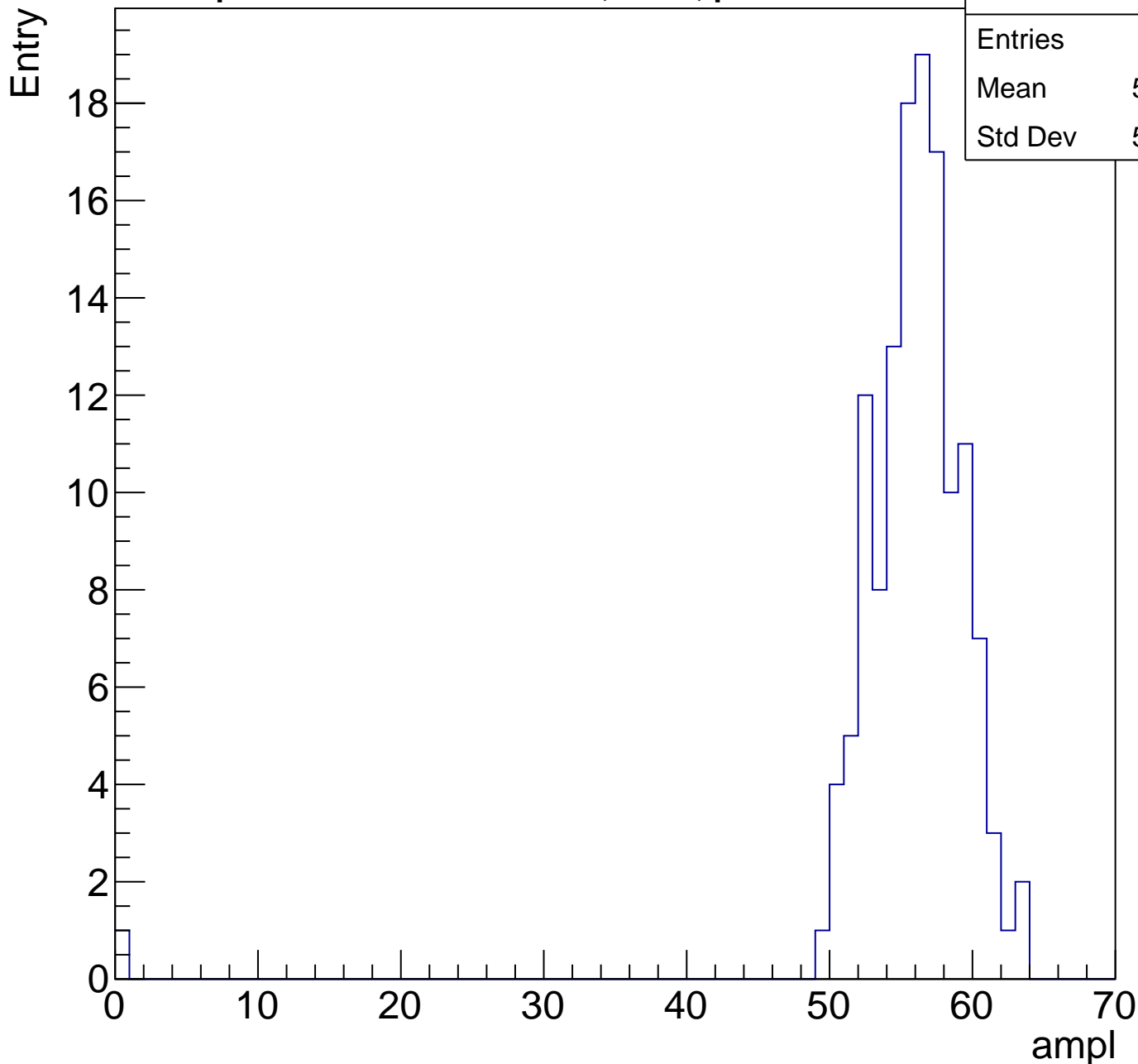
0 10 20 30 40 50 60 70



B1L001S, U20-ch93, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	55.28
Std Dev	5.638



B1L001S, U20-ch93, adc5

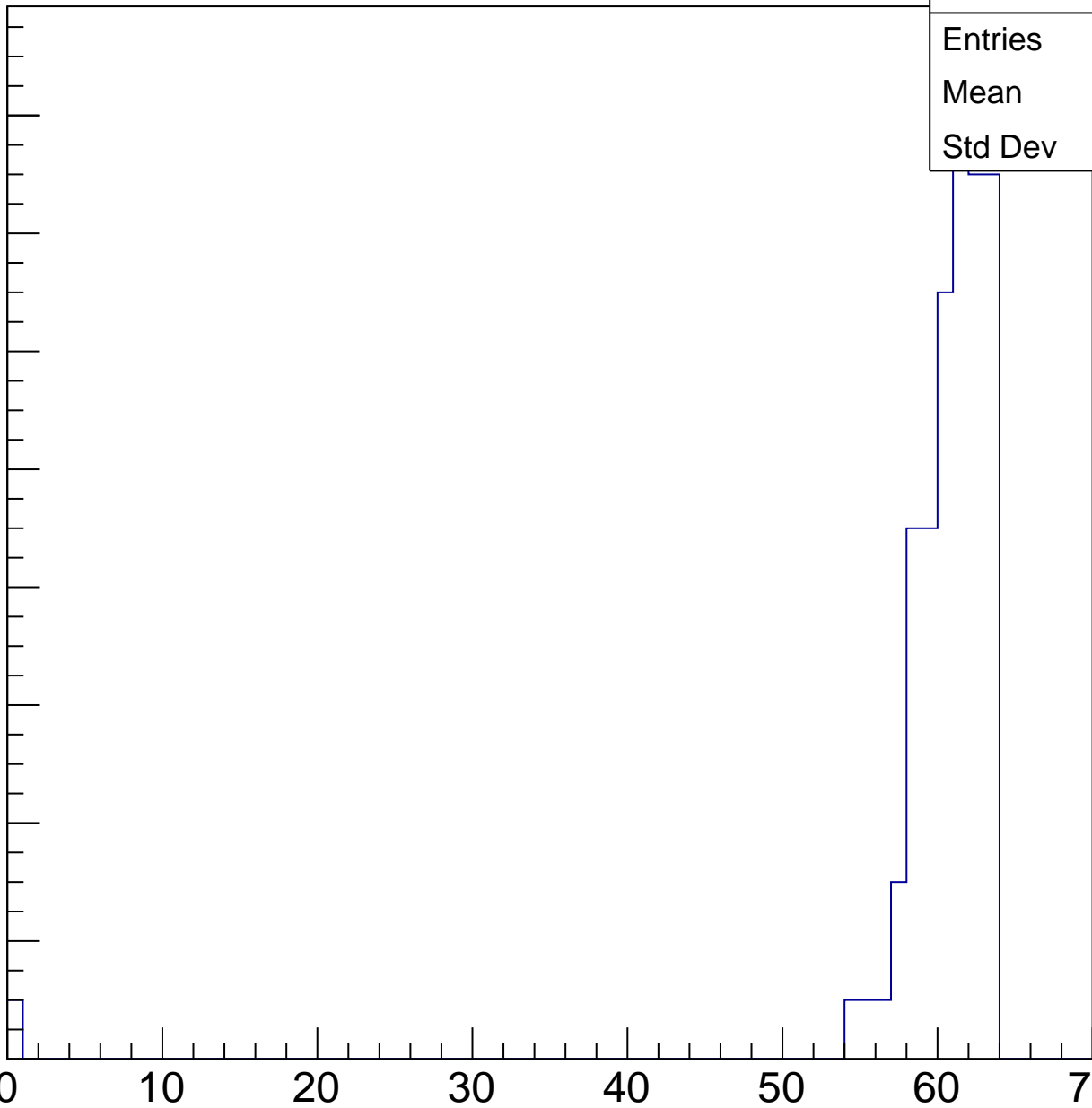
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	59.78
Std Dev	6.823

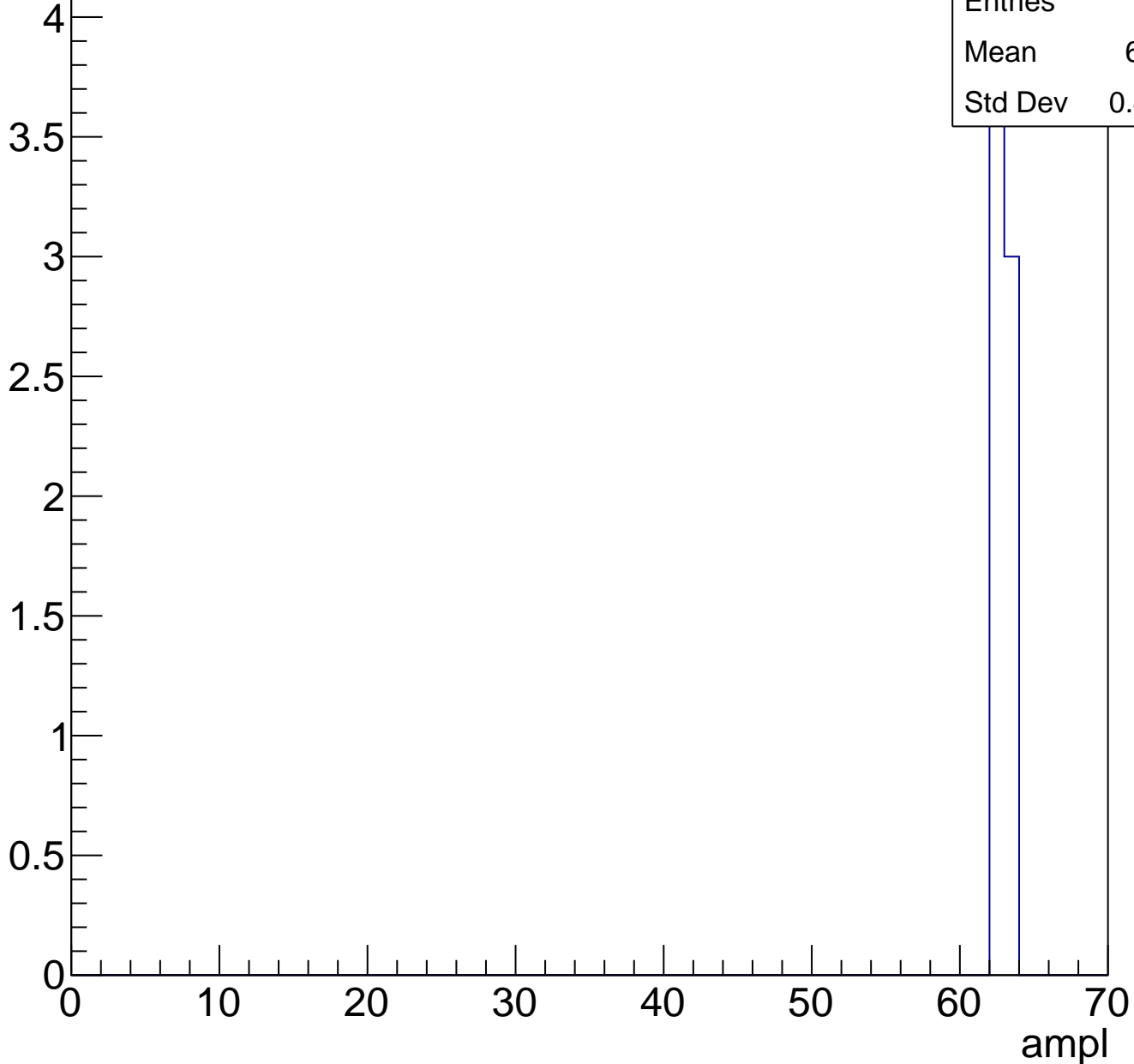
ampl



B1L001S, U20-ch93, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	7
Mean	62.43
Std Dev	0.4949

B1L001S, U20-ch93, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



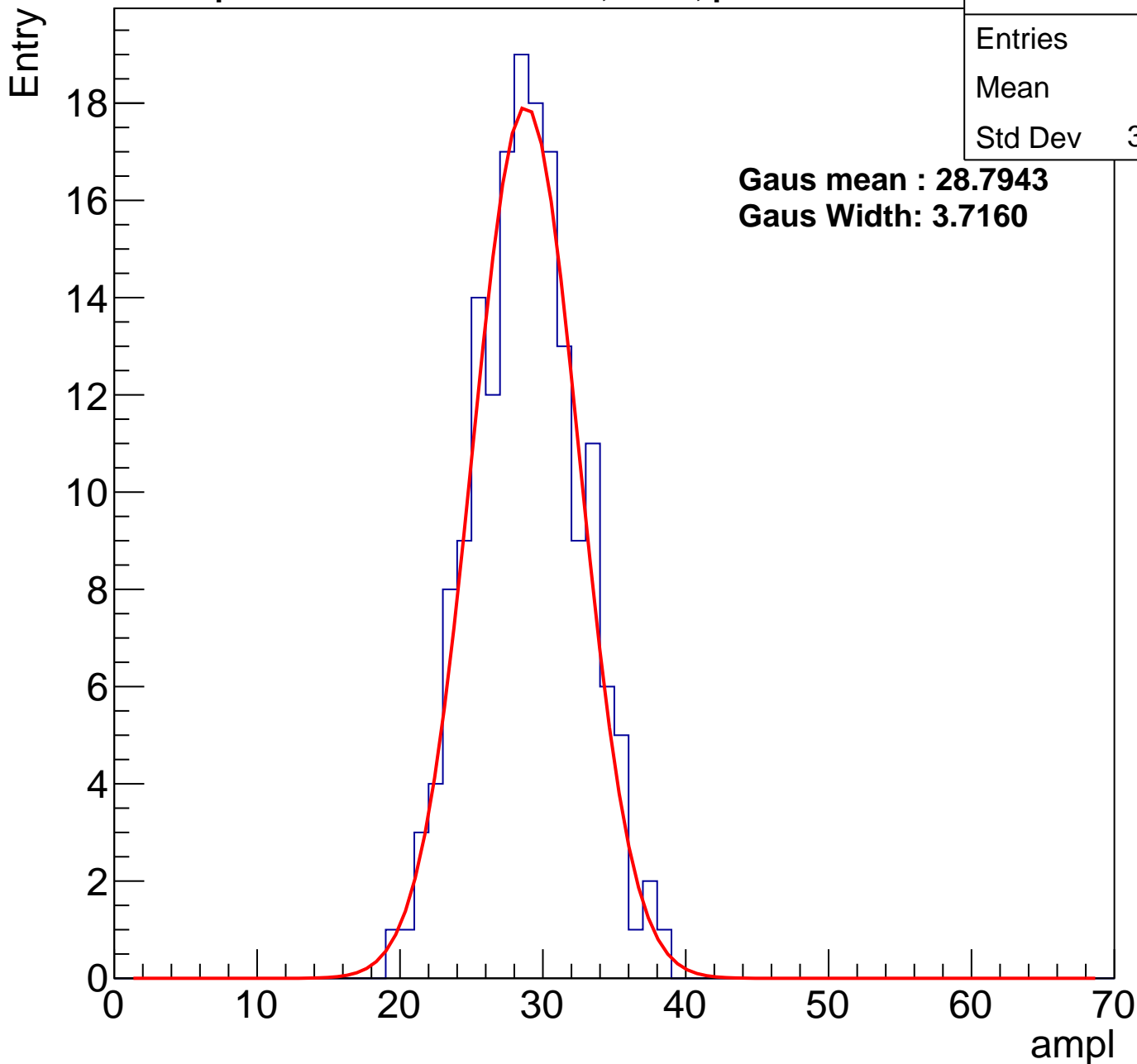
B1L001S, U20-ch94, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	171
Mean	28.4
Std Dev	3.709

Gaus mean : 28.7943

Gaus Width: 3.7160



B1L001S, U20-ch94, adc1

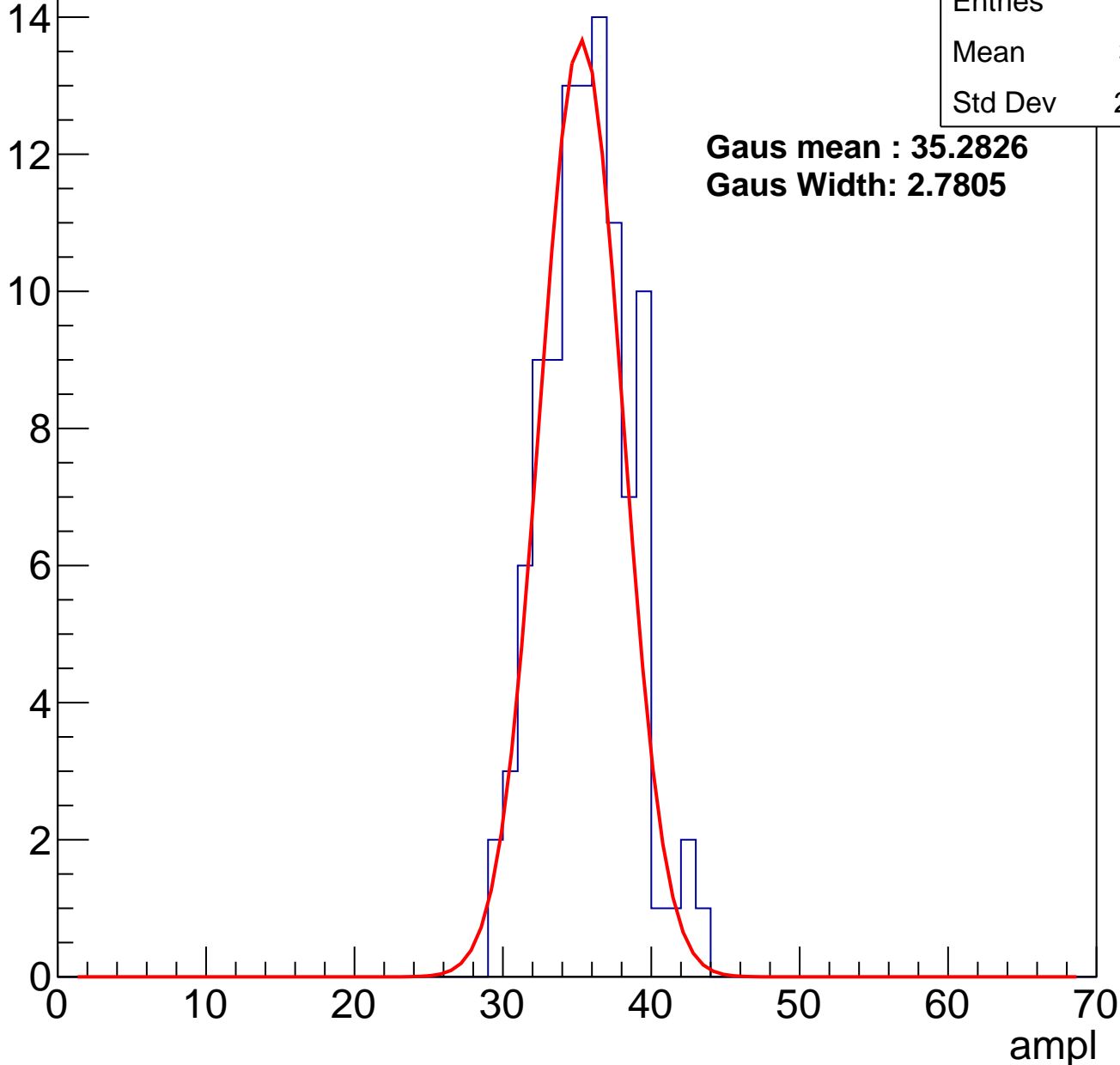
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	35.21
Std Dev	2.925

Gaus mean : 35.2826

Gaus Width: 2.7805

Entry



B1L001S, U20-ch94, adc2

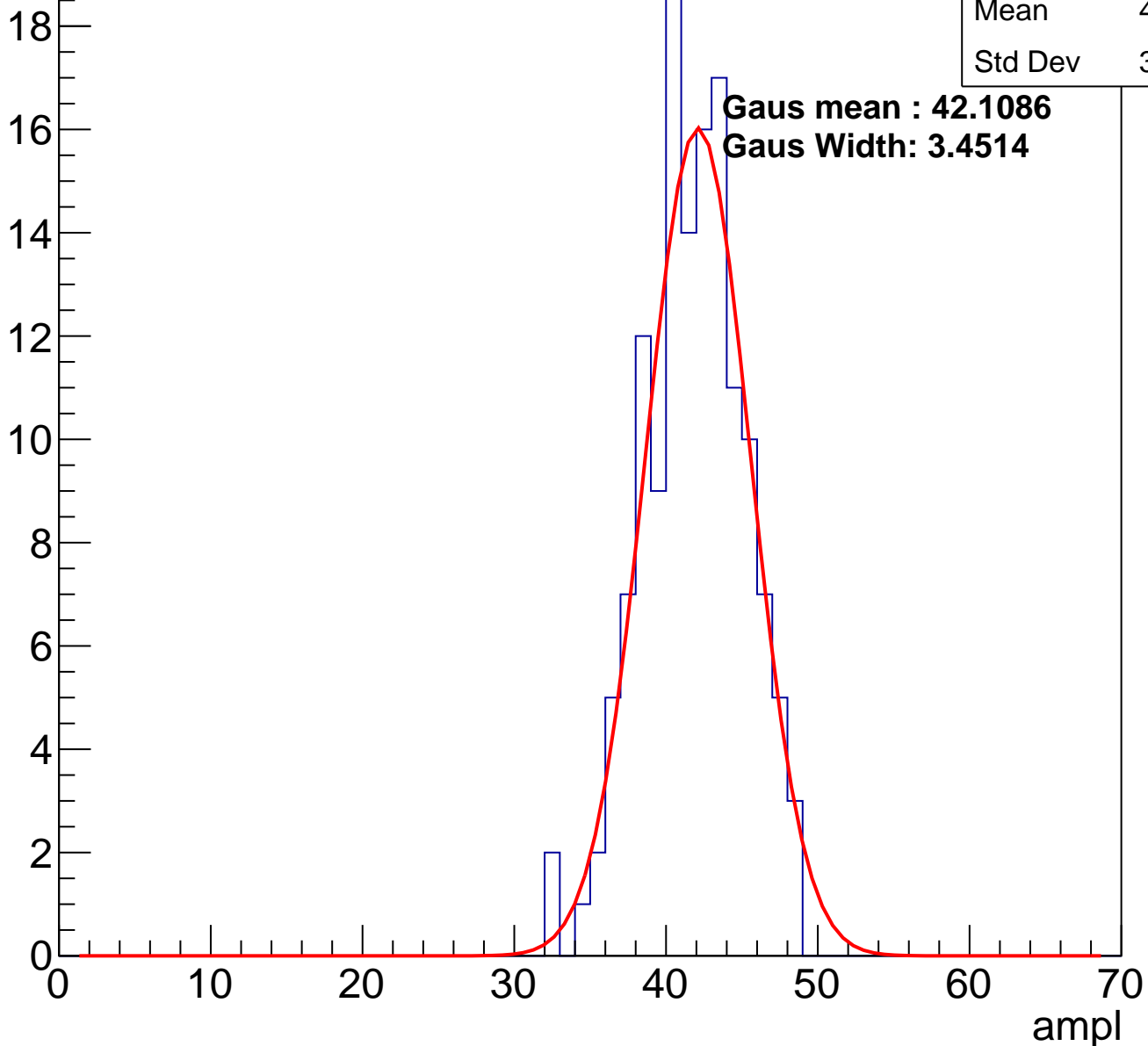
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	41.33
Std Dev	3.287

Gaus mean : 42.1086

Gaus Width: 3.4514

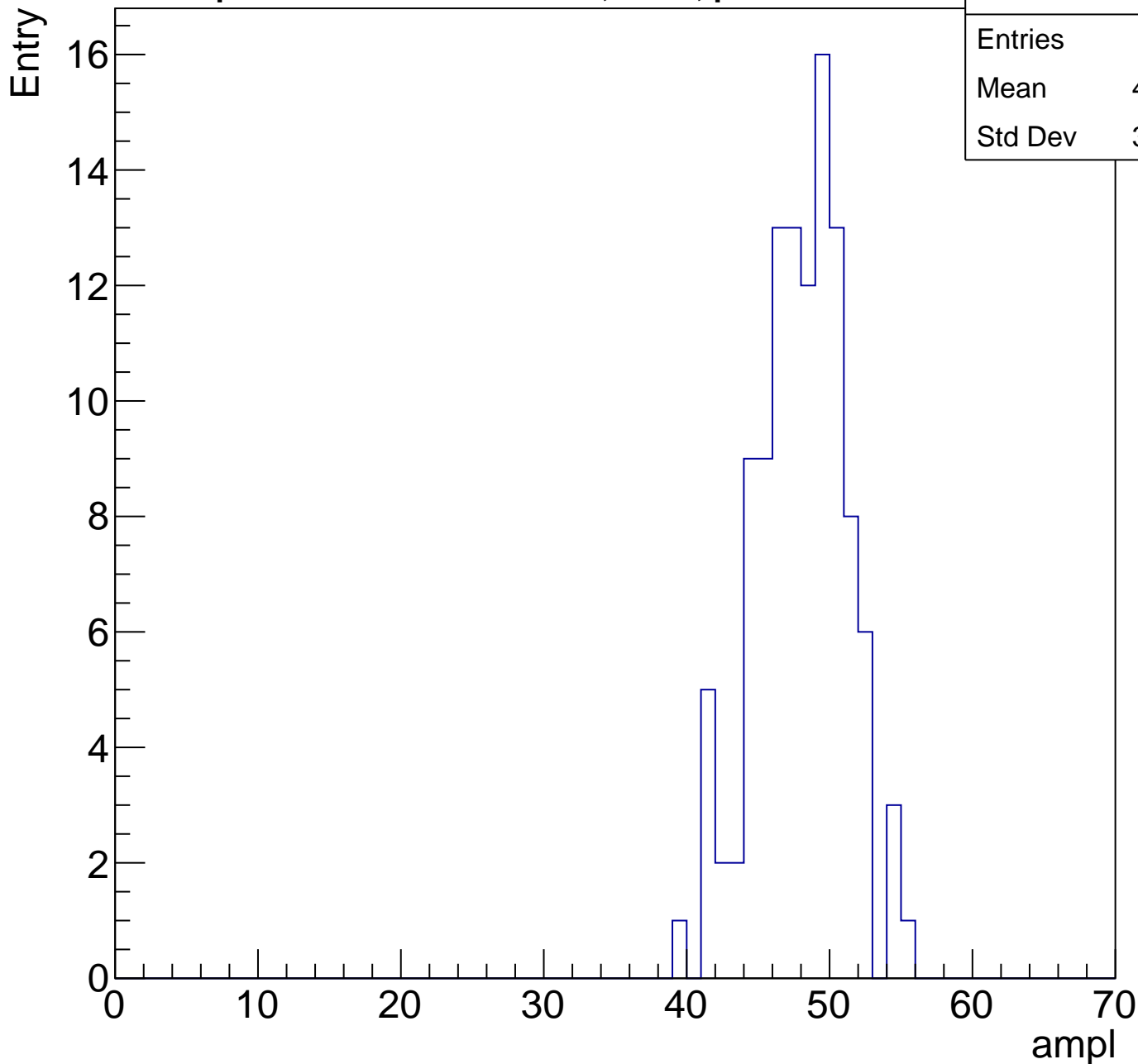
Entry



B1L001S, U20-ch94, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	47.53
Std Dev	3.129



B1L001S, U20-ch94, adc4

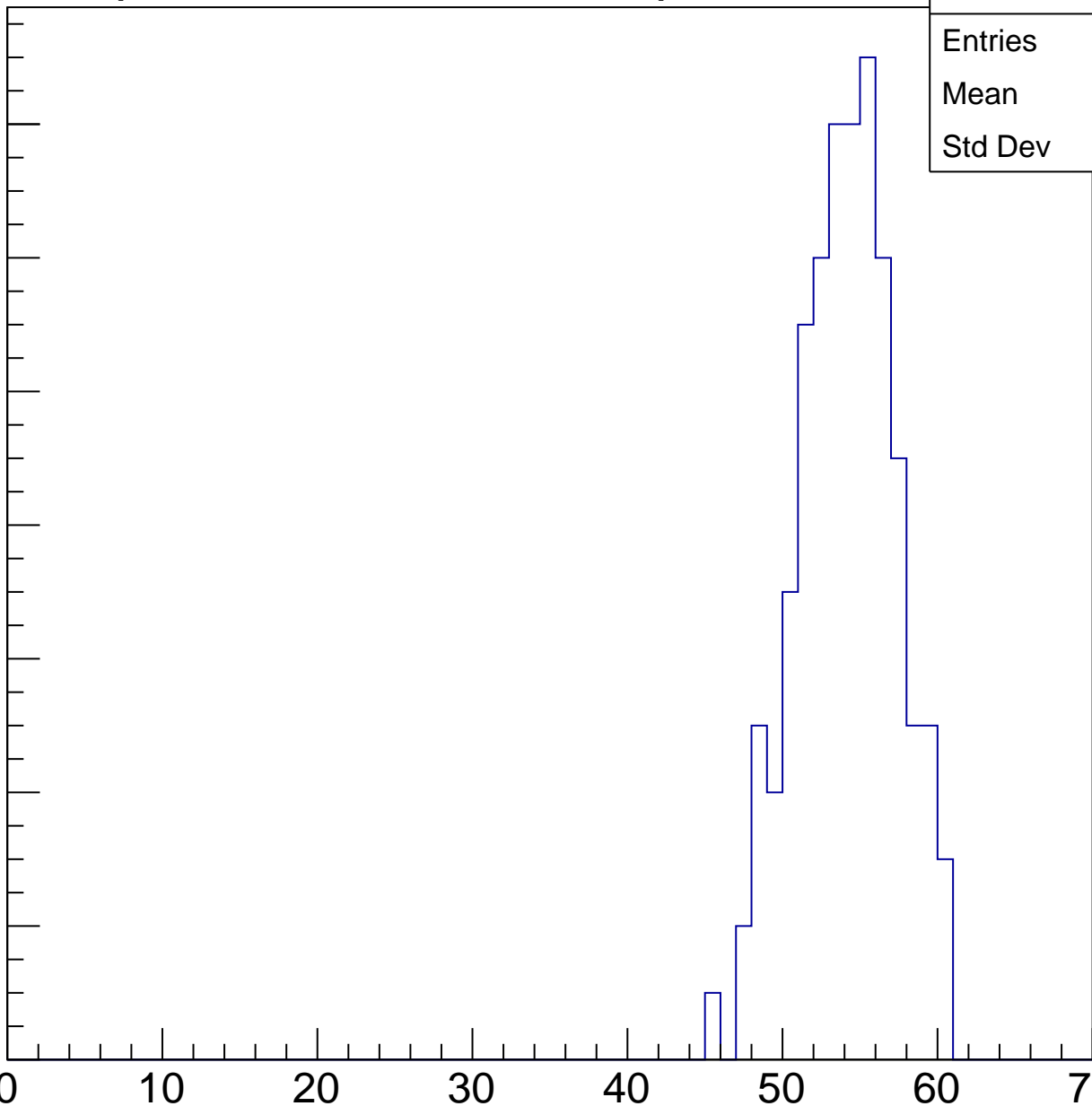
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	53.64
Std Dev	3.14

ampl



B1L001S, U20-ch94, adc5

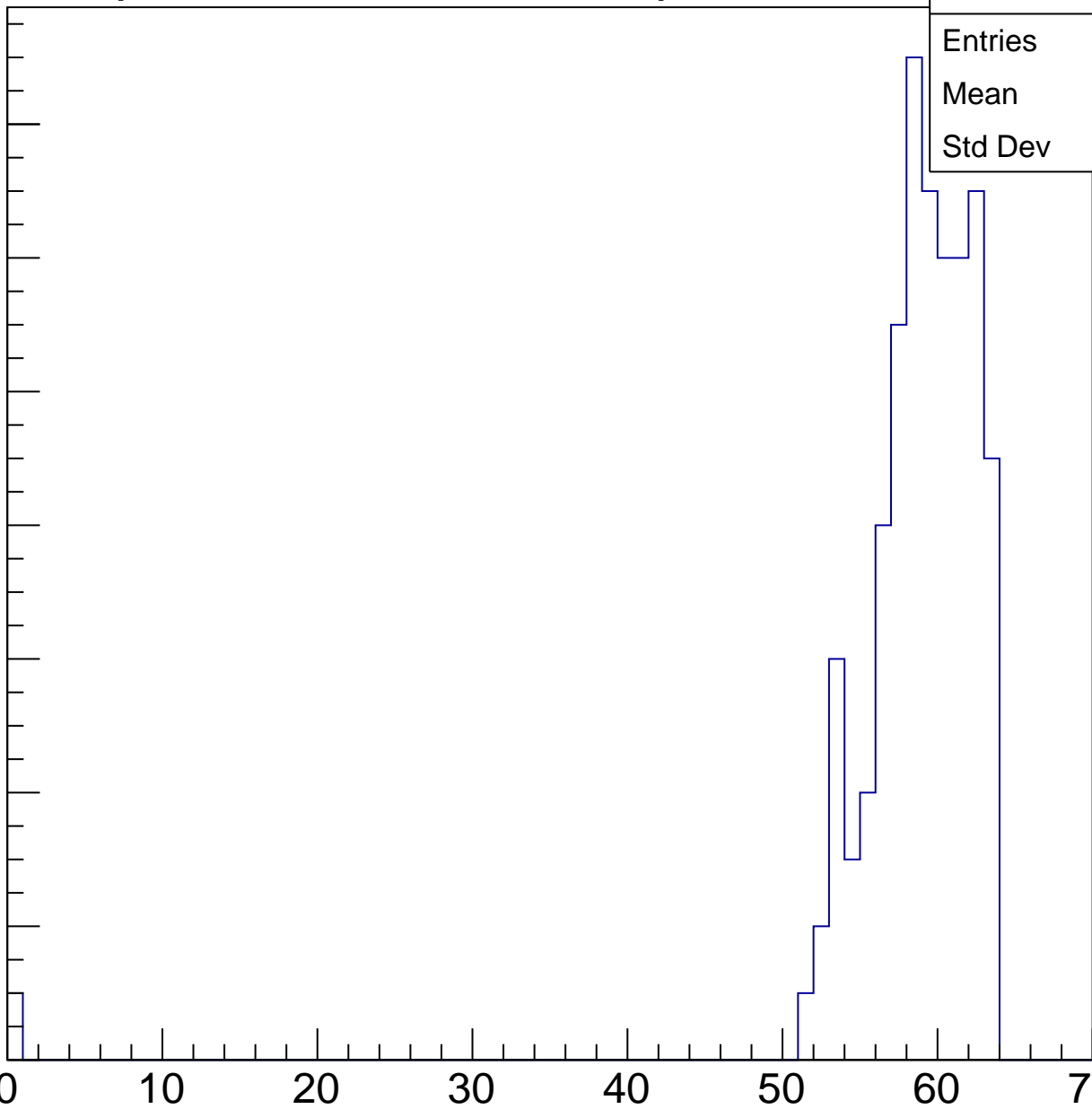
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	58.11
Std Dev	6.298

ampl

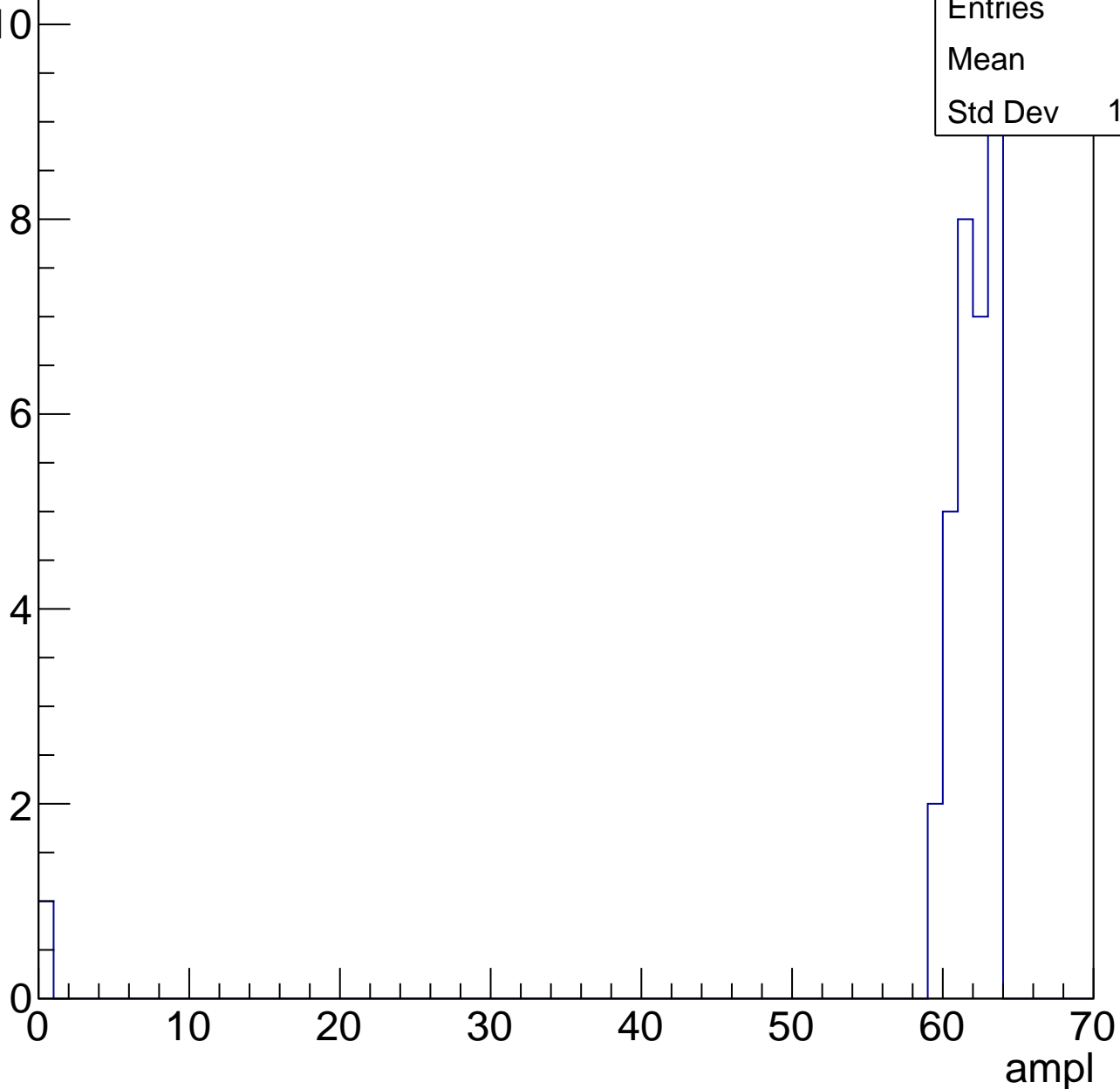


B1L001S, U20-ch94, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

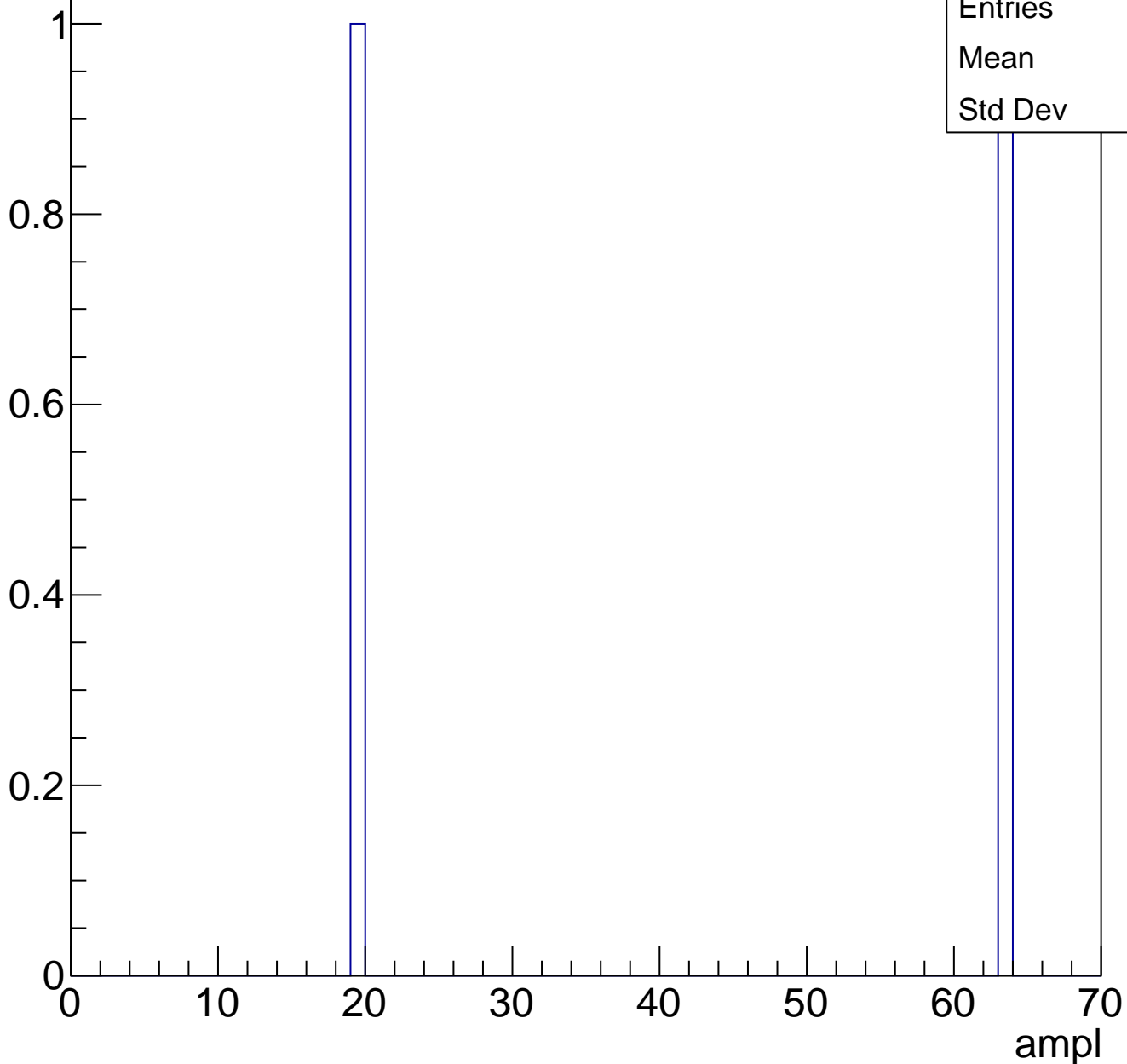
Entries	33
Mean	59.7
Std Dev	10.62



B1L001S, U20-ch94, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch95, adc0

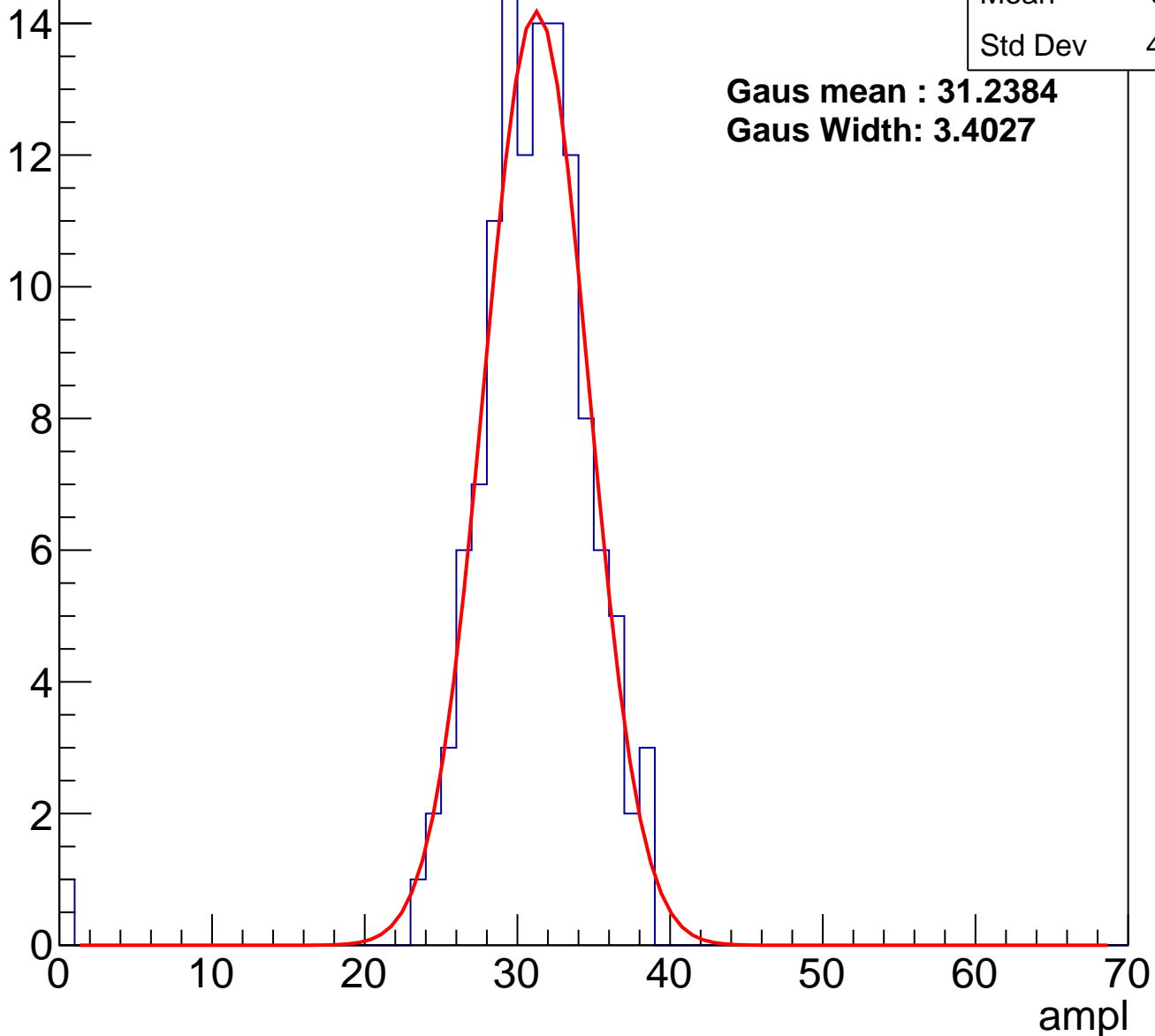
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	30.51
Std Dev	4.255

Gaus mean : 31.2384

Gaus Width: 3.4027

Entry

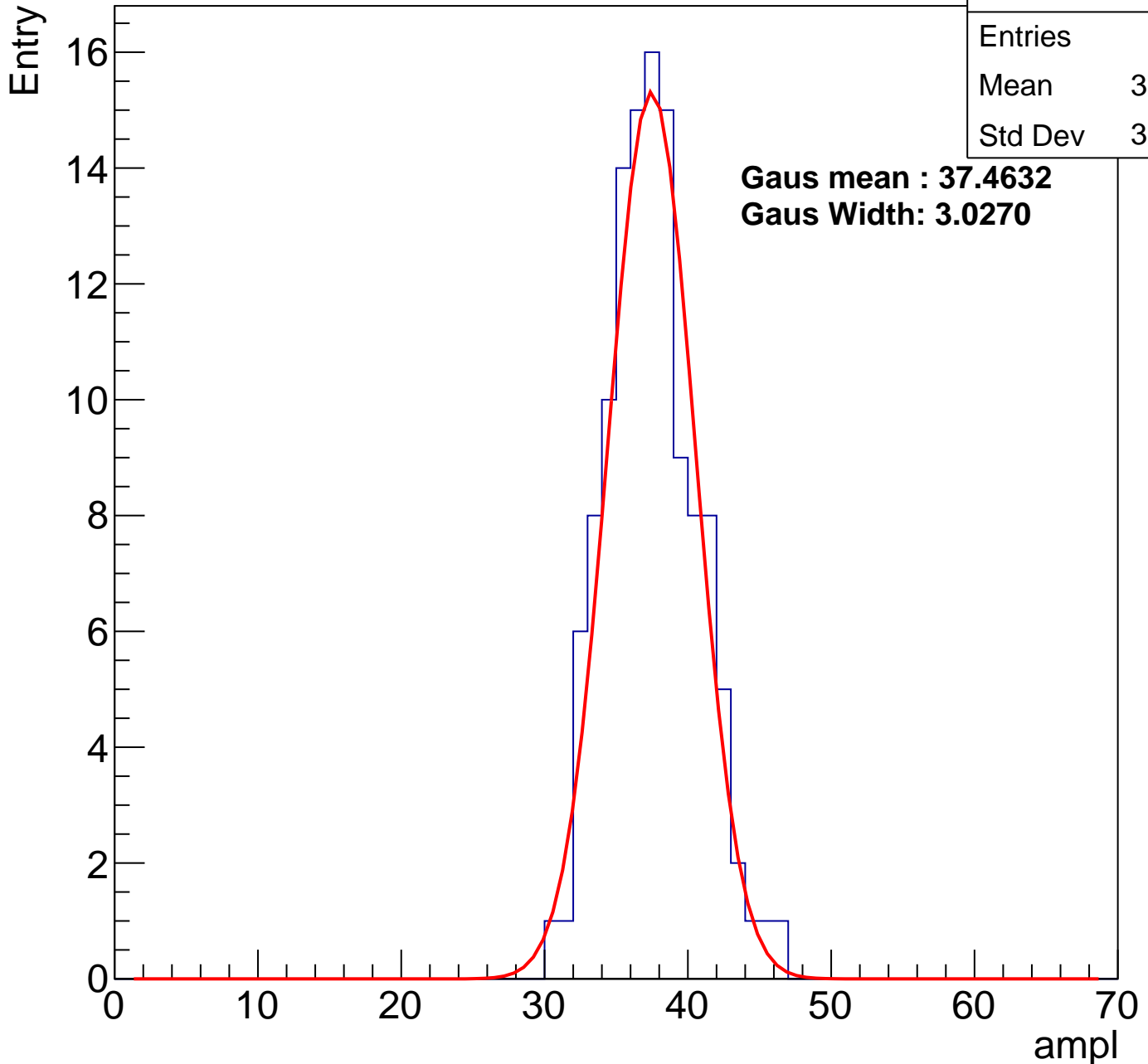


B1L001S, U20-ch95, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	37.02
Std Dev	3.104

Gaus mean : 37.4632
Gaus Width: 3.0270



B1L001S, U20-ch95, adc2

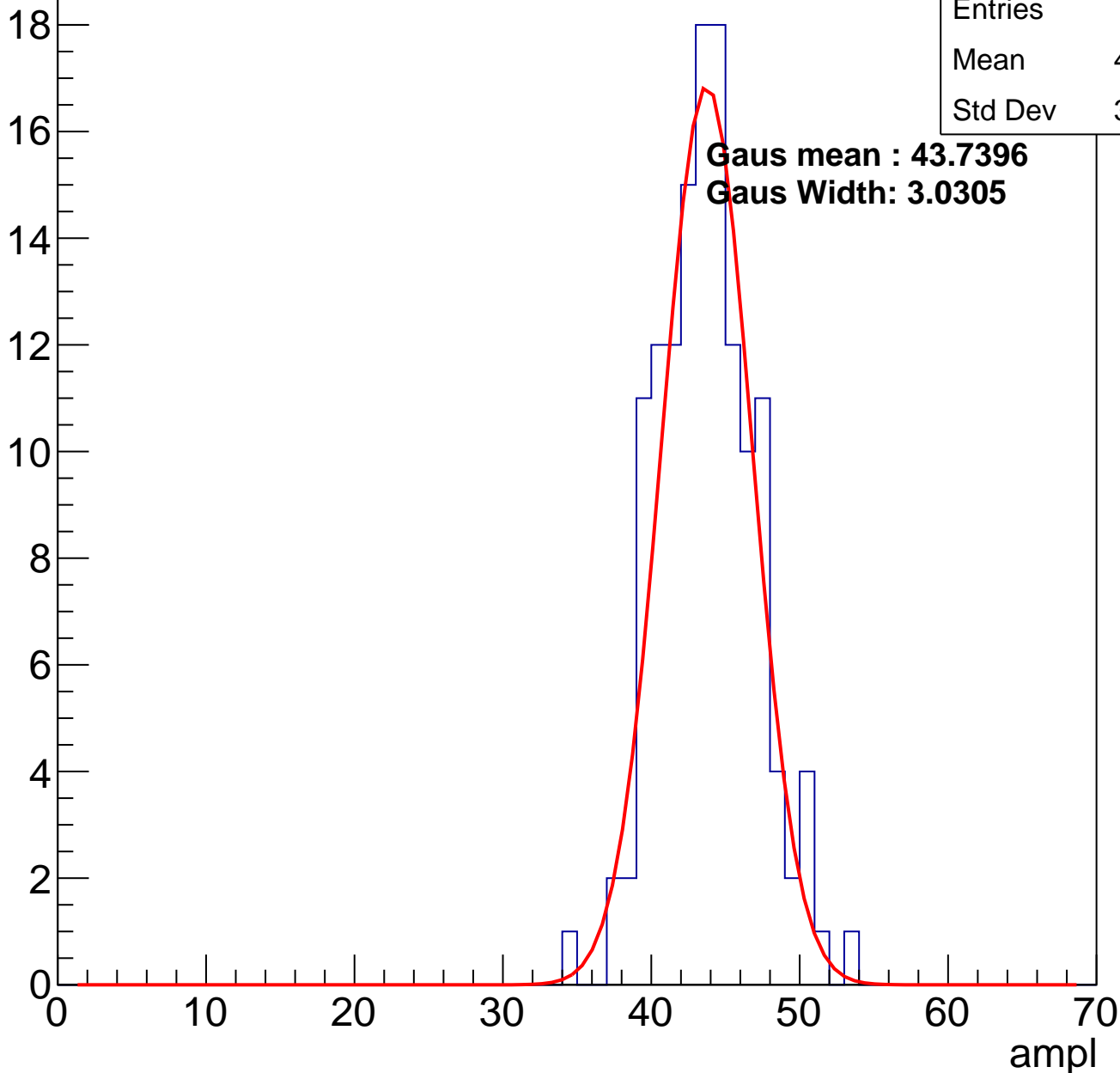
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	43.32
Std Dev	3.208

Gaus mean : 43.7396

Gaus Width: 3.0305

Entry



B1L001S, U20-ch95, adc3

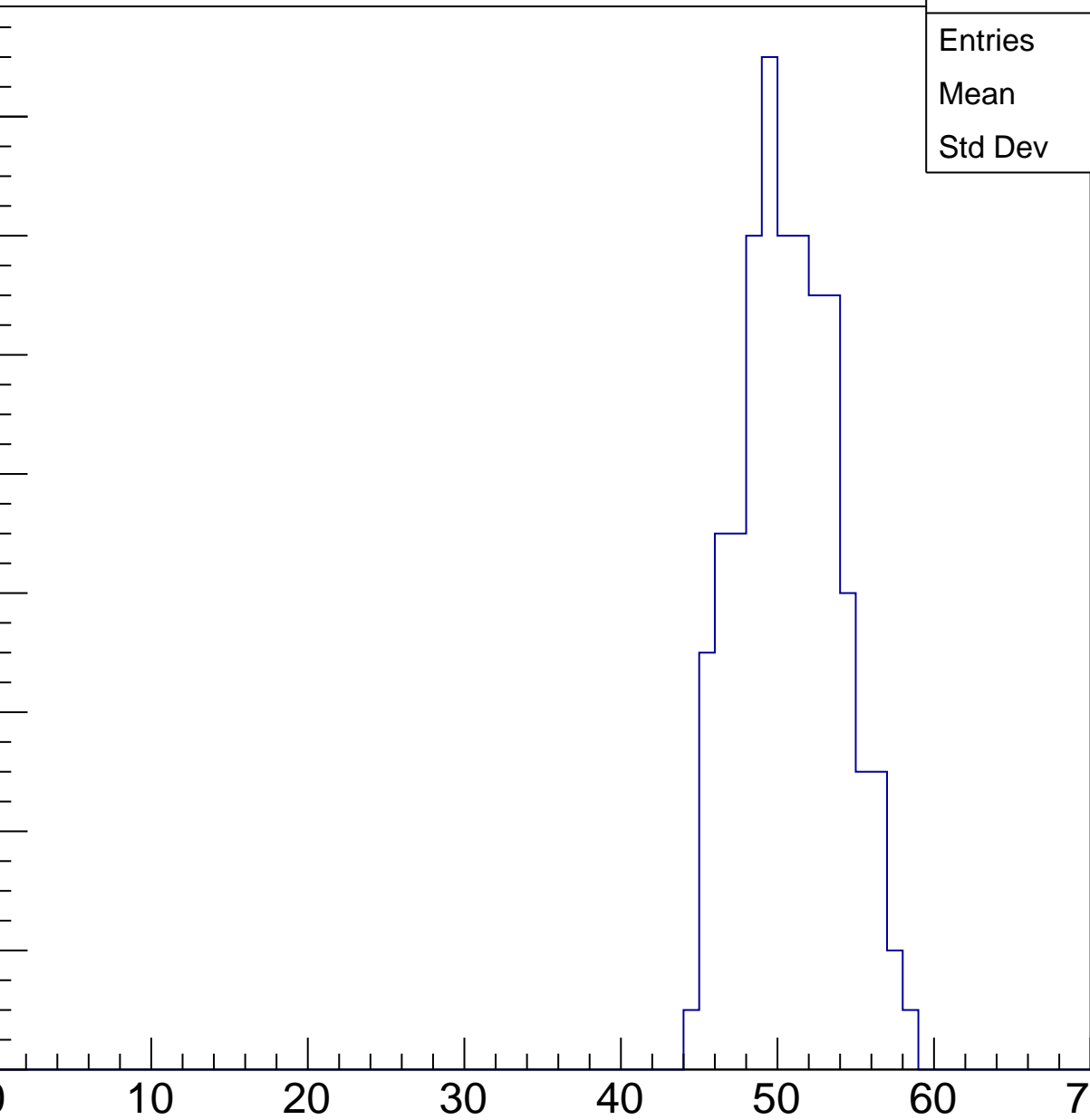
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	50.3
Std Dev	3.108

ampl



B1L001S, U20-ch95, adc4

calib_packv5_042523_0143.root, FC#2, port C2

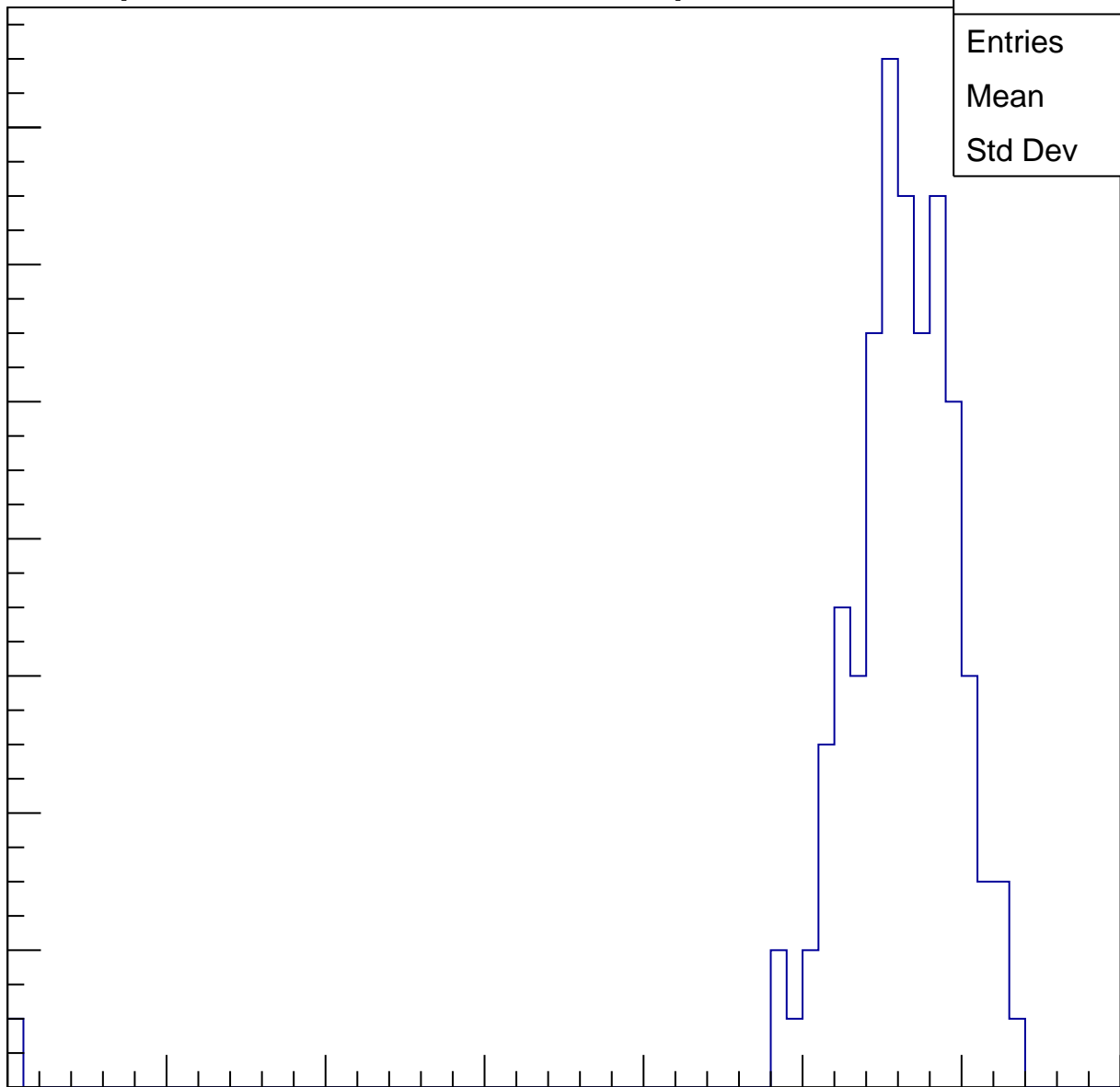
Entries	110
Mean	55.38
Std Dev	6.16

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch95, adc5

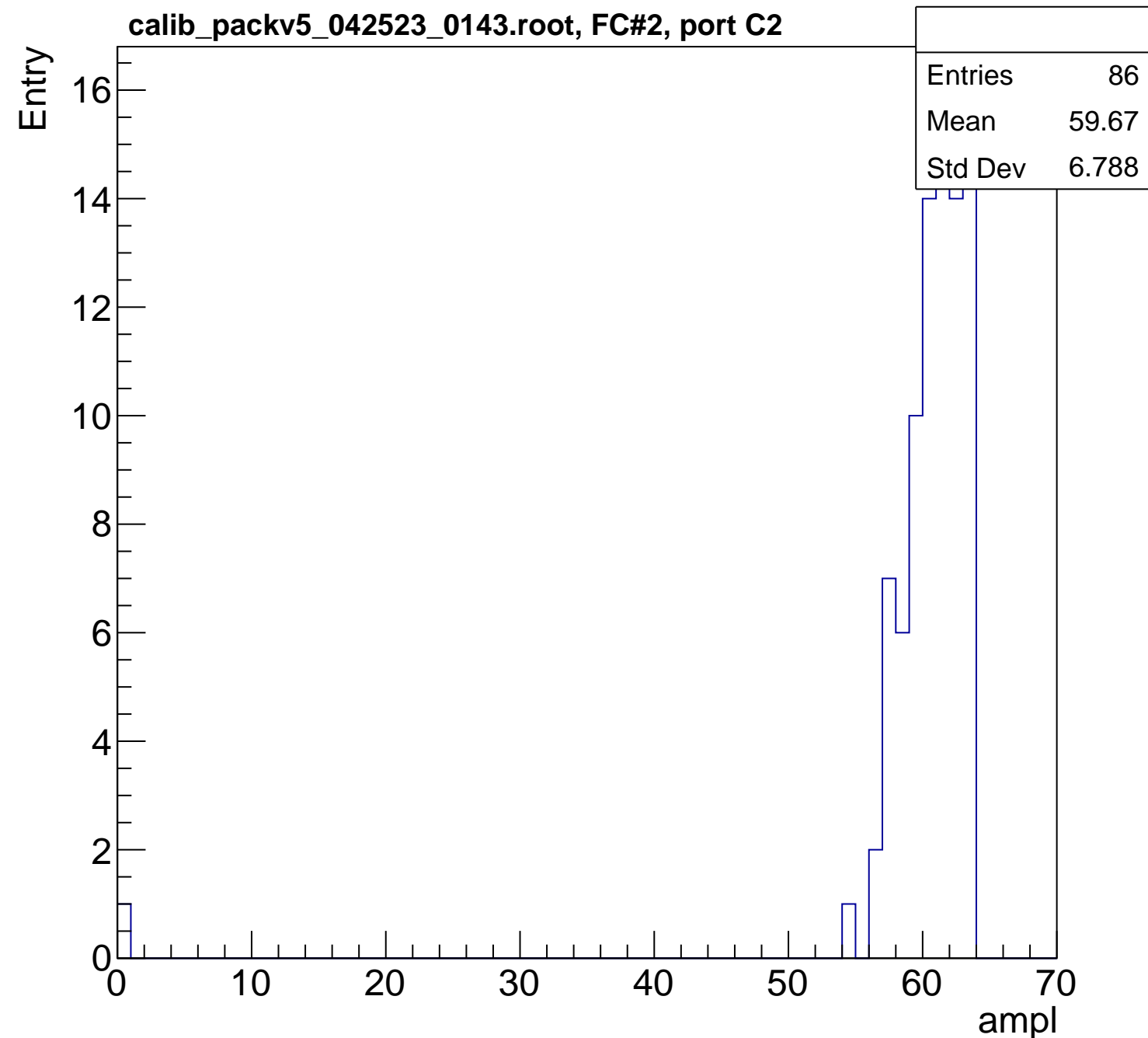
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.67
Std Dev	6.788

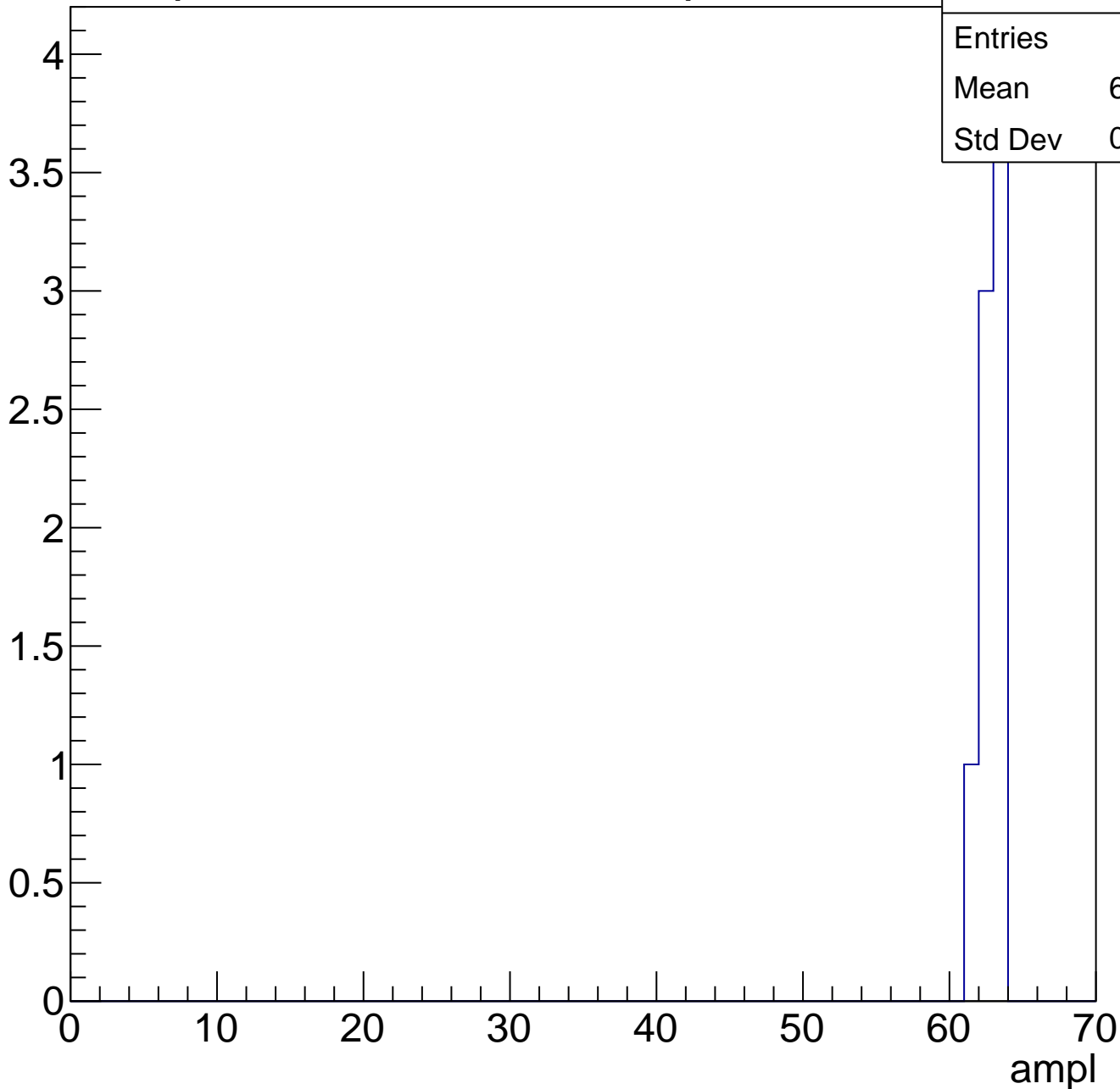
ampl



B1L001S, U20-ch95, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch95, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U20-ch96, adc0

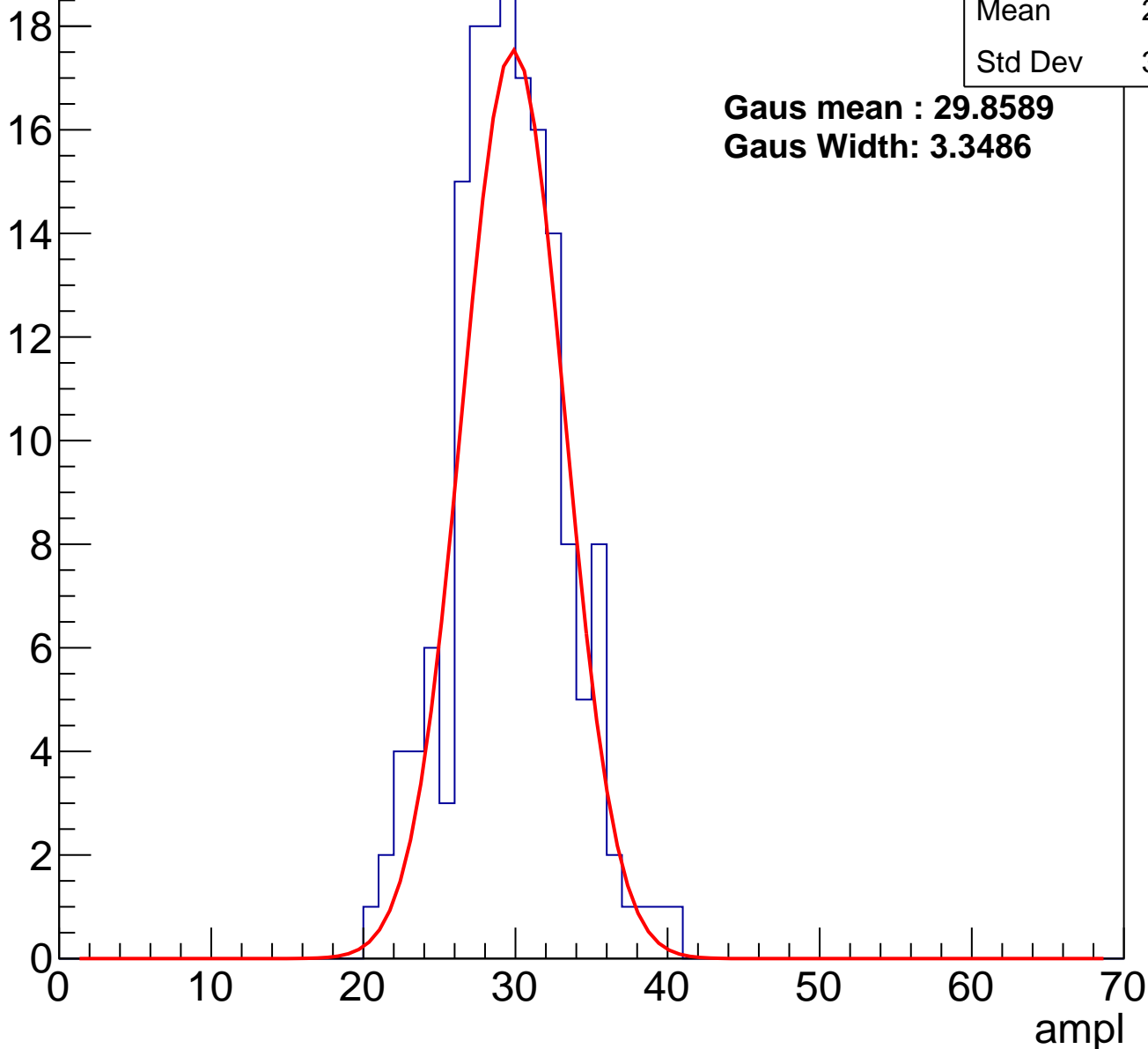
calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	29.18
Std Dev	3.645

Gaus mean : 29.8589

Gaus Width: 3.3486

Entry



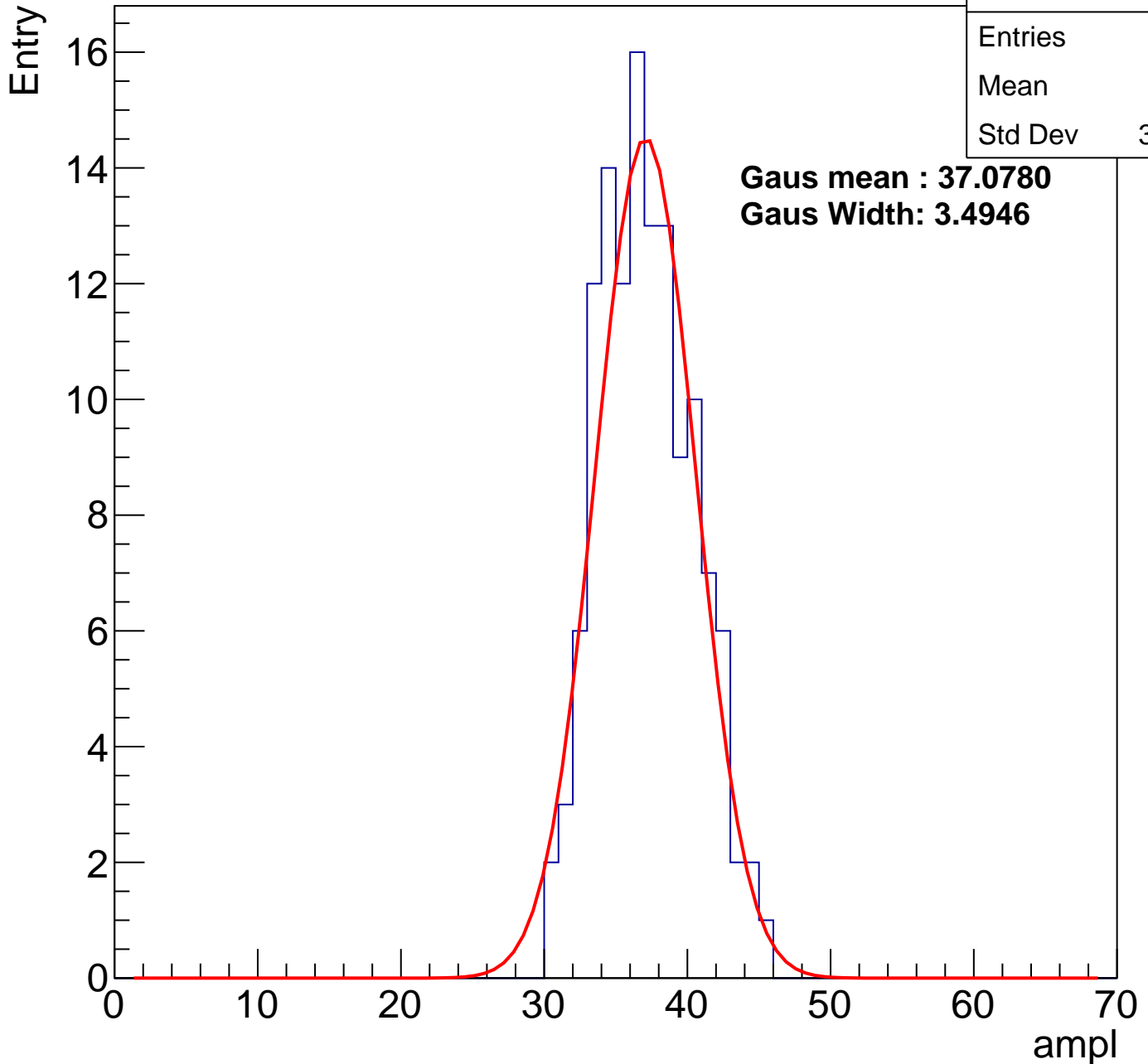
B1L001S, U20-ch96, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.7
Std Dev	3.261

Gaus mean : 37.0780

Gaus Width: 3.4946



B1L001S, U20-ch96, adc2

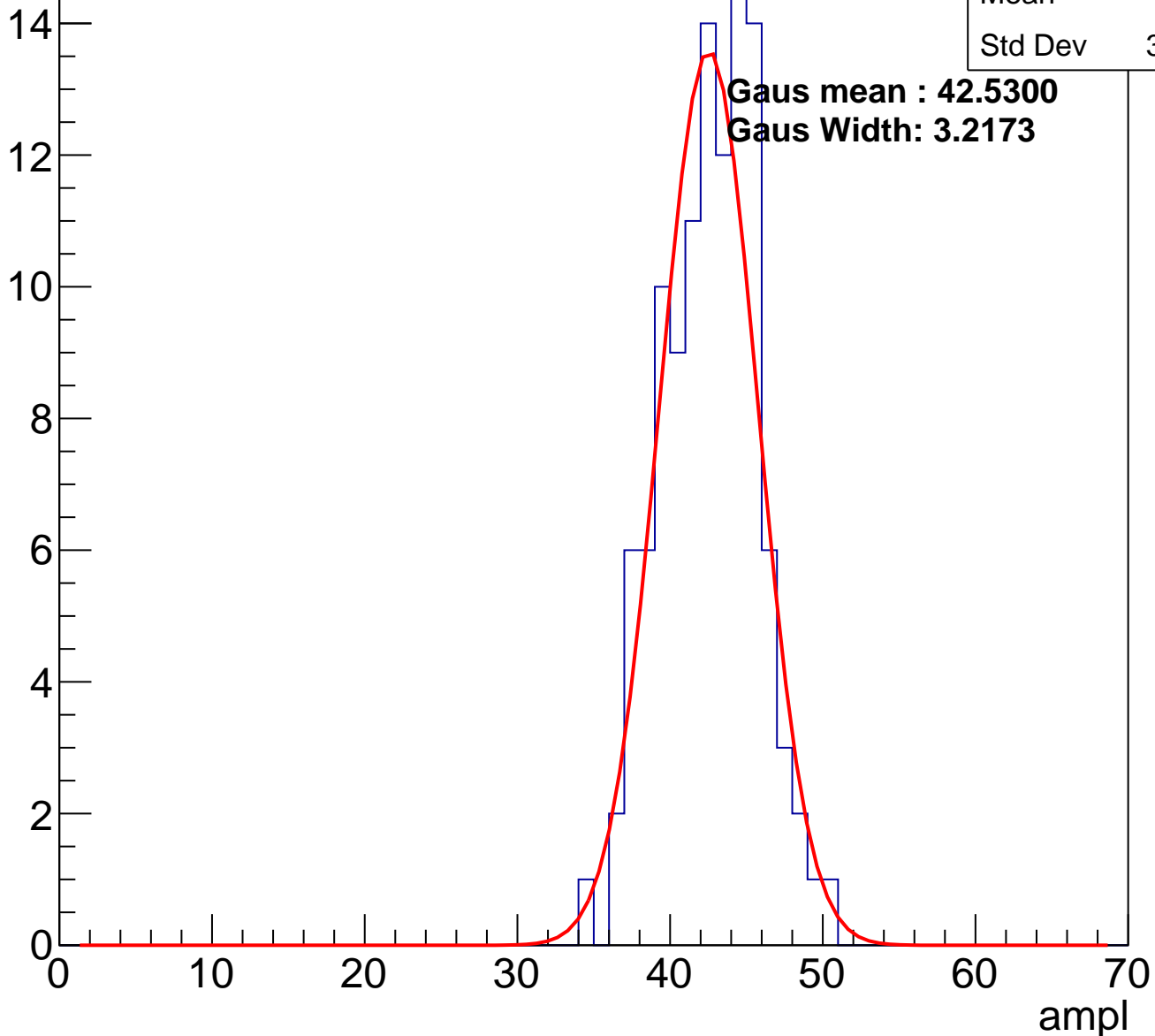
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	42.15
Std Dev	3.078

Gaus mean : 42.5300

Gaus Width: 3.2173

Entry



B1L001S, U20-ch96, adc3

calib_packv5_042523_0143.root, FC#2, port C2

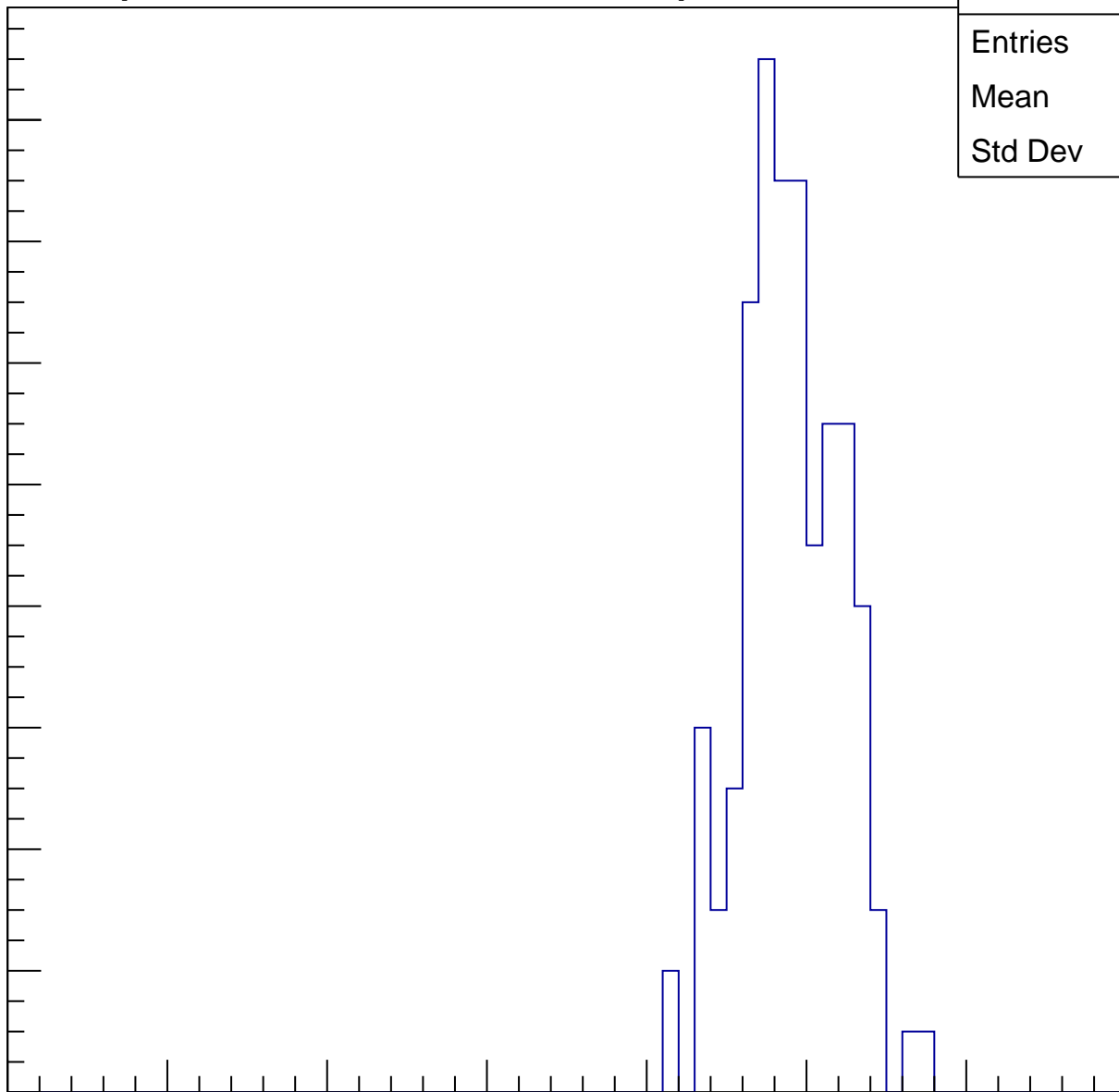
Entries	120
Mean	48.59
Std Dev	3.092

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

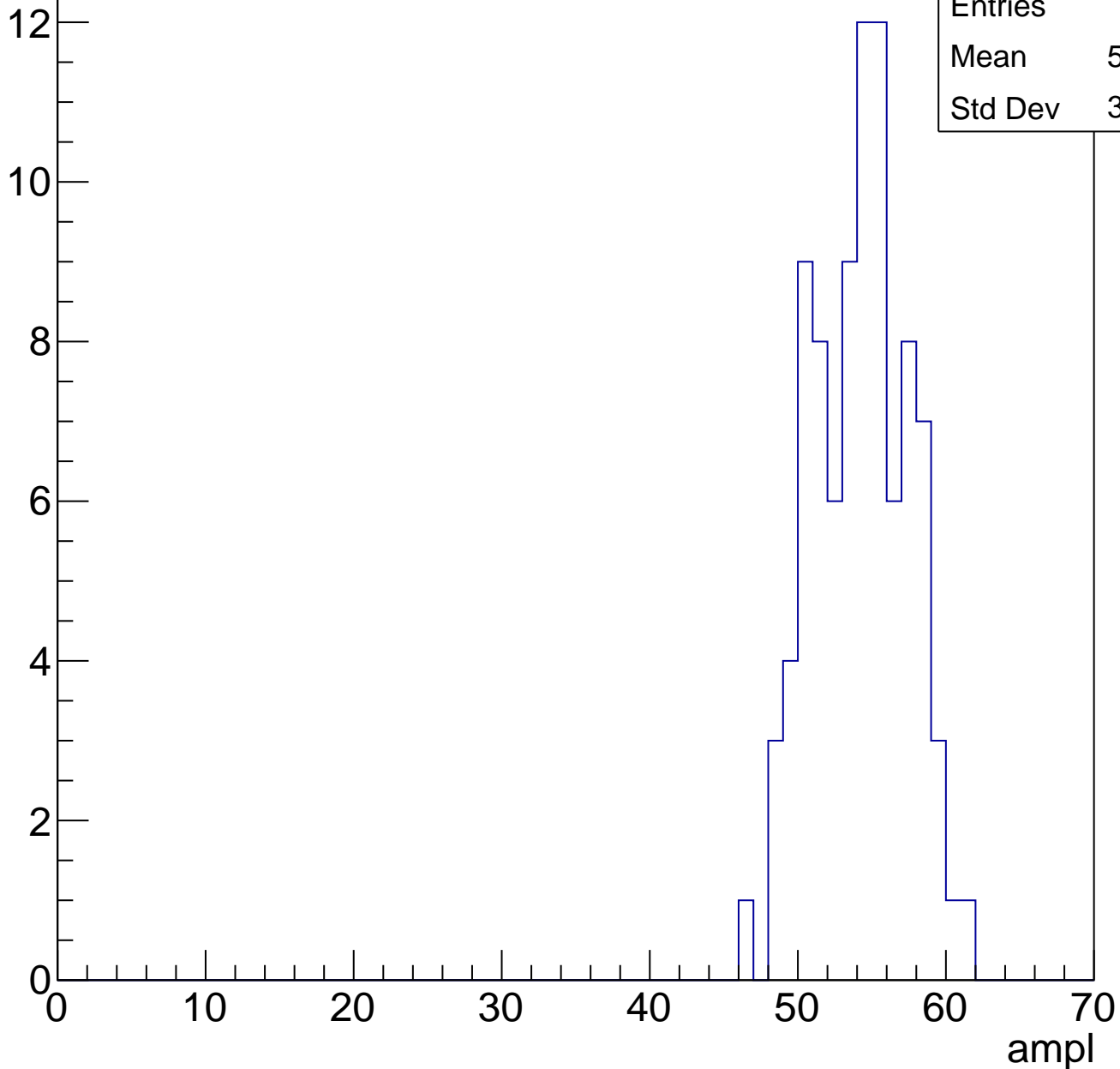


B1L001S, U20-ch96, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	90
Mean	53.74
Std Dev	3.154

Entry



B1L001S, U20-ch96, adc5

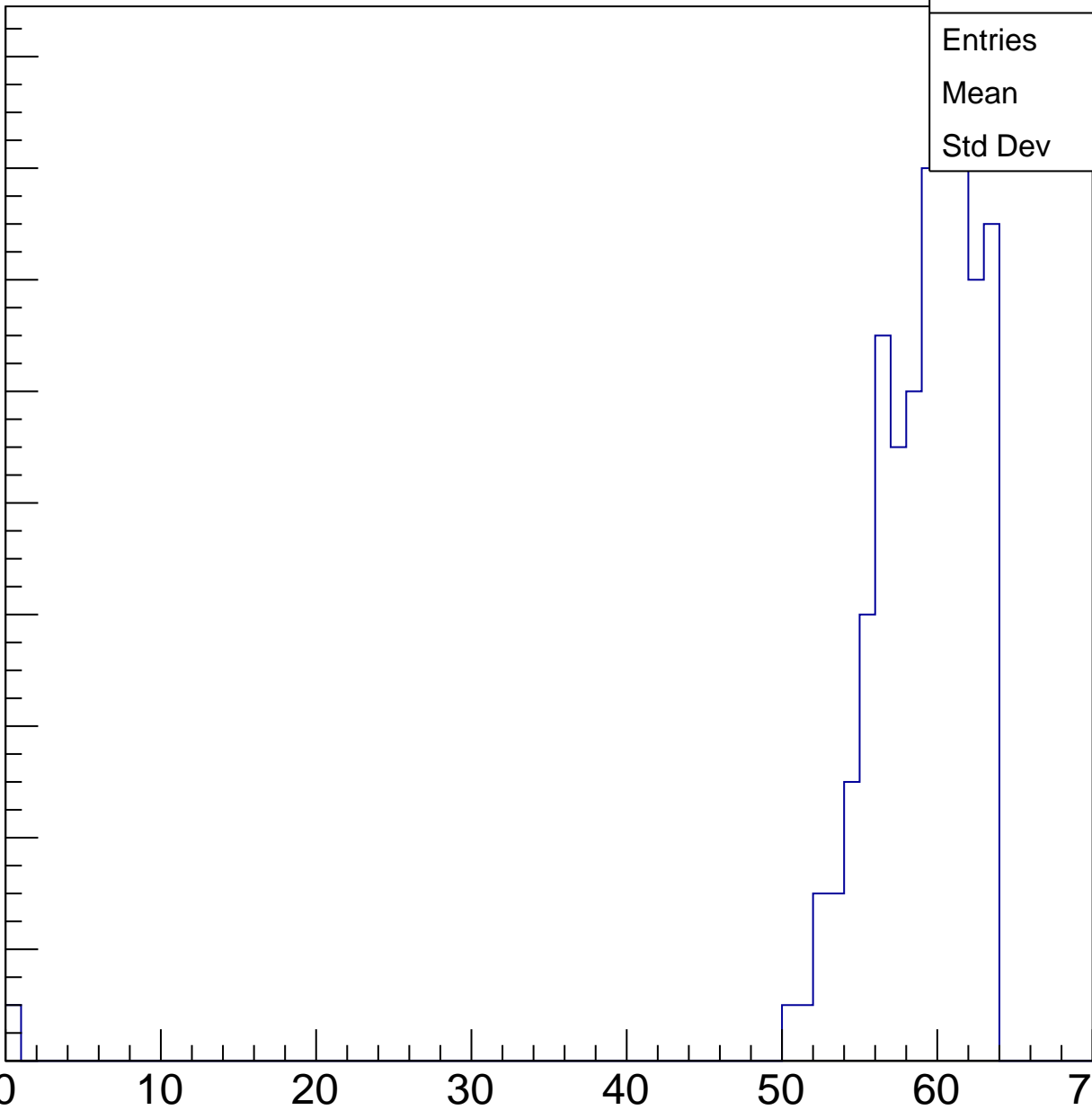
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	138
Mean	58.34
Std Dev	5.835

ampl

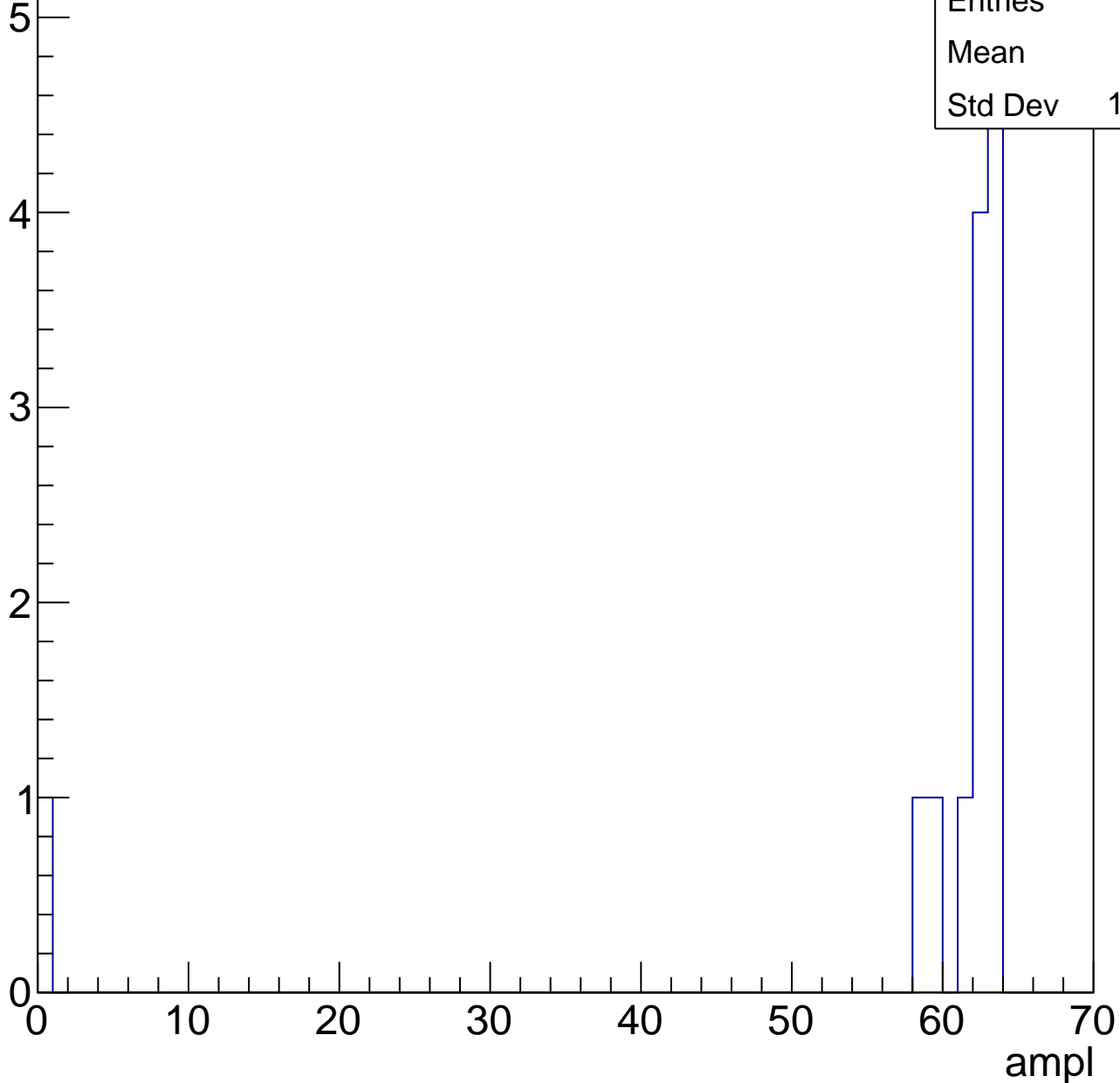


B1L001S, U20-ch96, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

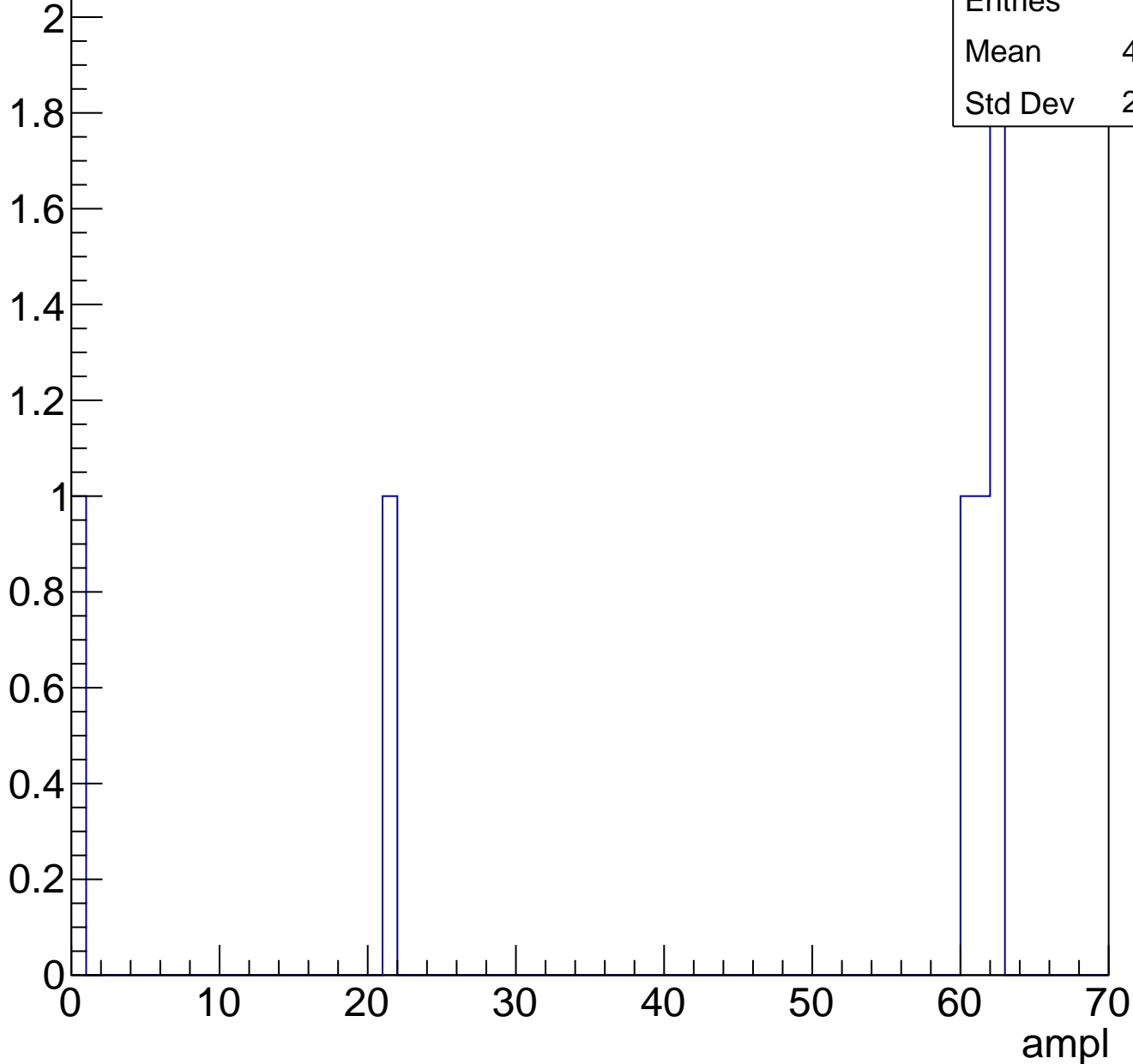
Entries	13
Mean	57
Std Dev	16.53



B1L001S, U20-ch96, adc7

calib_packv5_042523_0143.root, FC#2, port C2

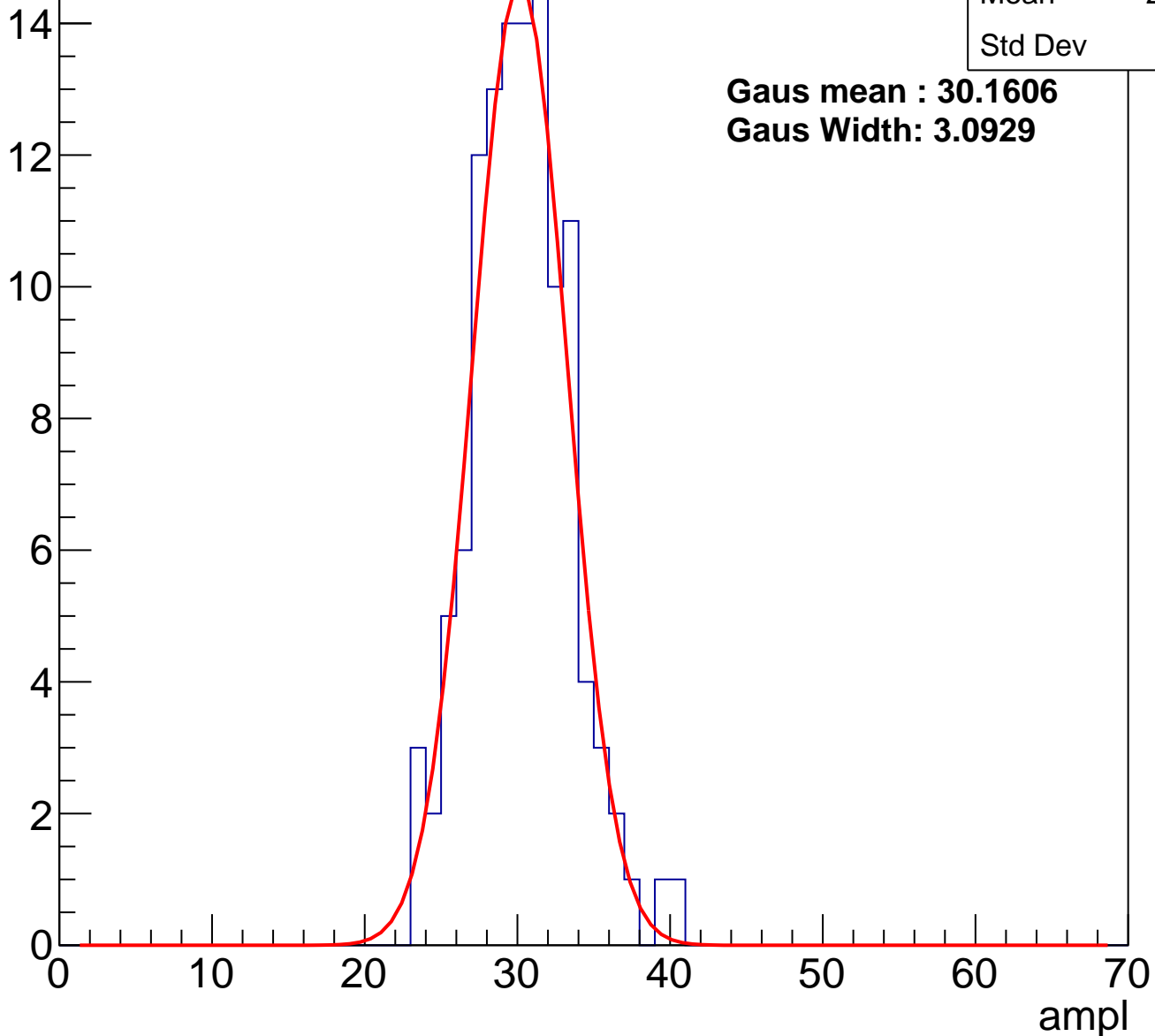
Entry



B1L001S, U20-ch97, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch97, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.66
Std Dev	3.373

Gaus mean : 35.8681

Gaus Width: 3.1058

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U20-ch97, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

147

Mean

43.1

Std Dev

3.435

Gaus mean : 43.6067

Gaus Width: 3.4953

0

10

20

30

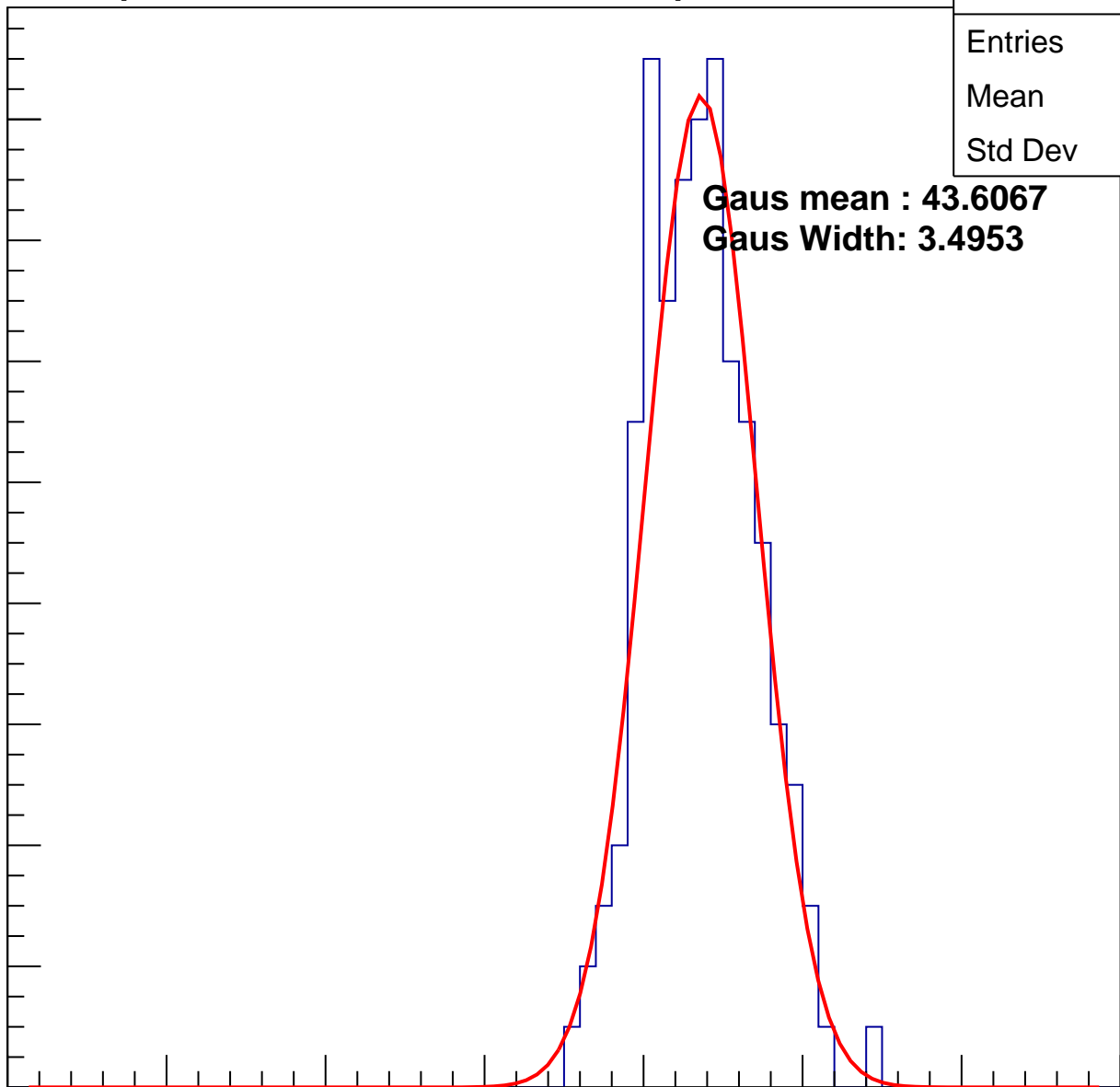
40

50

60

70

ampl

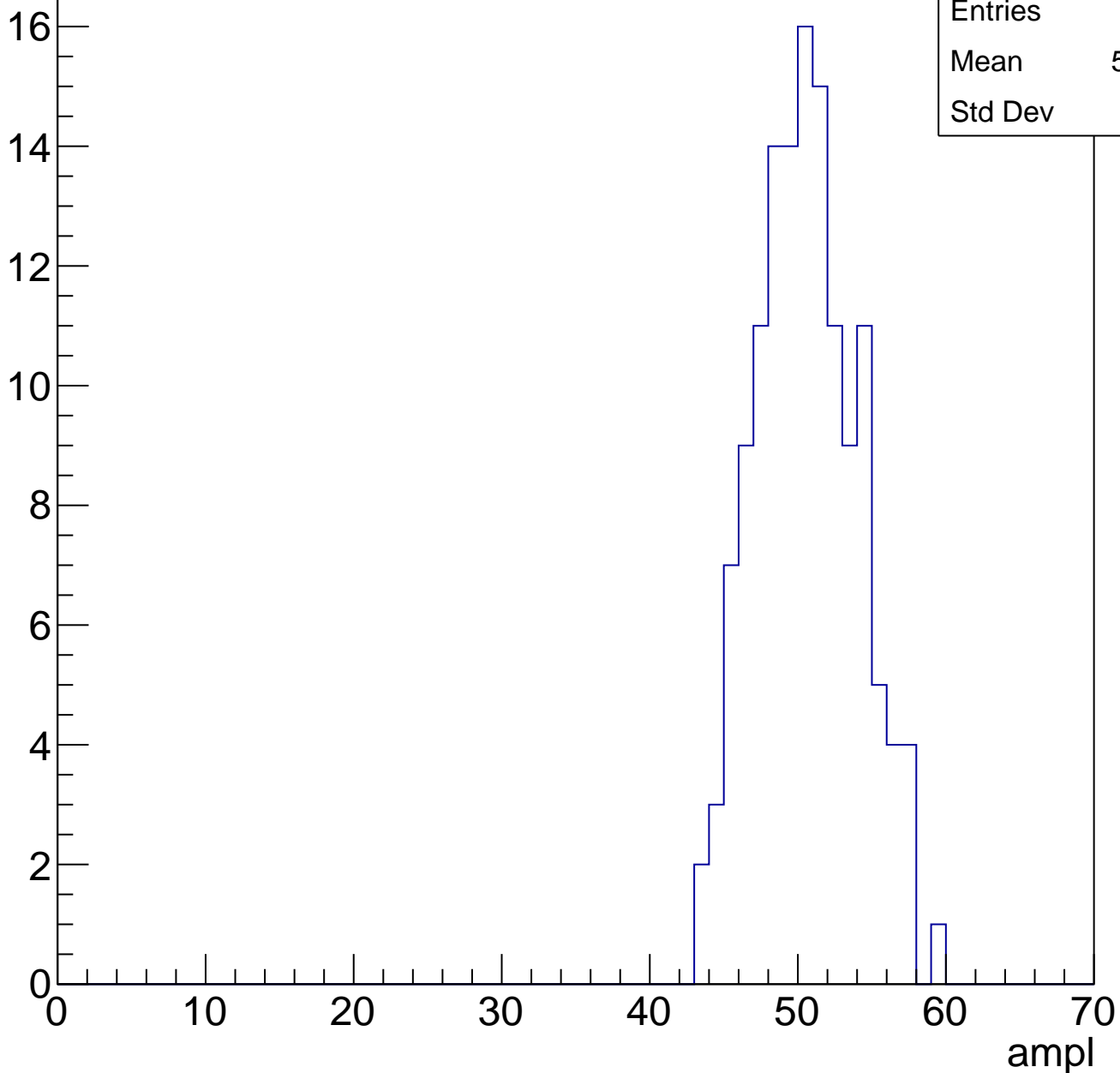


B1L001S, U20-ch97, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	50.12
Std Dev	3.38

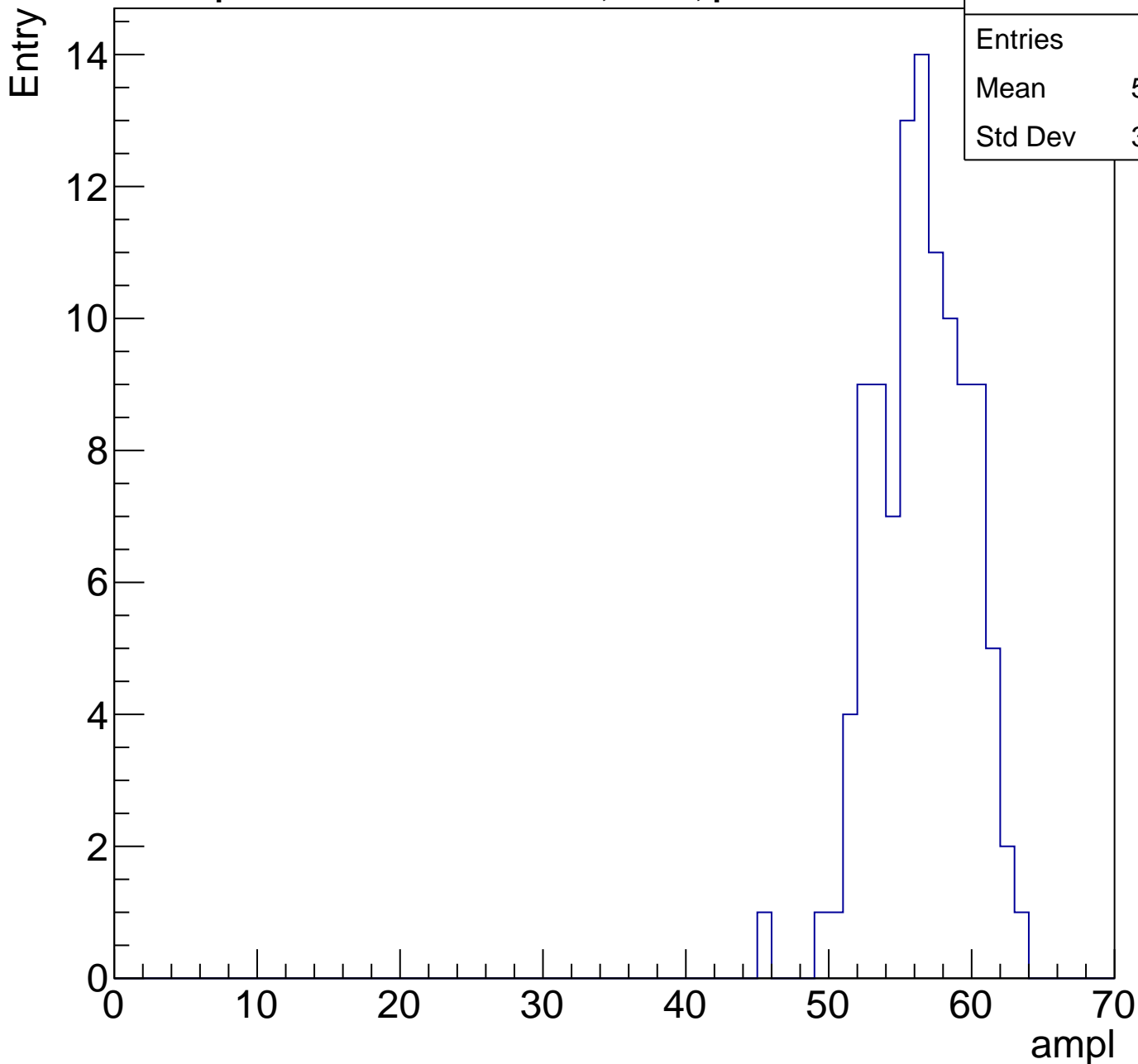
Entry



B1L001S, U20-ch97, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	56.04
Std Dev	3.212



B1L001S, U20-ch97, adc5

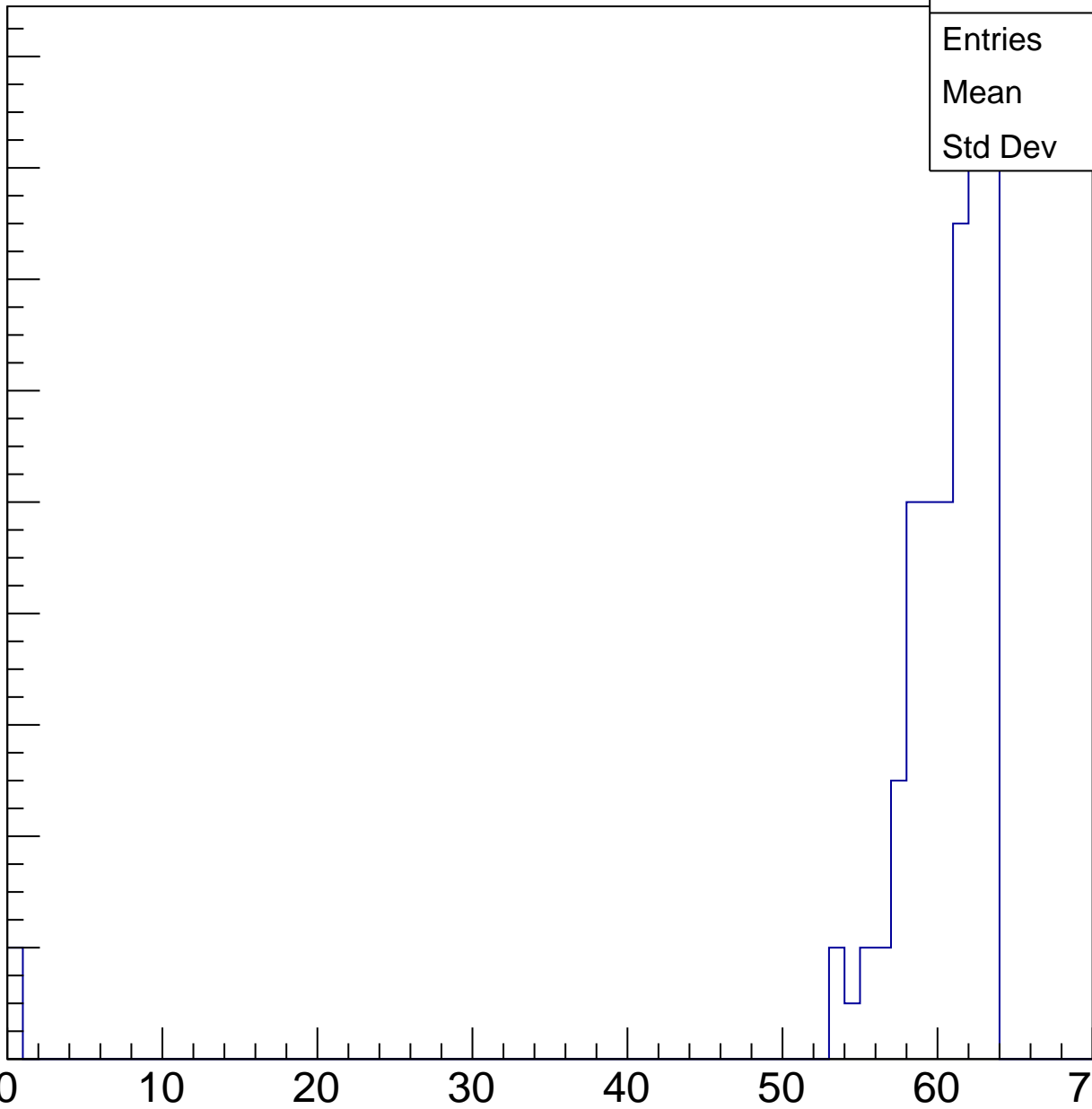
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.9
Std Dev	9.064

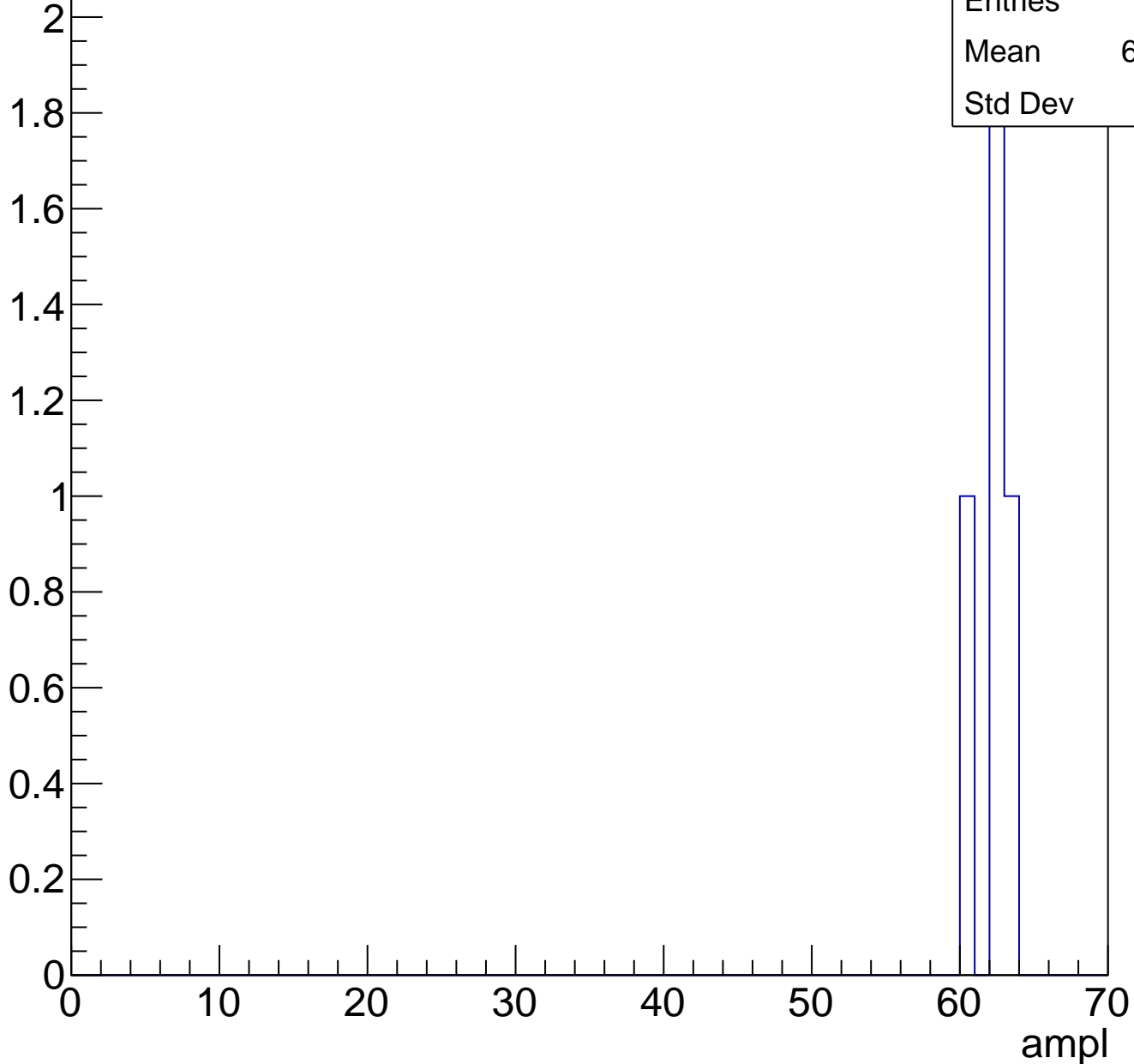
ampl



B1L001S, U20-ch97, adc6

calib_packv5_042523_0143.root, FC#2, port C2

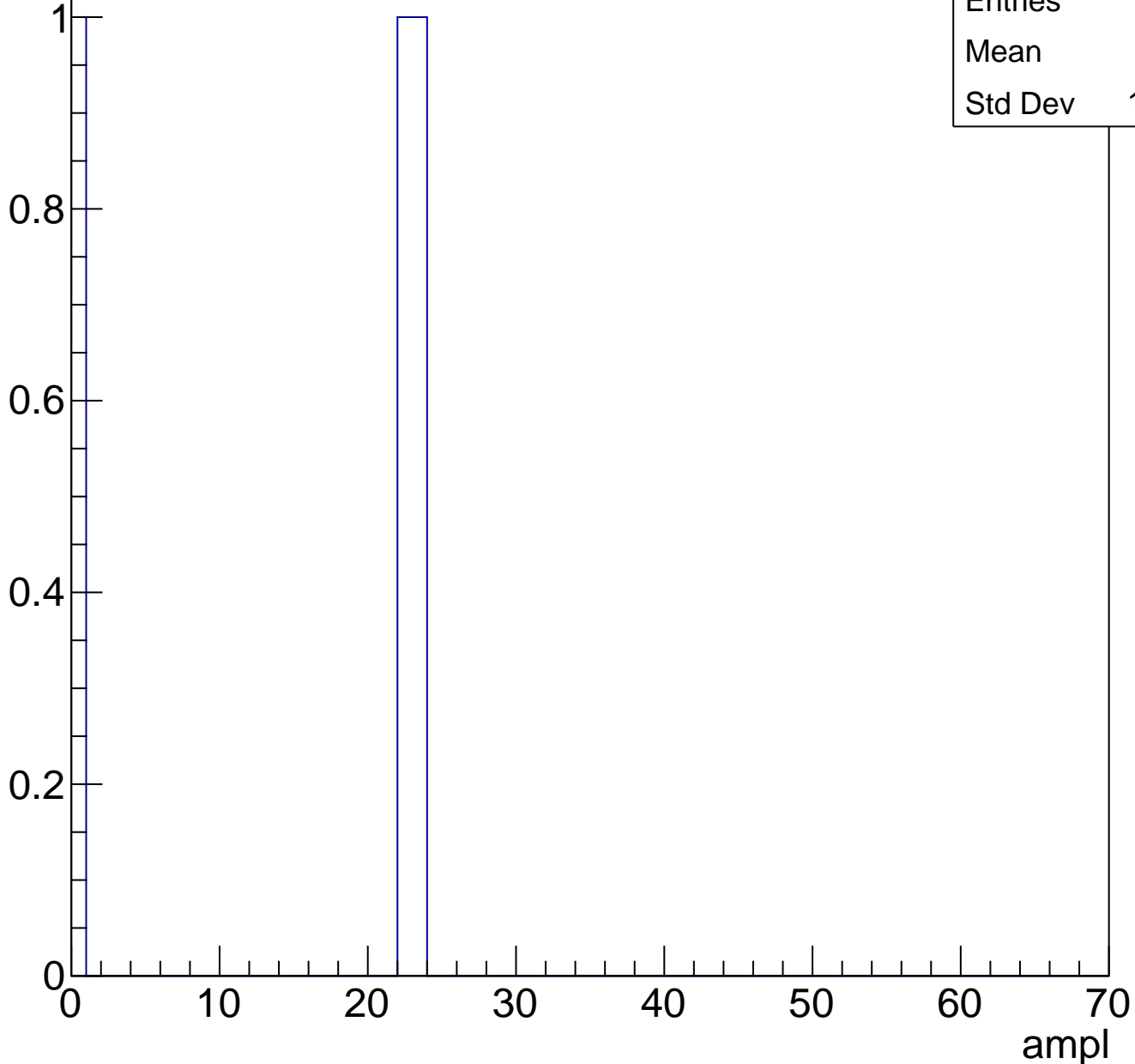
Entry



B1L001S, U20-ch97, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	15
Std Dev	10.61

B1L001S, U20-ch98, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	29.87
Std Dev	3.295

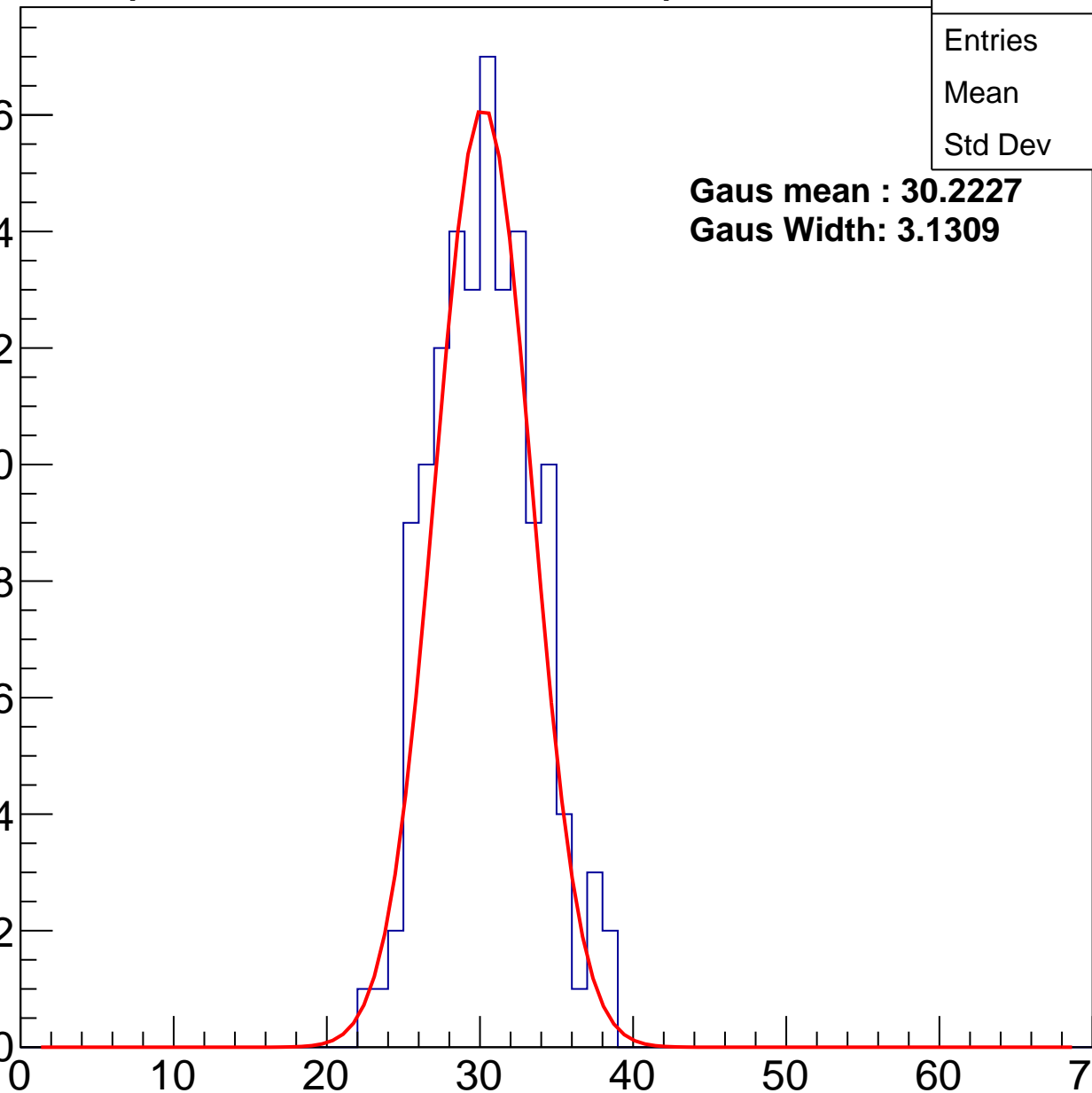
Gaus mean : 30.2227

Gaus Width: 3.1309

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch98, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	35.51
Std Dev	3.192

Gaus mean : 35.8906

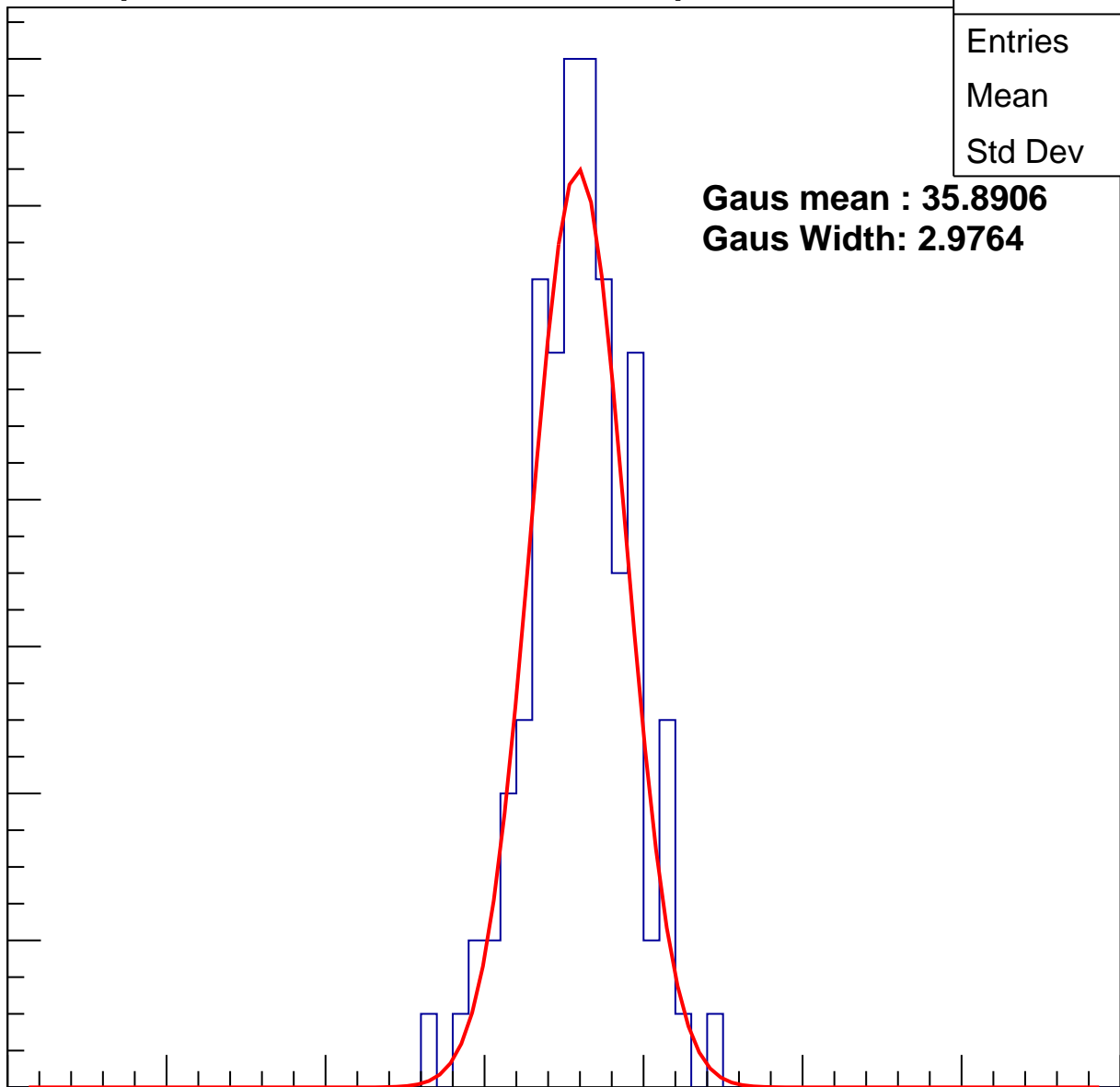
Gaus Width: 2.9764

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch98, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	41.54
Std Dev	3.072

Gaus mean : 42.2139

Gaus Width: 3.4100

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

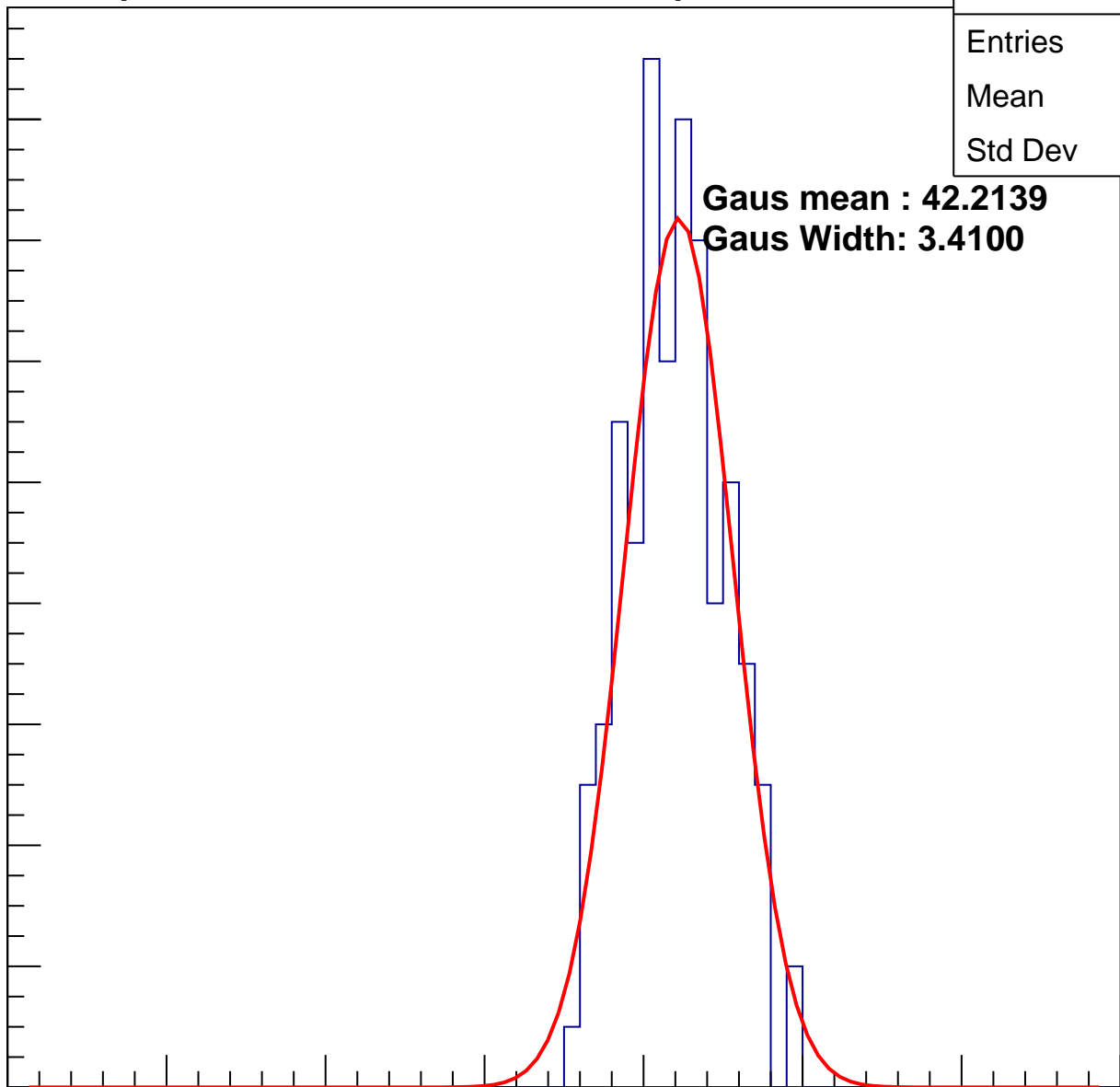
40

50

60

70

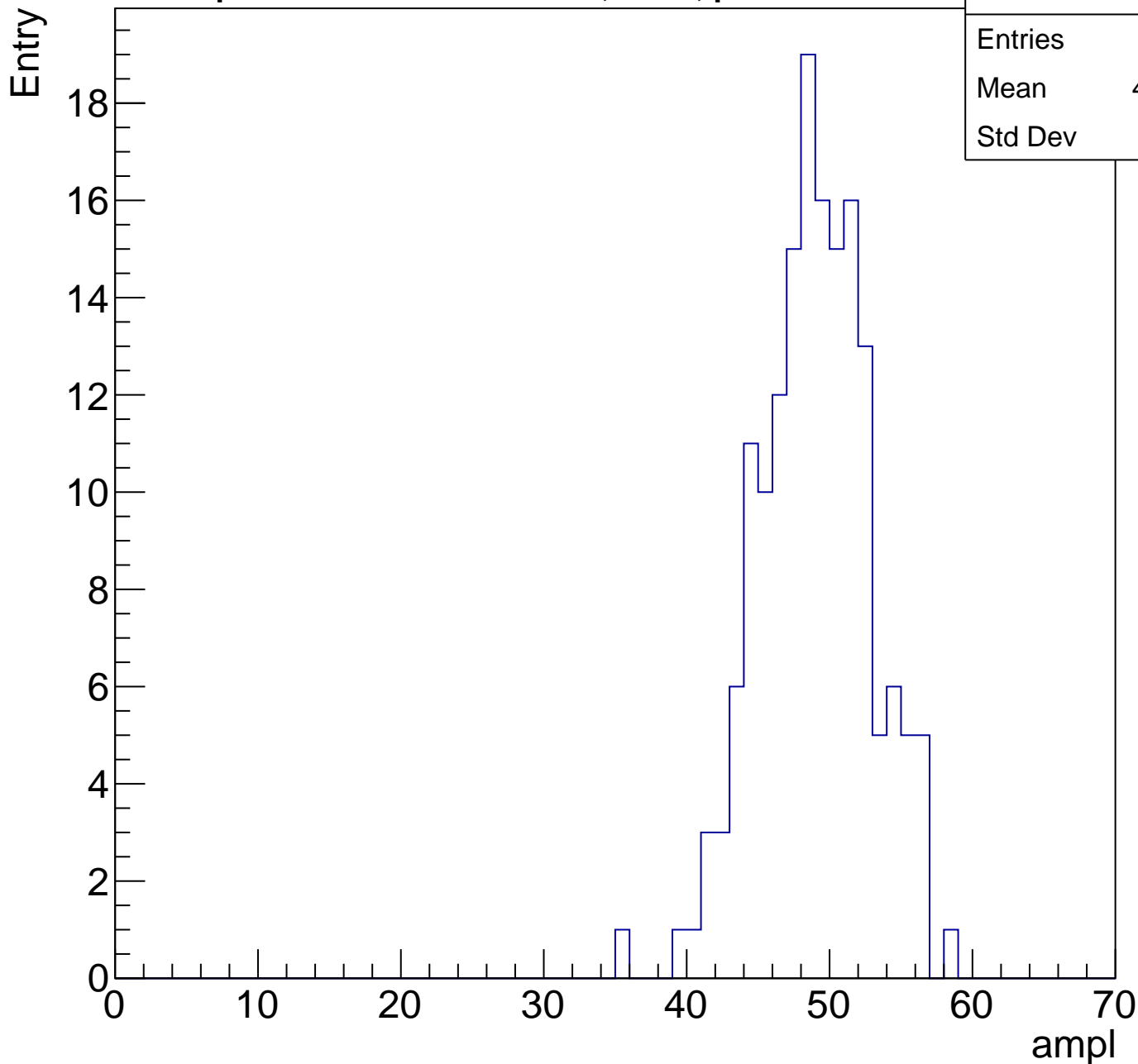
ampl



B1L001S, U20-ch98, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	48.49
Std Dev	3.86



B1L001S, U20-ch98, adc4

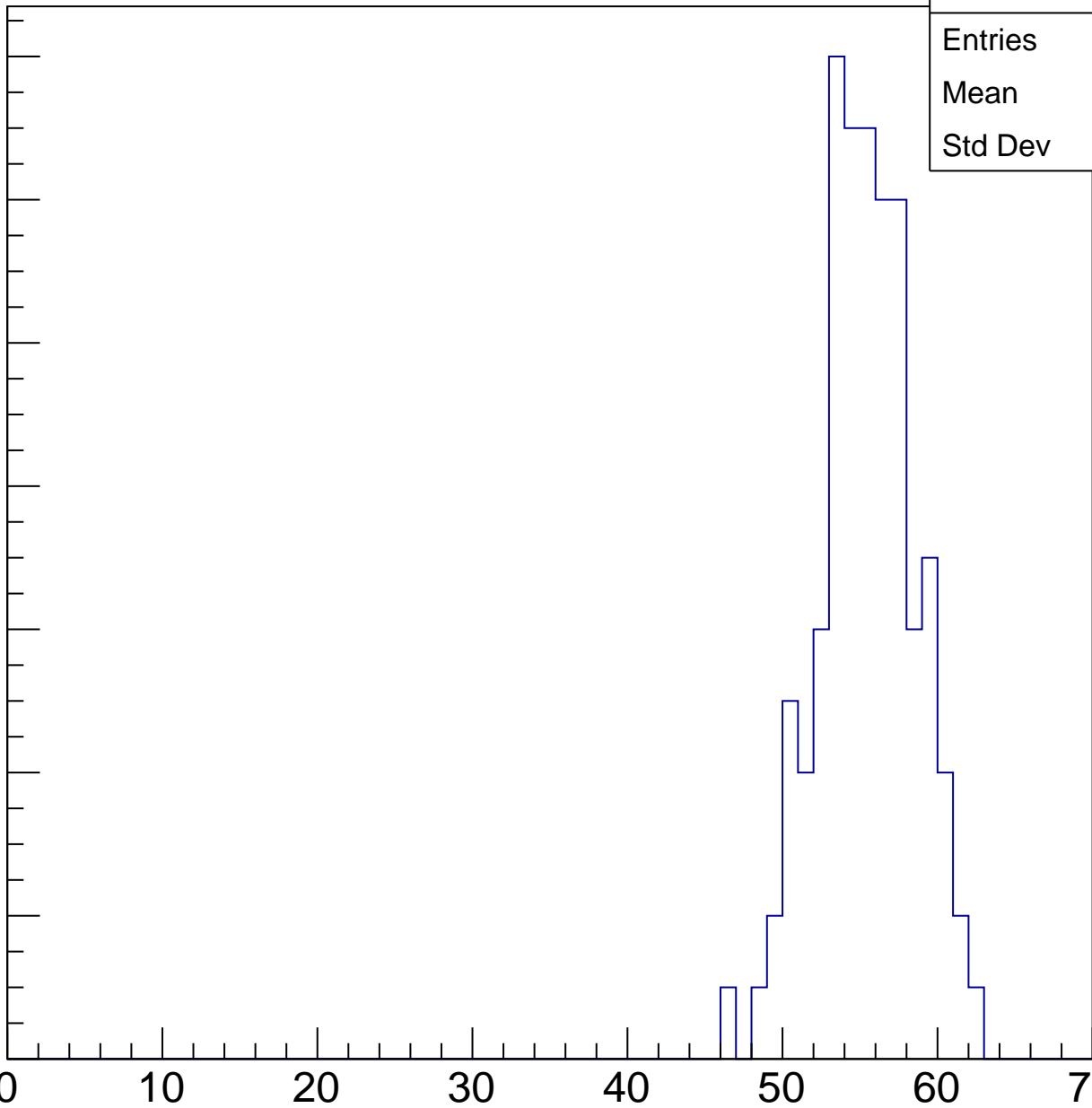
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	103
Mean	54.93
Std Dev	3.054

ampl



B1L001S, U20-ch98, adc5

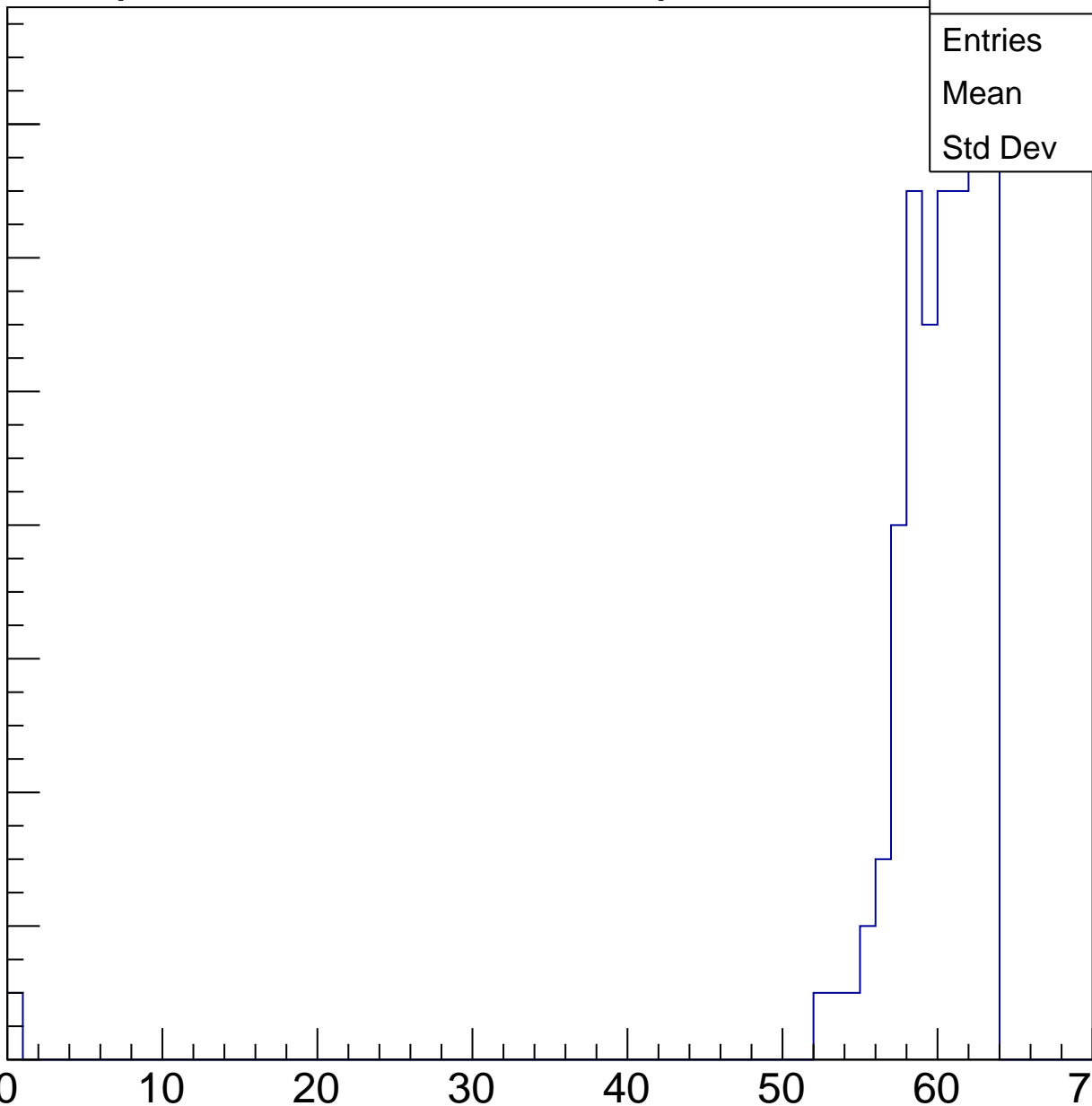
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	59.22
Std Dev	6.525

ampl

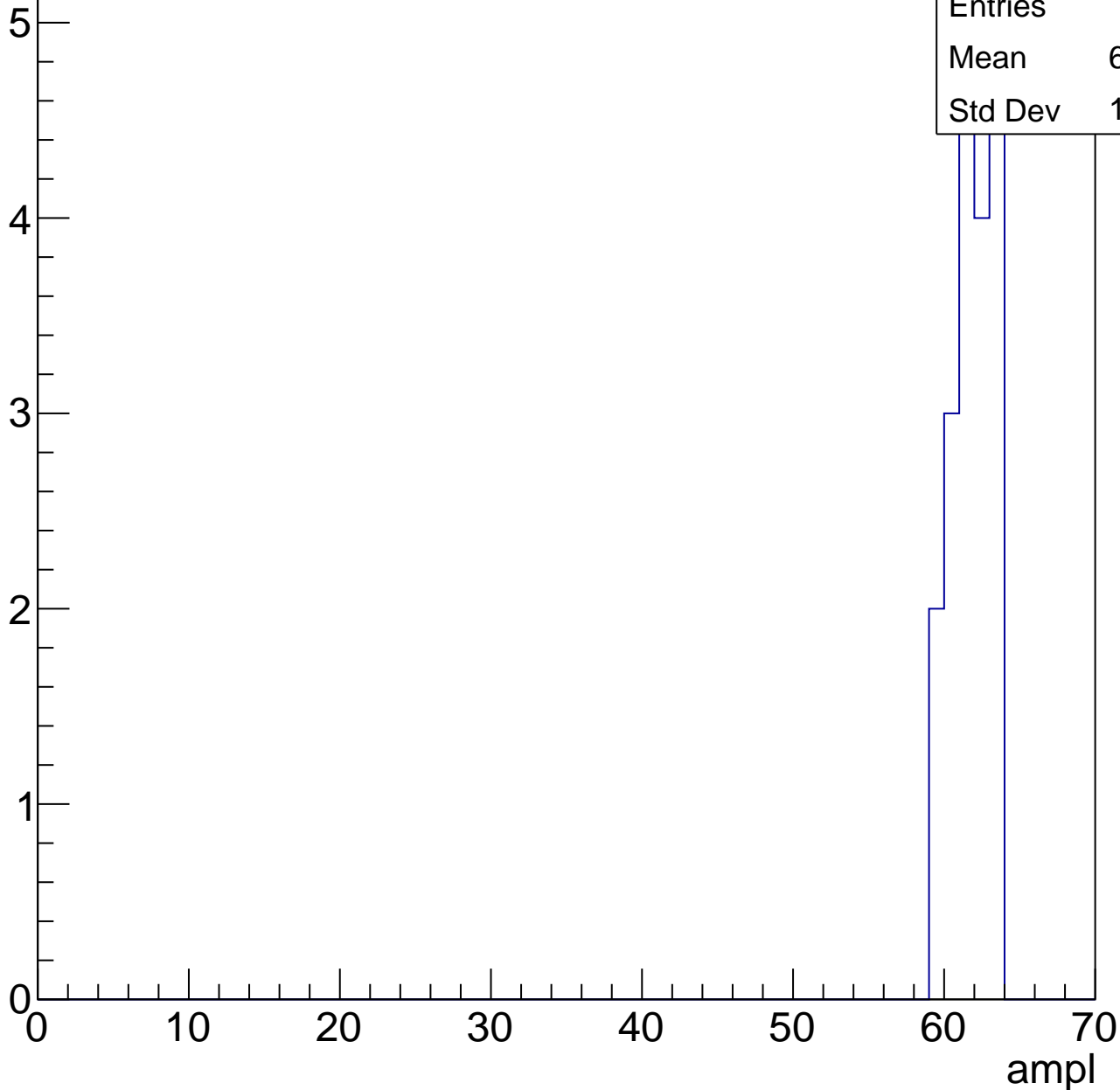


B1L001S, U20-ch98, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	61.37
Std Dev	1.306



B1L001S, U20-ch98, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U20-ch99, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	30.09
Std Dev	3.753

Gaus mean : 30.6486

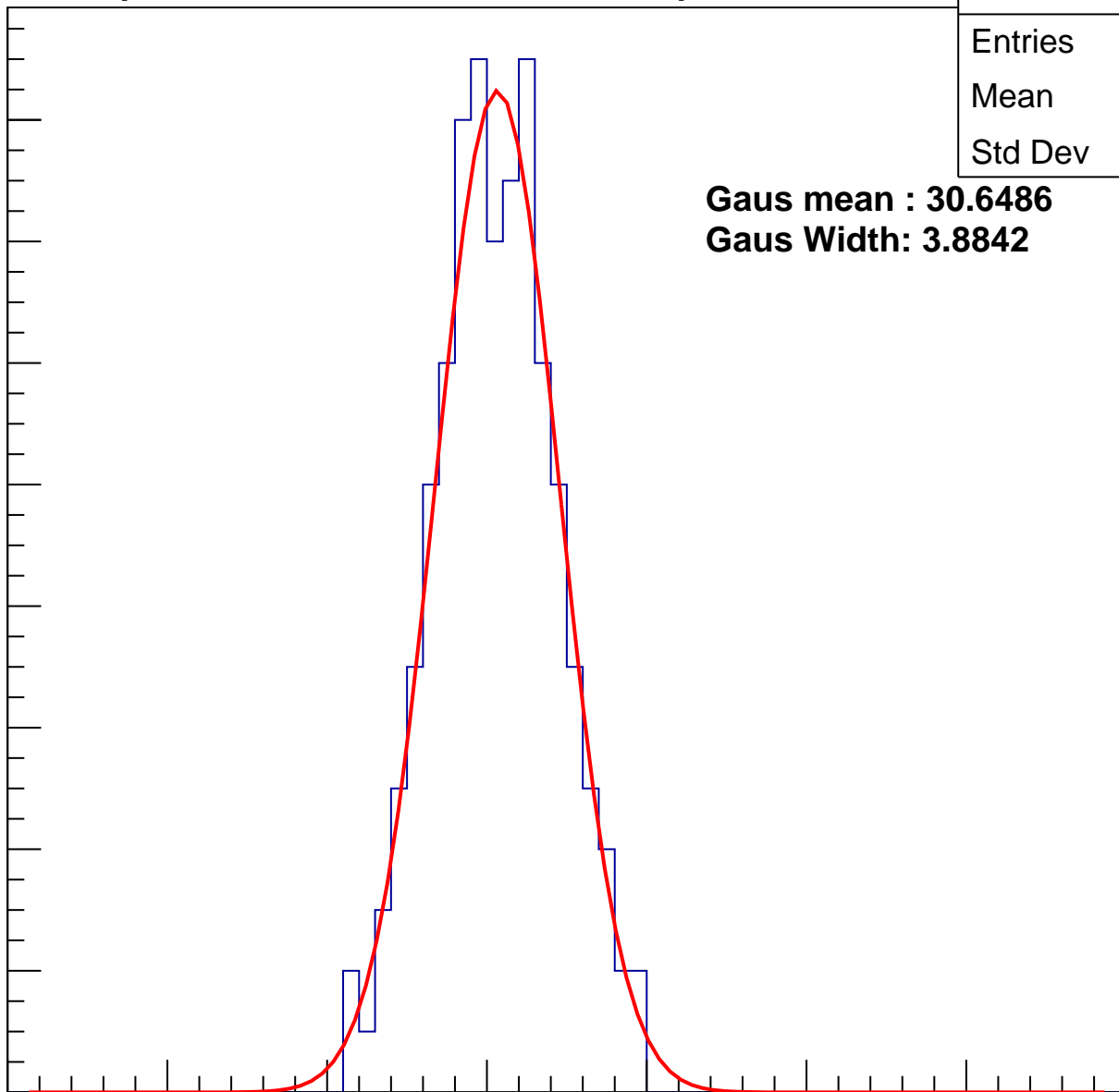
Gaus Width: 3.8842

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch99, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	36.95
Std Dev	3.162

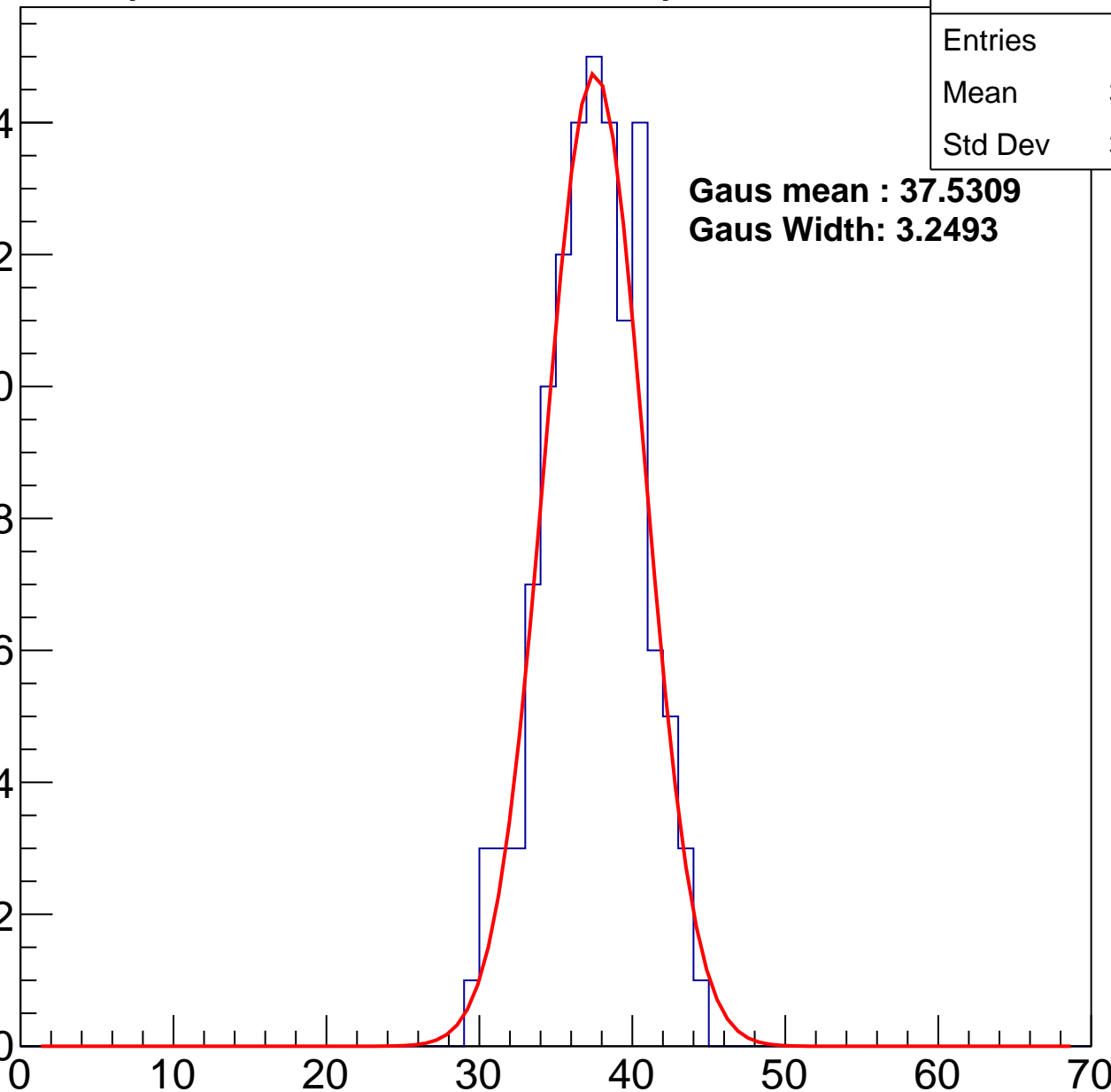
Gaus mean : 37.5309

Gaus Width: 3.2493

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch99, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	43.03
Std Dev	3.008

Gaus mean : 43.3536

Gaus Width: 2.7235

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

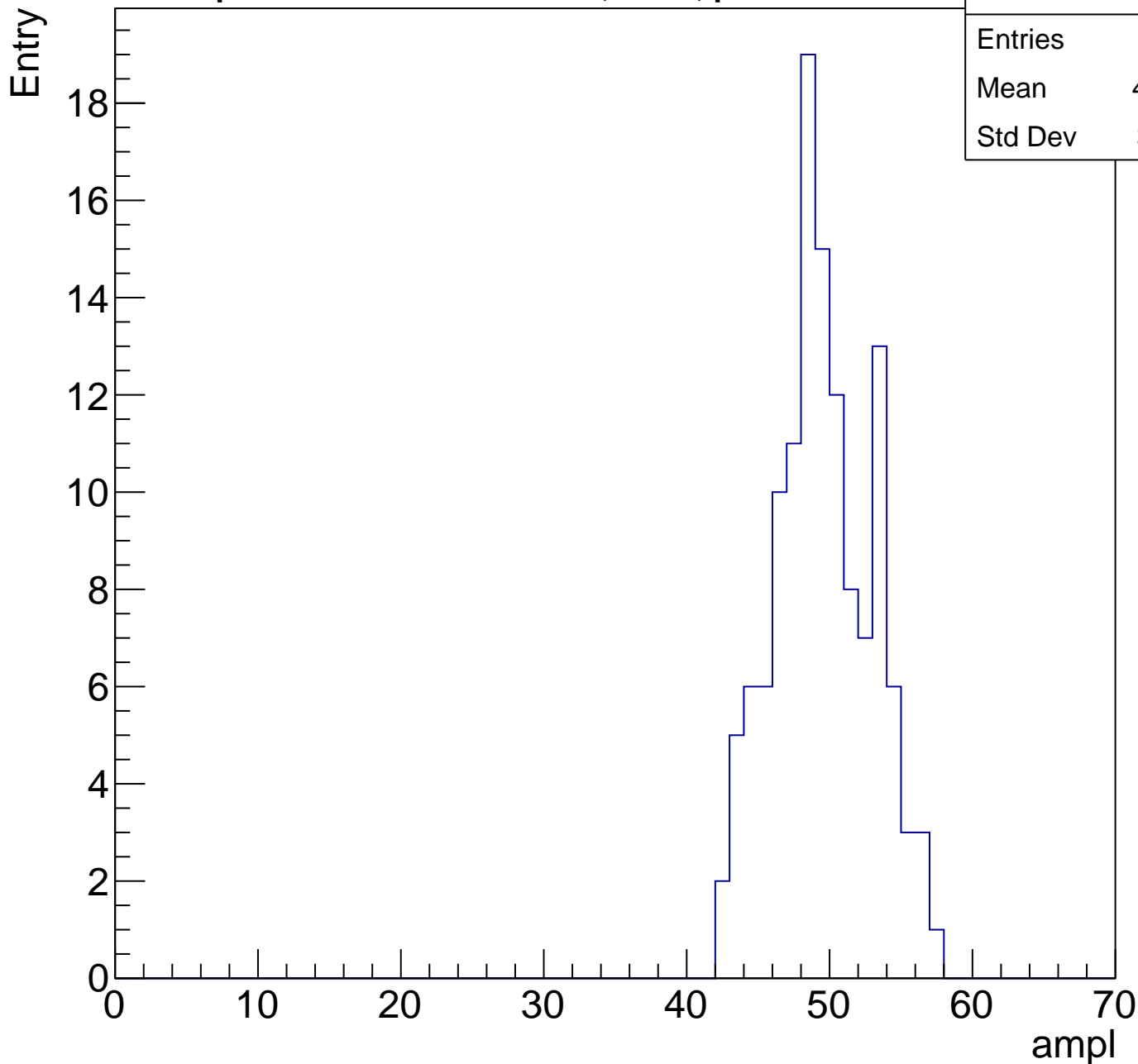
60

70

B1L001S, U20-ch99, adc3

calib_packv5_042523_0143.root, FC#2, port C2

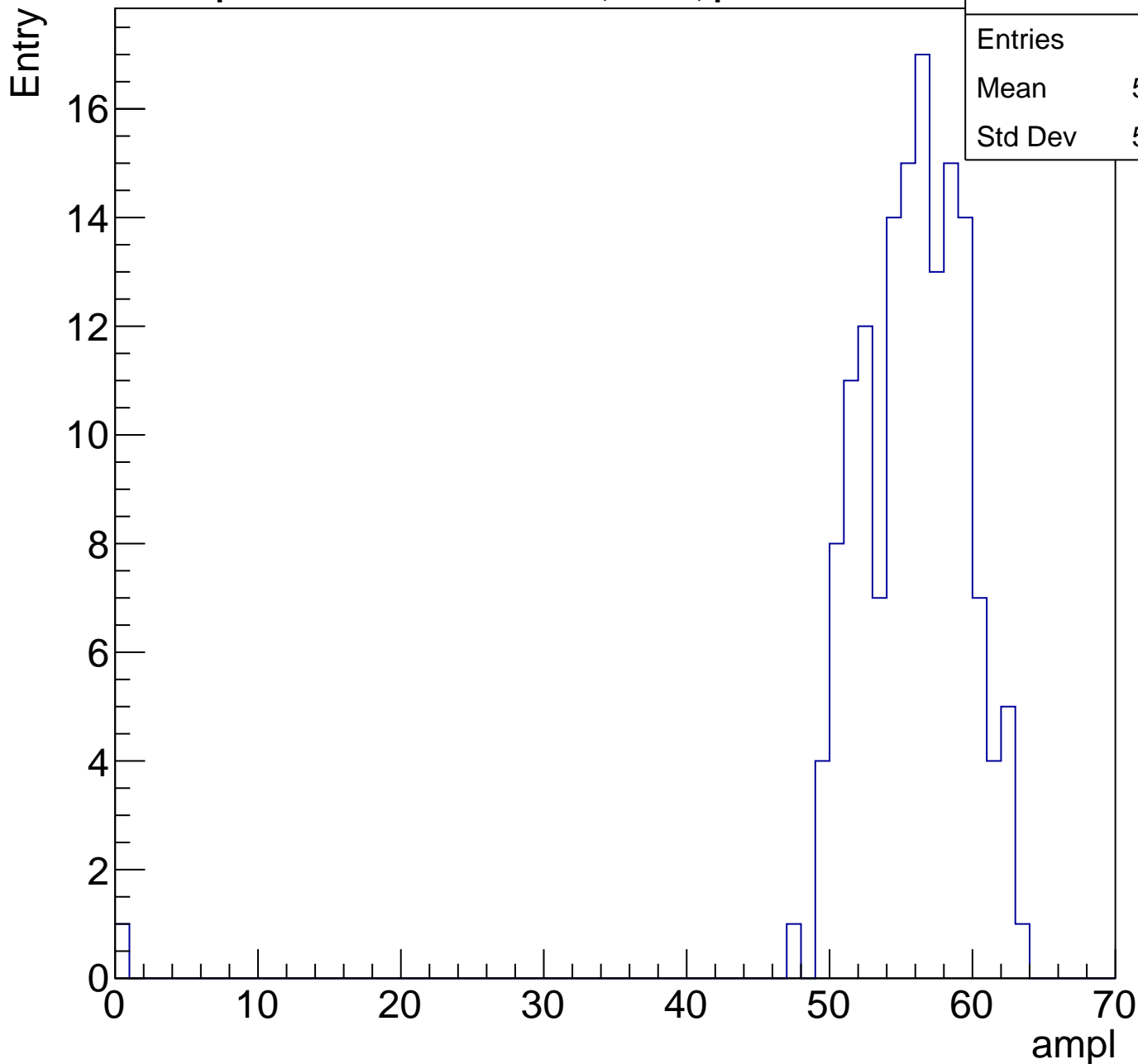
Entries	127
Mean	49.07
Std Dev	3.391



B1L001S, U20-ch99, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	55.07
Std Dev	5.684



B1L001S, U20-ch99, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries	75
Mean	59.81
Std Dev	7.244

ampl

0

10

20

30

40

50

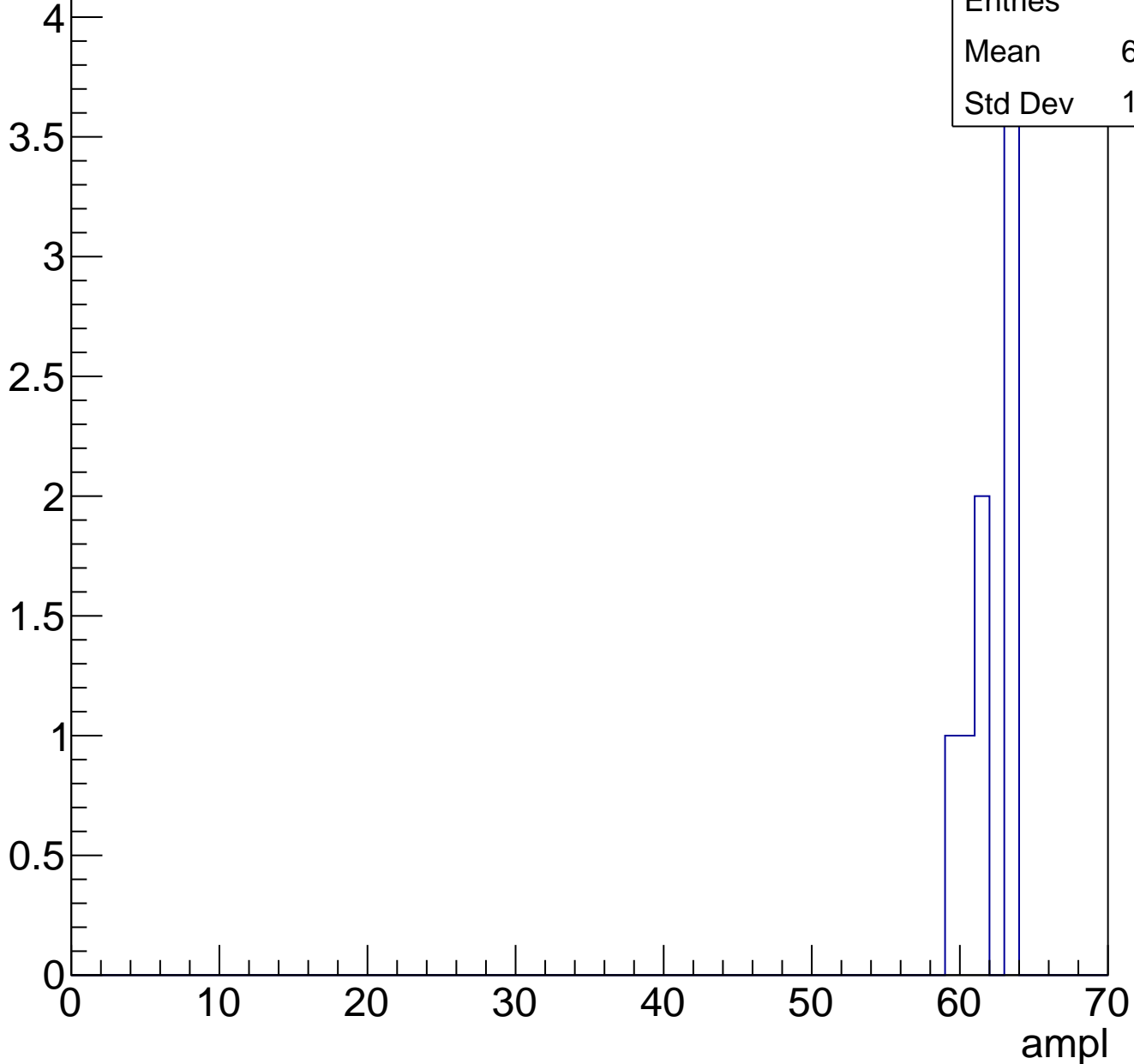
60

70

B1L001S, U20-ch99, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch99, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U20-ch100, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	28.32
Std Dev	3.497

Gaus mean : 28.6138

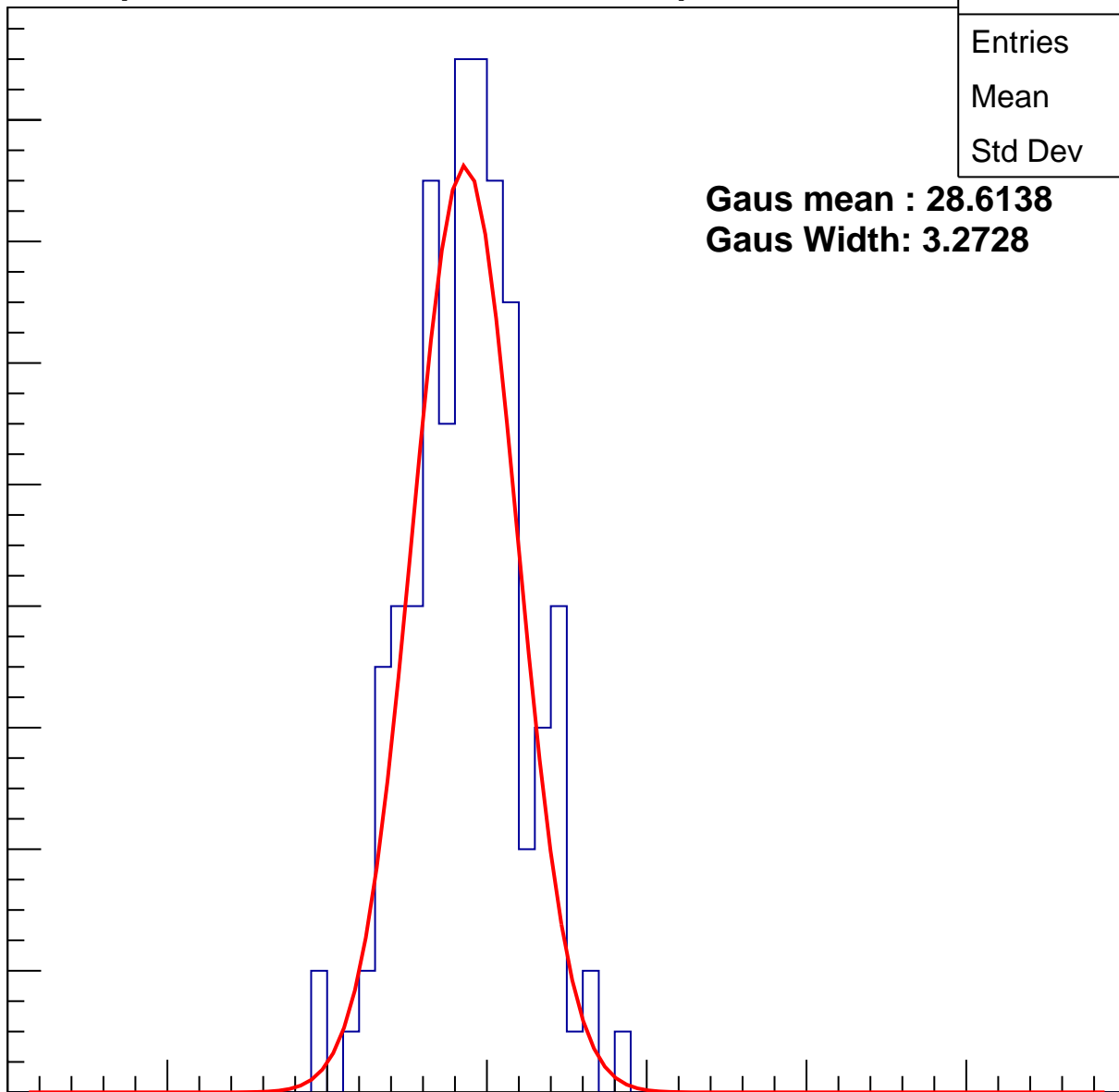
Gaus Width: 3.2728

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch100, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	34.75
Std Dev	2.825

Gaus mean : 35.1996

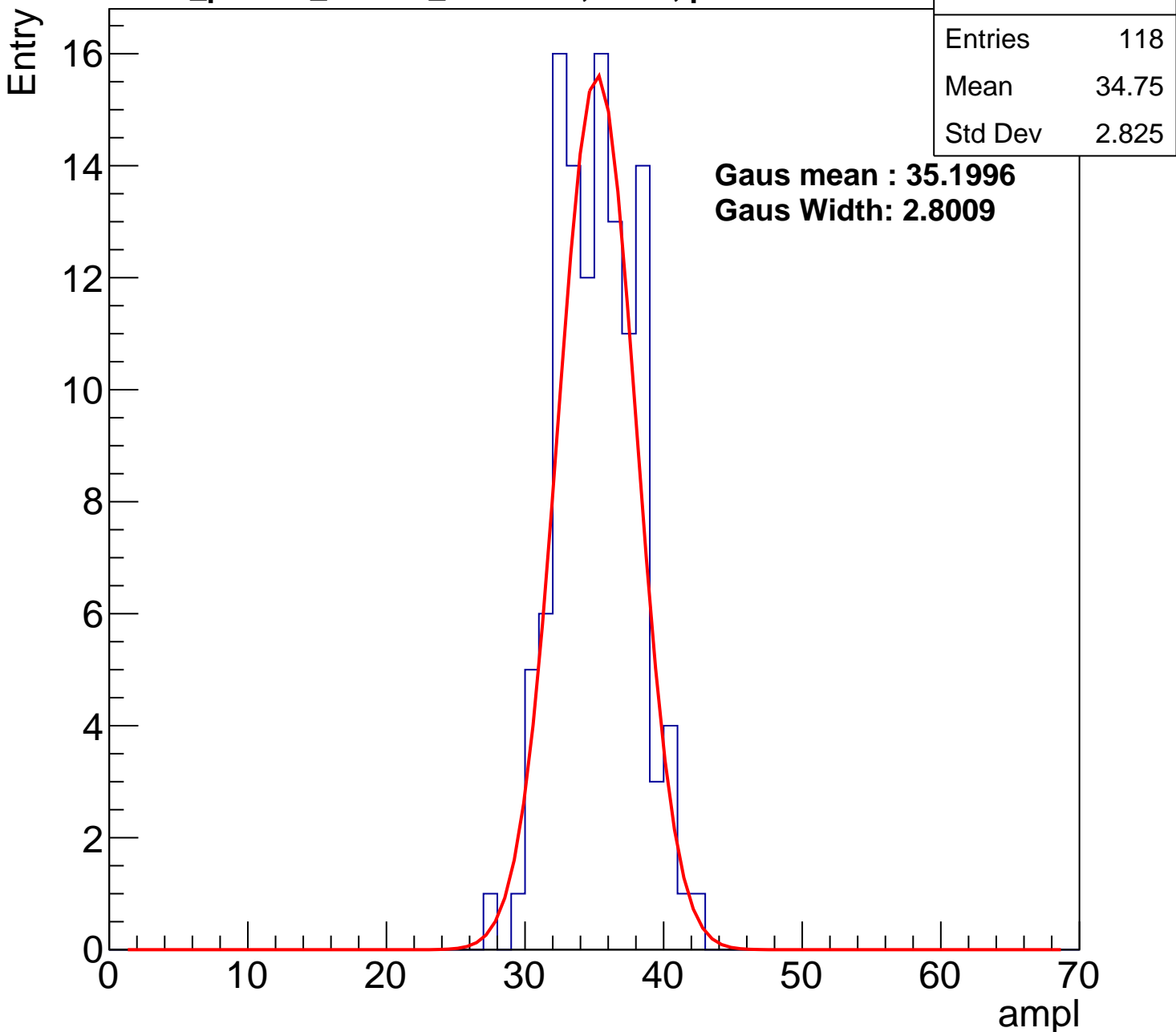
Gaus Width: 2.8009

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch100, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	123
Mean	40.93
Std Dev	3.086

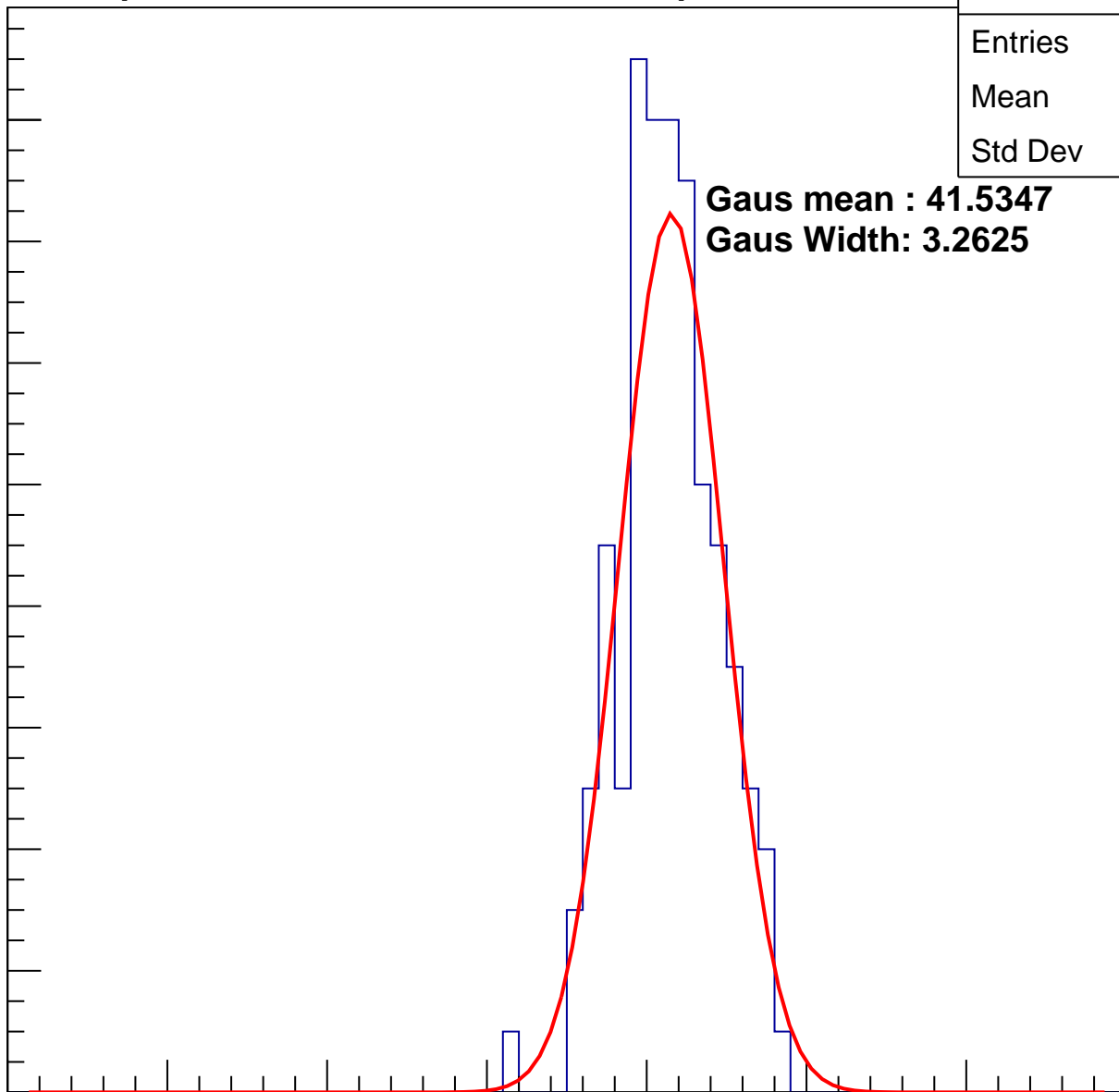
Gaus mean : 41.5347

Gaus Width: 3.2625

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

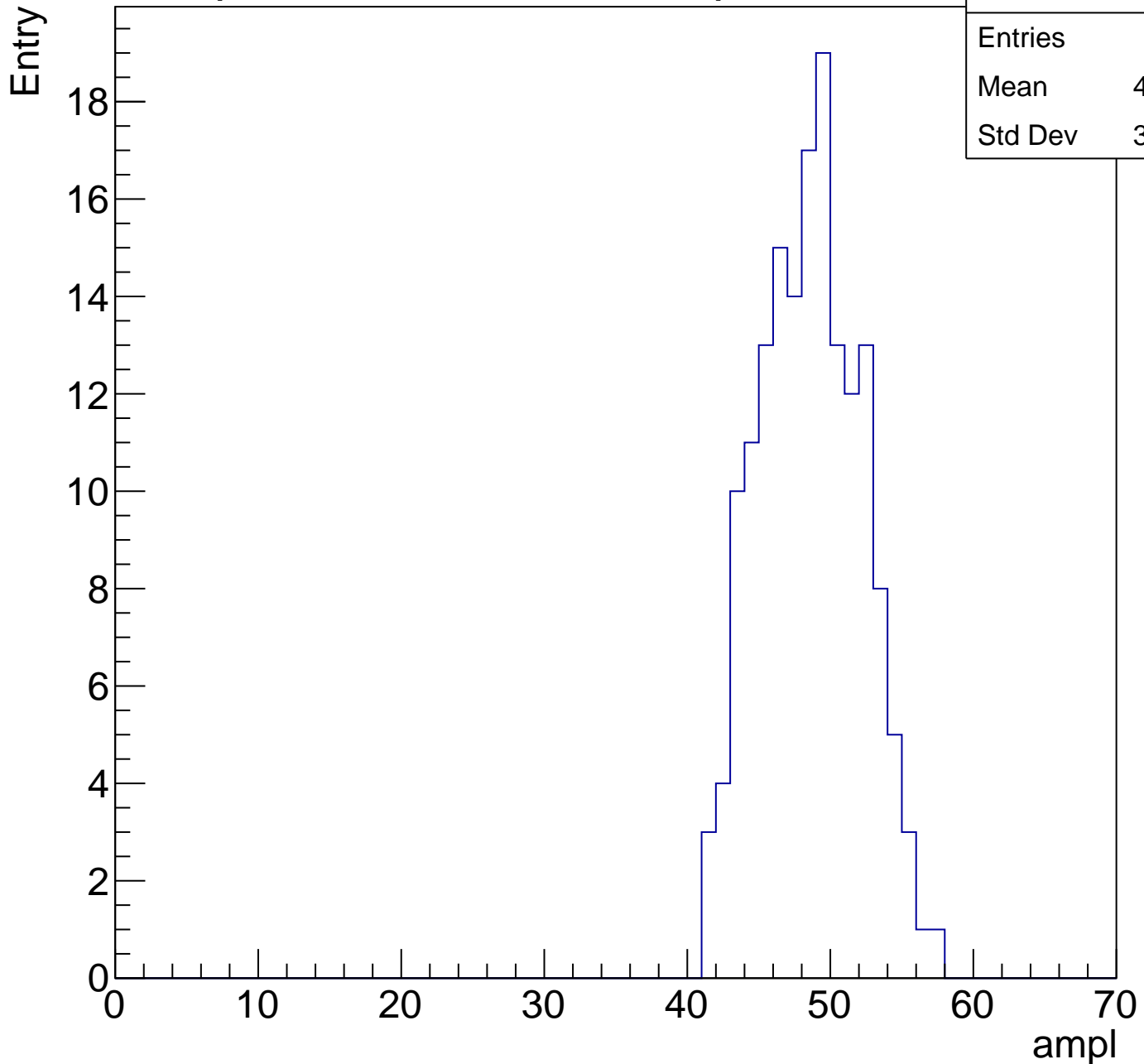
ampl



B1L001S, U20-ch100, adc3

calib_packv5_042523_0143.root, FC#2, port C2

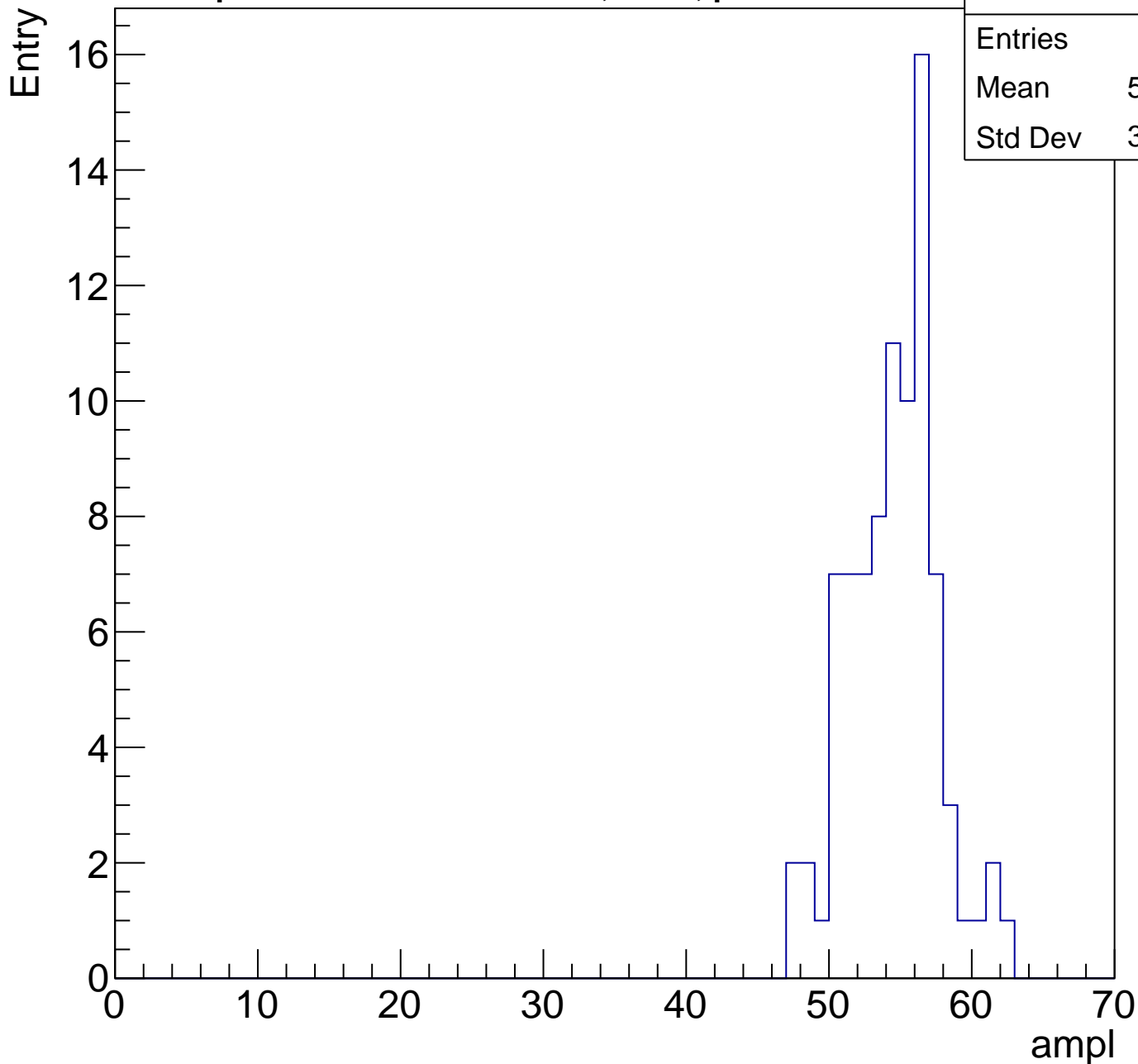
Entries	162
Mean	48.12
Std Dev	3.483



B1L001S, U20-ch100, adc4

calib_packv5_042523_0143.root, FC#2, port C2

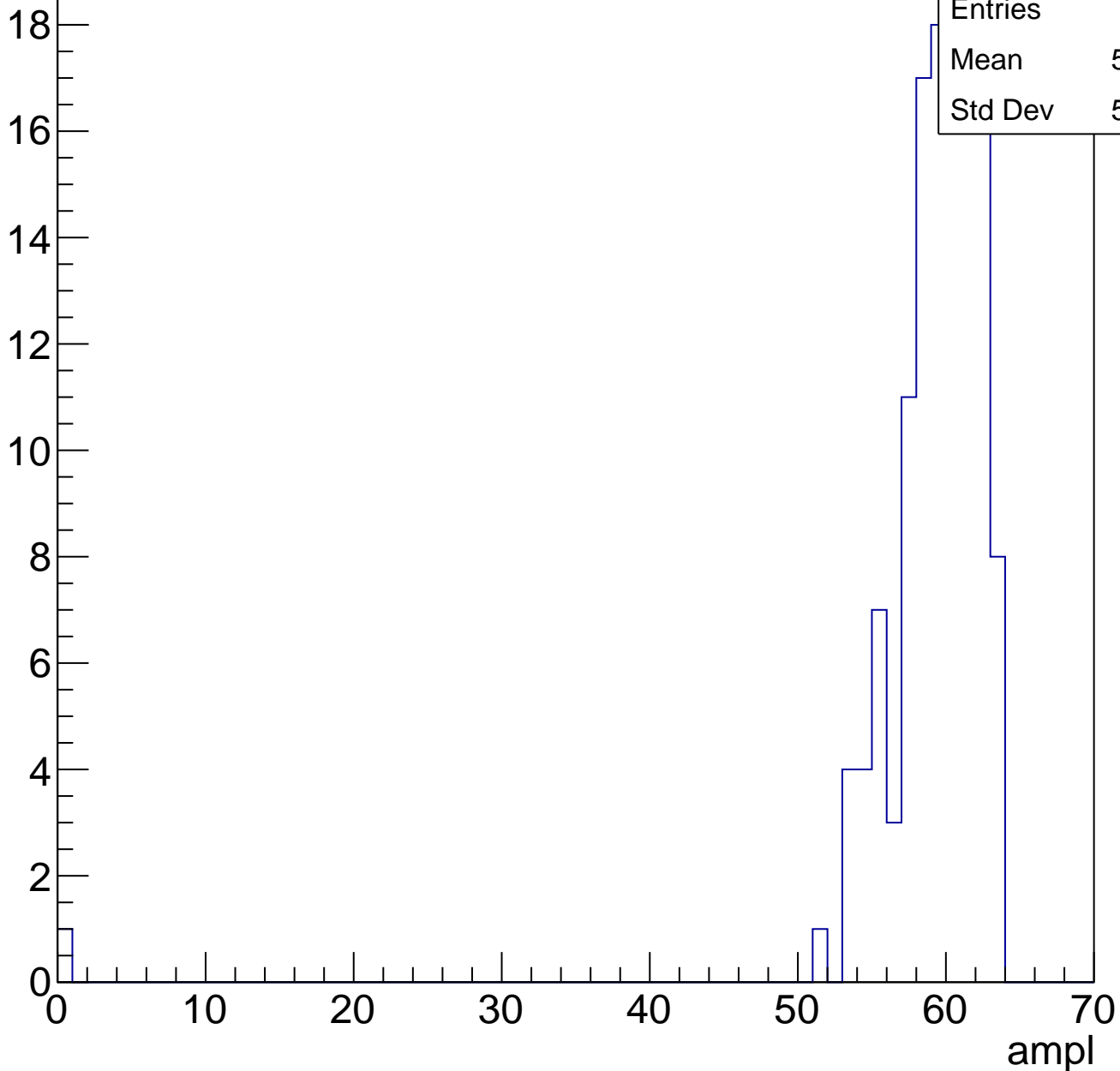
Entries	86
Mean	54.07
Std Dev	3.076



B1L001S, U20-ch100, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch100, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

Entries	21
Mean	58.9
Std Dev	13.27

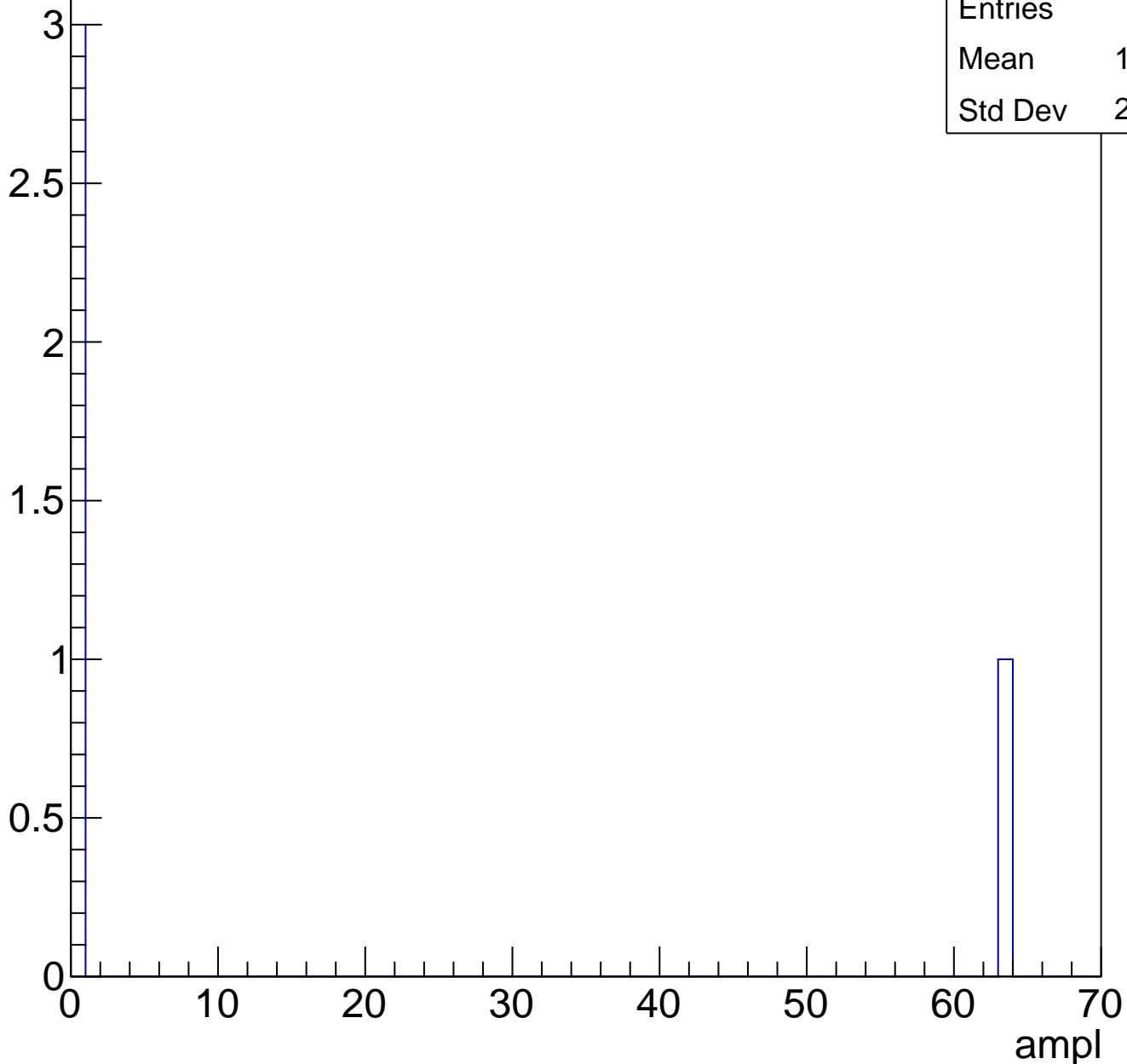
ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch100, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch101, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	29.39
Std Dev	3.298

Gaus mean : 29.9274

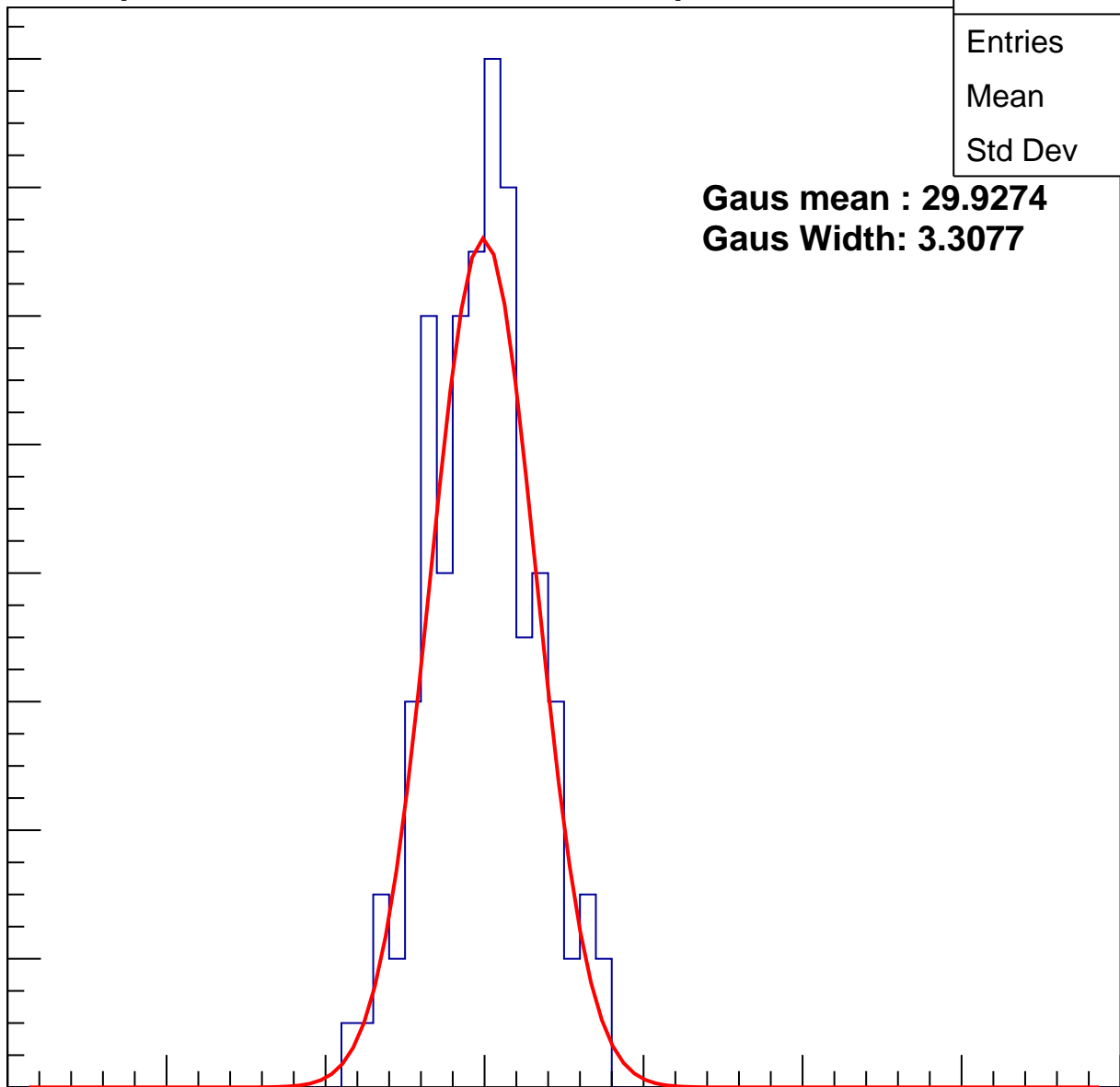
Gaus Width: 3.3077

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch101, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	34.78
Std Dev	2.925

Gaus mean : 35.3864

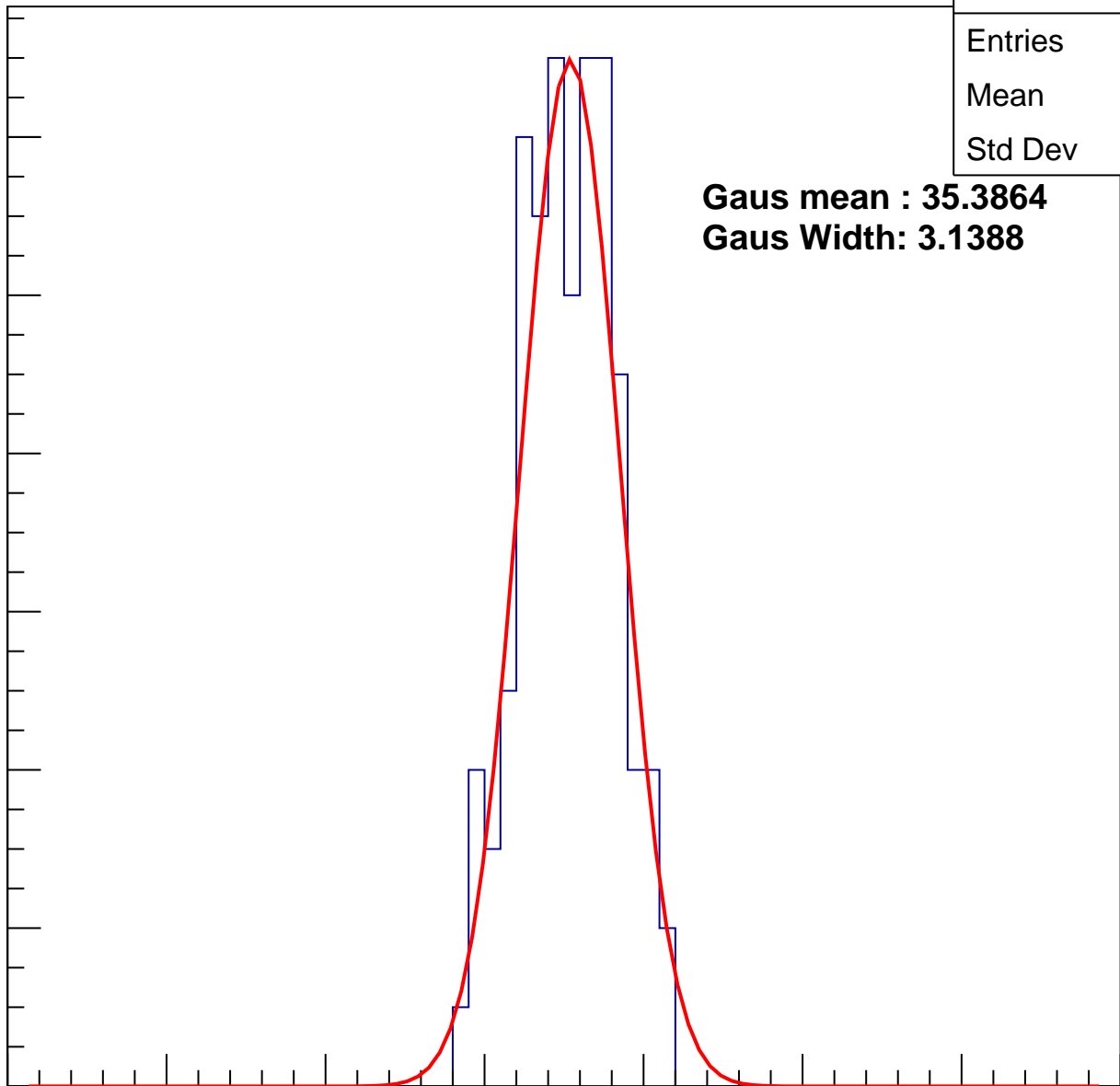
Gaus Width: 3.1388

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch101, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

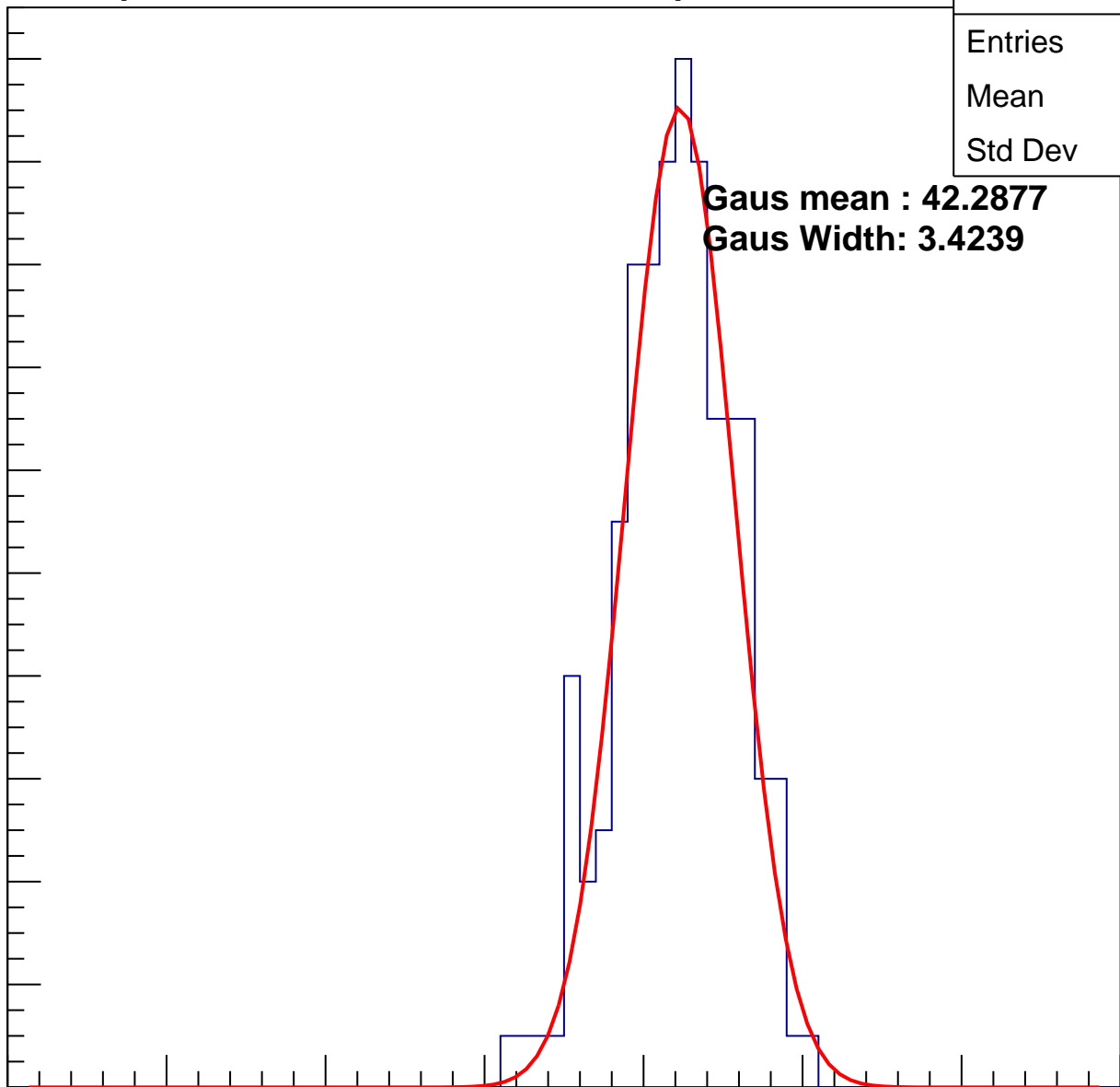
Entries	173
Mean	41.6
Std Dev	3.627

Gaus mean : 42.2877

Gaus Width: 3.4239

0 10 20 30 40 50 60 70

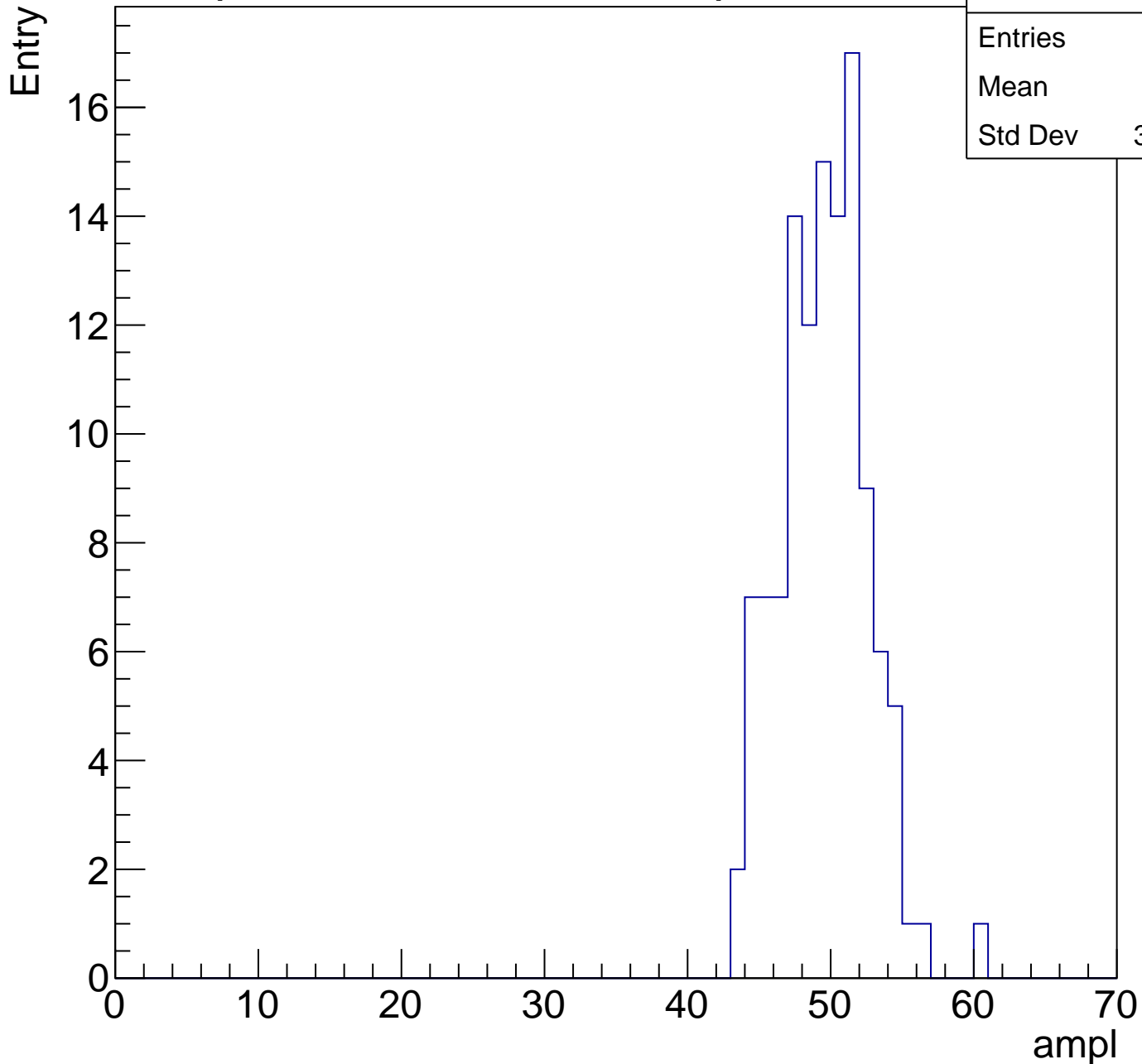
ampl



B1L001S, U20-ch101, adc3

calib_packv5_042523_0143.root, FC#2, port C2

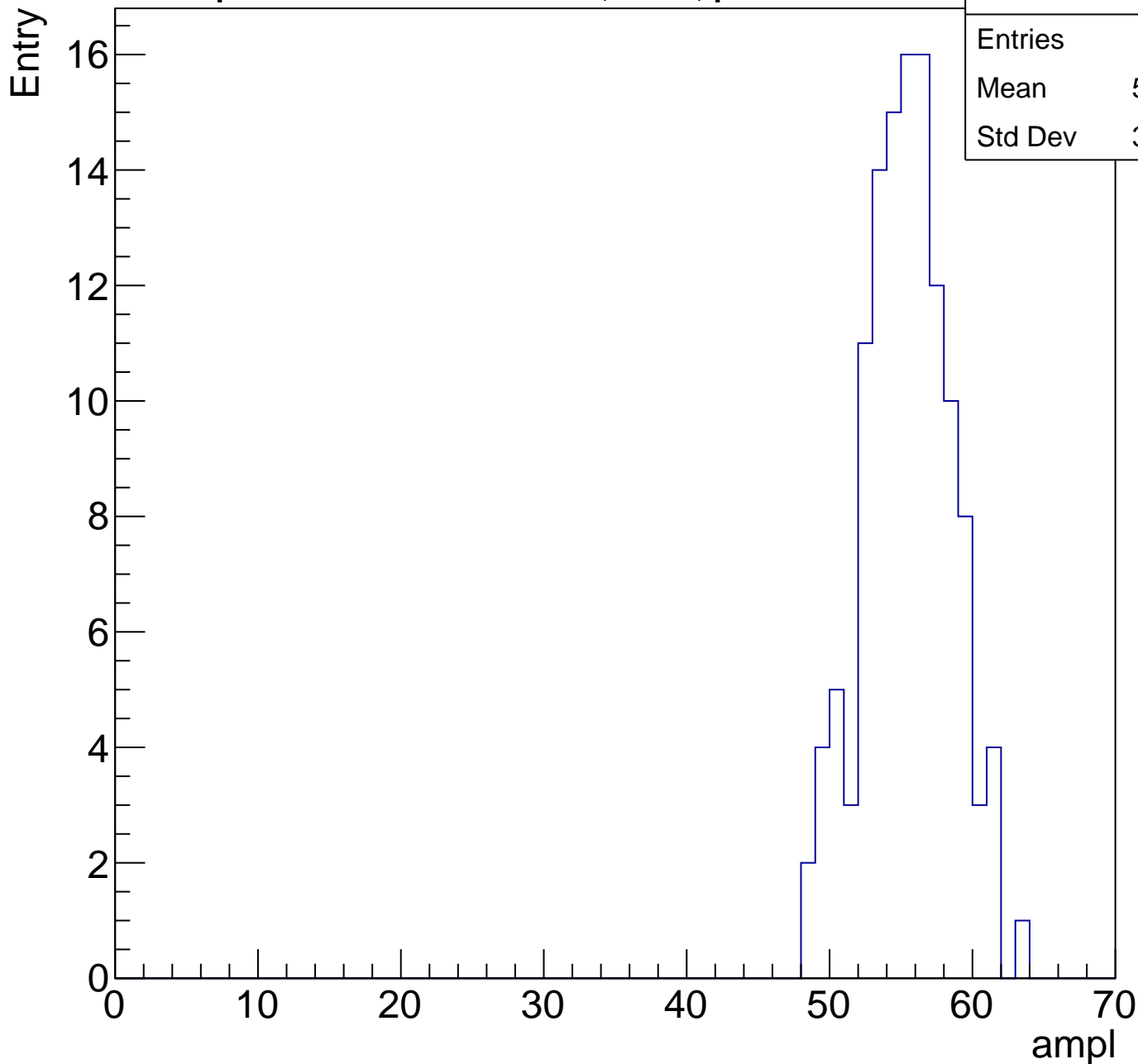
Entries	118
Mean	49.1
Std Dev	3.024



B1L001S, U20-ch101, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	54.98
Std Dev	3.045



B1L001S, U20-ch101, adc5

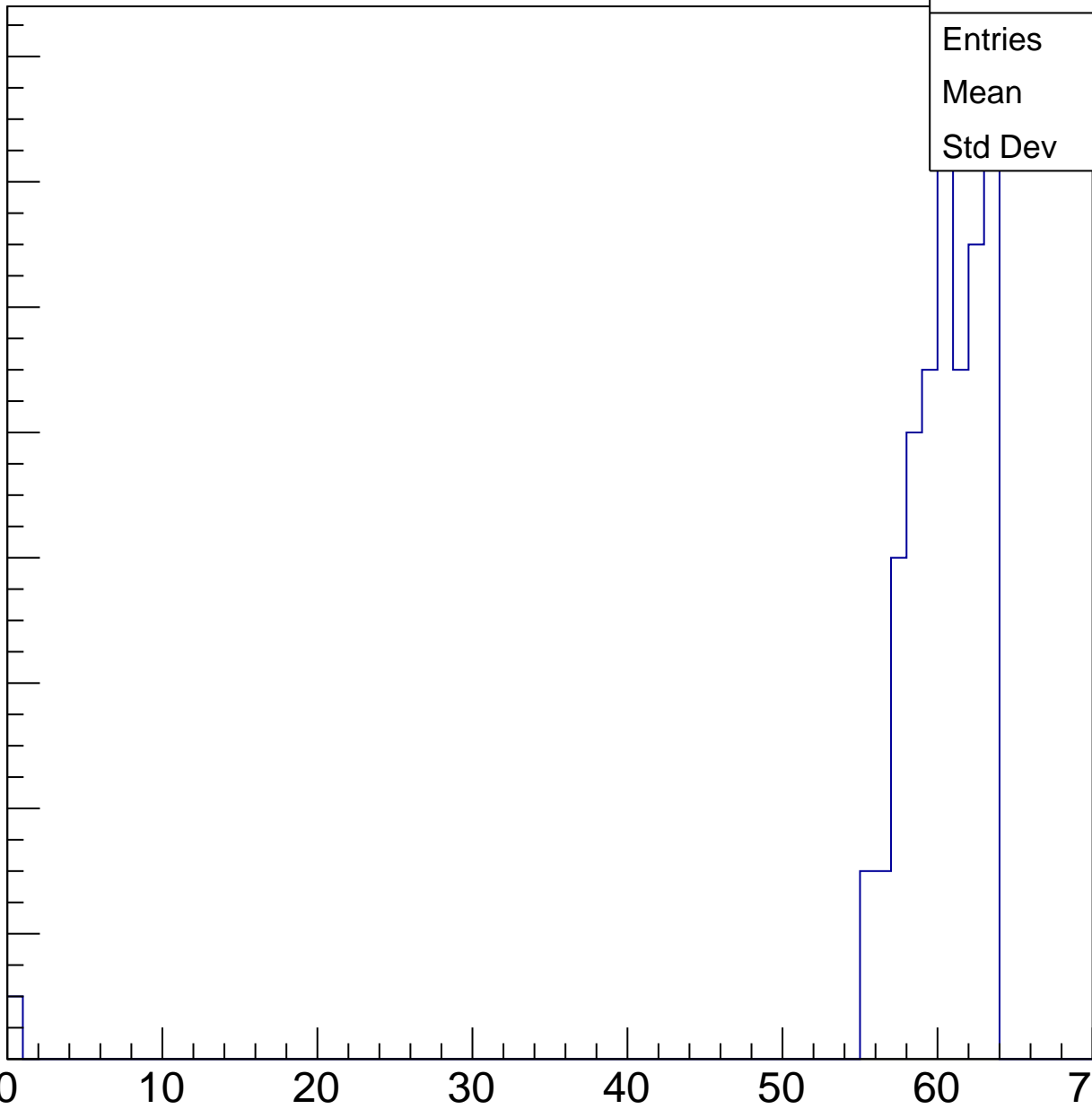
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	59.34
Std Dev	6.634

ampl

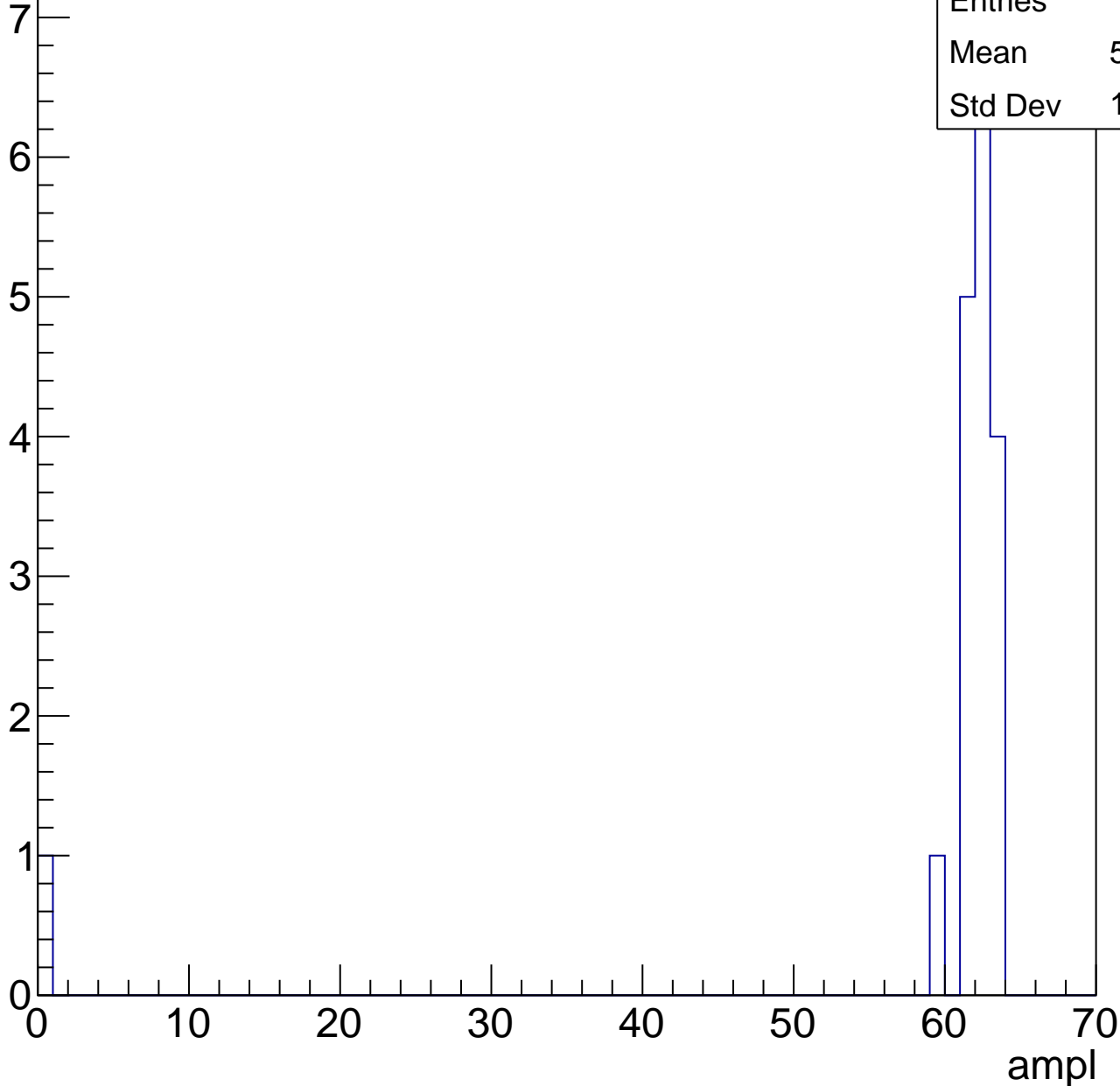


B1L001S, U20-ch101, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	58.33
Std Dev	14.18



B1L001S, U20-ch101, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch102, adc0

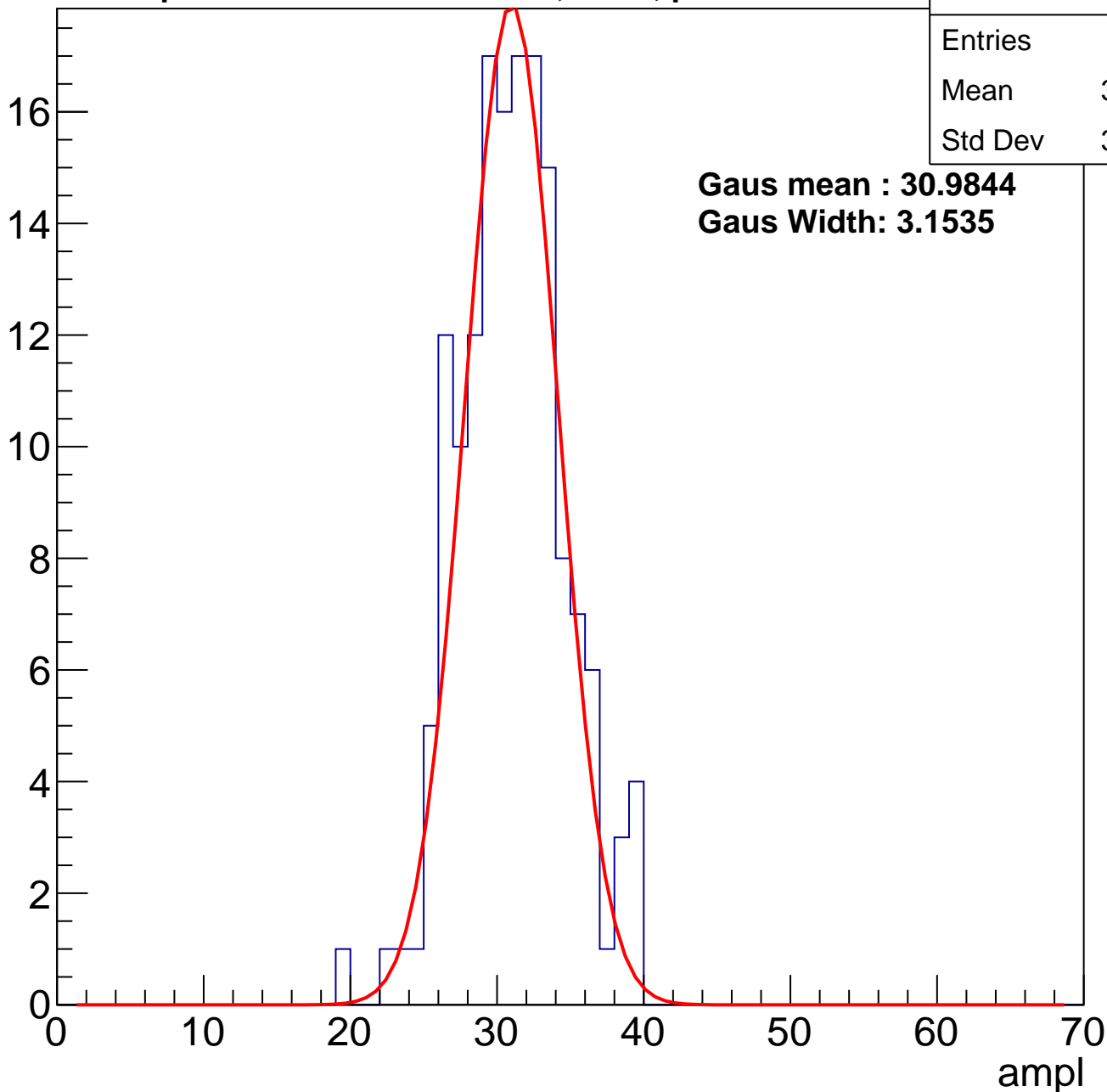
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	30.58
Std Dev	3.598

Gaus mean : 30.9844

Gaus Width: 3.1535

Entry



B1L001S, U20-ch102, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.99
Std Dev	2.918

Gaus mean : 37.6555

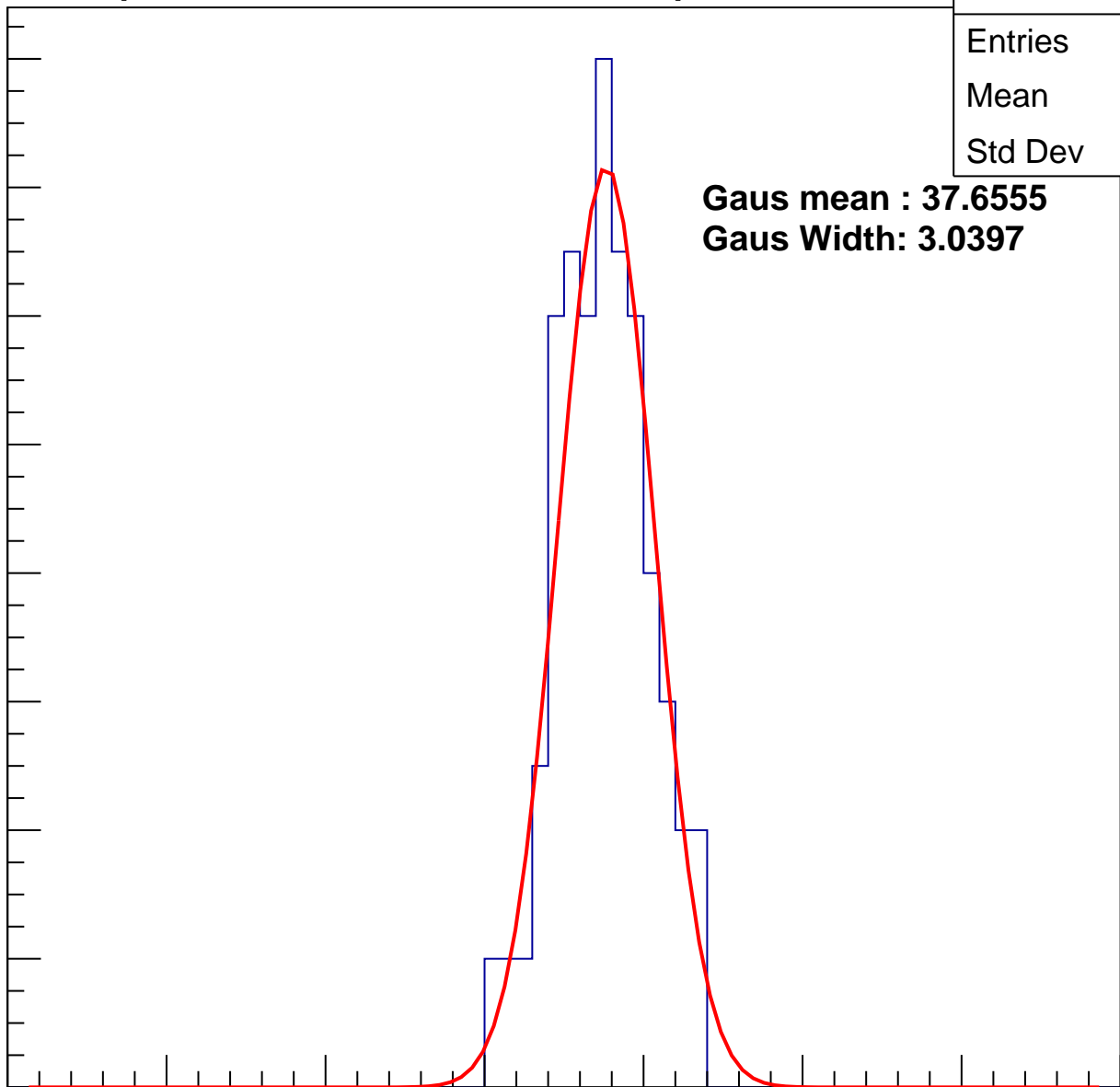
Gaus Width: 3.0397

Entry

16
14
12
10
8
6
4
2
0

ampl

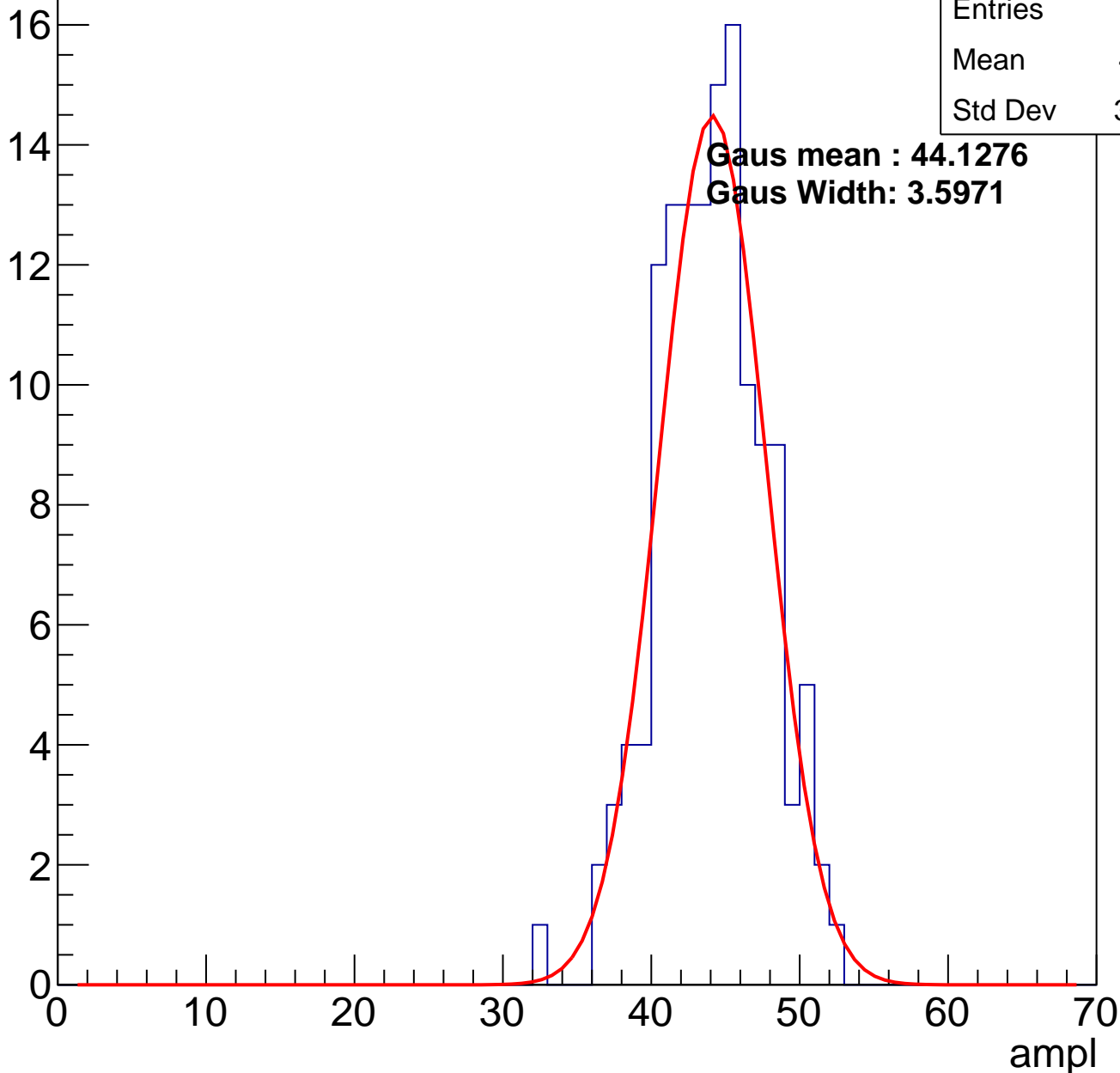
0 10 20 30 40 50 60 70



B1L001S, U20-ch102, adc2

calib_packv5_042523_0143.root, FC#2, port C2

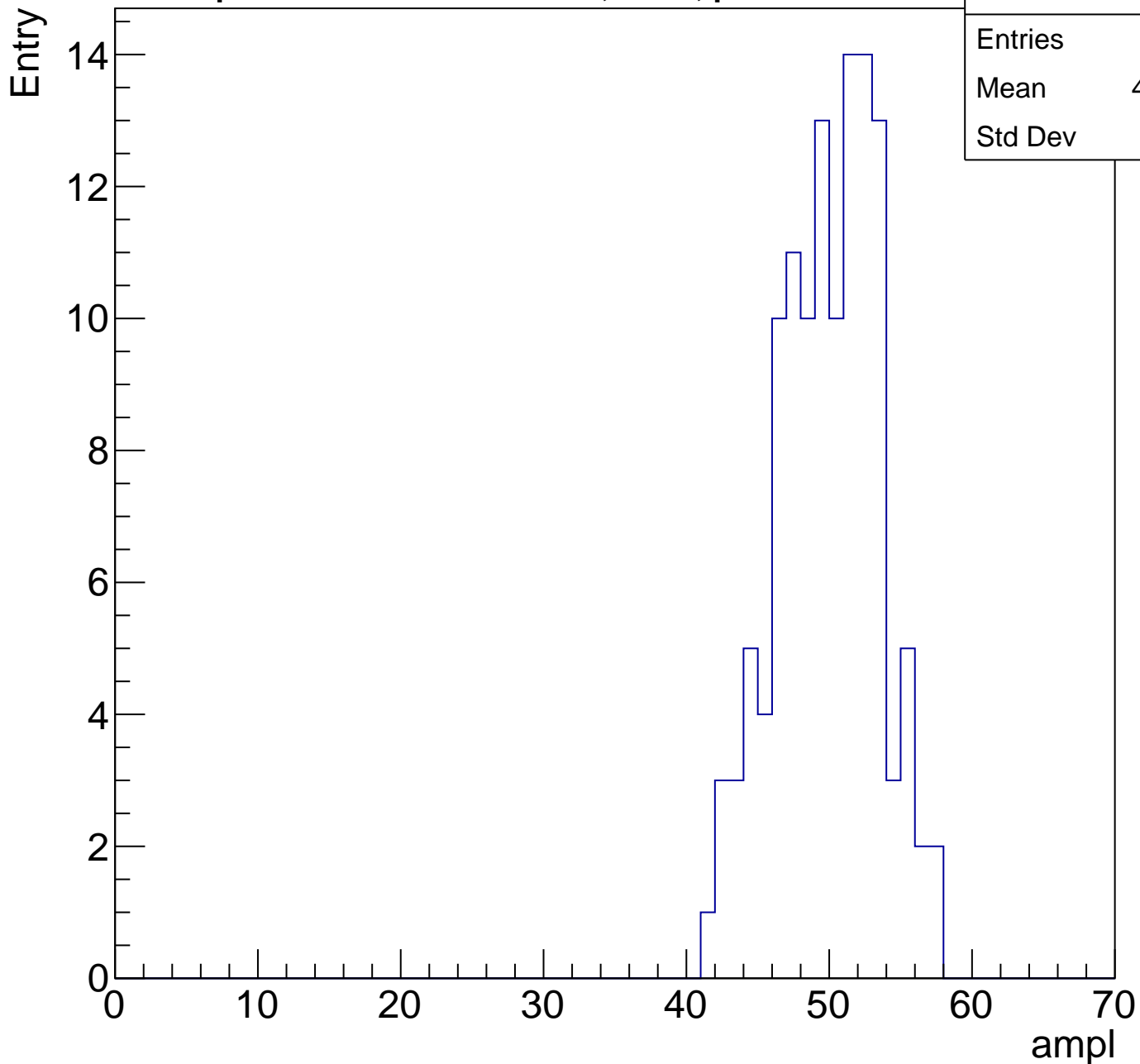
Entry



B1L001S, U20-ch102, adc3

calib_packv5_042523_0143.root, FC#2, port C2

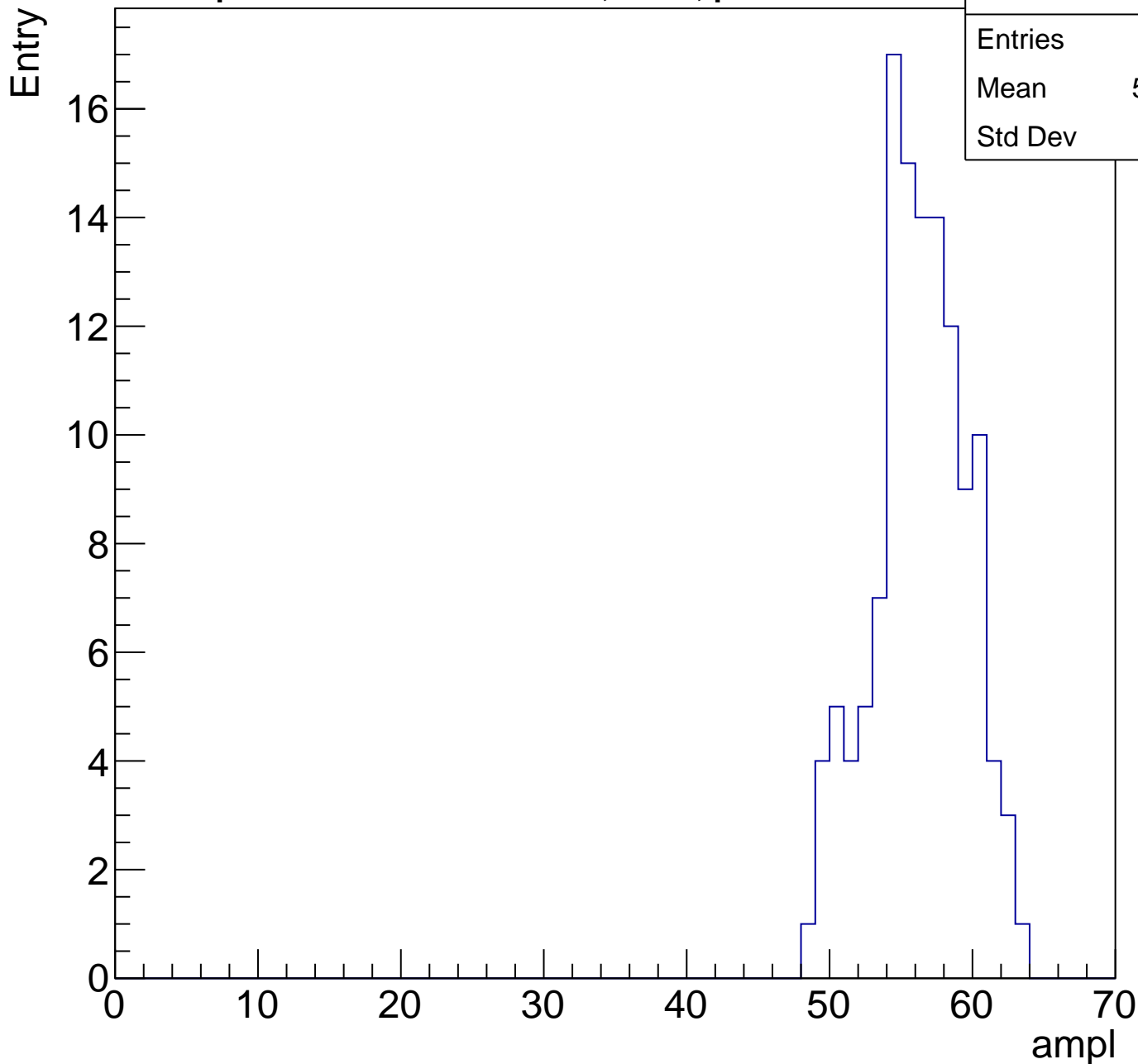
Entries	123
Mean	49.46
Std Dev	3.5



B1L001S, U20-ch102, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	55.79
Std Dev	3.25



B1L001S, U20-ch102, adc5

calib_packv5_042523_0143.root, FC#2, port C2

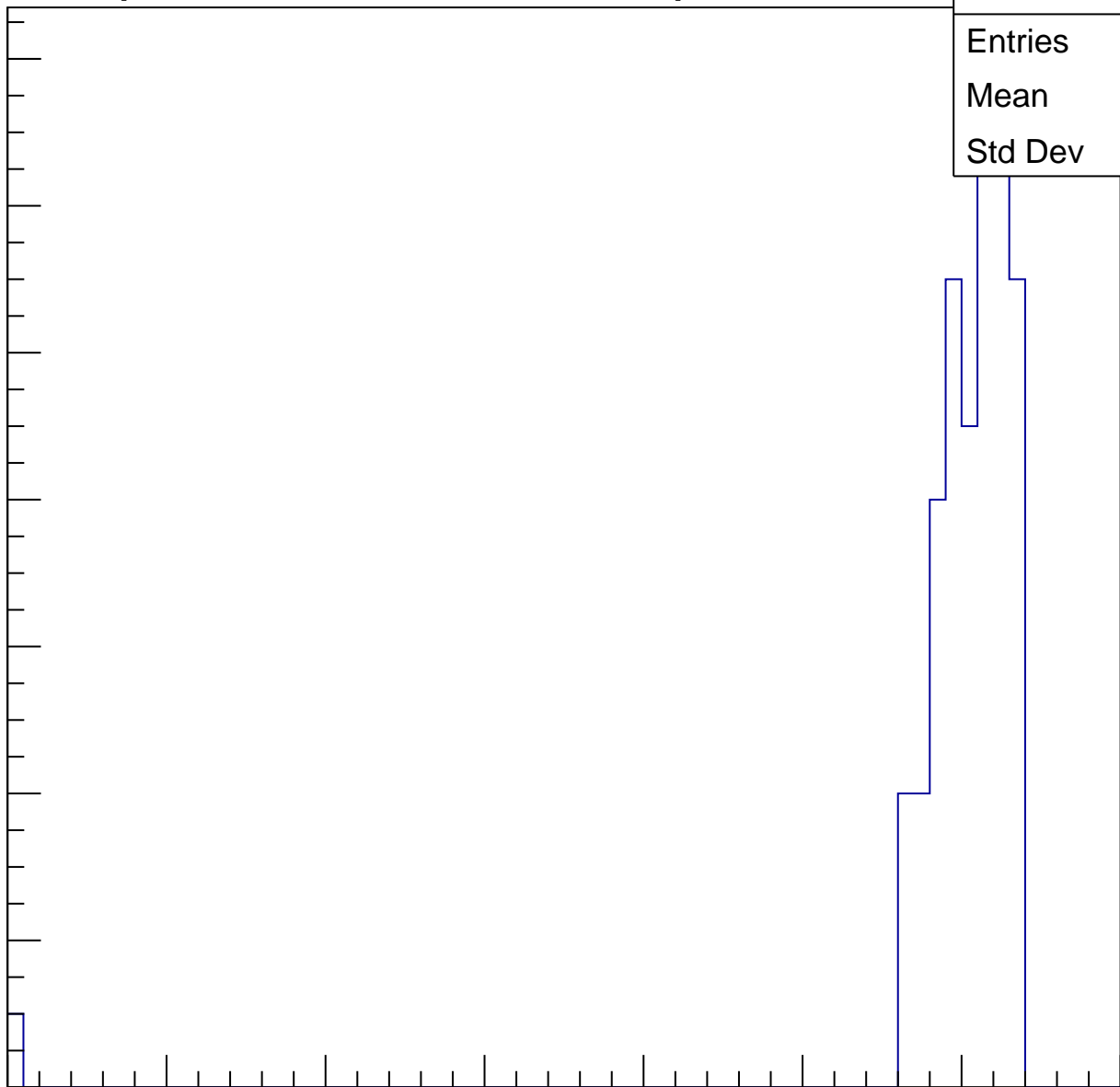
Entries	75
Mean	59.44
Std Dev	7.197

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

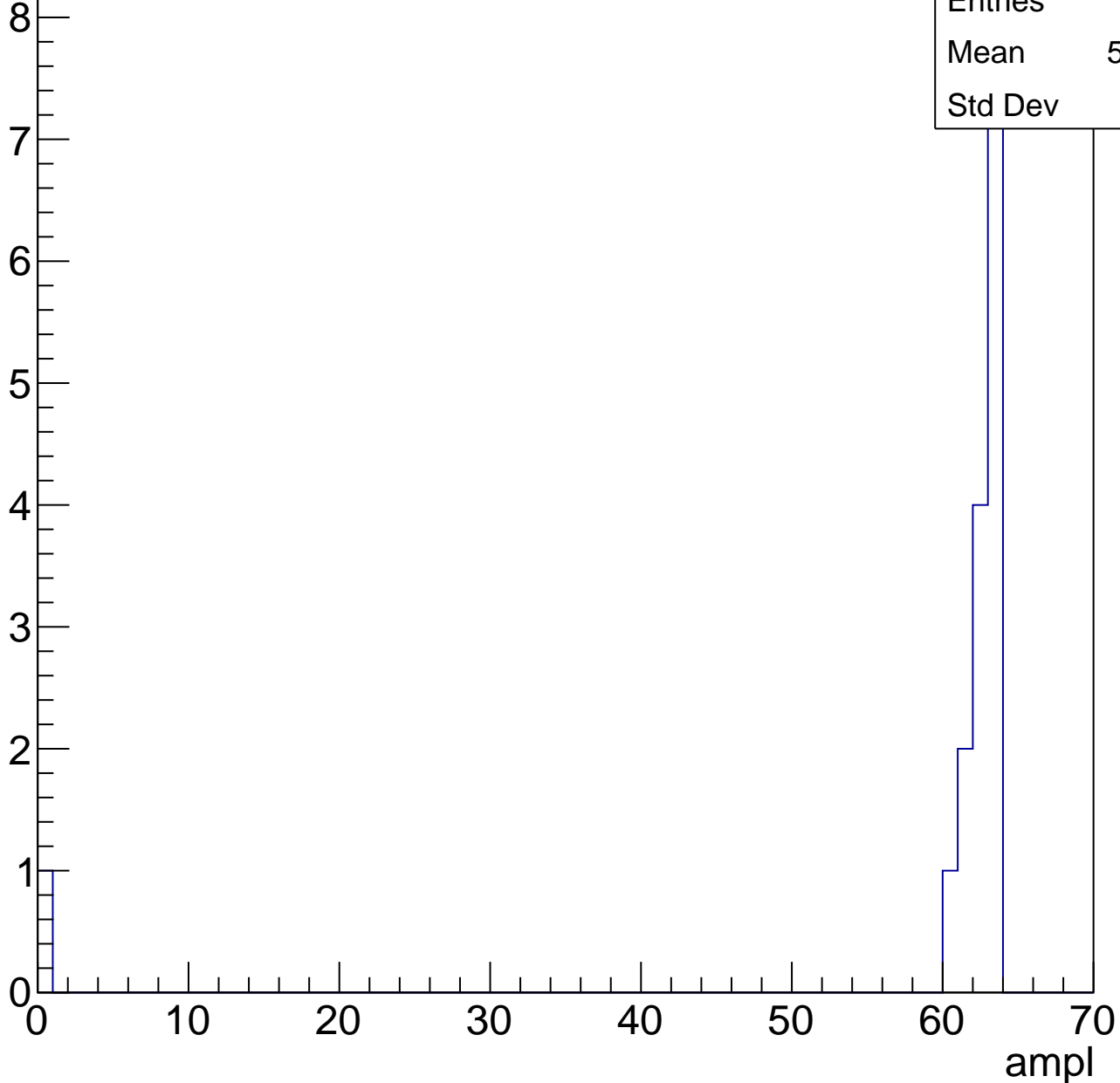


B1L001S, U20-ch102, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	58.38
Std Dev	15.1



B1L001S, U20-ch102, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U20-ch103, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

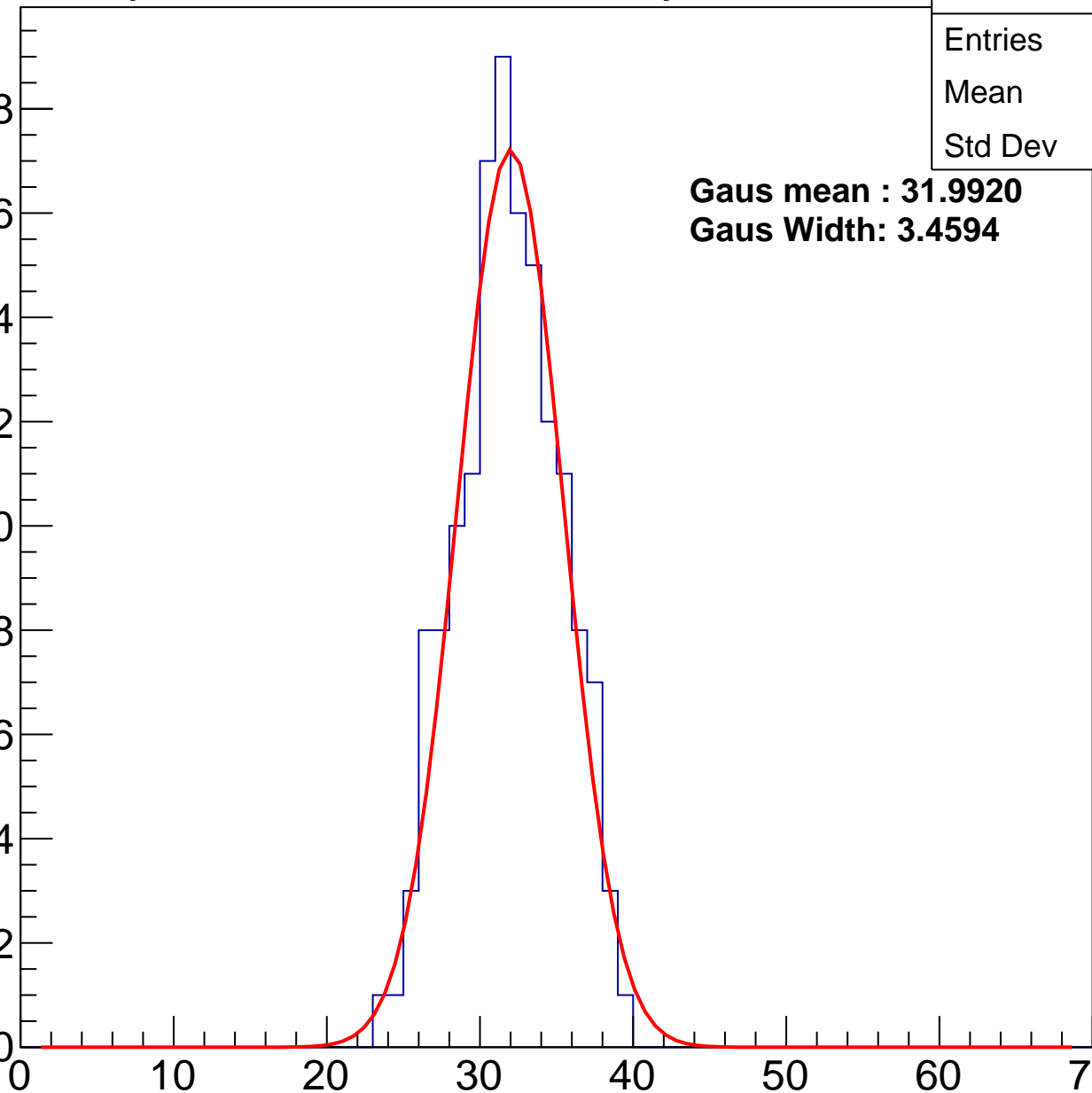
18
16
14
12
10
8
6
4
2
0

Entries	151
Mean	31.42
Std Dev	3.352

Gaus mean : 31.9920

Gaus Width: 3.4594

ampl



B1L001S, U20-ch103, adc1

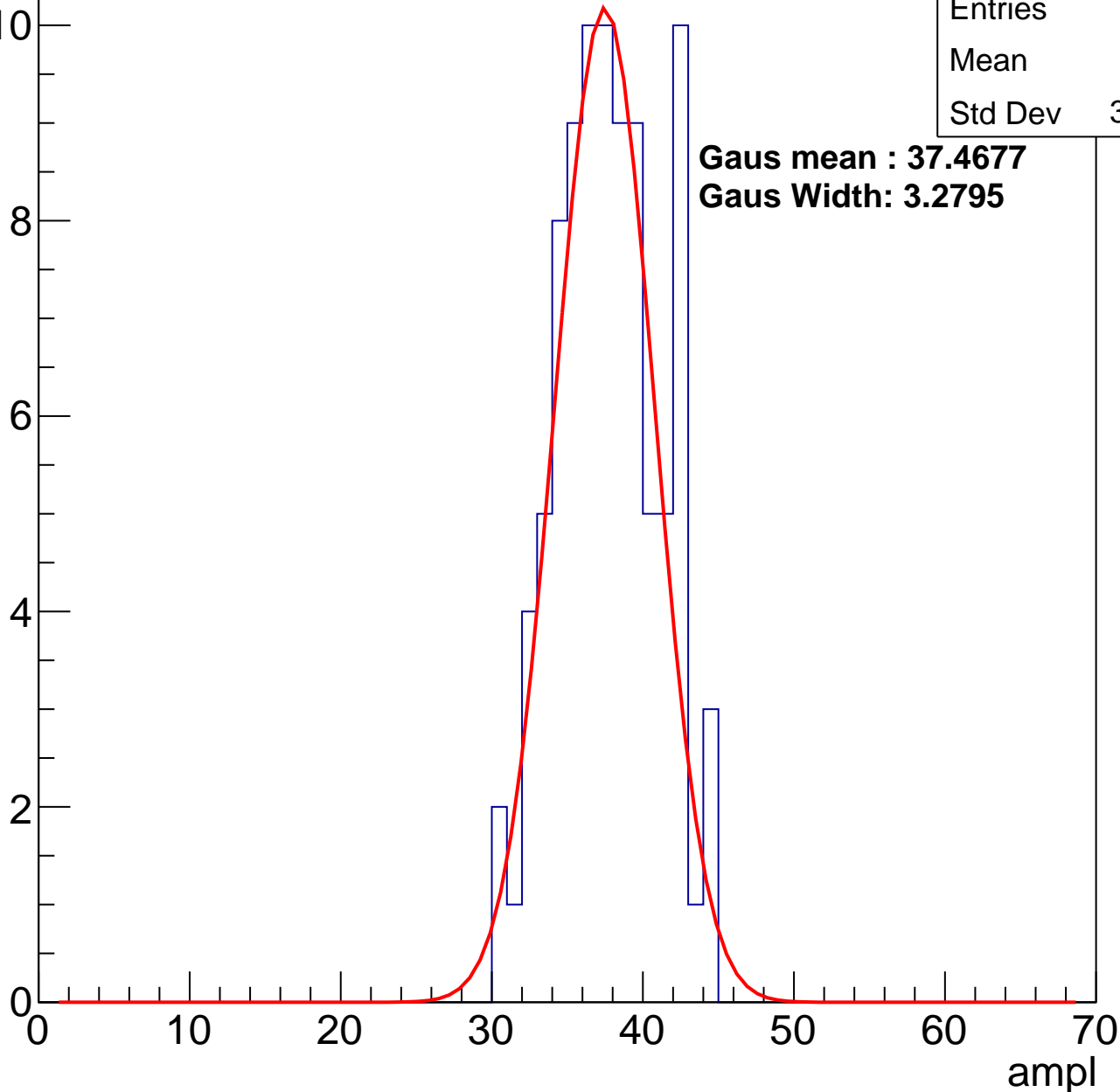
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	91
Mean	37.3
Std Dev	3.359

Gaus mean : 37.4677

Gaus Width: 3.2795



B1L001S, U20-ch103, adc2

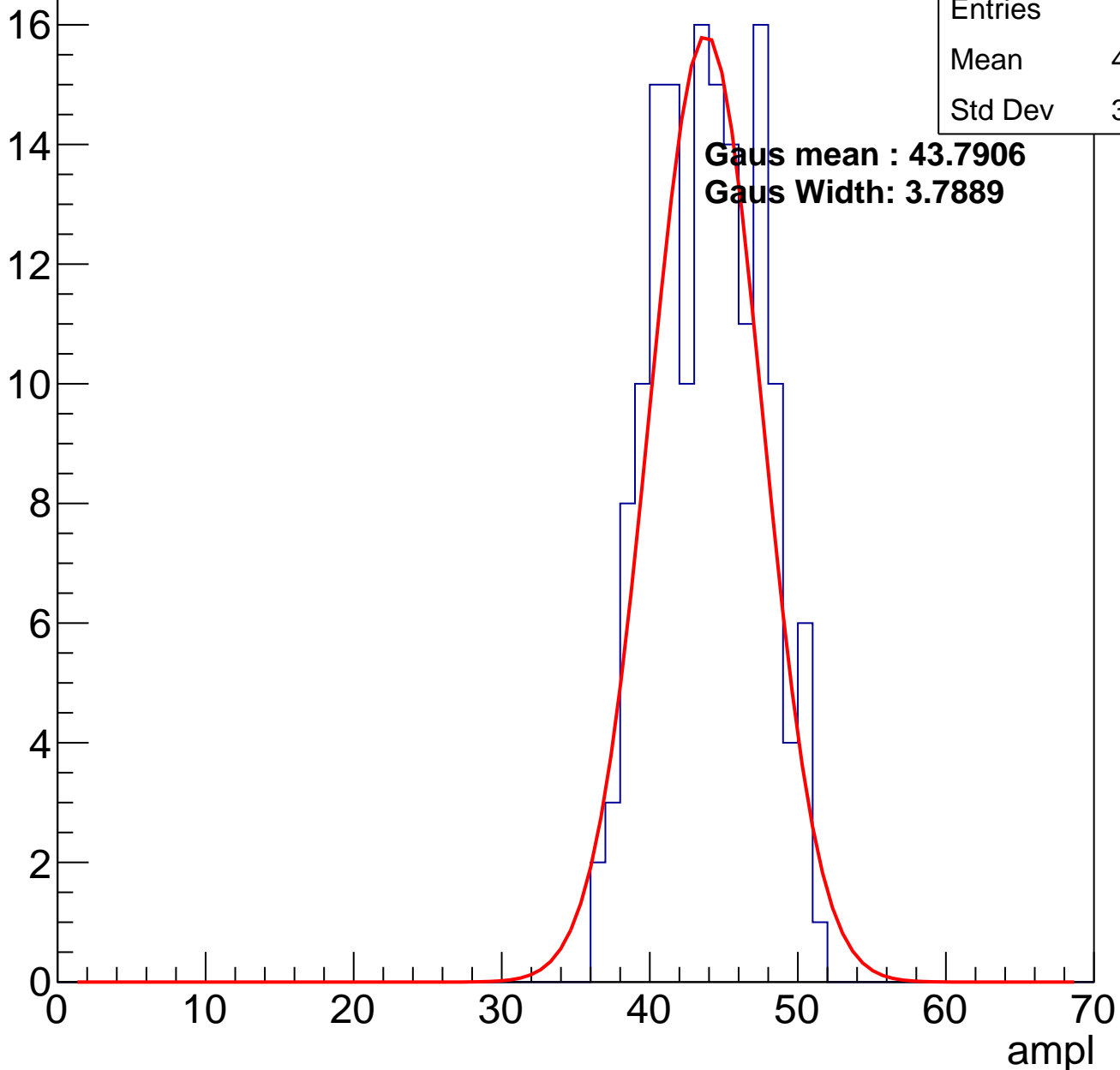
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	43.43
Std Dev	3.508

Gaus mean : 43.7906

Gaus Width: 3.7889

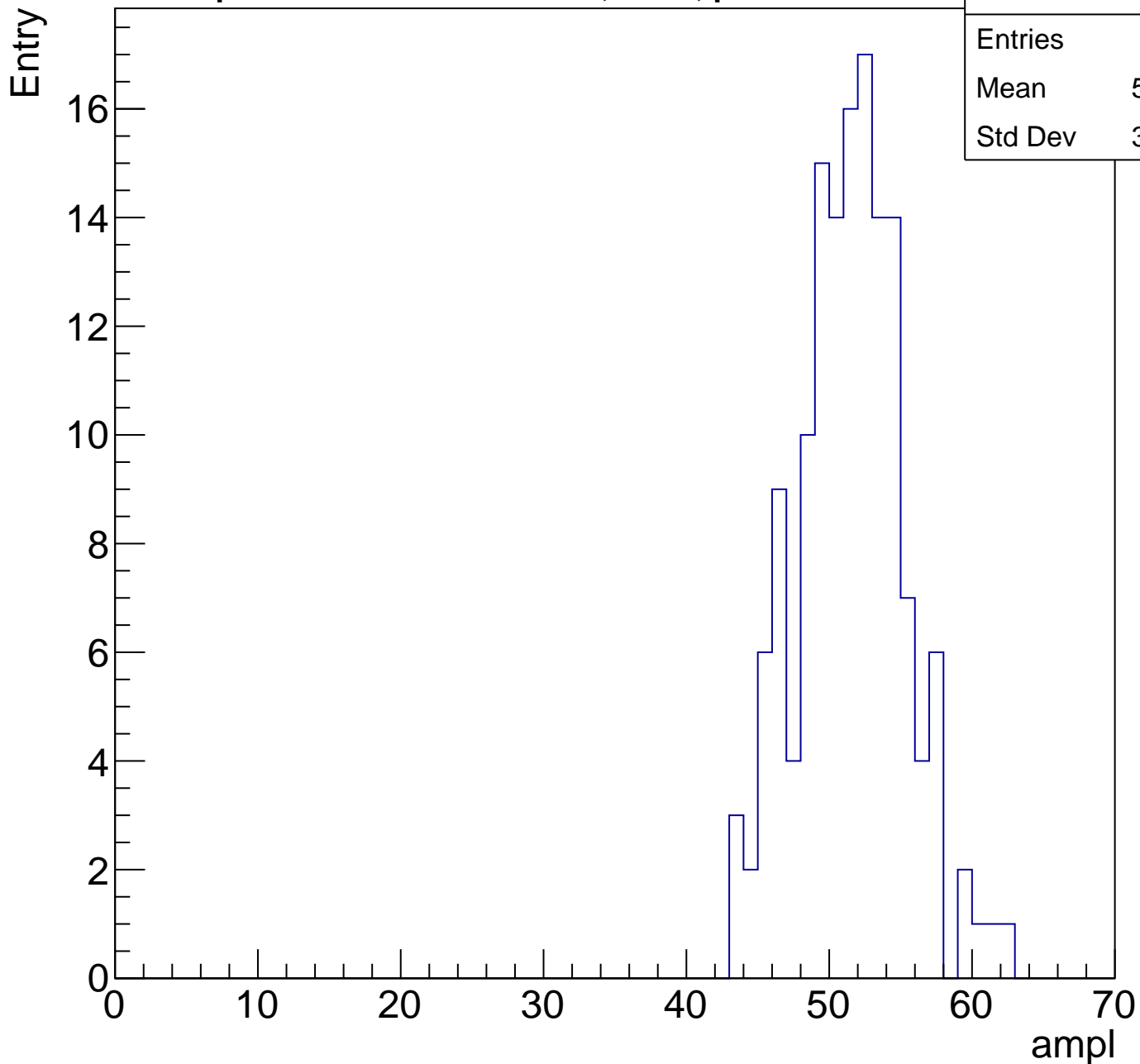
Entry



B1L001S, U20-ch103, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	51.05
Std Dev	3.716



B1L001S, U20-ch103, adc4

calib_packv5_042523_0143.root, FC#2, port C2

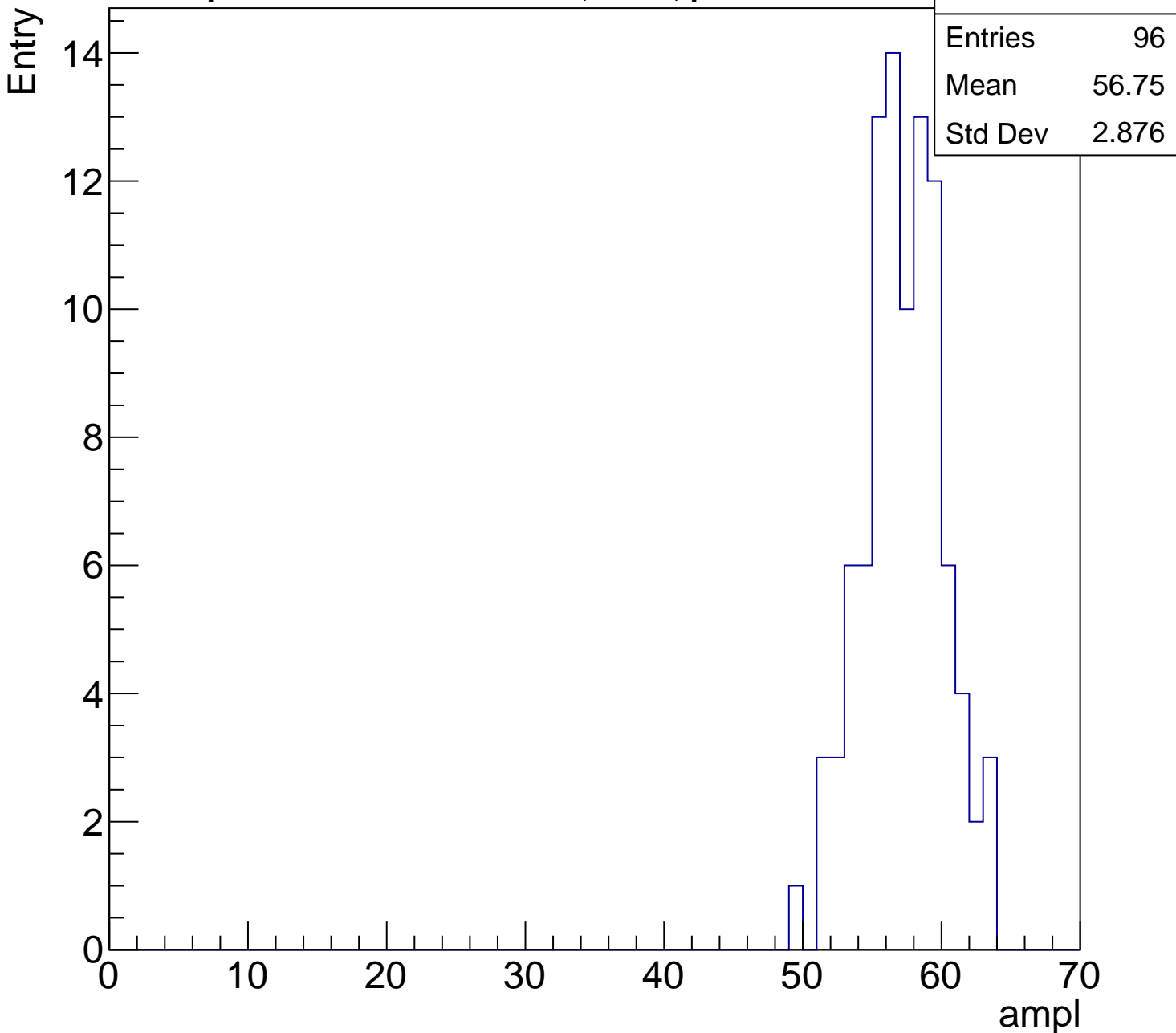
Entries	96
Mean	56.75
Std Dev	2.876

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch103, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	80
Mean	59.94
Std Dev	7.004

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

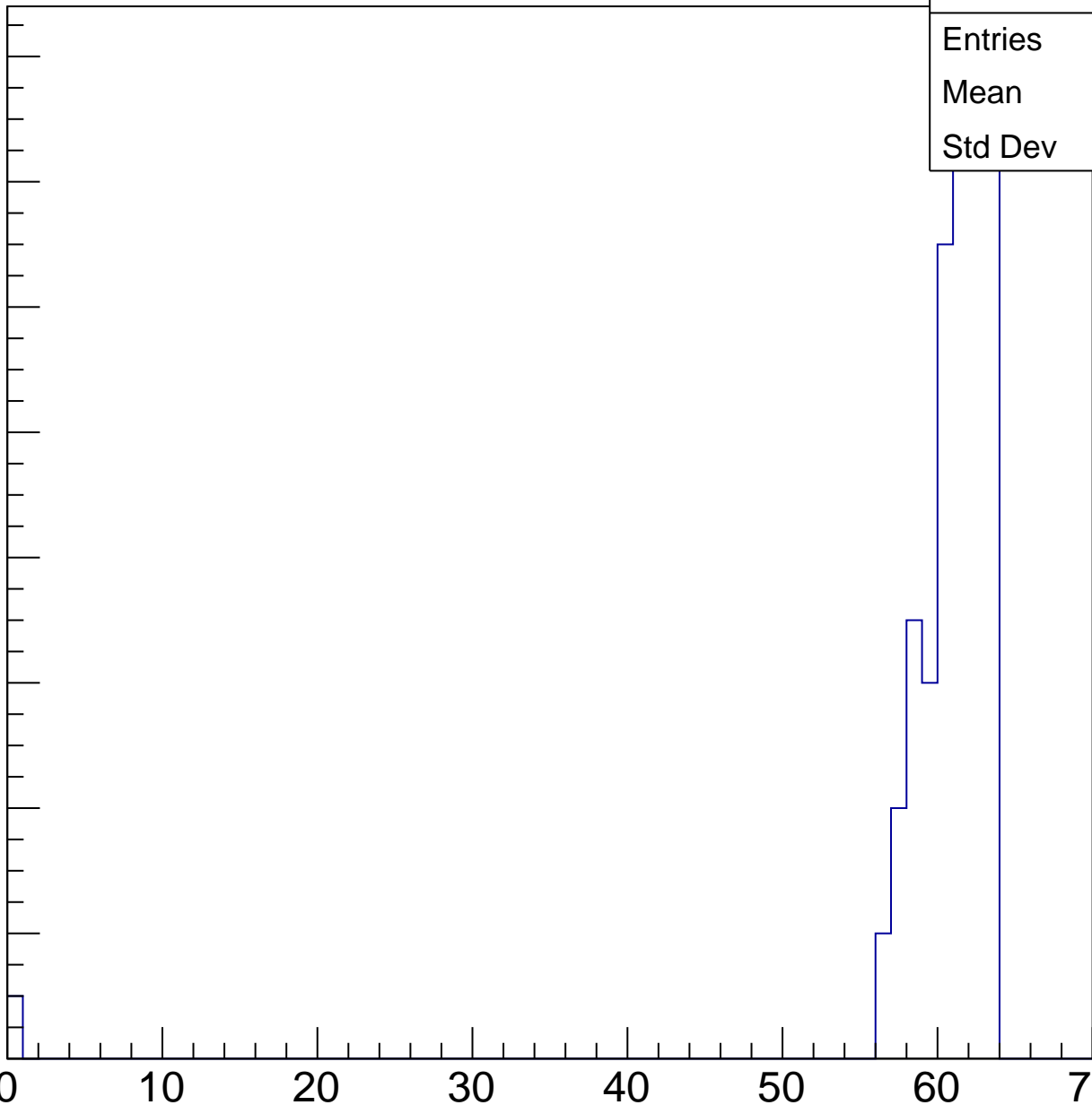
40

50

60

70

ampl



B1L001S, U20-ch103, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch103, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U20-ch104, adc0

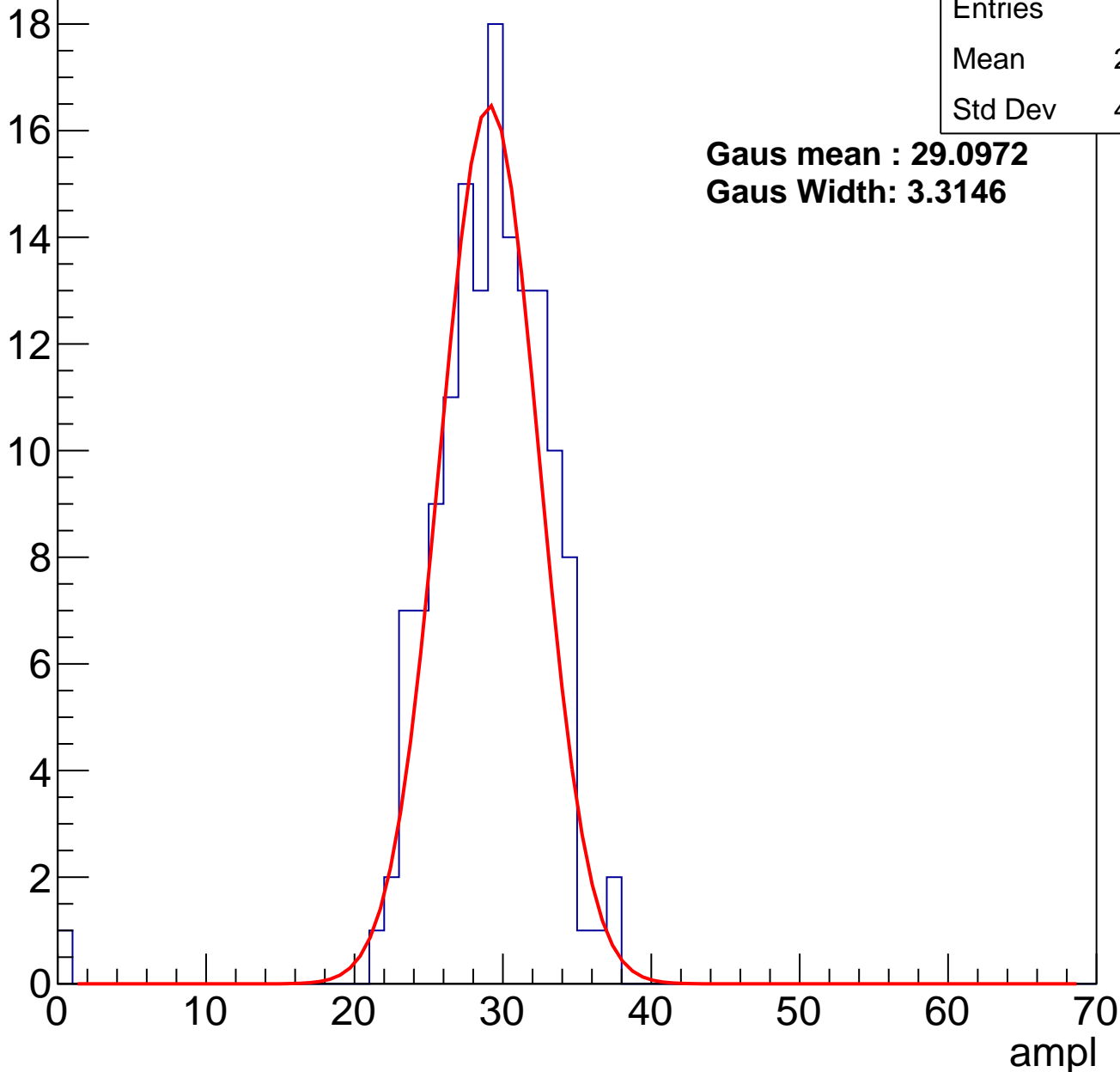
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	28.64
Std Dev	4.124

Gaus mean : 29.0972

Gaus Width: 3.3146

Entry



B1L001S, U20-ch104, adc1

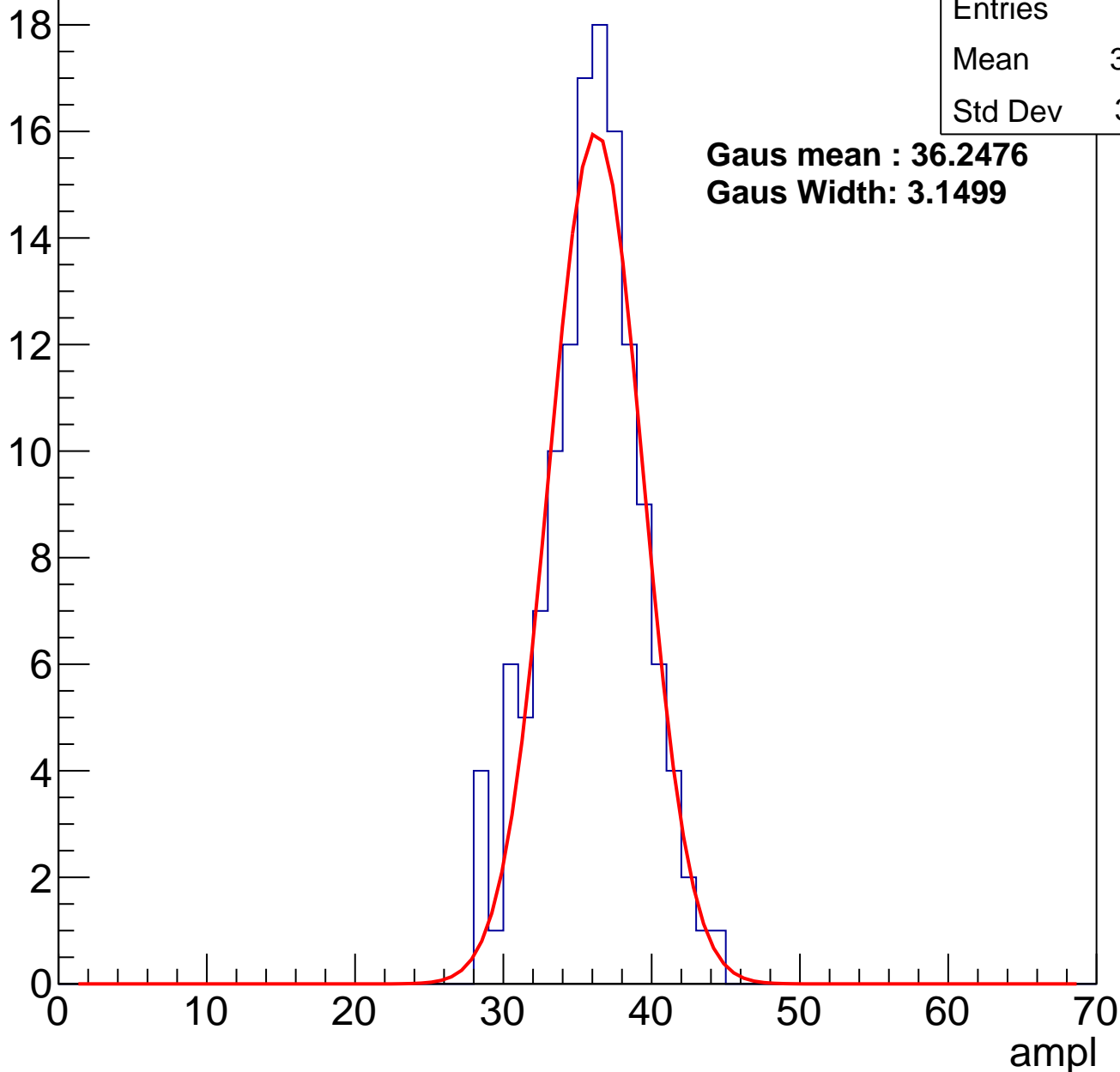
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	35.53
Std Dev	3.281

Gaus mean : 36.2476

Gaus Width: 3.1499

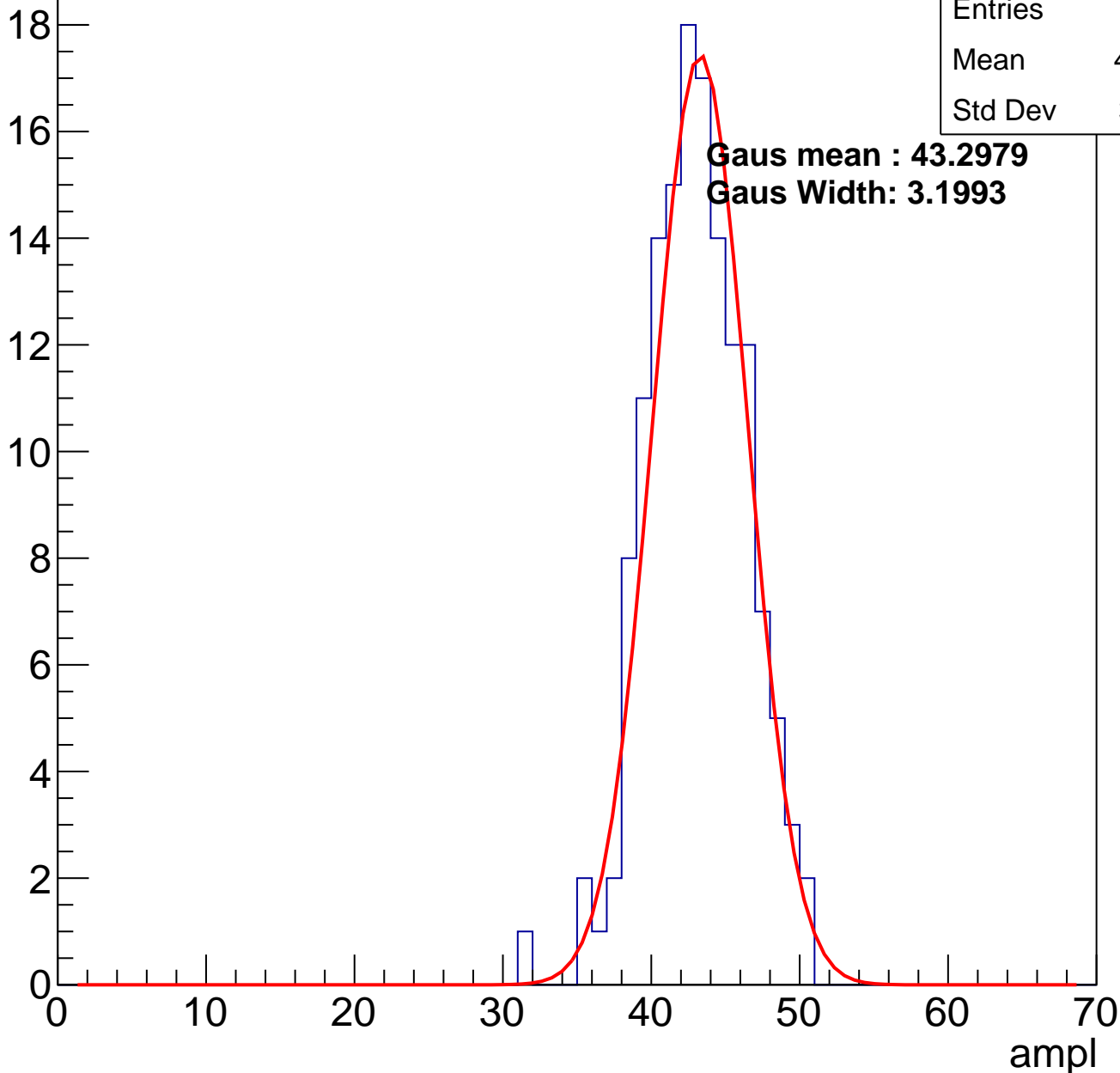
Entry



B1L001S, U20-ch104, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch104, adc3

calib_packv5_042523_0143.root, FC#2, port C2

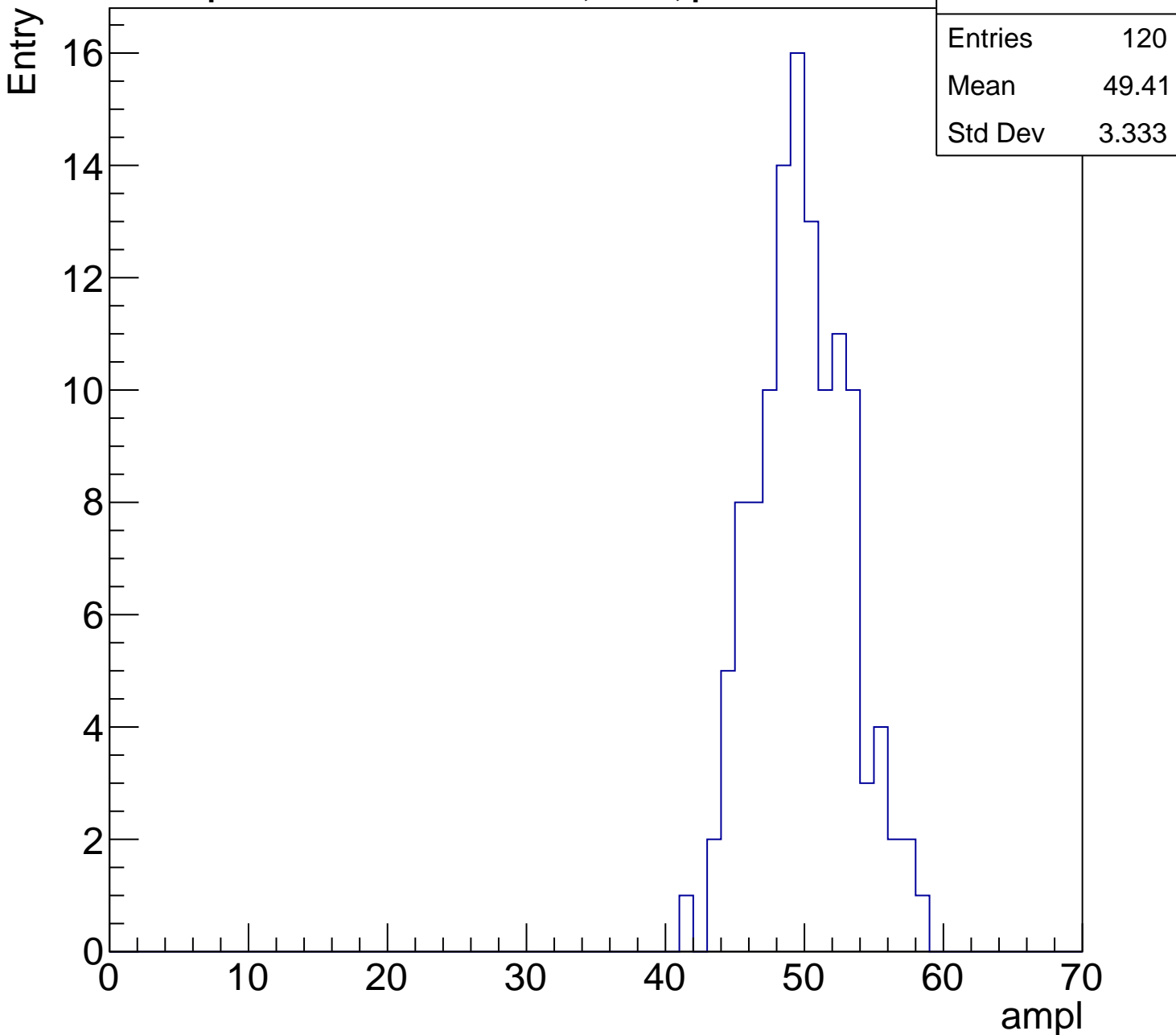
Entries	120
Mean	49.41
Std Dev	3.333

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

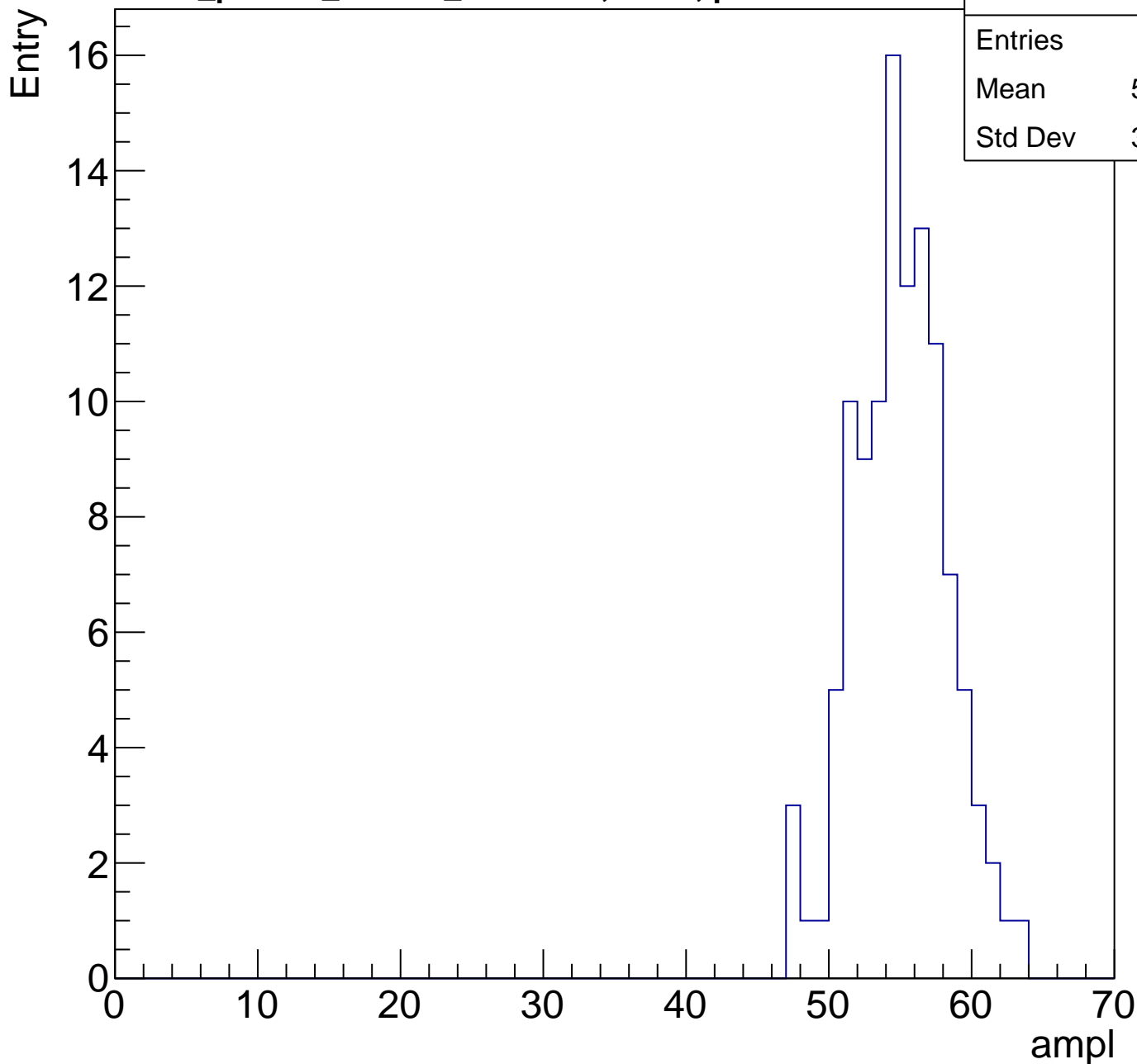
ampl



B1L001S, U20-ch104, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	54.57
Std Dev	3.218



B1L001S, U20-ch104, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

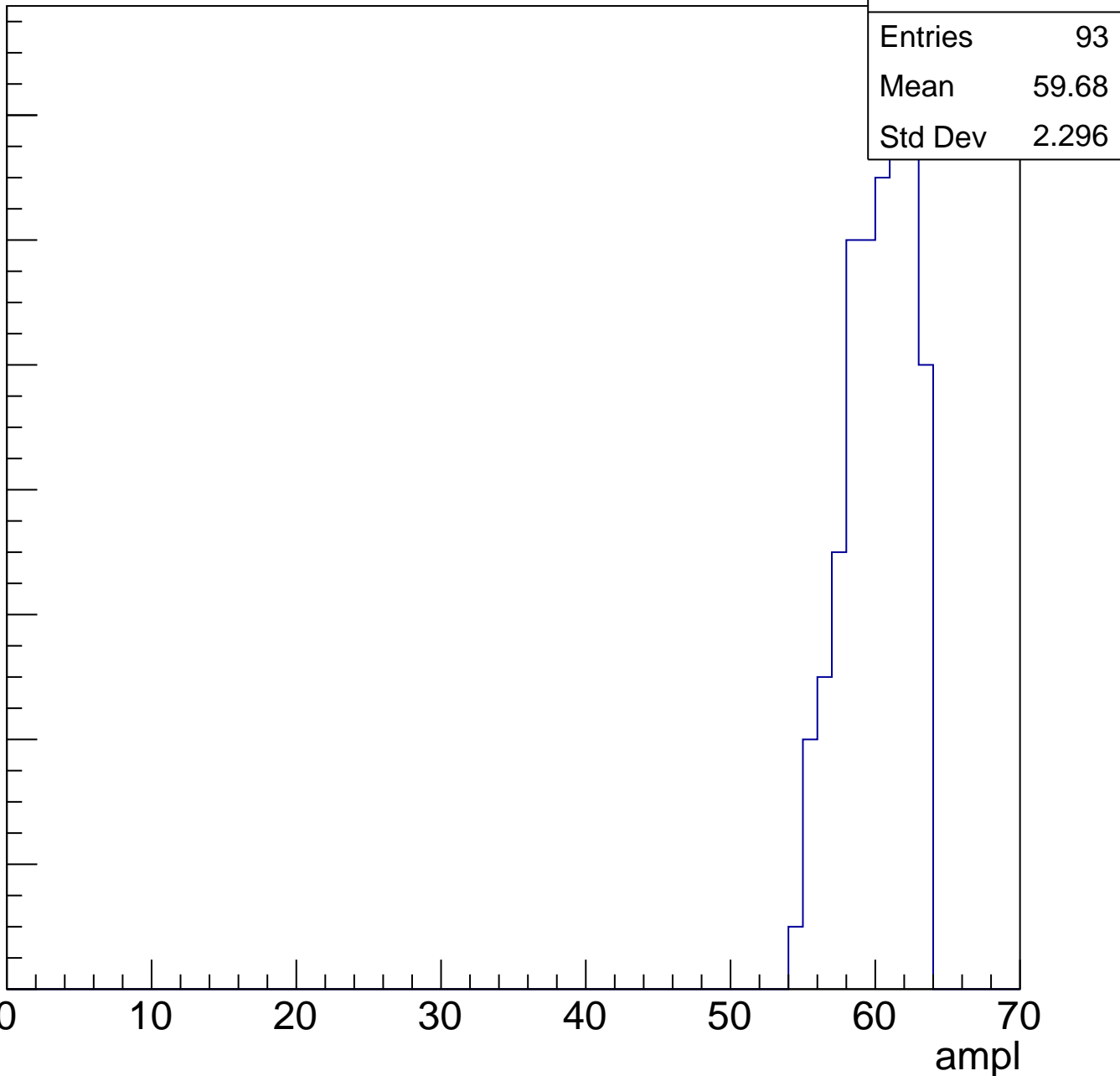
50

60

70

ampl

Entries	93
Mean	59.68
Std Dev	2.296

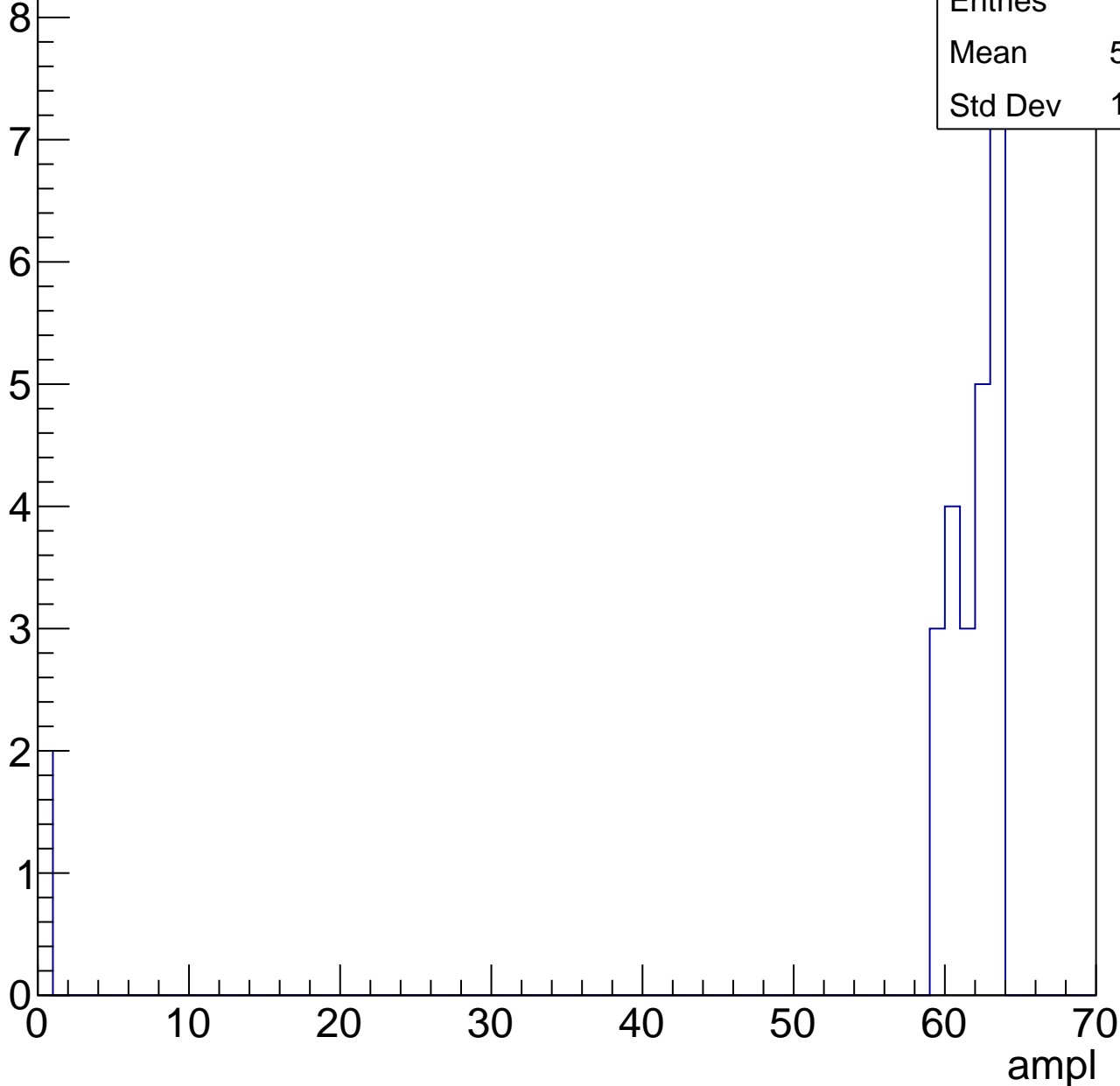


B1L001S, U20-ch104, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

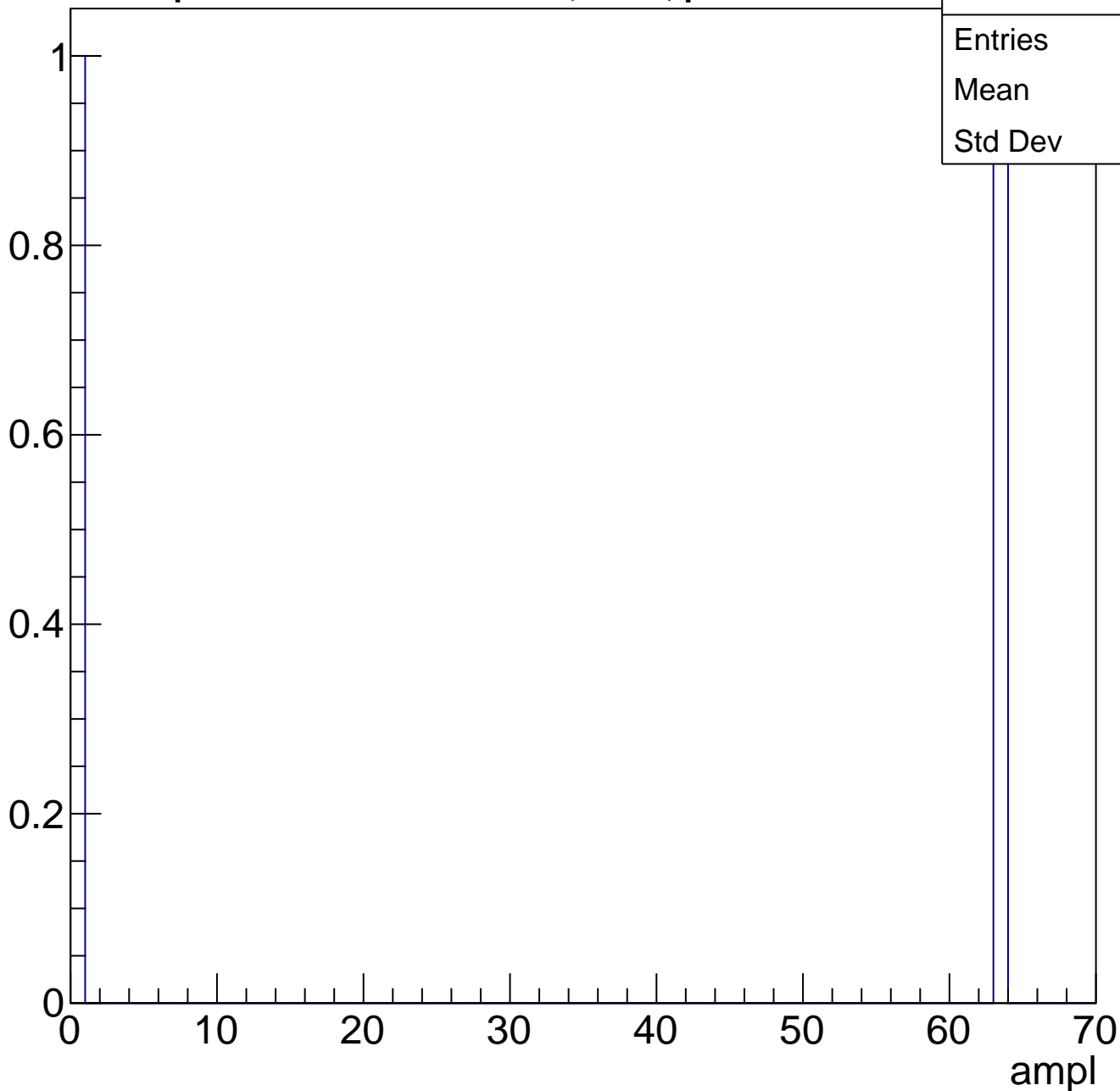
Entries	25
Mean	56.56
Std Dev	16.74



B1L001S, U20-ch104, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch105, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	29.47
Std Dev	3.475

Gaus mean : 29.9926

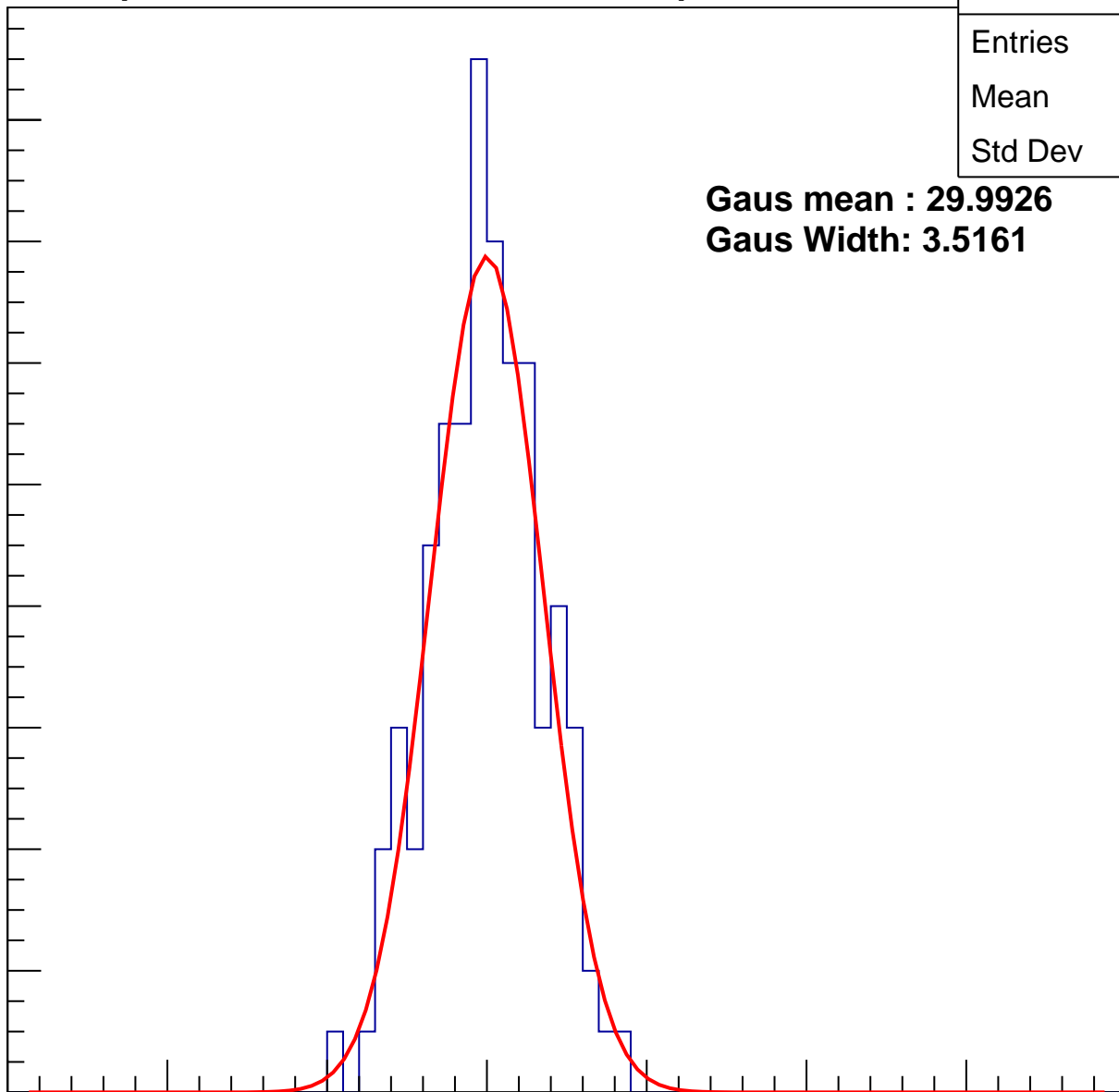
Gaus Width: 3.5161

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch105, adc1

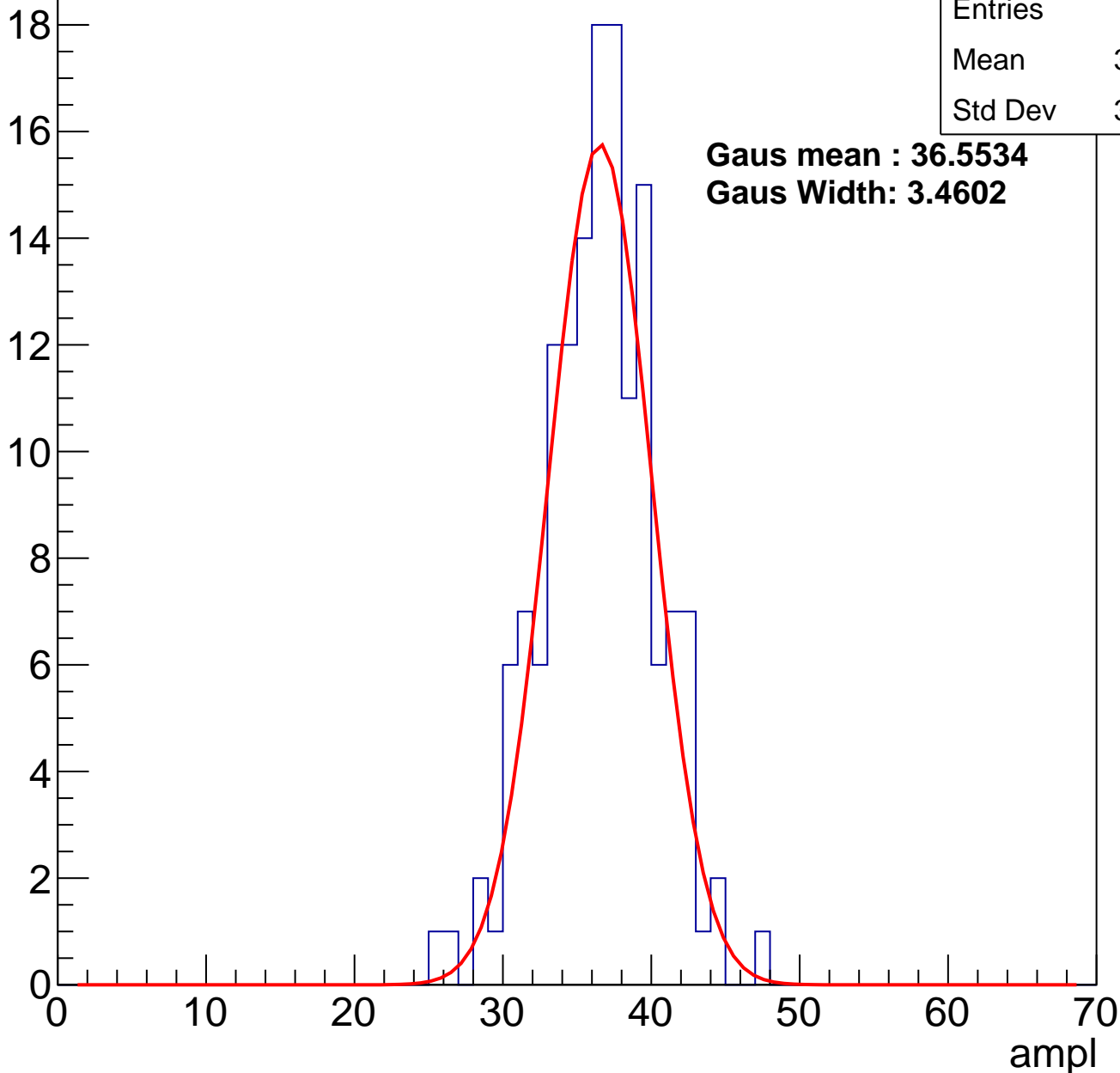
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	36.05
Std Dev	3.742

Gaus mean : 36.5534

Gaus Width: 3.4602

Entry



B1L001S, U20-ch105, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

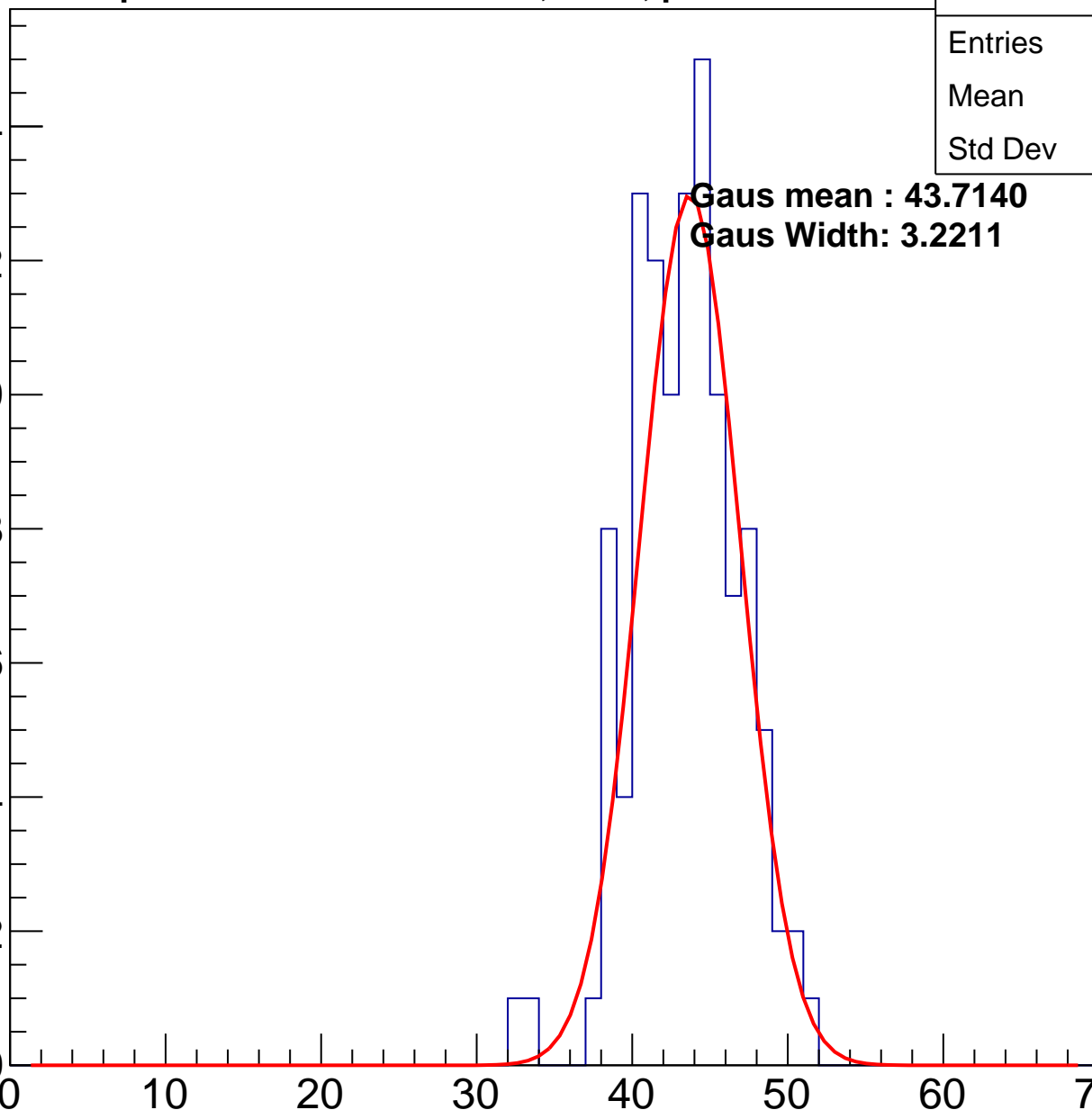
70

ampl

Entries	113
Mean	42.92
Std Dev	3.392

Gaus mean : 43.7140

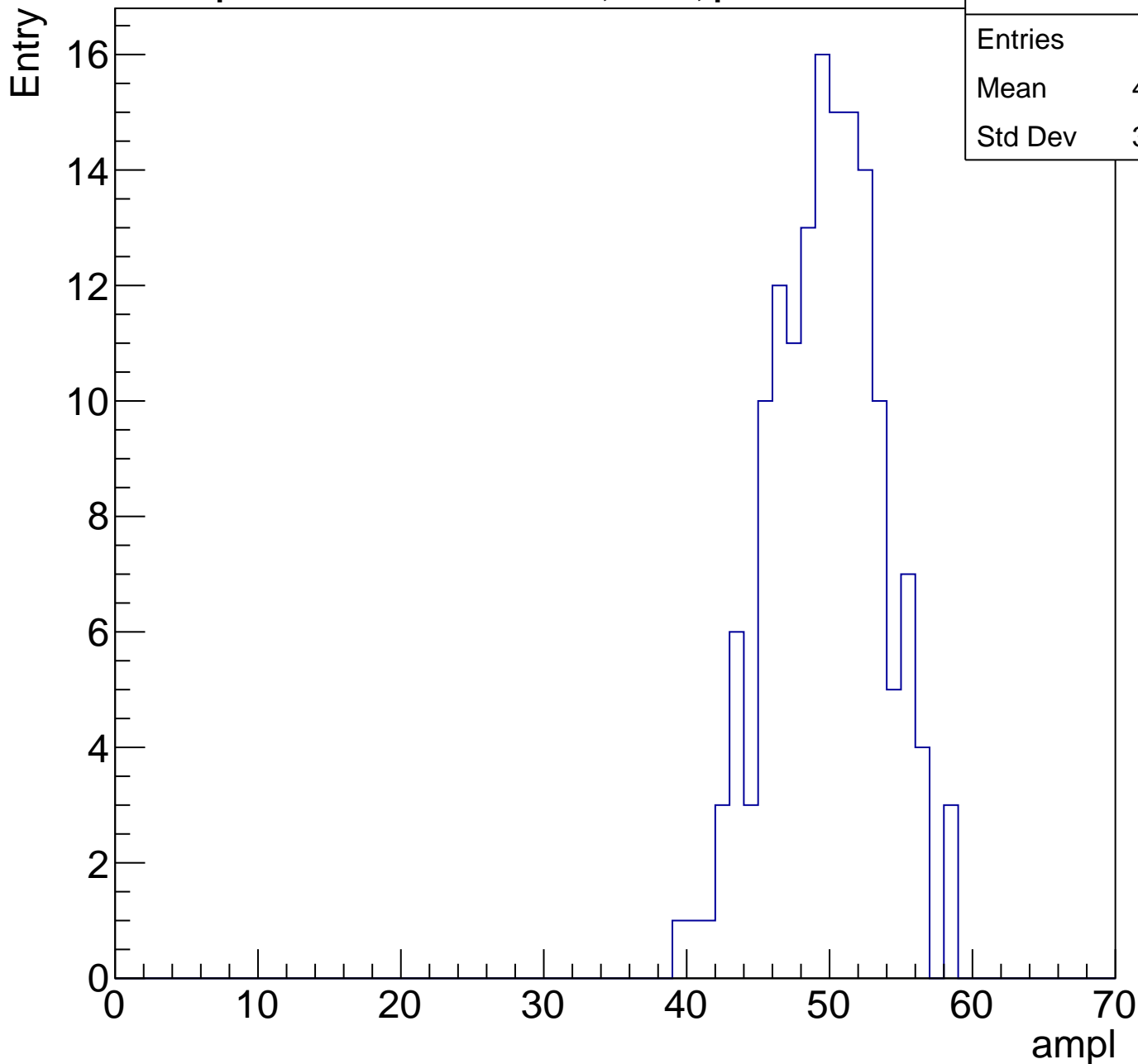
Gaus Width: 3.2211



B1L001S, U20-ch105, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	49.26
Std Dev	3.808



B1L001S, U20-ch105, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

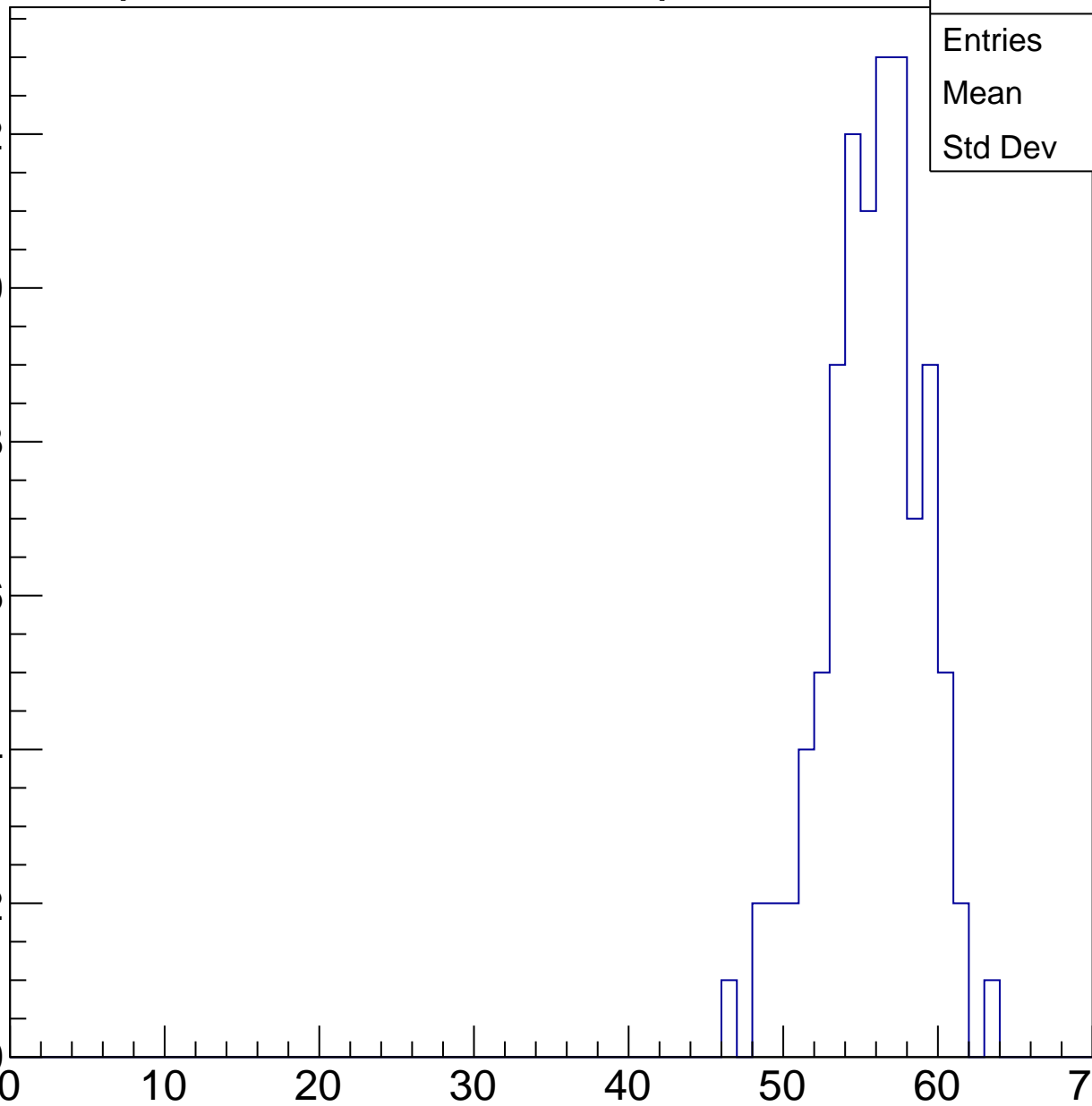
40

50

60

ampl

Entries	98
Mean	55.36
Std Dev	3.16



B1L001S, U20-ch105, adc5

calib_packv5_042523_0143.root, FC#2, port C2

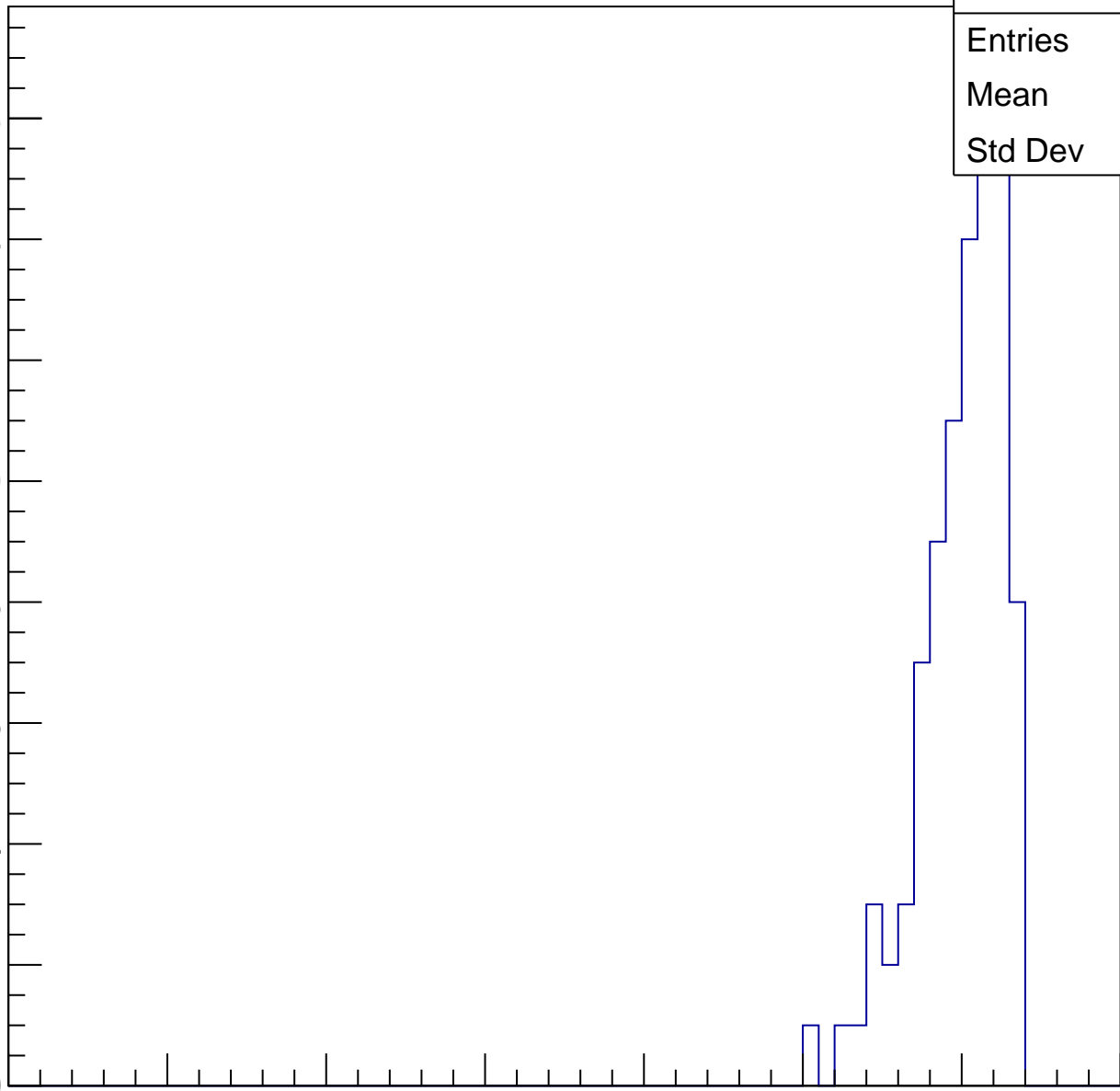
Entries	94
Mean	59.57
Std Dev	2.668

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch105, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	19
Mean	55.68
Std Dev	19.14

B1L001S, U20-ch105, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch106, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	29.22
Std Dev	3.163

Gaus mean : 29.7420

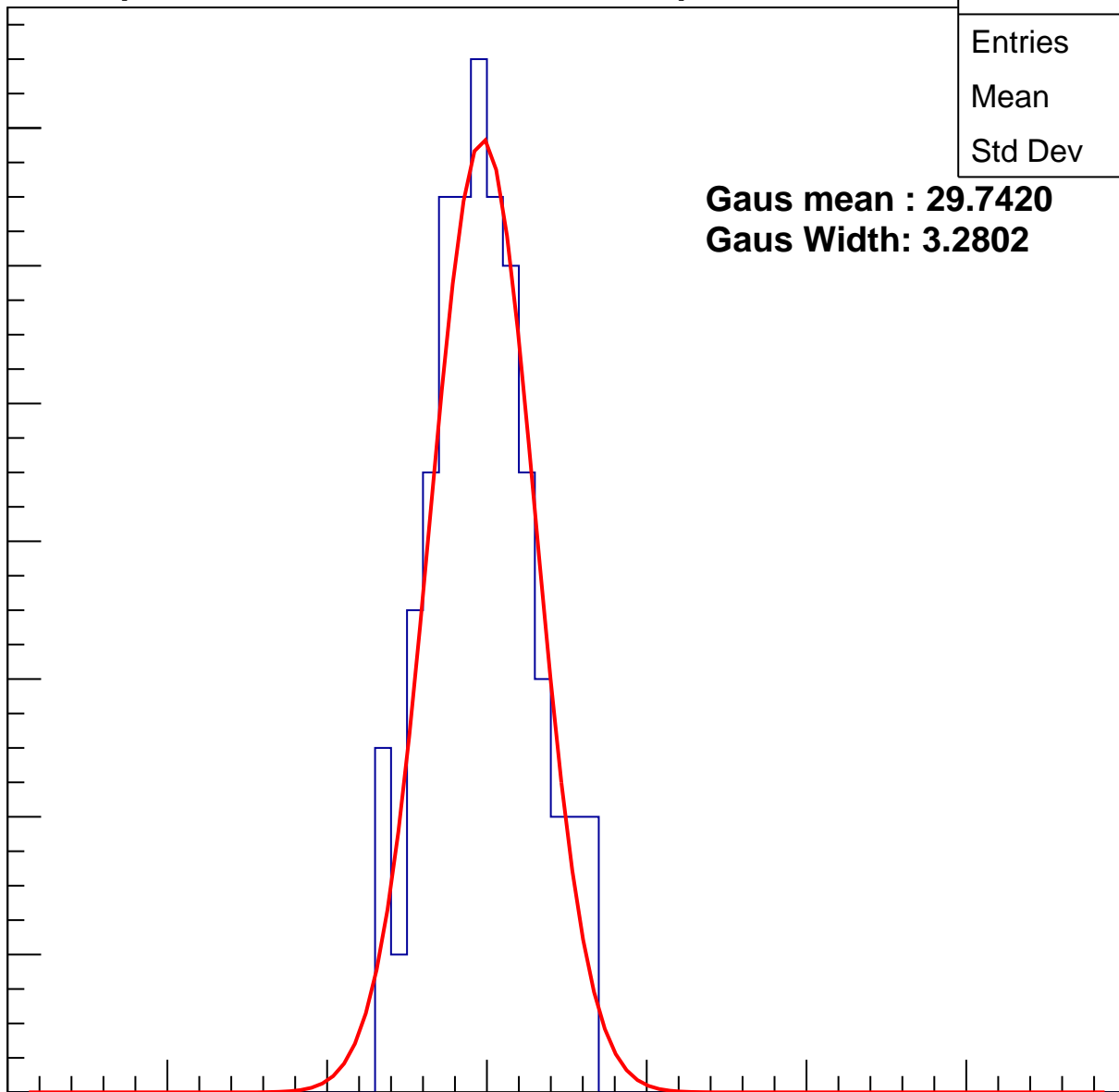
Gaus Width: 3.2802

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



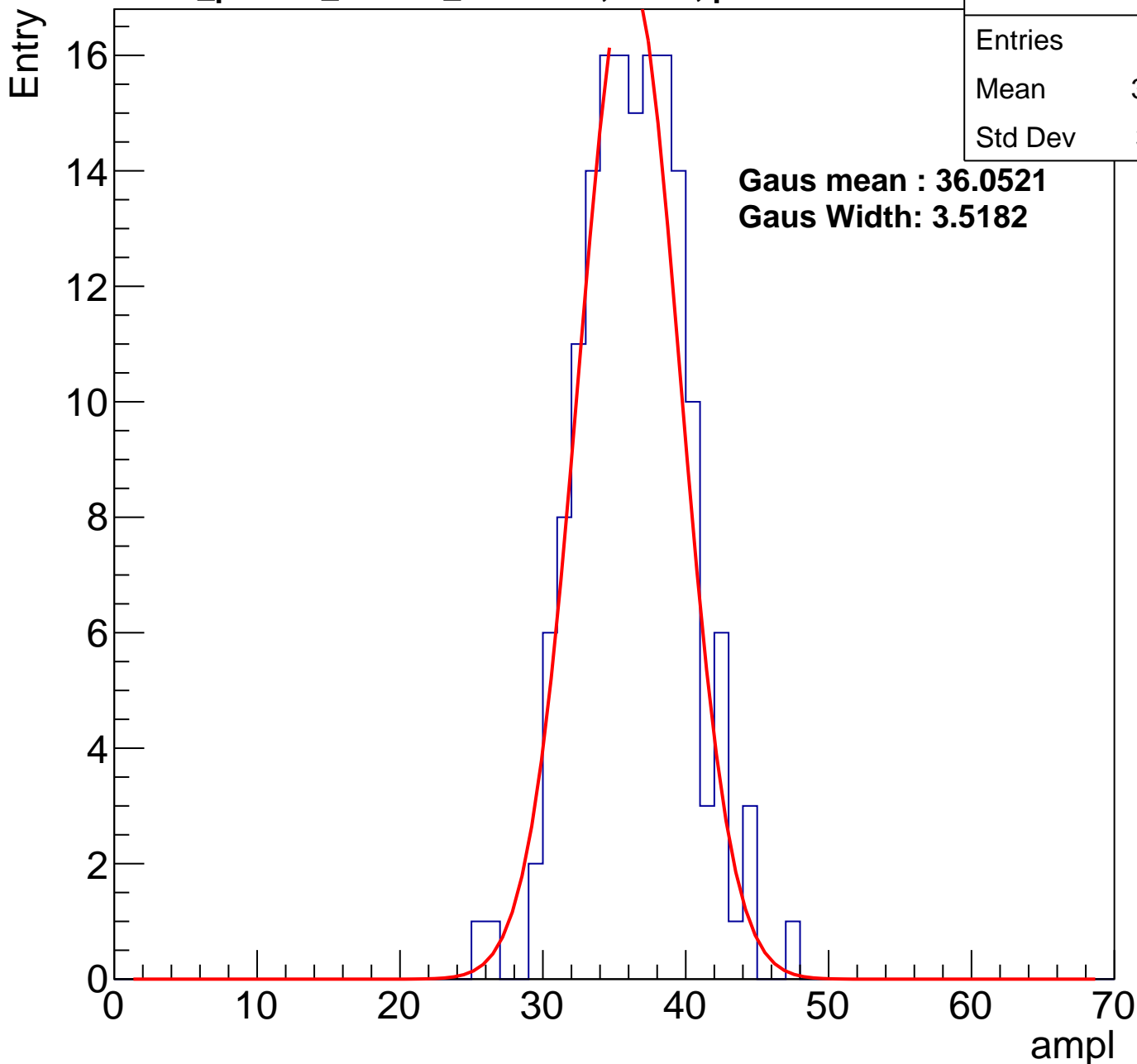
B1L001S, U20-ch106, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	35.86
Std Dev	3.651

Gaus mean : 36.0521

Gaus Width: 3.5182



B1L001S, U20-ch106, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

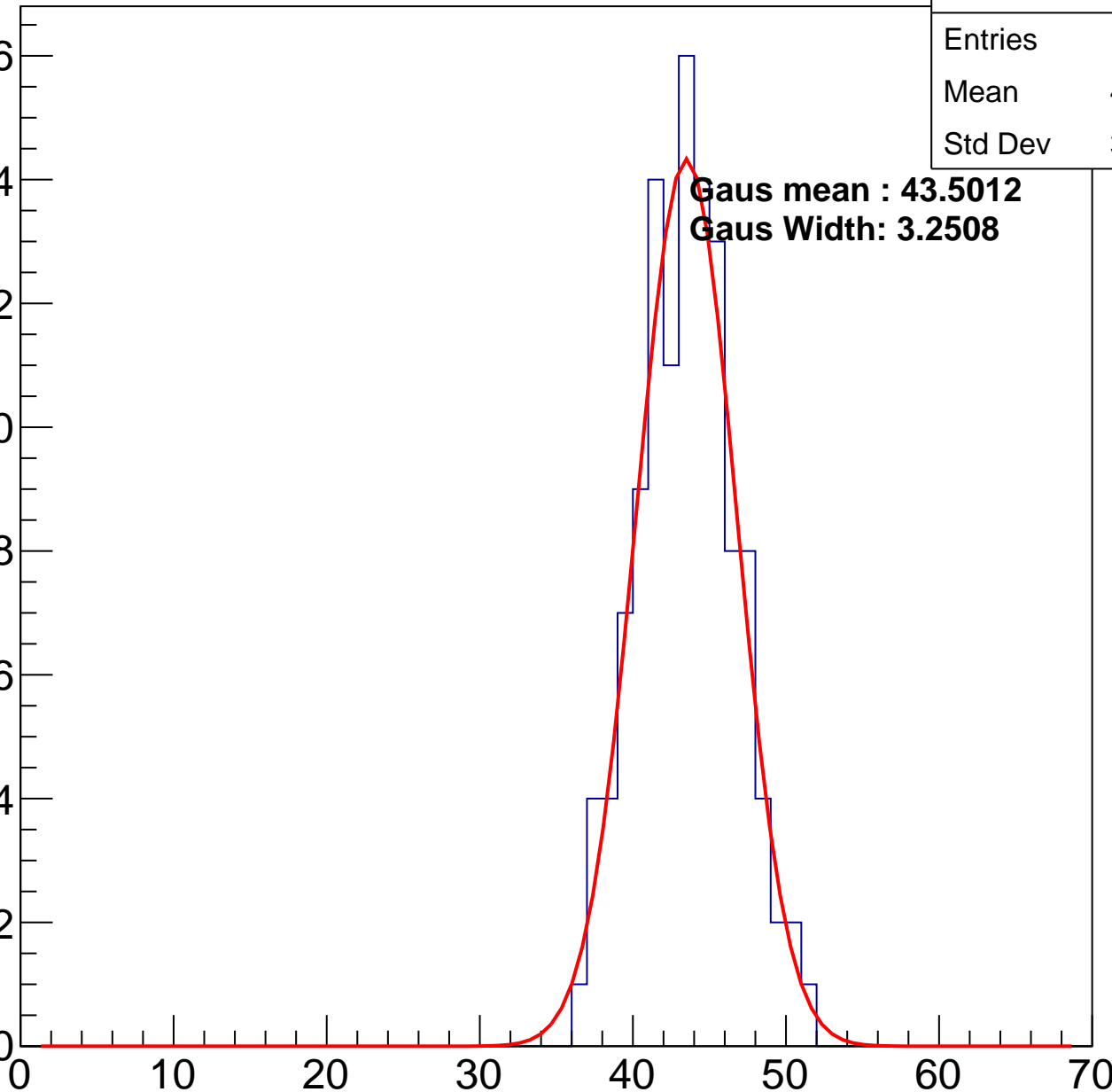
16
14
12
10
8
6
4
2
0

Entries	118
Mean	43.04
Std Dev	3.126

Gaus mean : 43.5012

Gaus Width: 3.2508

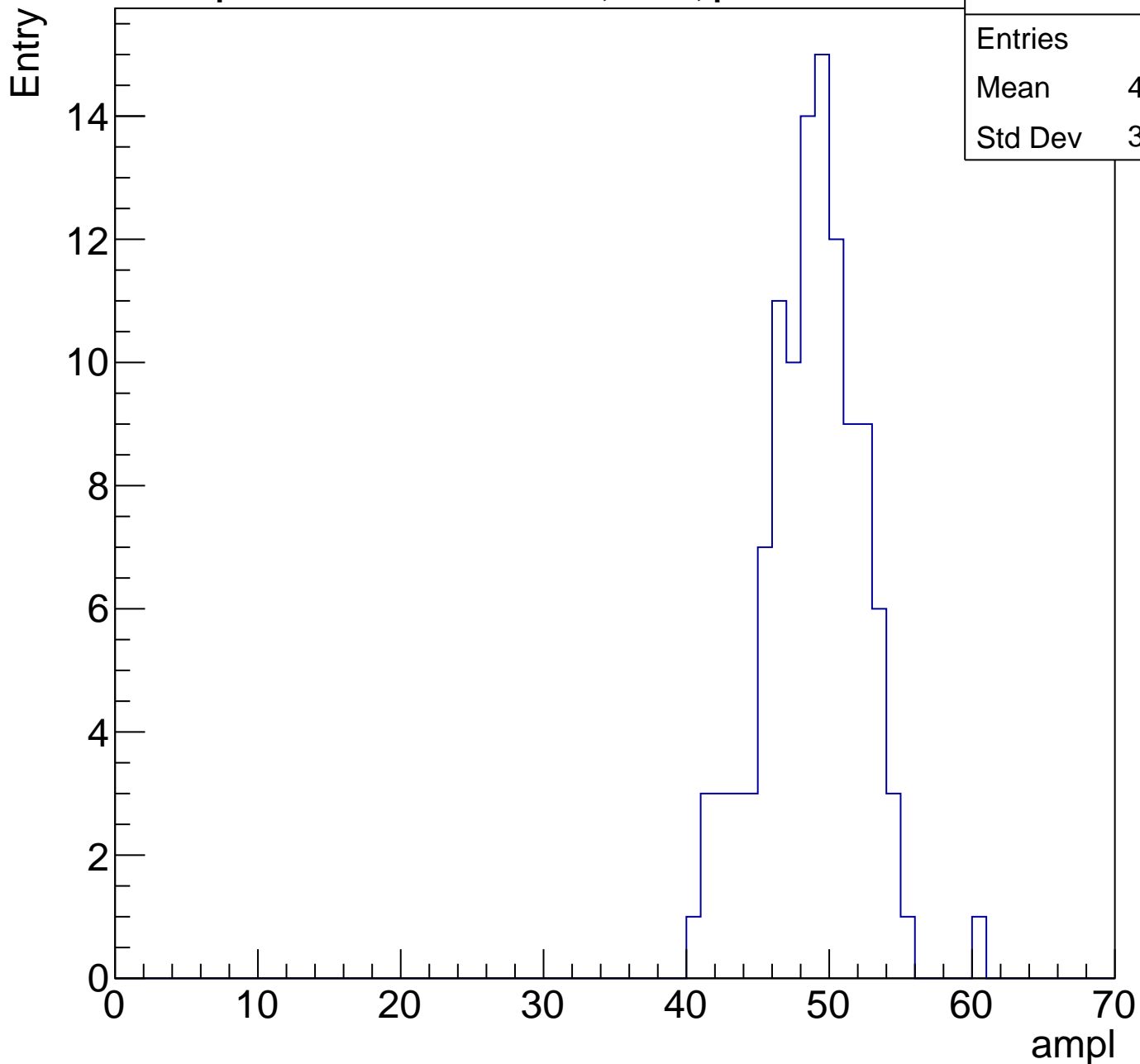
ampl



B1L001S, U20-ch106, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	48.38
Std Dev	3.399



B1L001S, U20-ch106, adc4

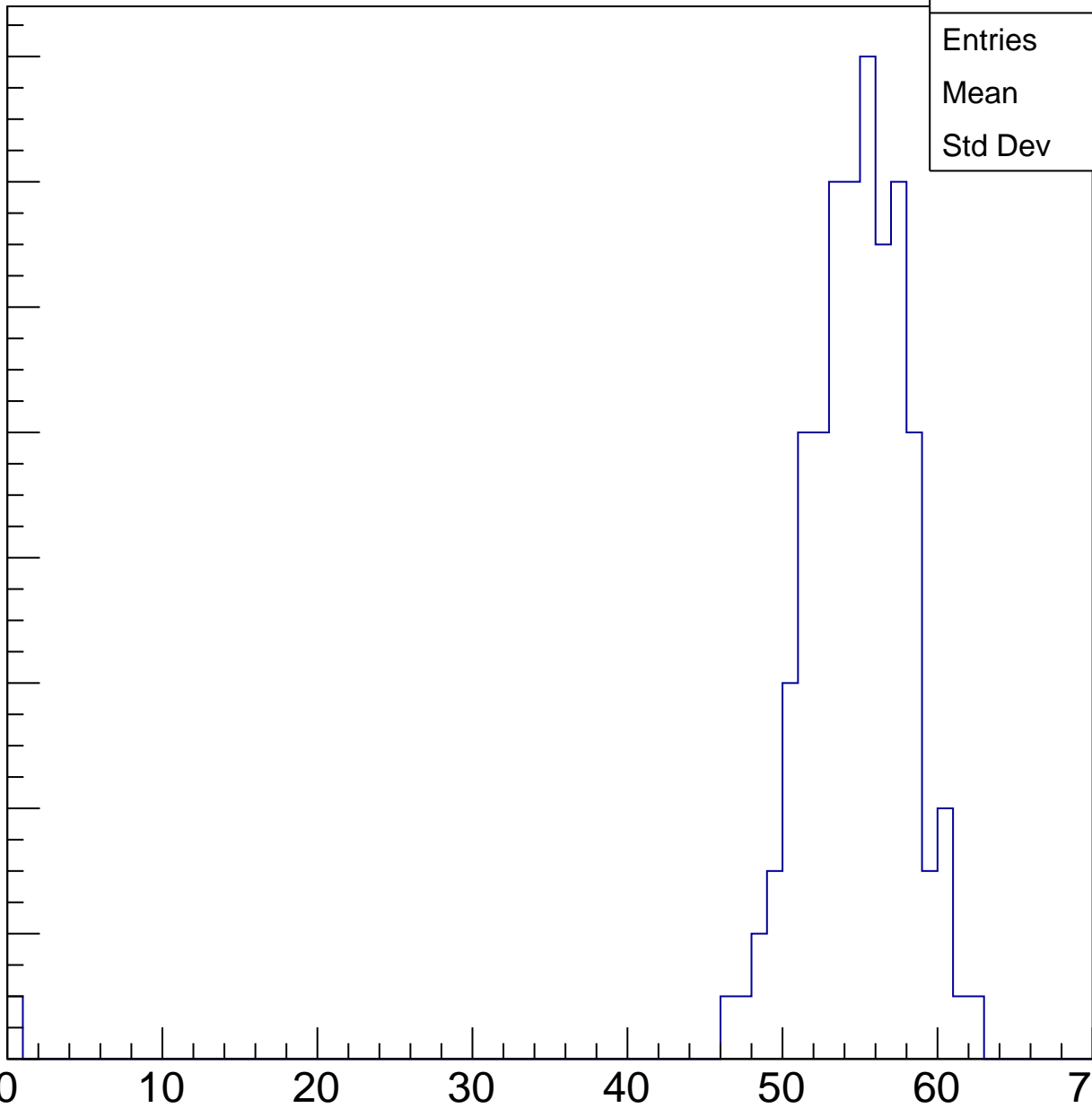
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	53.95
Std Dev	5.751

ampl



B1L001S, U20-ch106, adc5

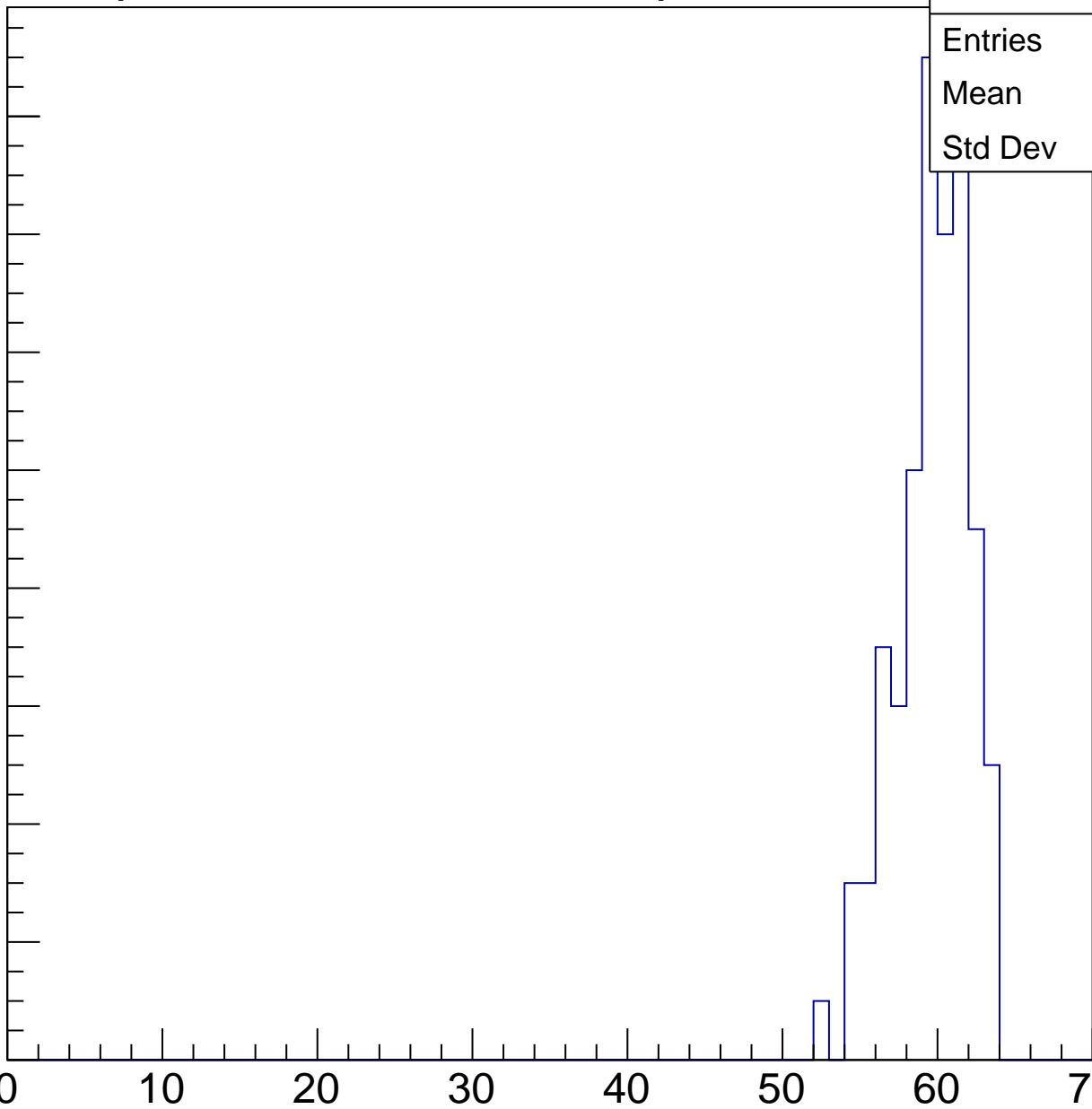
calib_packv5_042523_0143.root, FC#2, port C2

Entries	92
Mean	59.2
Std Dev	2.351

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch106, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

26

Mean

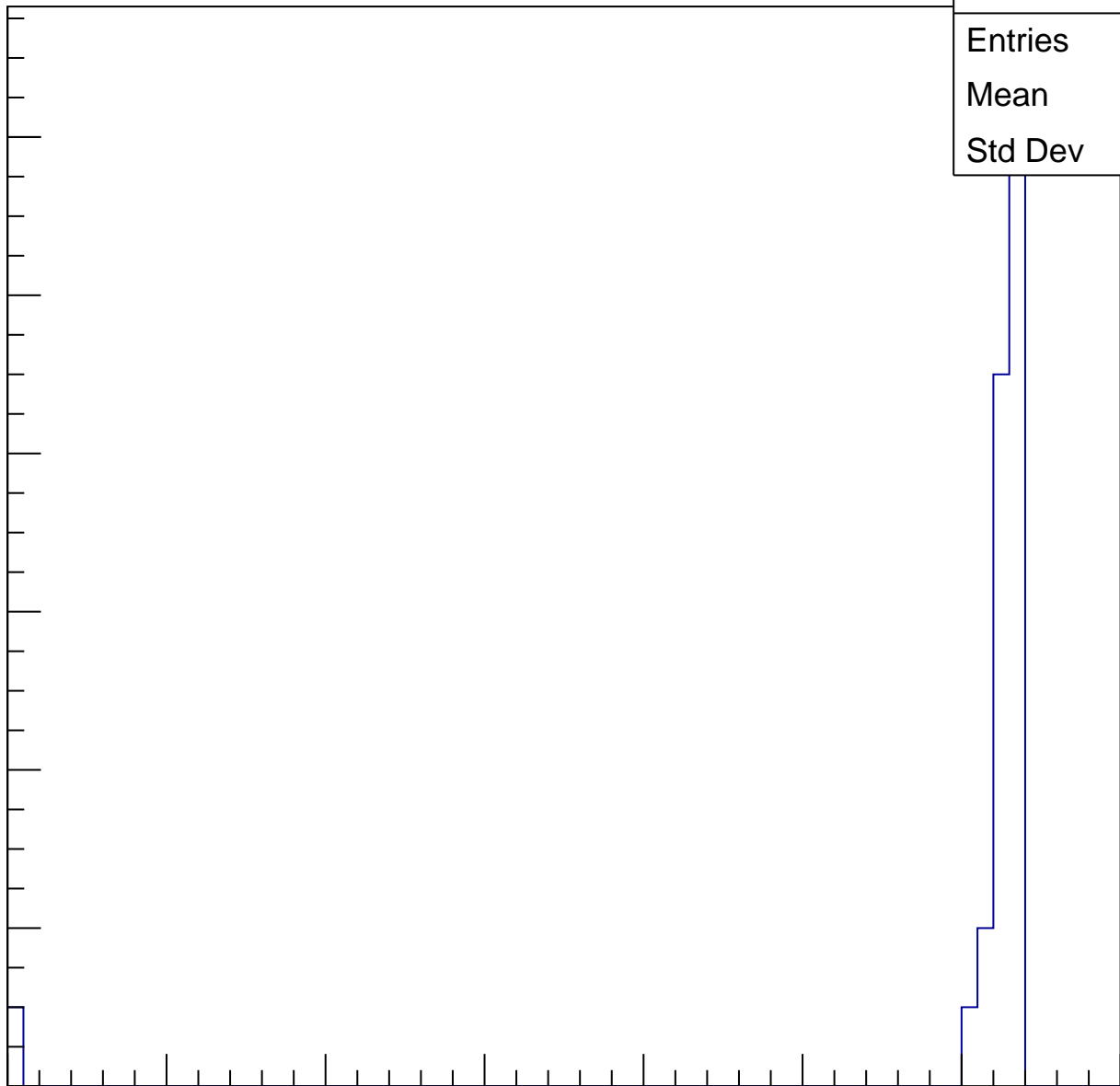
59.96

Std Dev

12.02

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch106, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch107, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	29.48
Std Dev	3.314

Gaus mean : 29.7488

Gaus Width: 3.6020

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

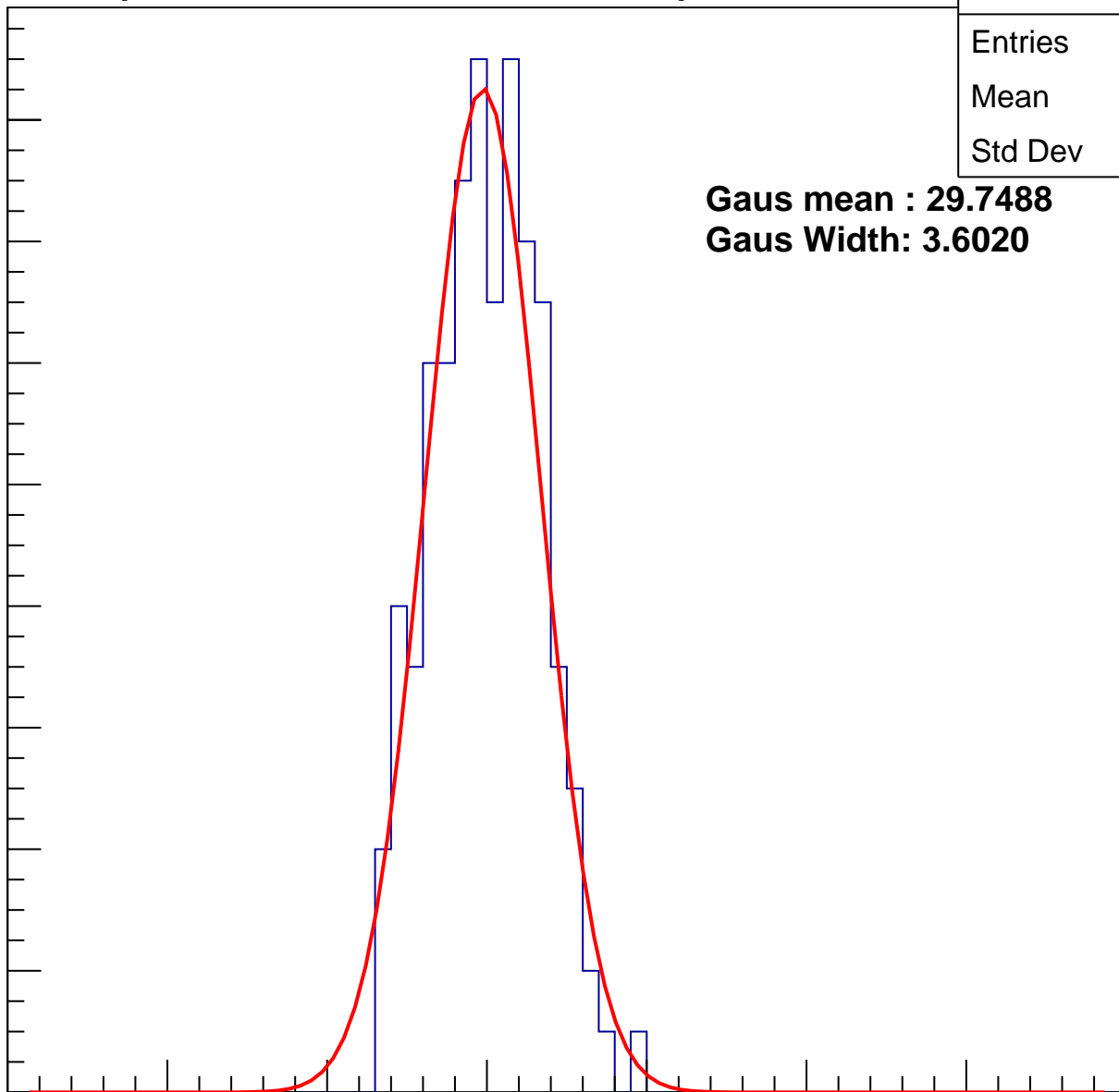
40

50

60

70

ampl



B1L001S, U20-ch107, adc1

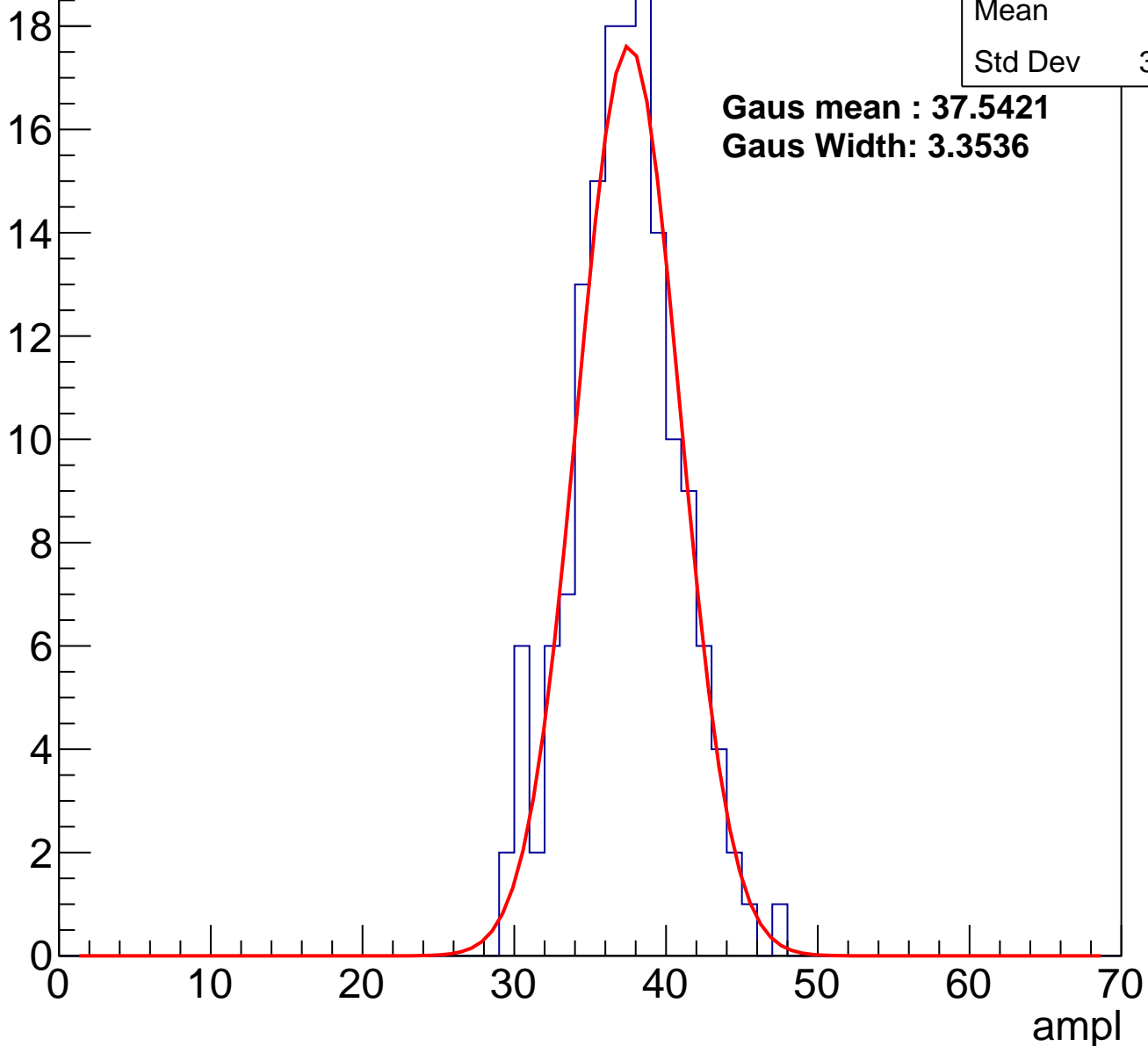
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	36.9
Std Dev	3.444

Gaus mean : 37.5421

Gaus Width: 3.3536

Entry



B1L001S, U20-ch107, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

138

Mean

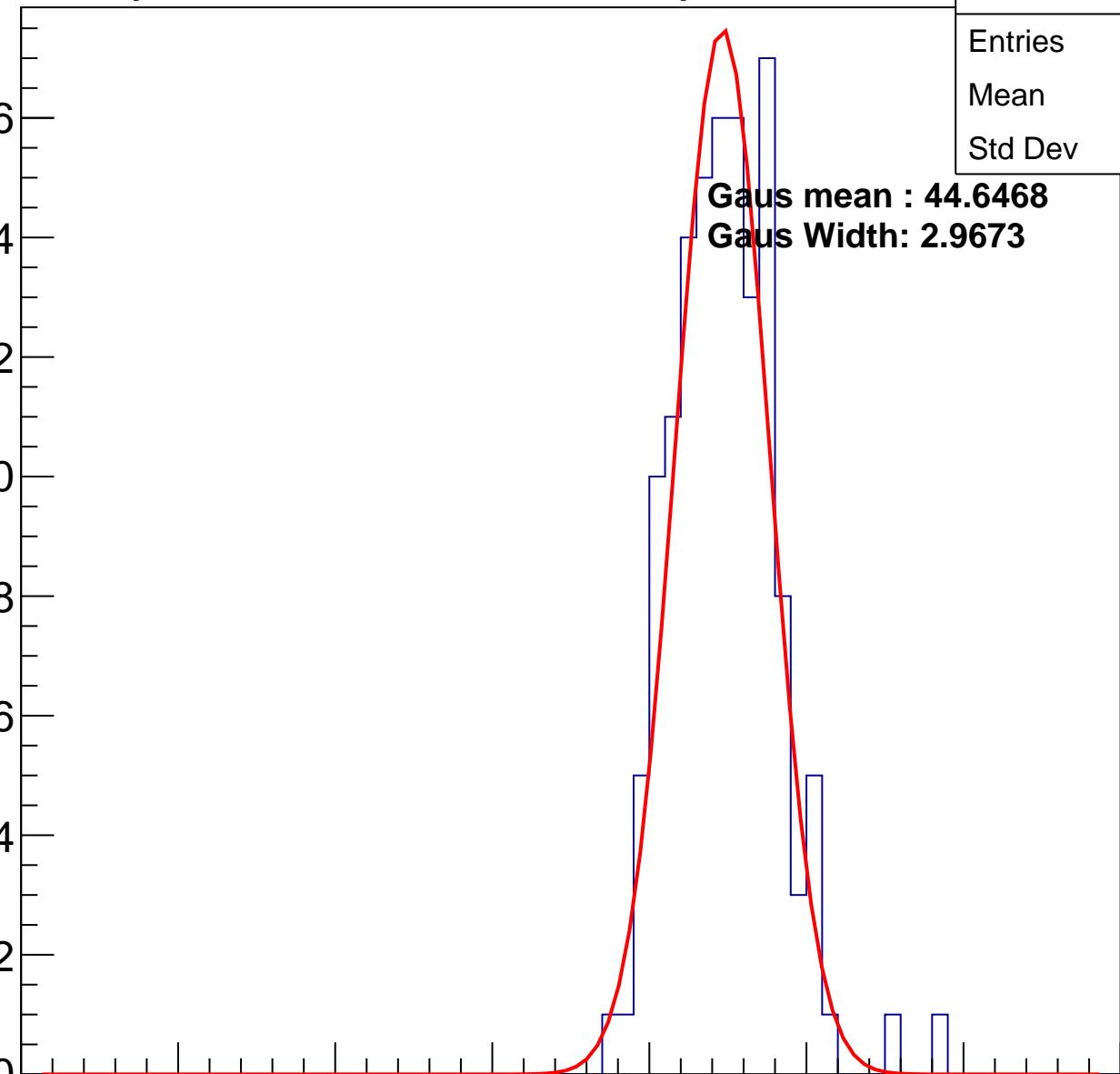
44.35

Std Dev

3.29

Gaus mean : 44.6468

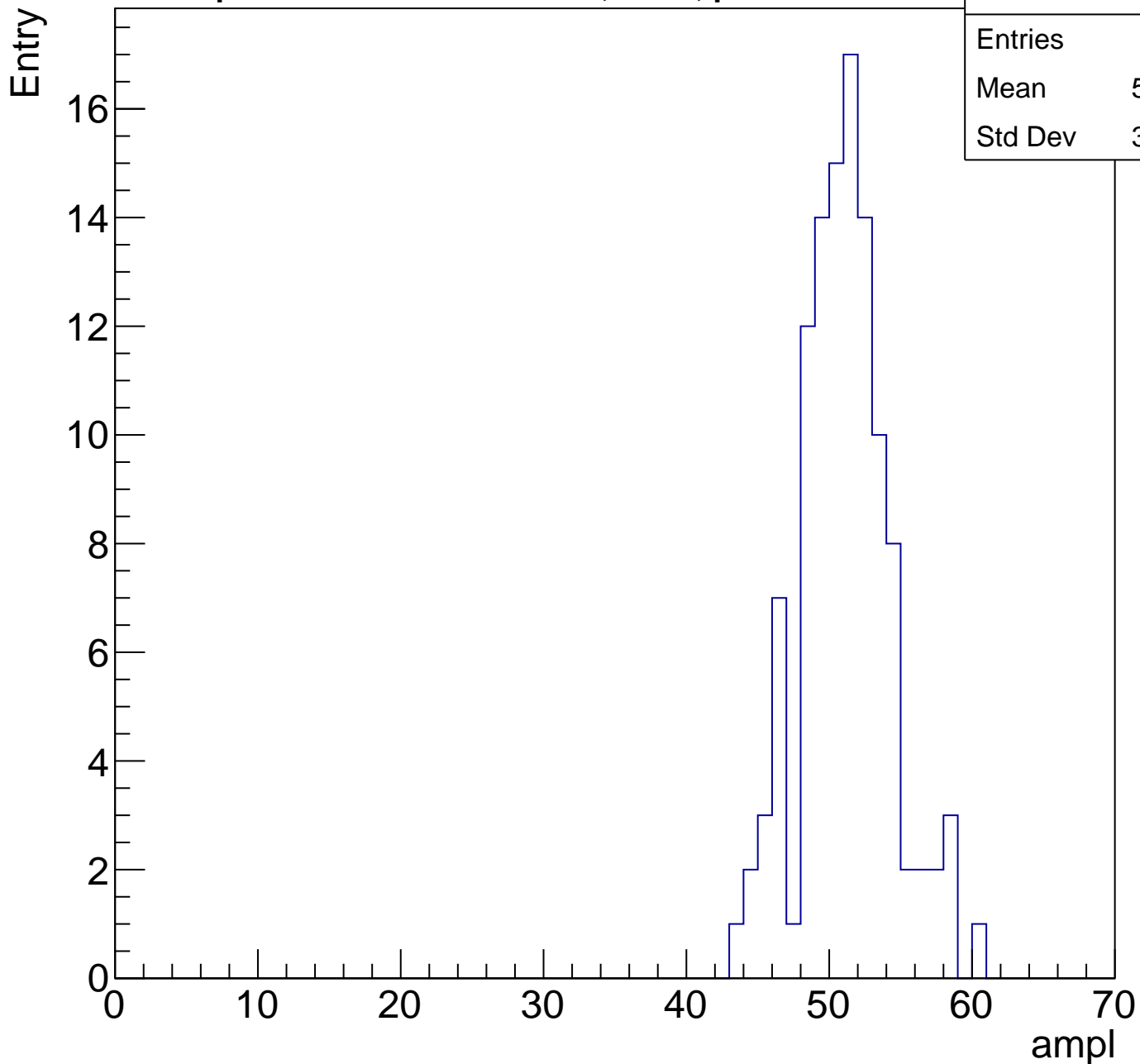
Gaus Width: 2.9673



B1L001S, U20-ch107, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	50.65
Std Dev	3.154



B1L001S, U20-ch107, adc4

calib_packv5_042523_0143.root, FC#2, port C2

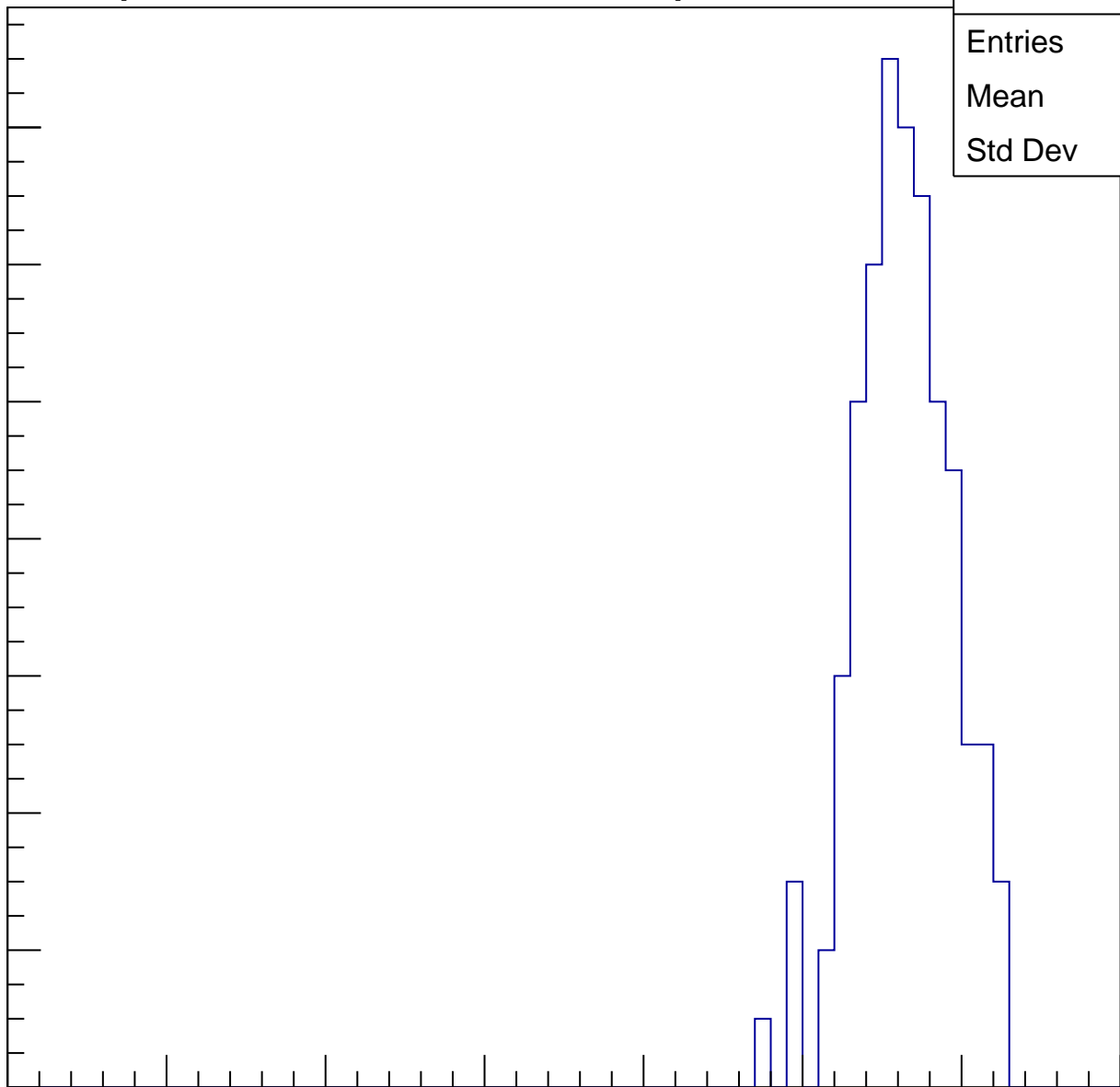
Entries	108
Mean	55.91
Std Dev	3.002

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch107, adc5

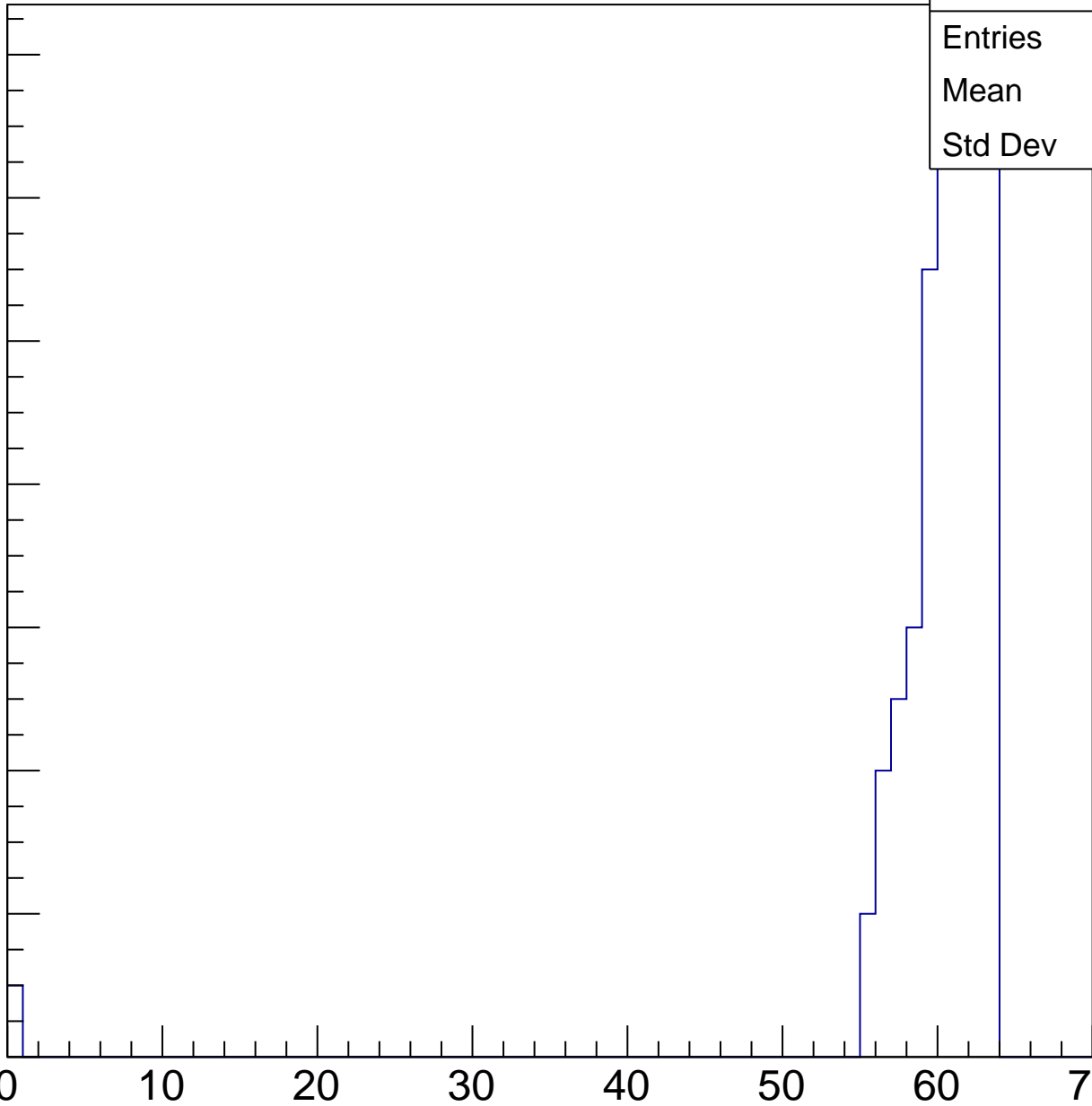
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	84
Mean	59.52
Std Dev	6.876

ampl

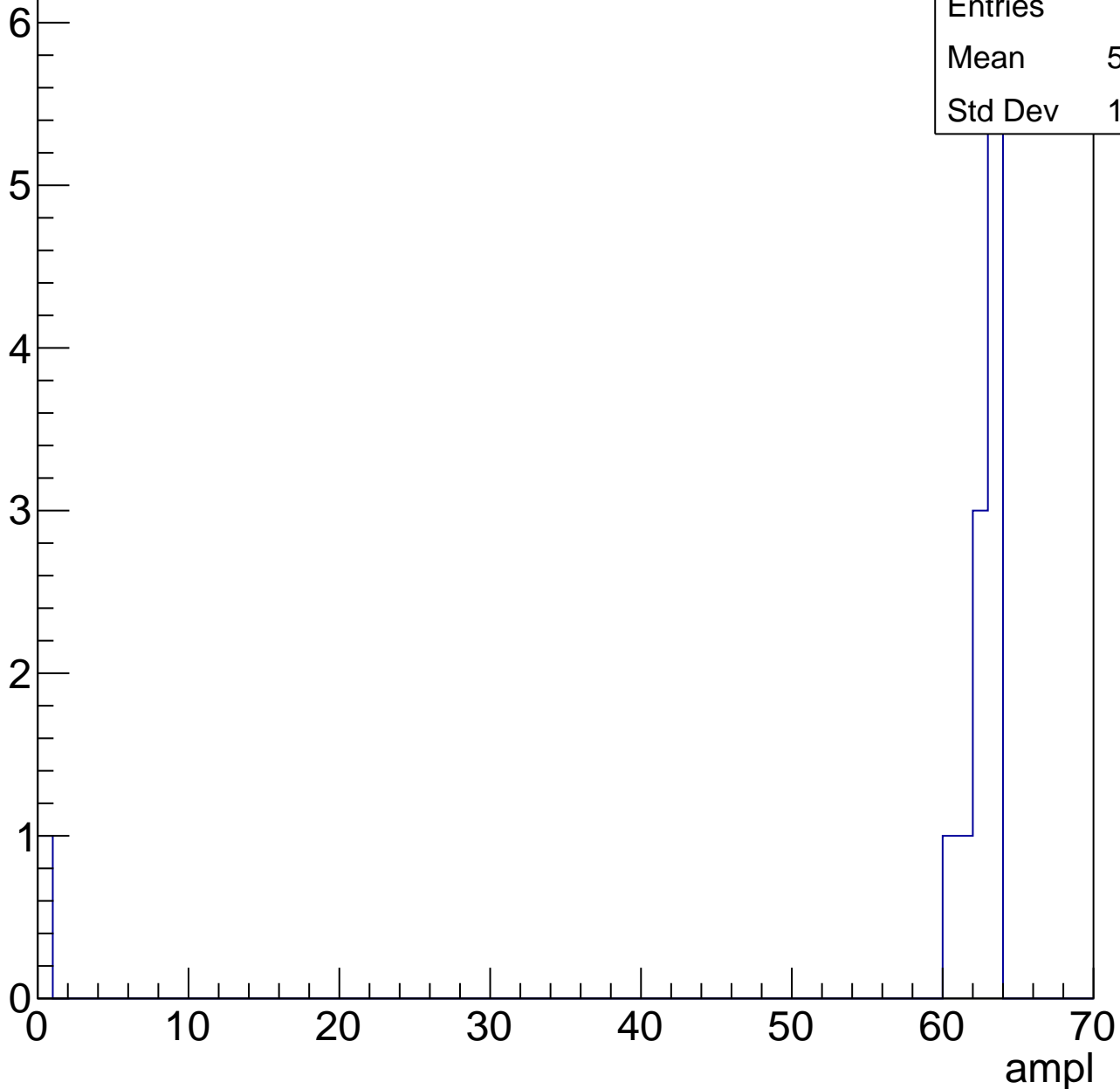


B1L001S, U20-ch107, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	57.08
Std Dev	17.24



B1L001S, U20-ch107, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L001S, U20-ch108, adc0

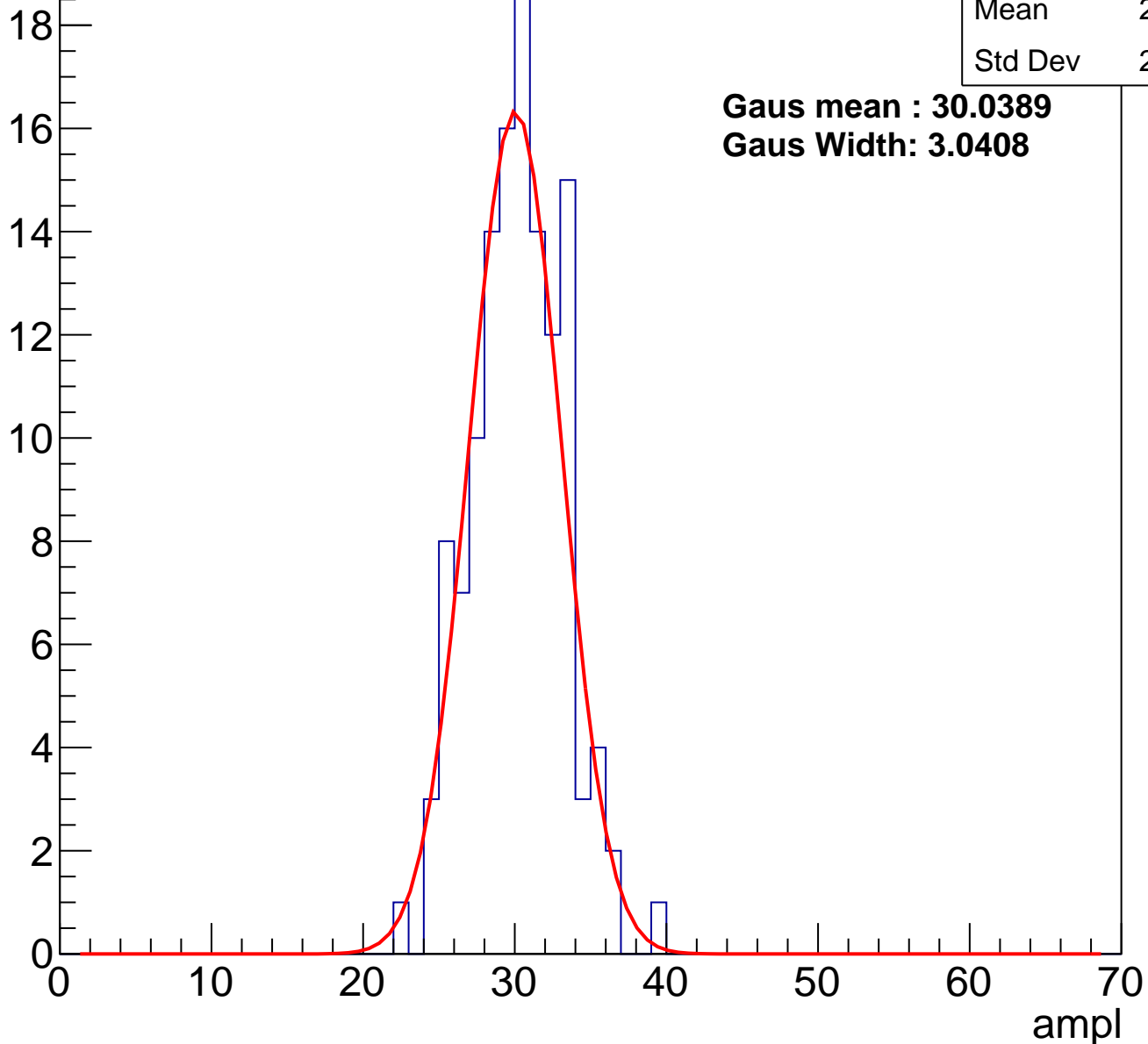
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	29.75
Std Dev	2.968

Gaus mean : 30.0389

Gaus Width: 3.0408

Entry



B1L001S, U20-ch108, adc1

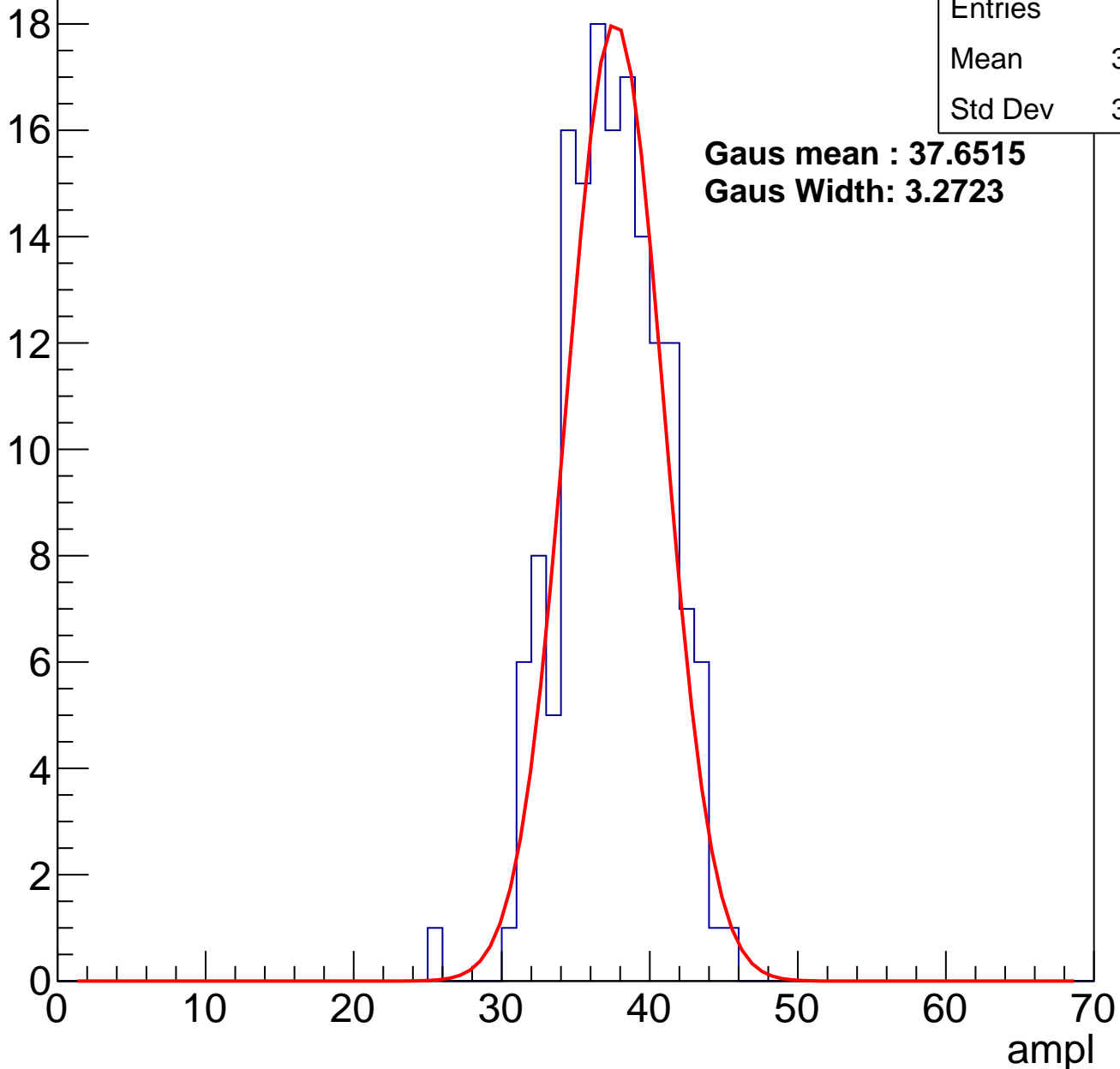
calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	37.03
Std Dev	3.385

Gaus mean : 37.6515

Gaus Width: 3.2723

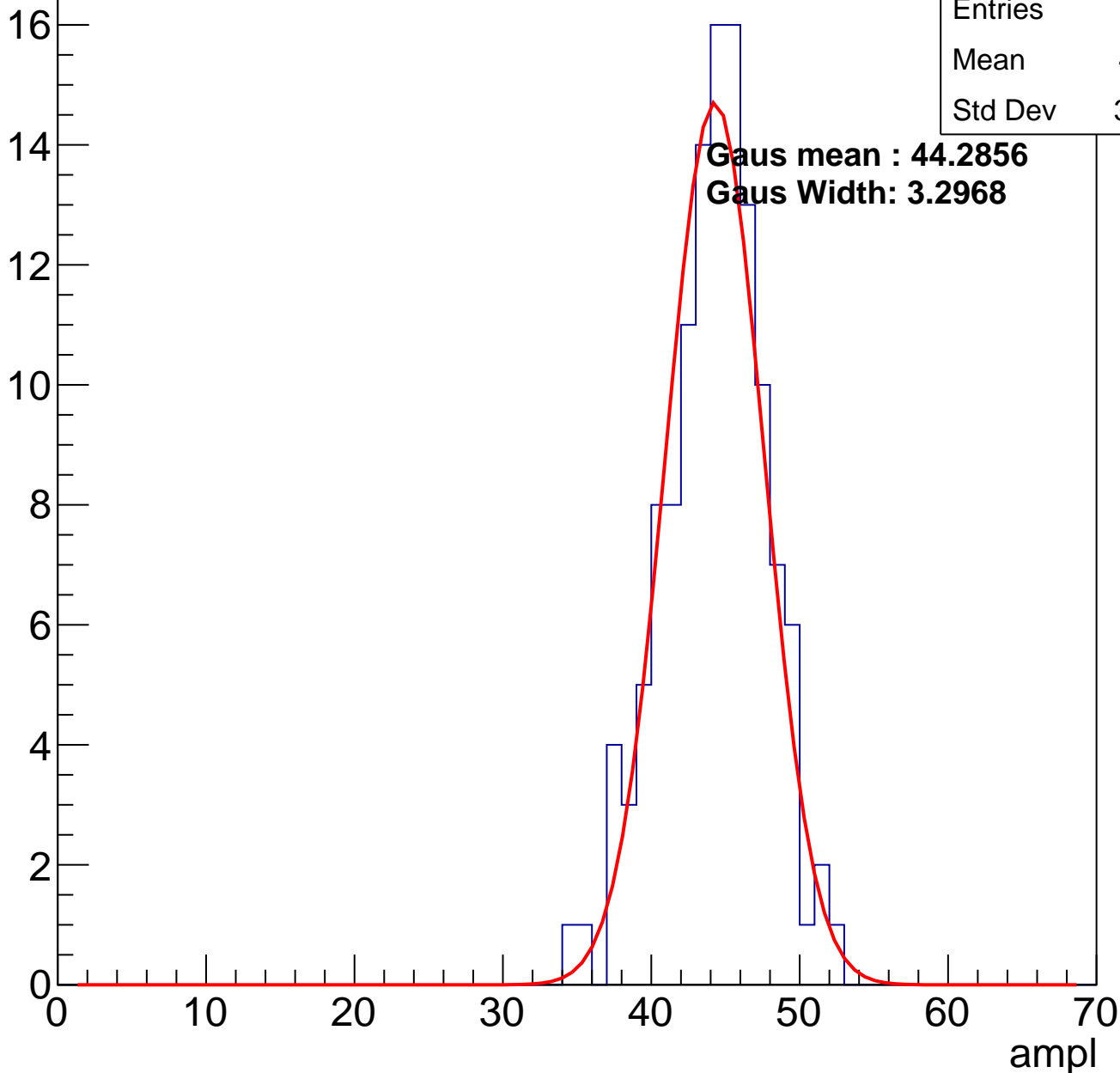
Entry



B1L001S, U20-ch108, adc2

calib_packv5_042523_0143.root, FC#2, port C2

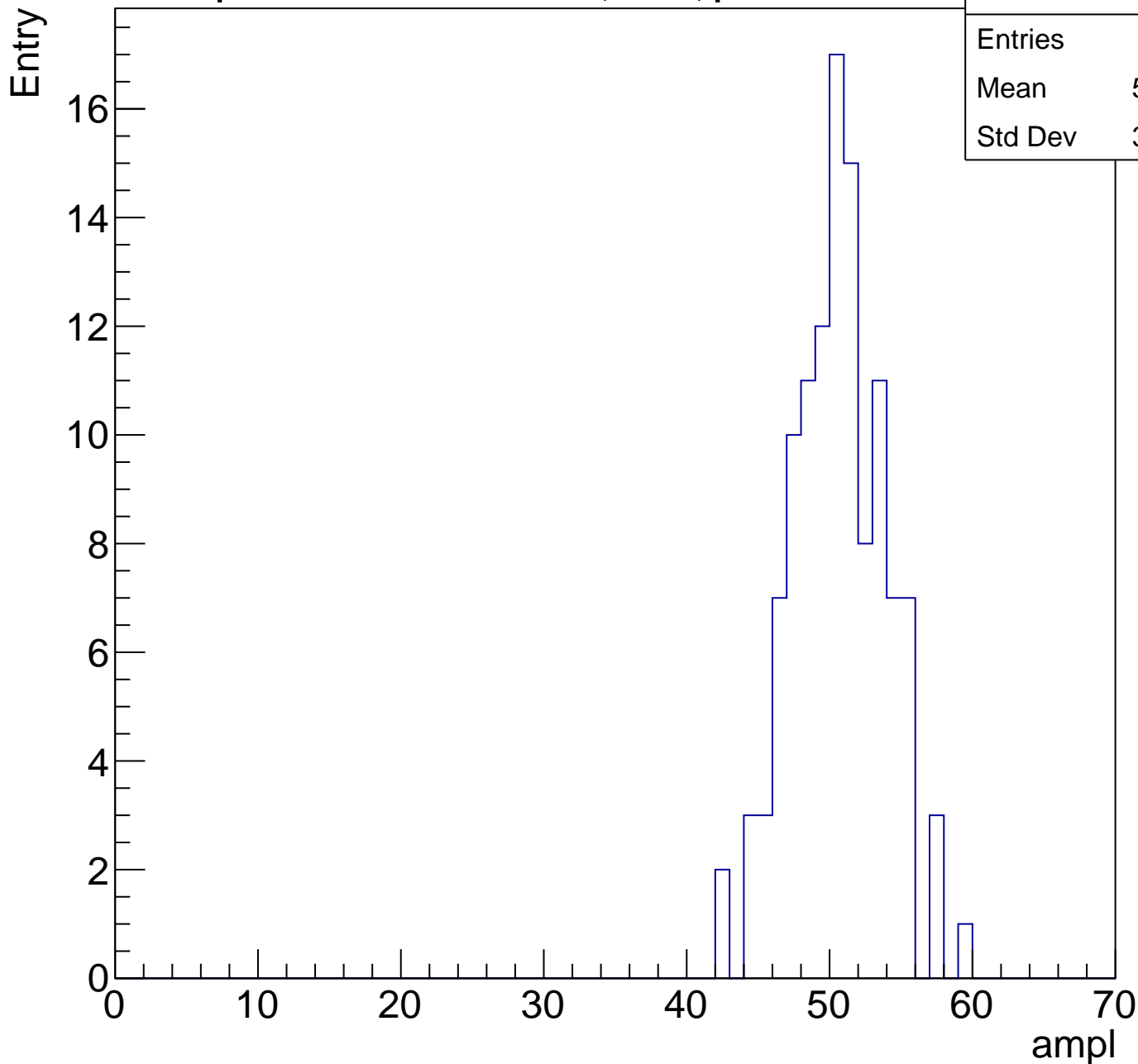
Entry



B1L001S, U20-ch108, adc3

calib_packv5_042523_0143.root, FC#2, port C2

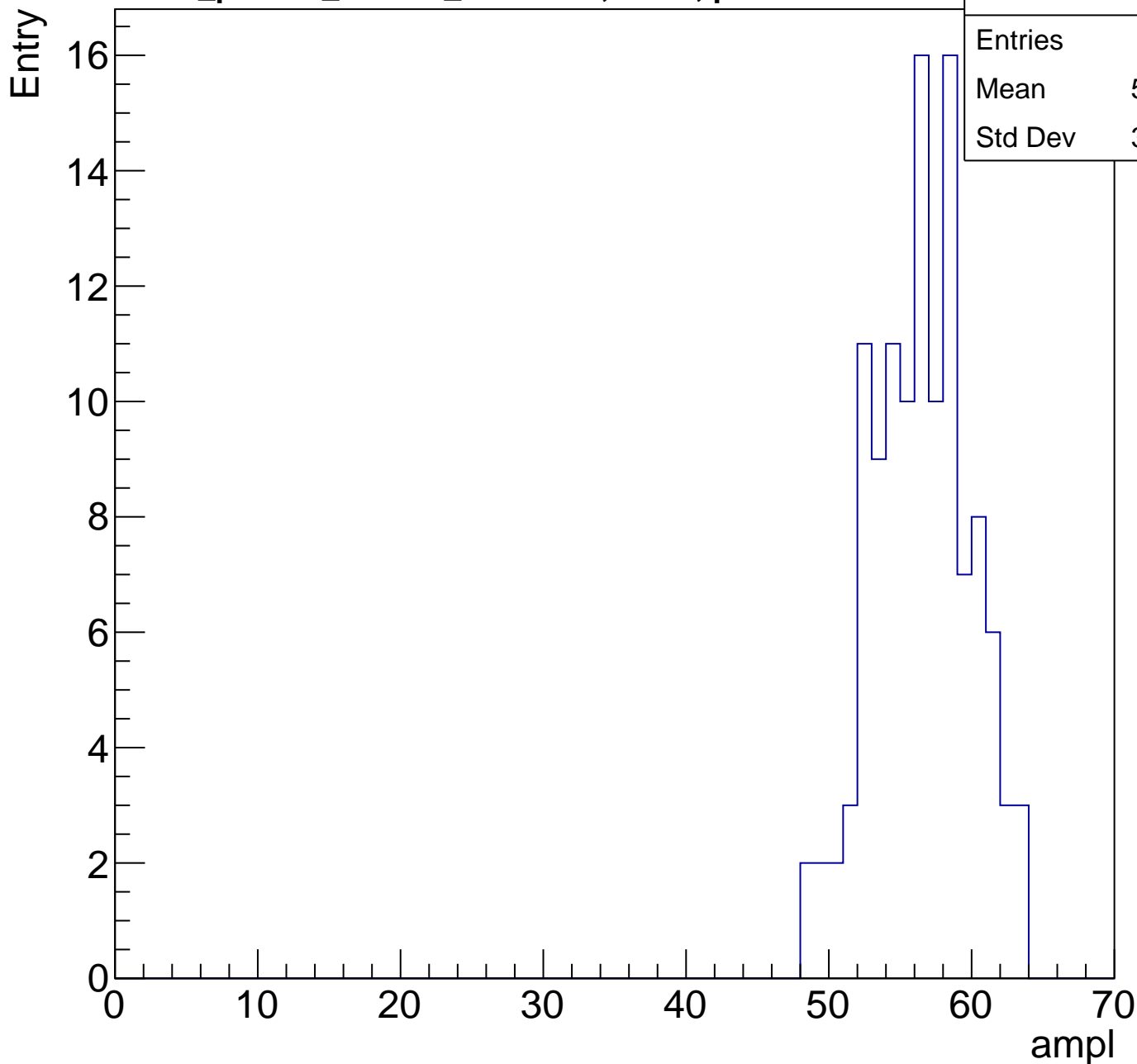
Entries	117
Mean	50.14
Std Dev	3.239



B1L001S, U20-ch108, adc4

calib_packv5_042523_0143.root, FC#2, port C2

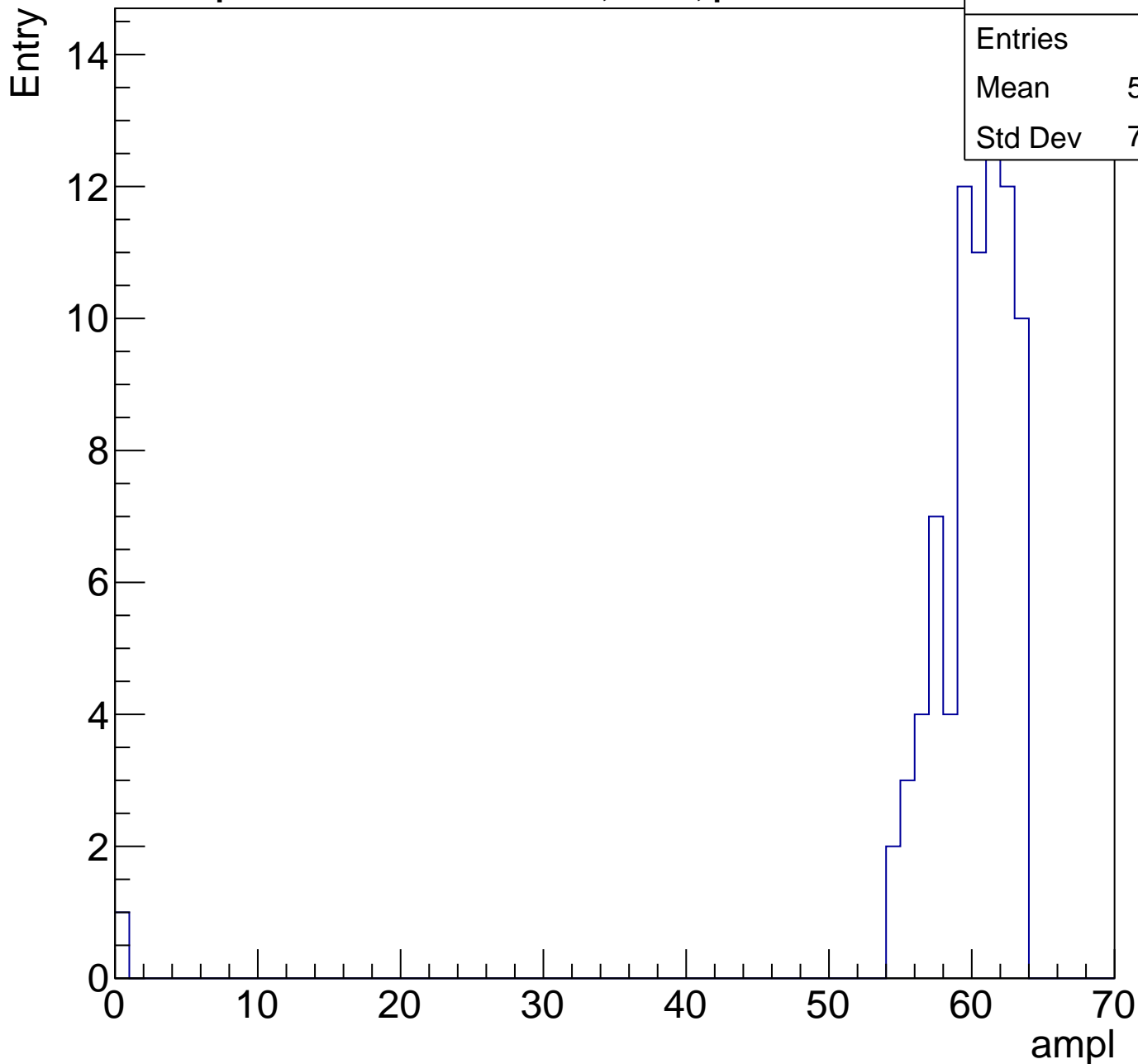
Entries	119
Mean	56.03
Std Dev	3.373



B1L001S, U20-ch108, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	80
Mean	59.05
Std Dev	7.055

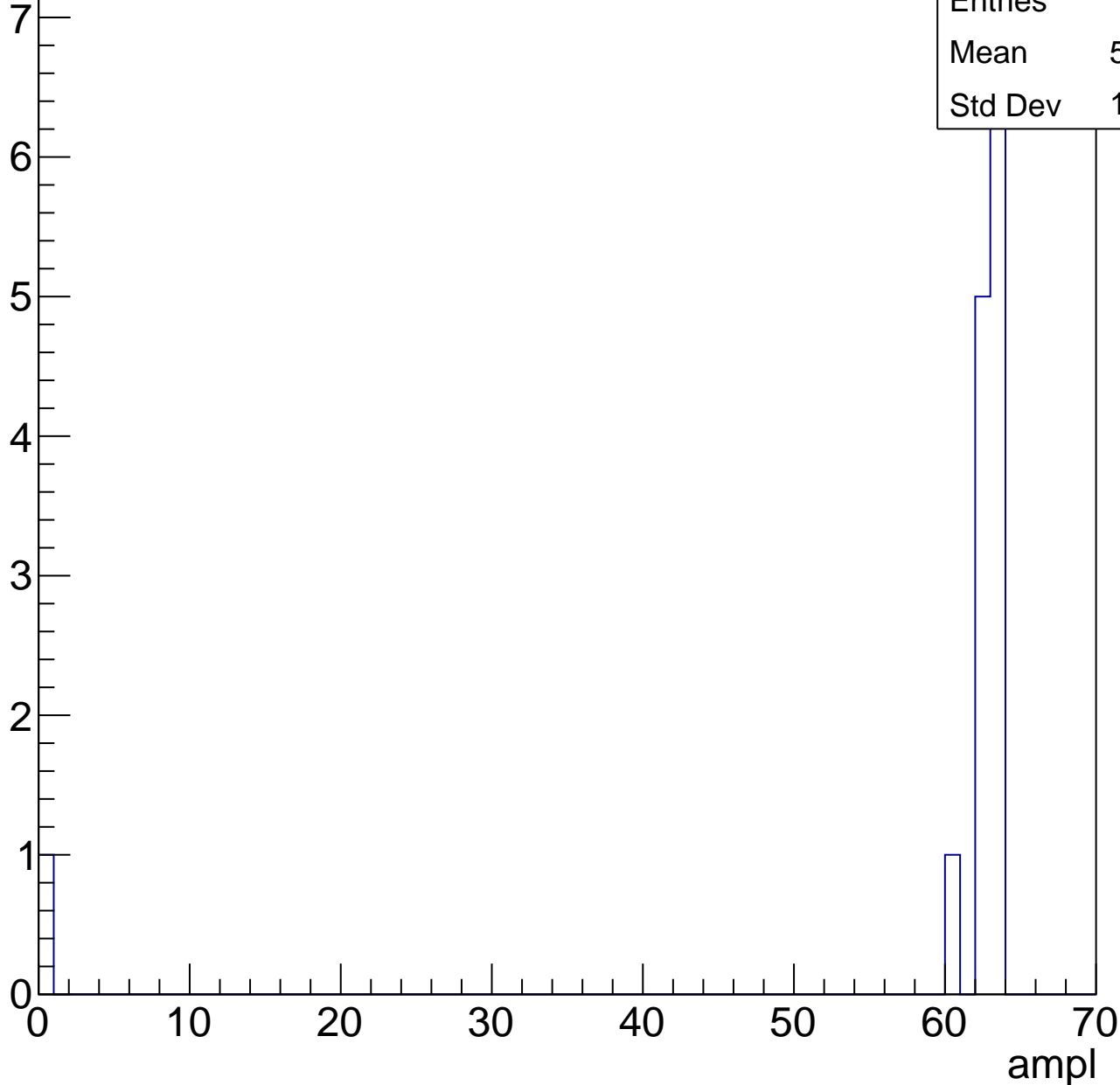


B1L001S, U20-ch108, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.93
Std Dev	16.09



B1L001S, U20-ch108, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	24
Std Dev	0

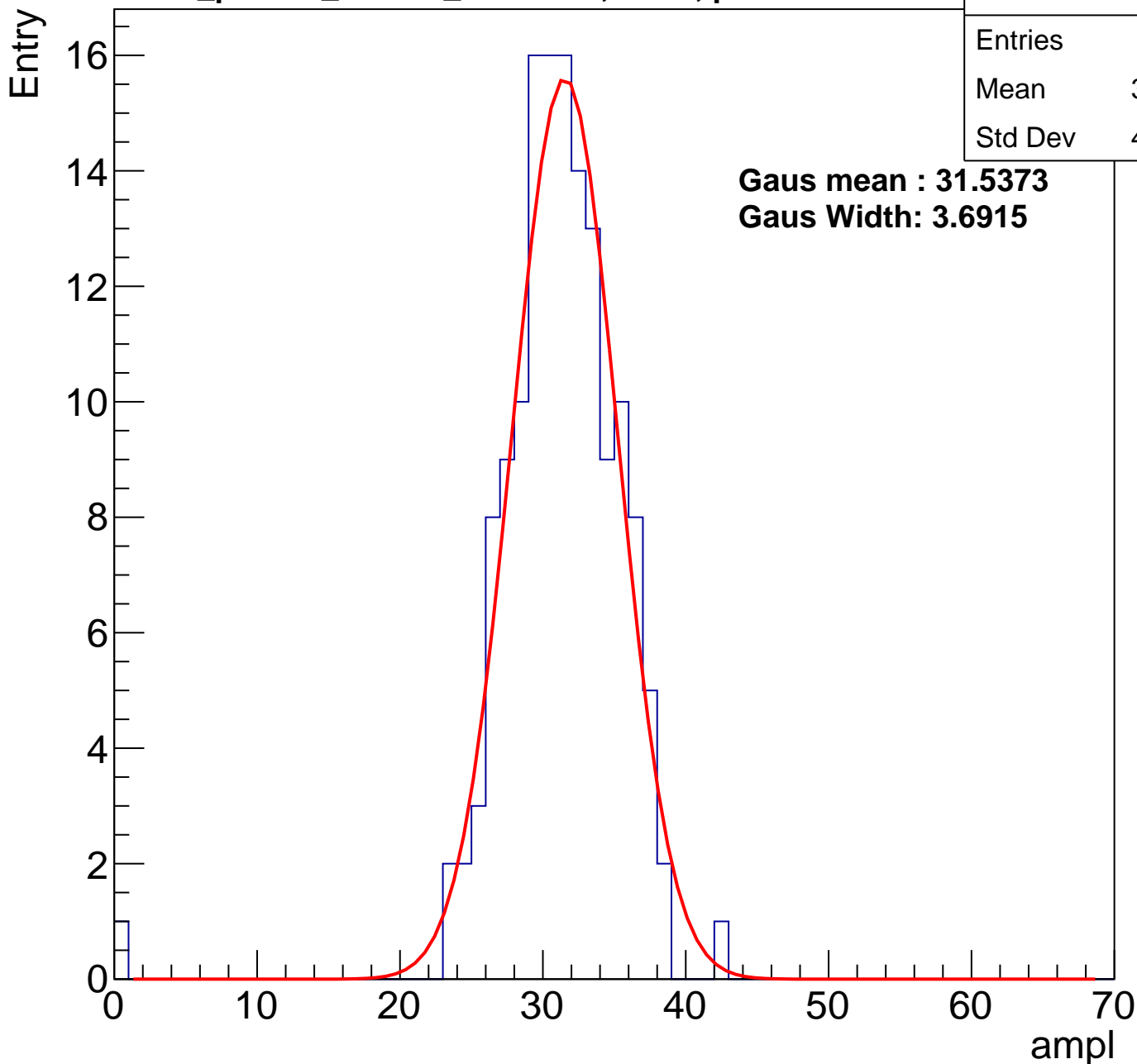
B1L001S, U20-ch109, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	30.79
Std Dev	4.322

Gaus mean : 31.5373

Gaus Width: 3.6915



B1L001S, U20-ch109, adc1

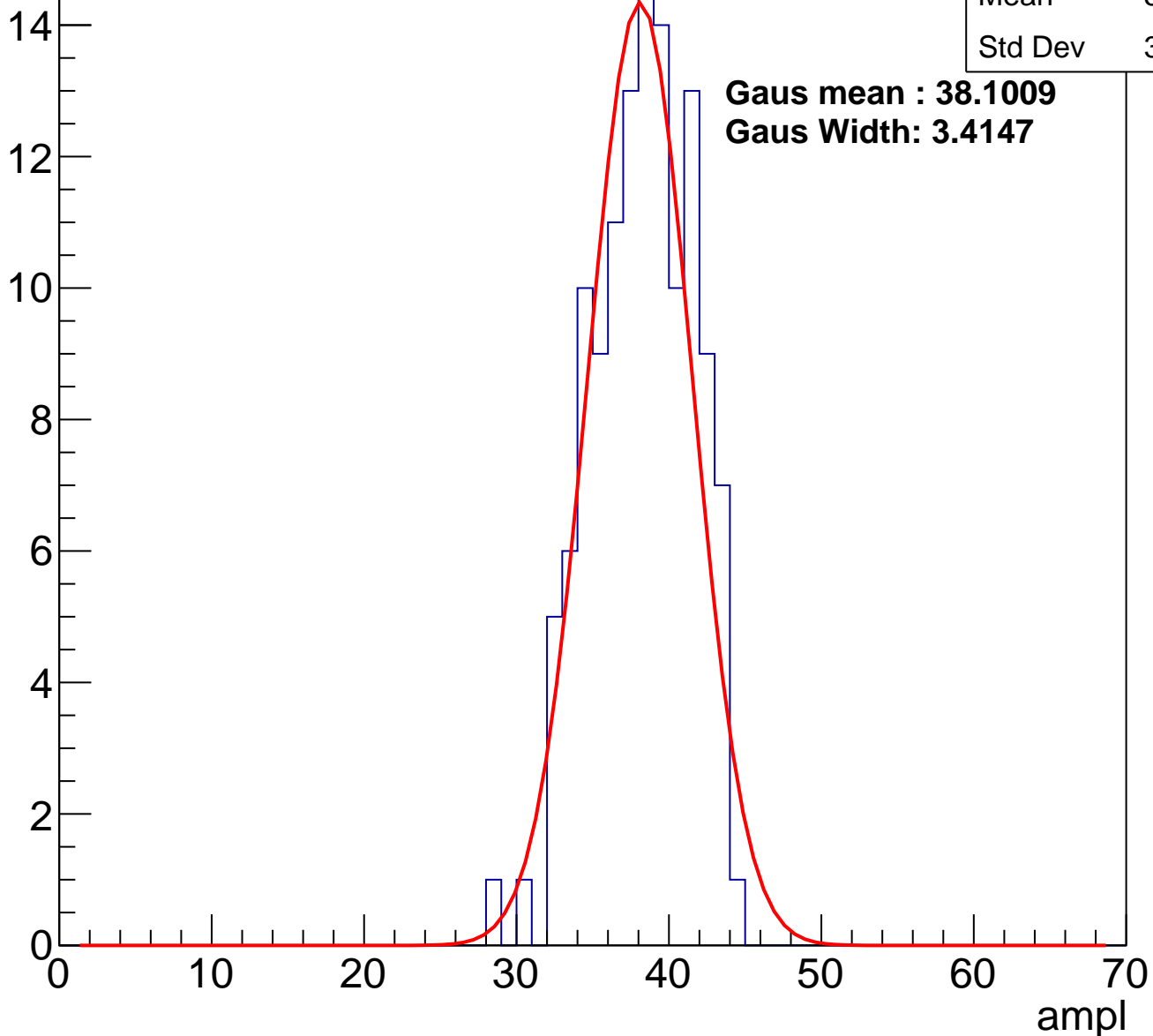
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	37.76
Std Dev	3.238

Gaus mean : 38.1009

Gaus Width: 3.4147

Entry



B1L001S, U20-ch109, adc2

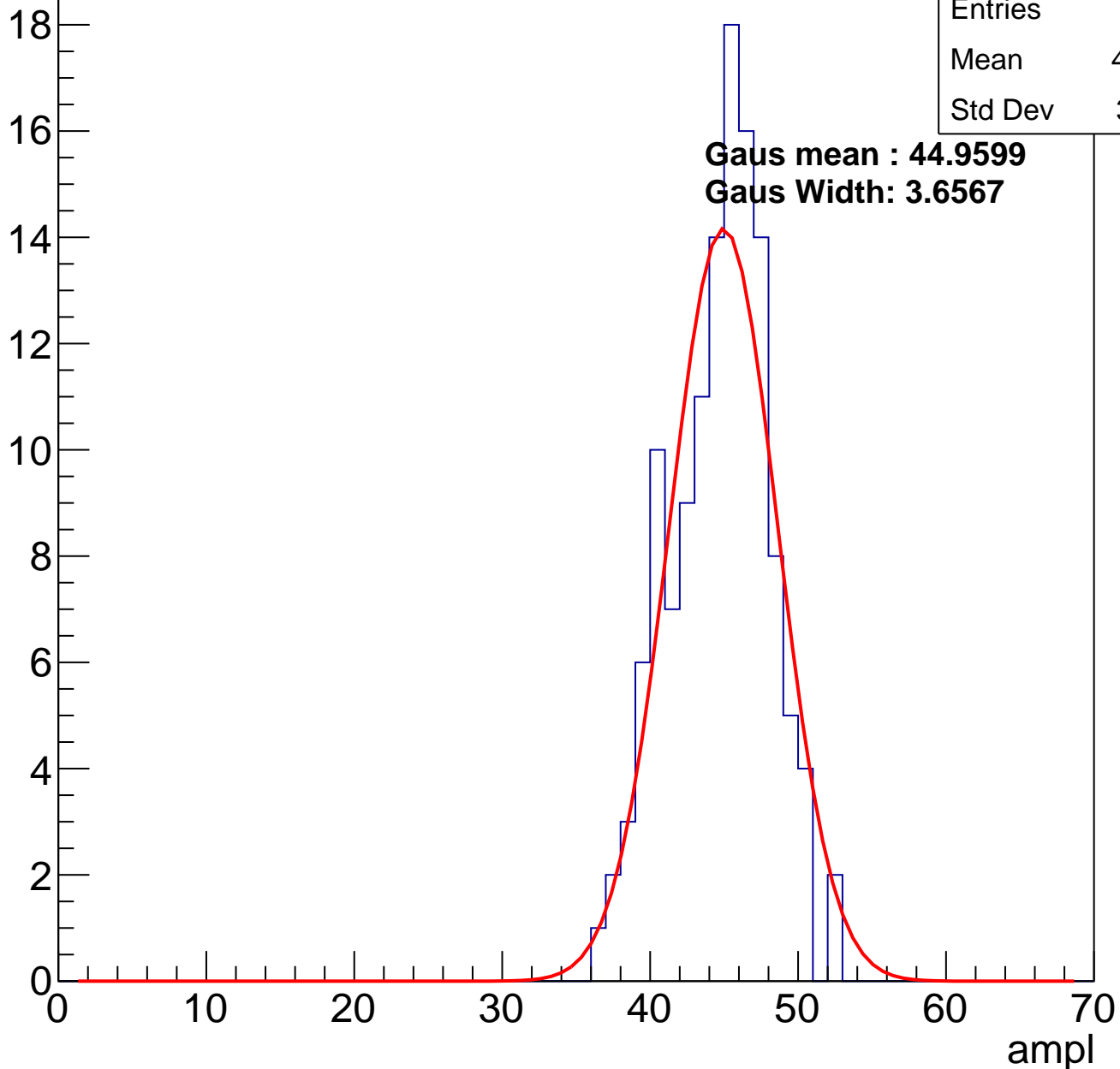
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	44.22
Std Dev	3.301

Gaus mean : 44.9599

Gaus Width: 3.6567

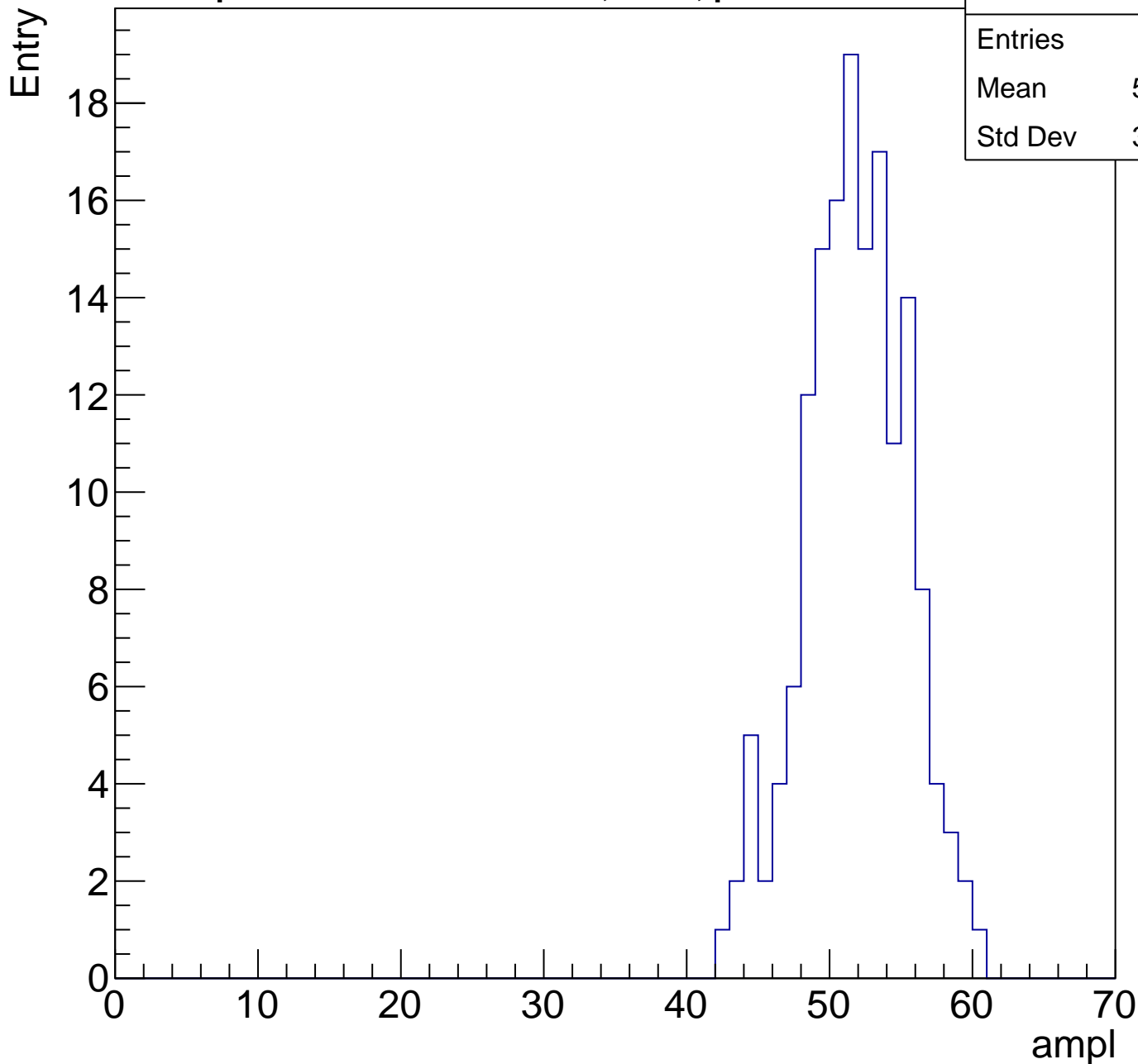
Entry



B1L001S, U20-ch109, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	51.32
Std Dev	3.566



B1L001S, U20-ch109, adc4

calib_packv5_042523_0143.root, FC#2, port C2

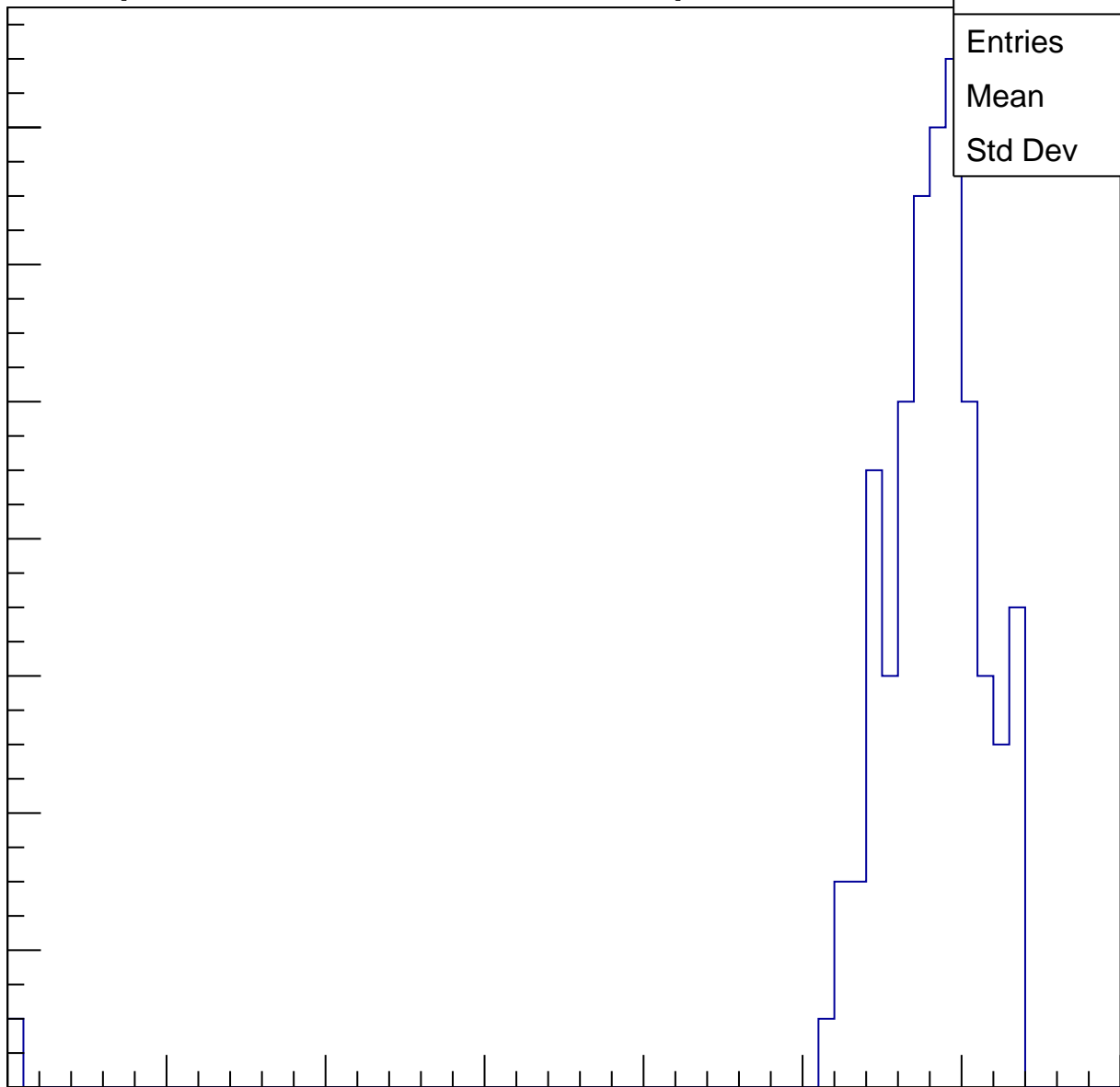
Entries	103
Mean	57.25
Std Dev	6.35

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch109, adc5

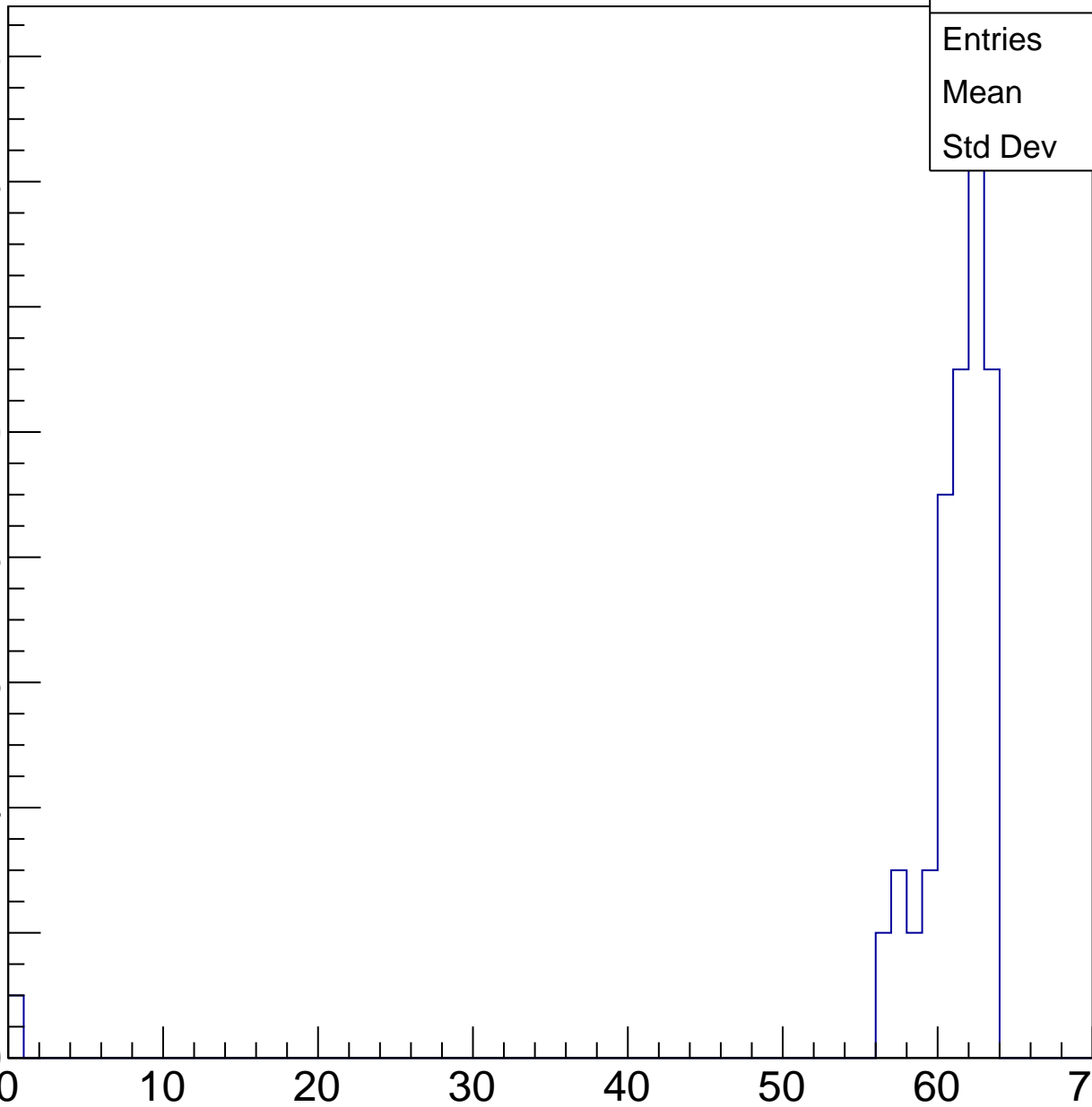
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	58
Mean	59.86
Std Dev	8.14

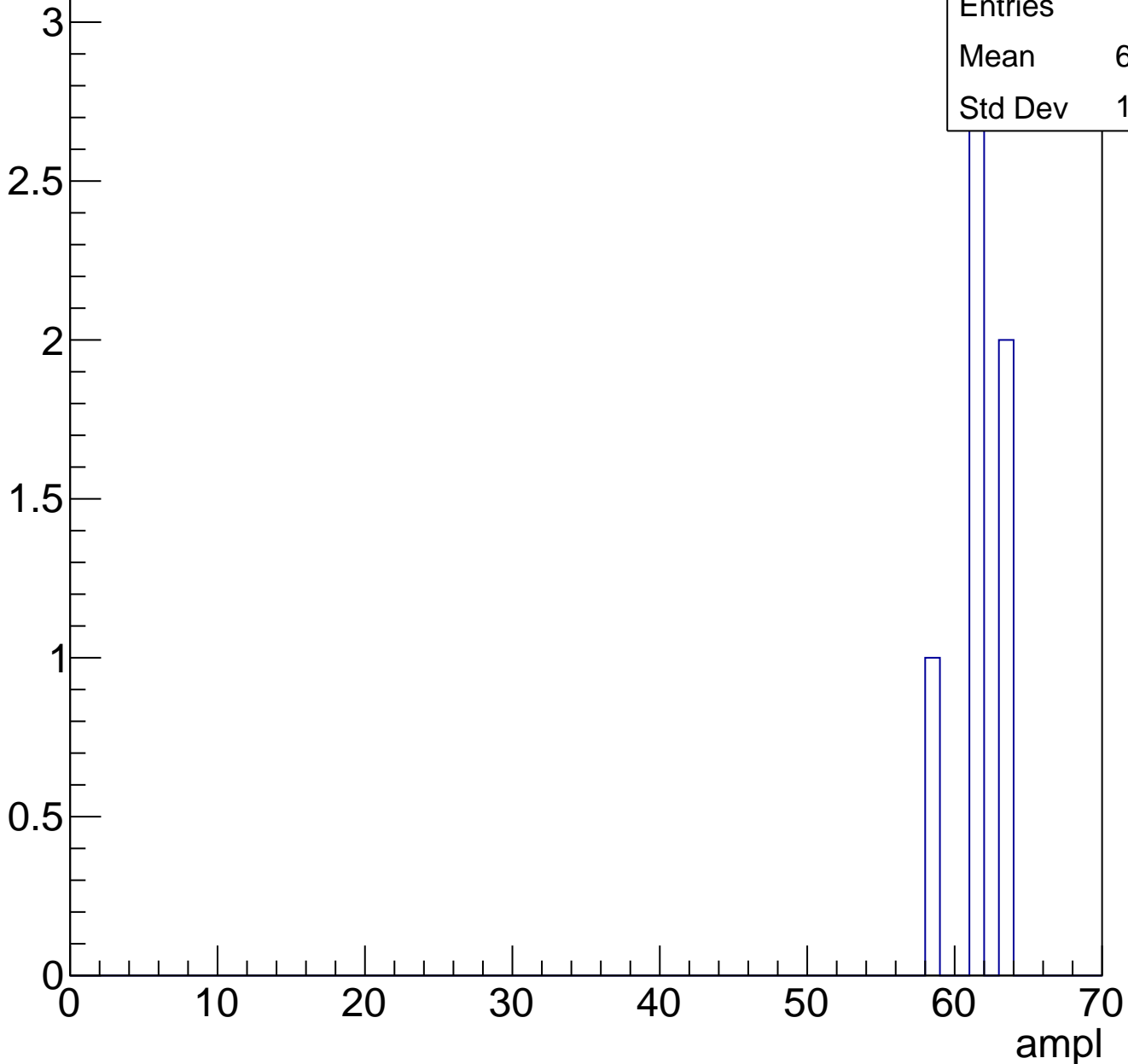
ampl



B1L001S, U20-ch109, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch109, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	26
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L001S, U20-ch110, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	29.55
Std Dev	4.117

Gaus mean : 30.5465

Gaus Width: 3.9114

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

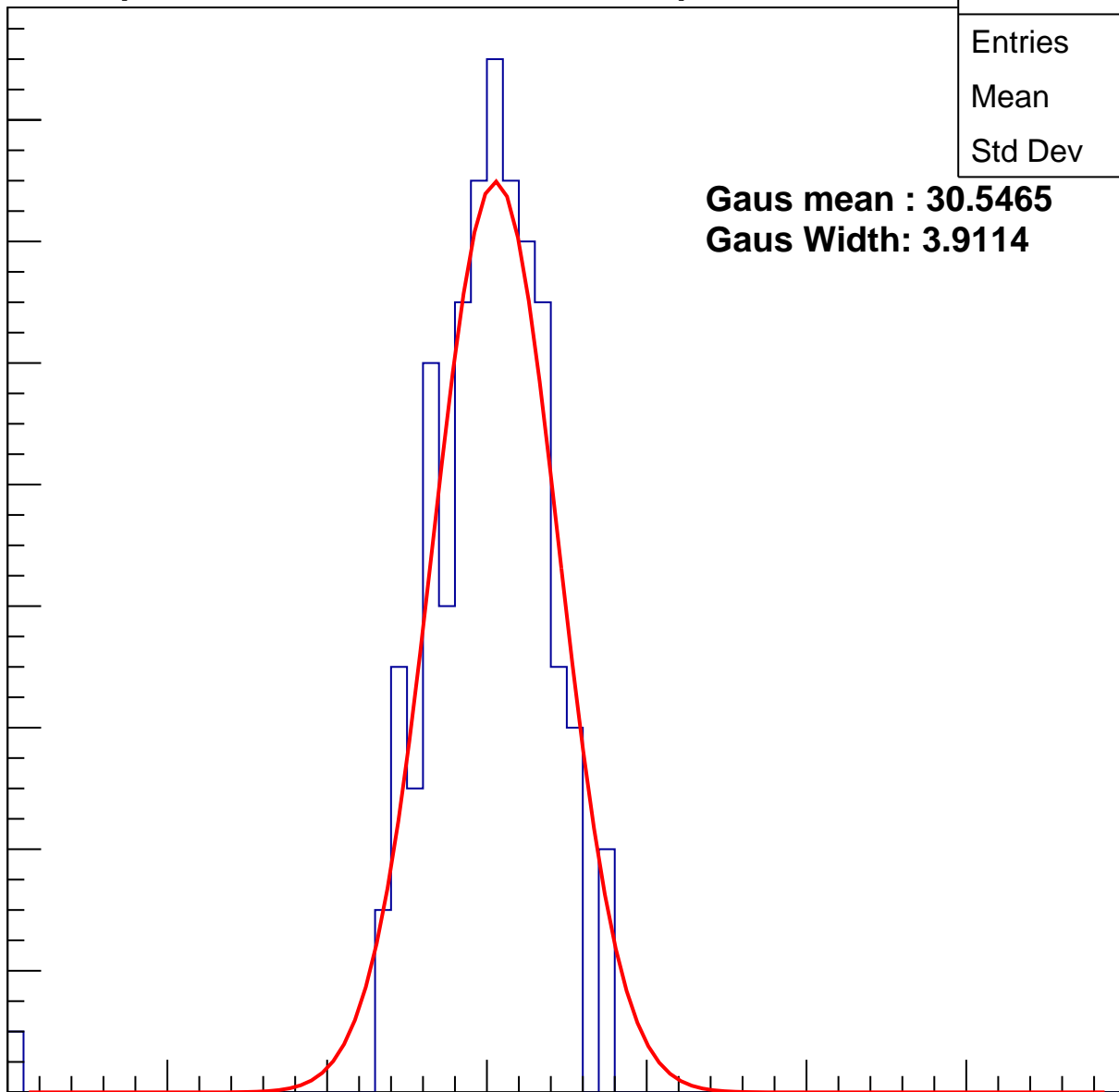
40

50

60

70

ampl



B1L001S, U20-ch110, adc1

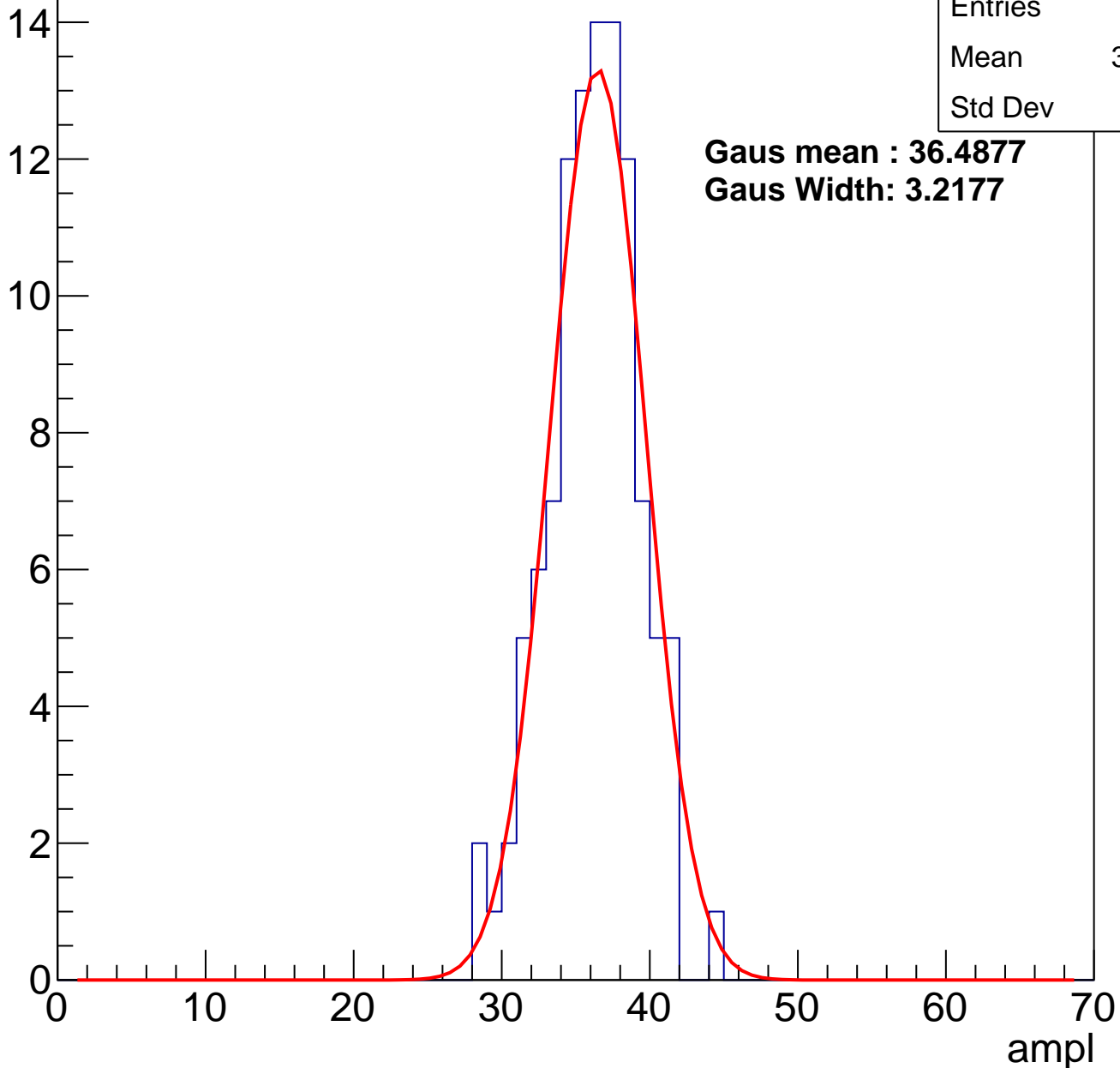
calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	35.72
Std Dev	3.04

Gaus mean : 36.4877

Gaus Width: 3.2177

Entry



B1L001S, U20-ch110, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	41.99
Std Dev	3.194

Gaus mean : 42.6977

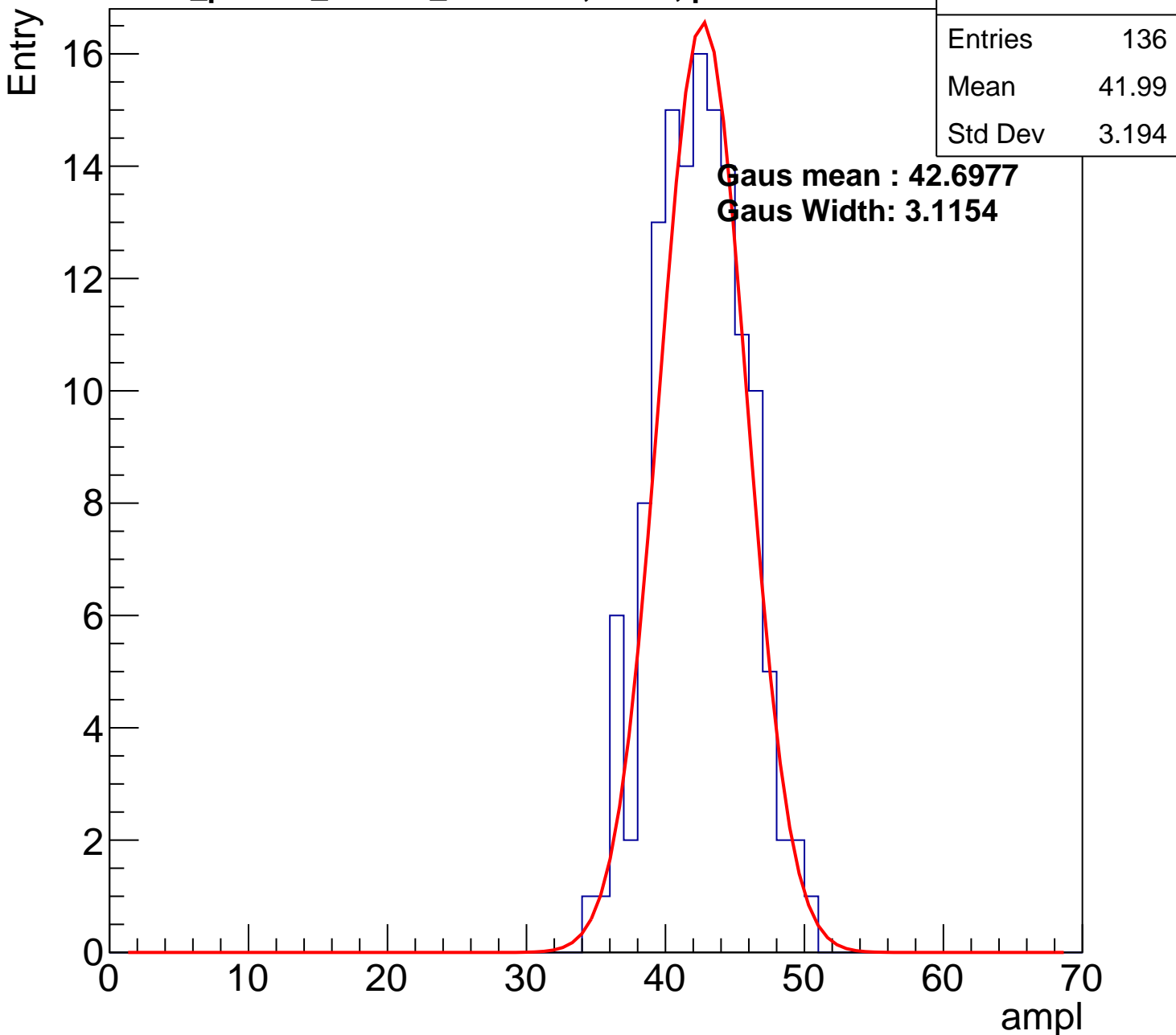
Gaus Width: 3.1154

Entry

16
14
12
10
8
6
4
2
0

ampl

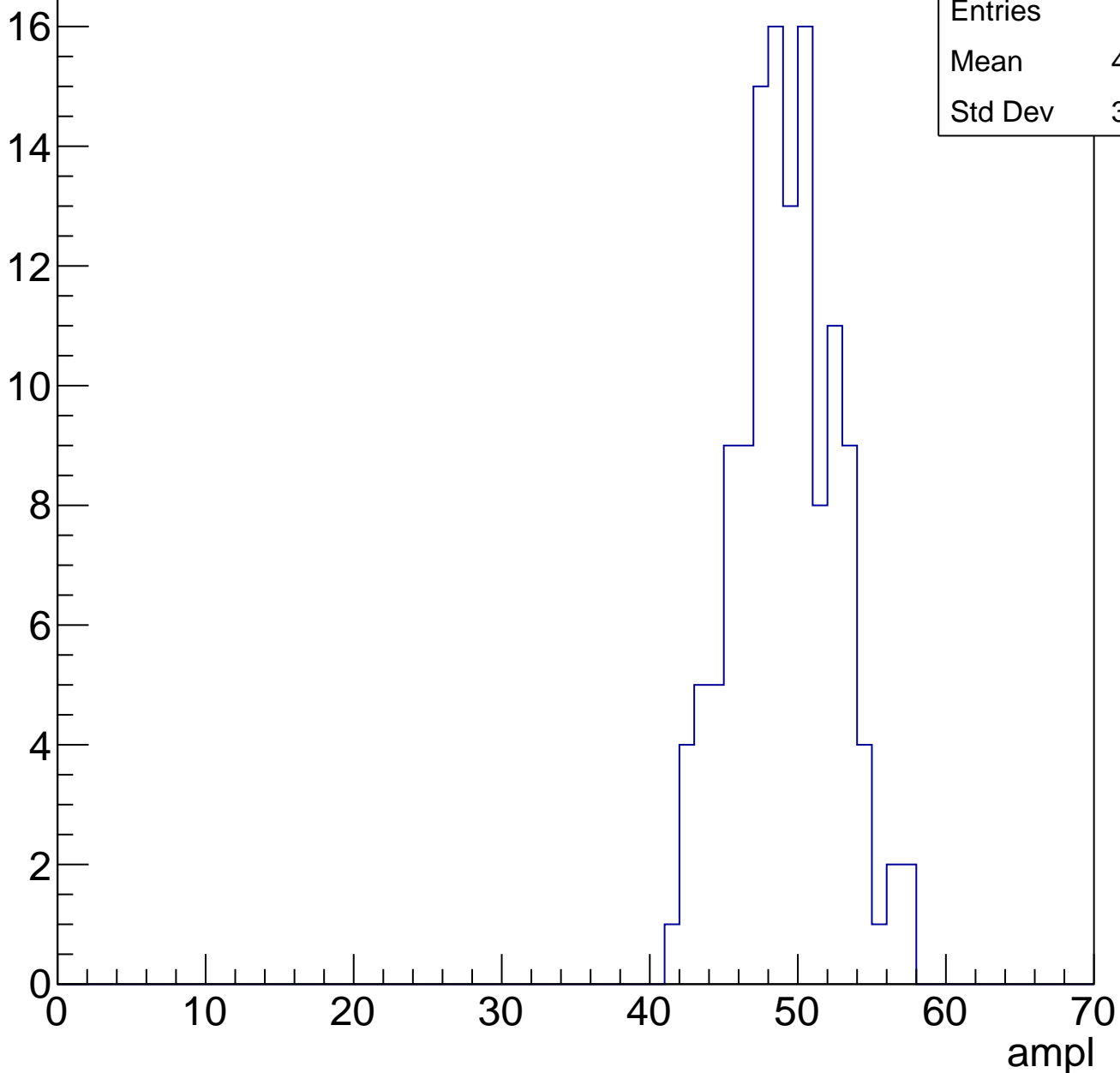
0 10 20 30 40 50 60 70



B1L001S, U20-ch110, adc3

calib_packv5_042523_0143.root, FC#2, port C2

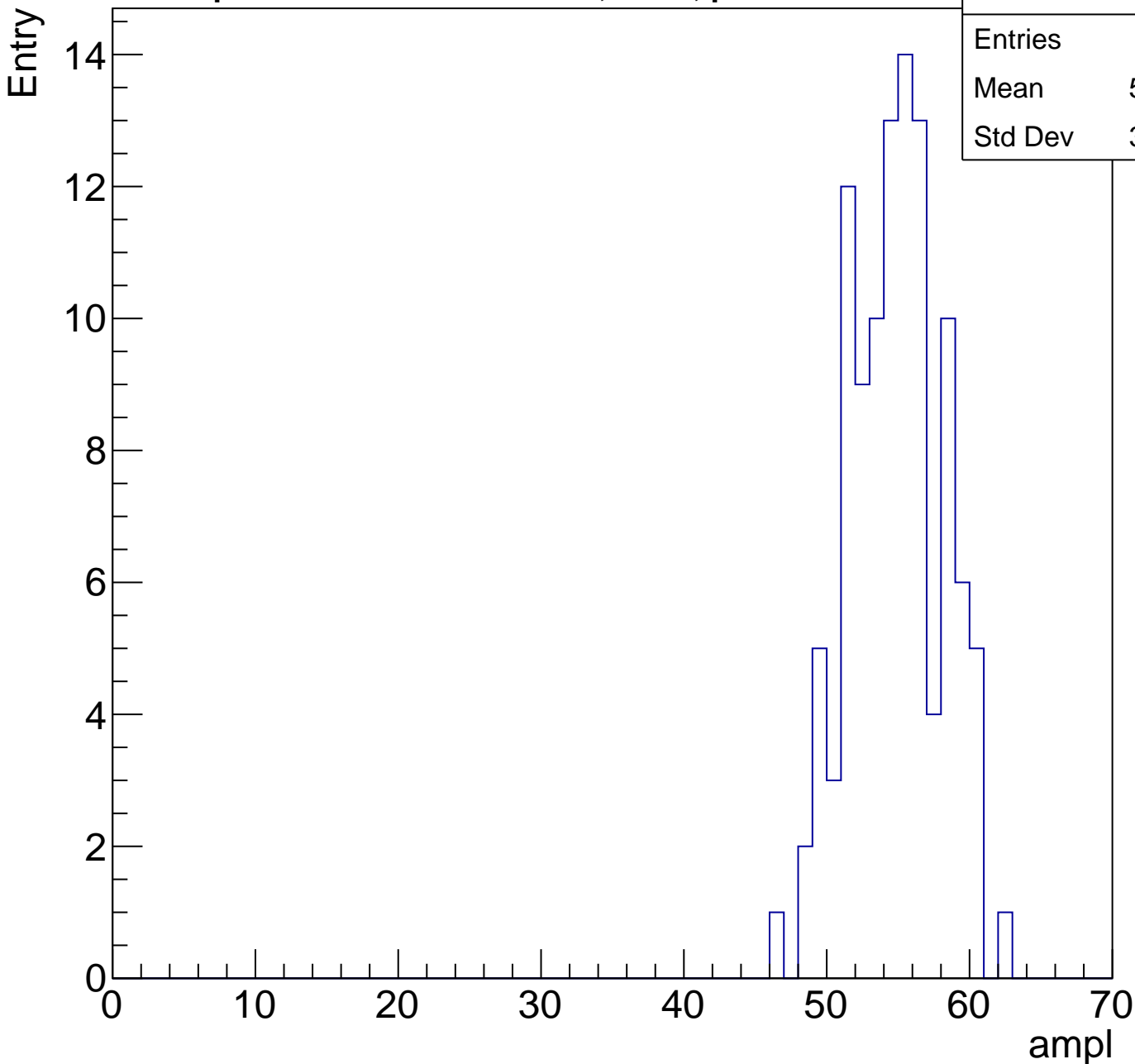
Entry



B1L001S, U20-ch110, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	54.36
Std Dev	3.199



B1L001S, U20-ch110, adc5

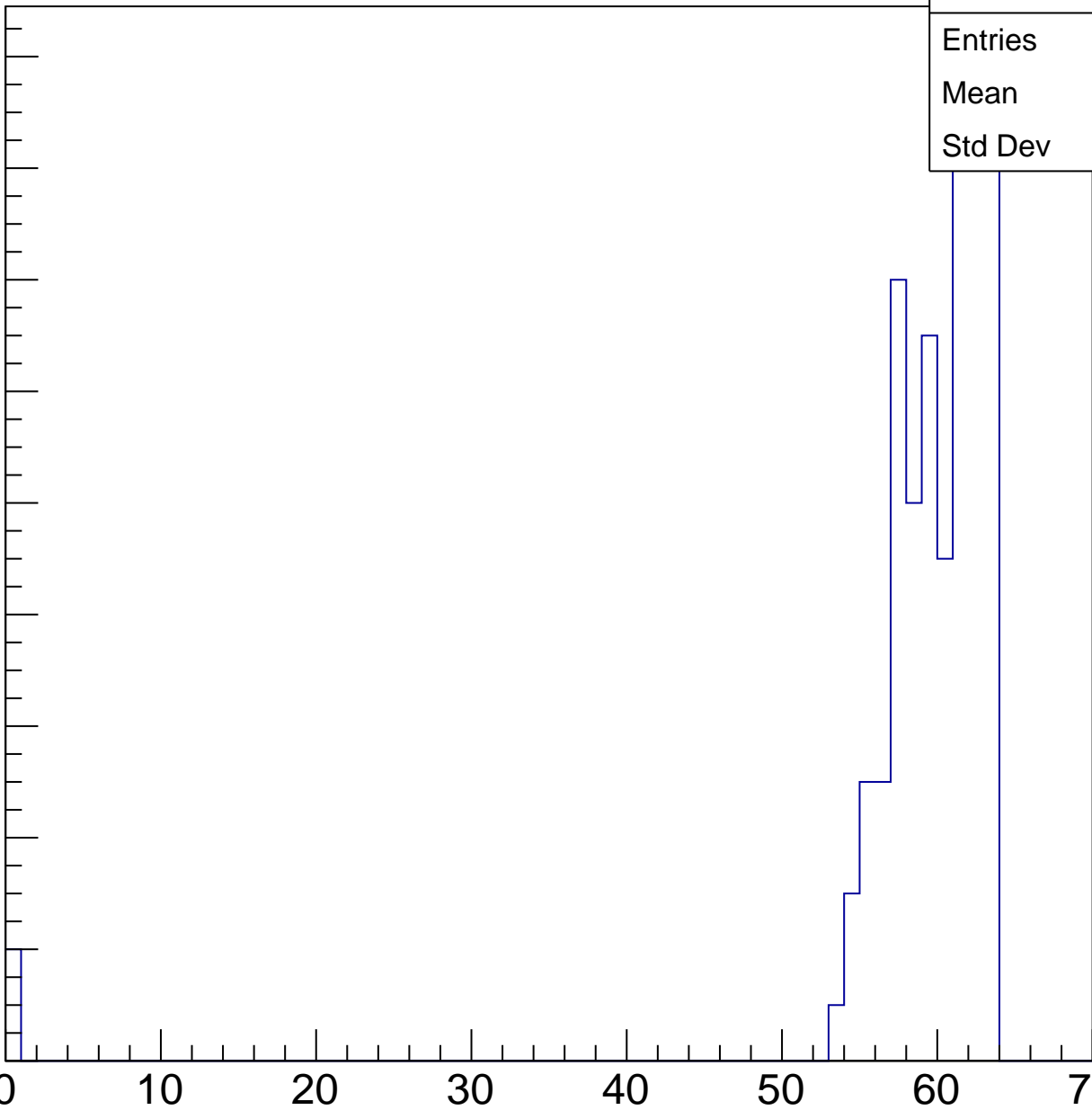
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	113
Mean	58.55
Std Dev	8.274

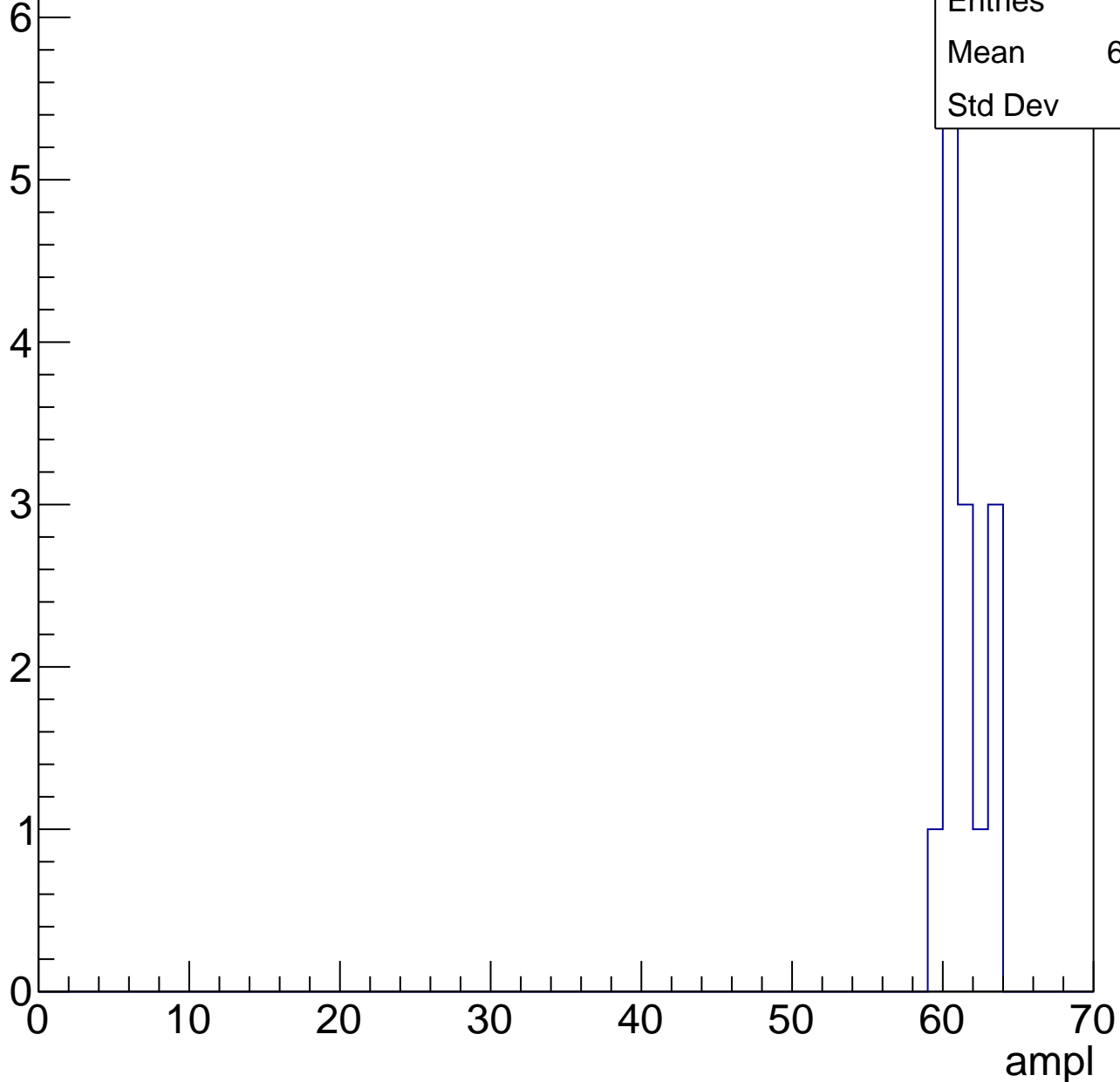
ampl



B1L001S, U20-ch110, adc6

calib_packv5_042523_0143.root, FC#2, port C2

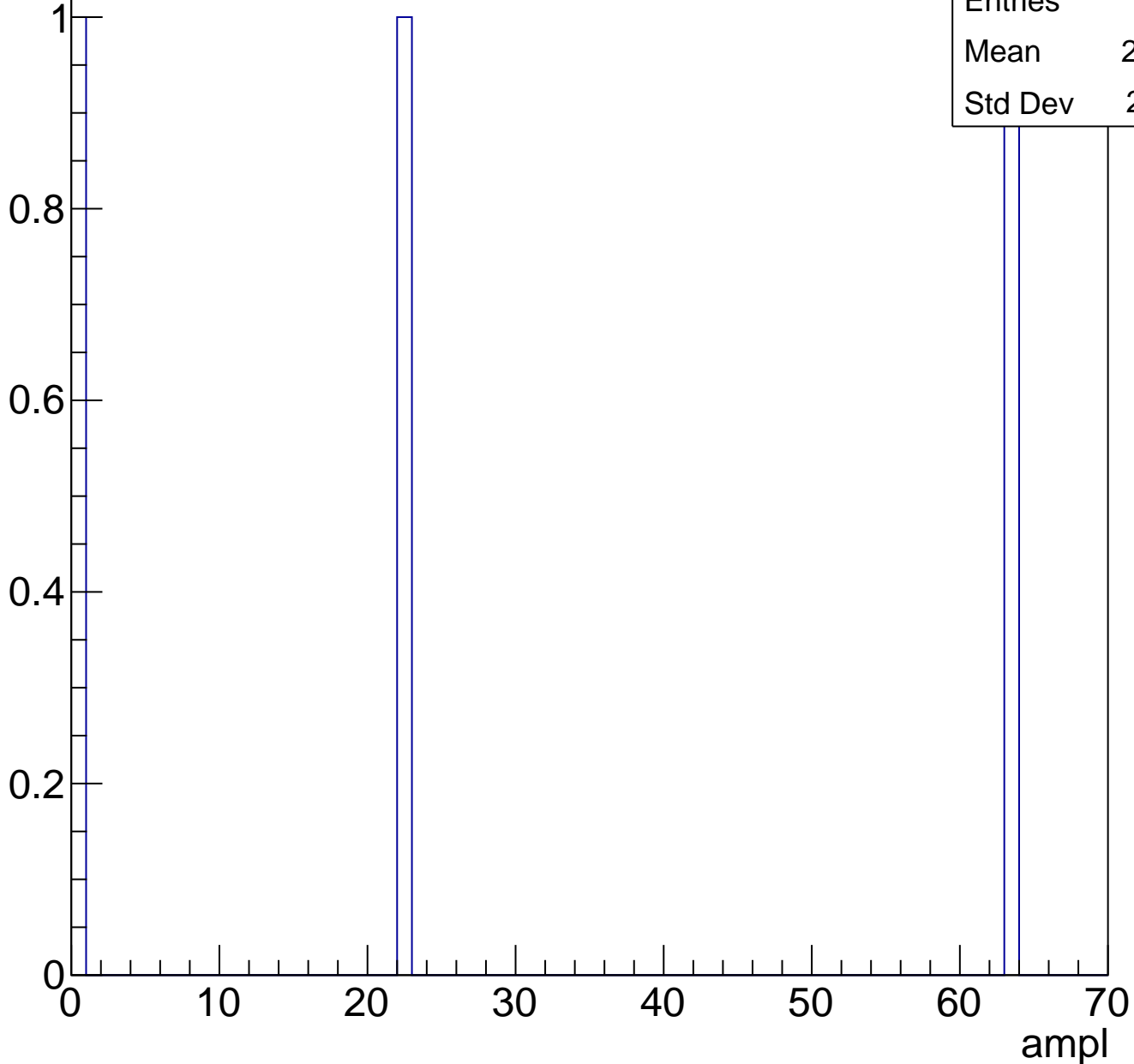
Entry



B1L001S, U20-ch110, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28.33
Std Dev	26.11

B1L001S, U20-ch111, adc0

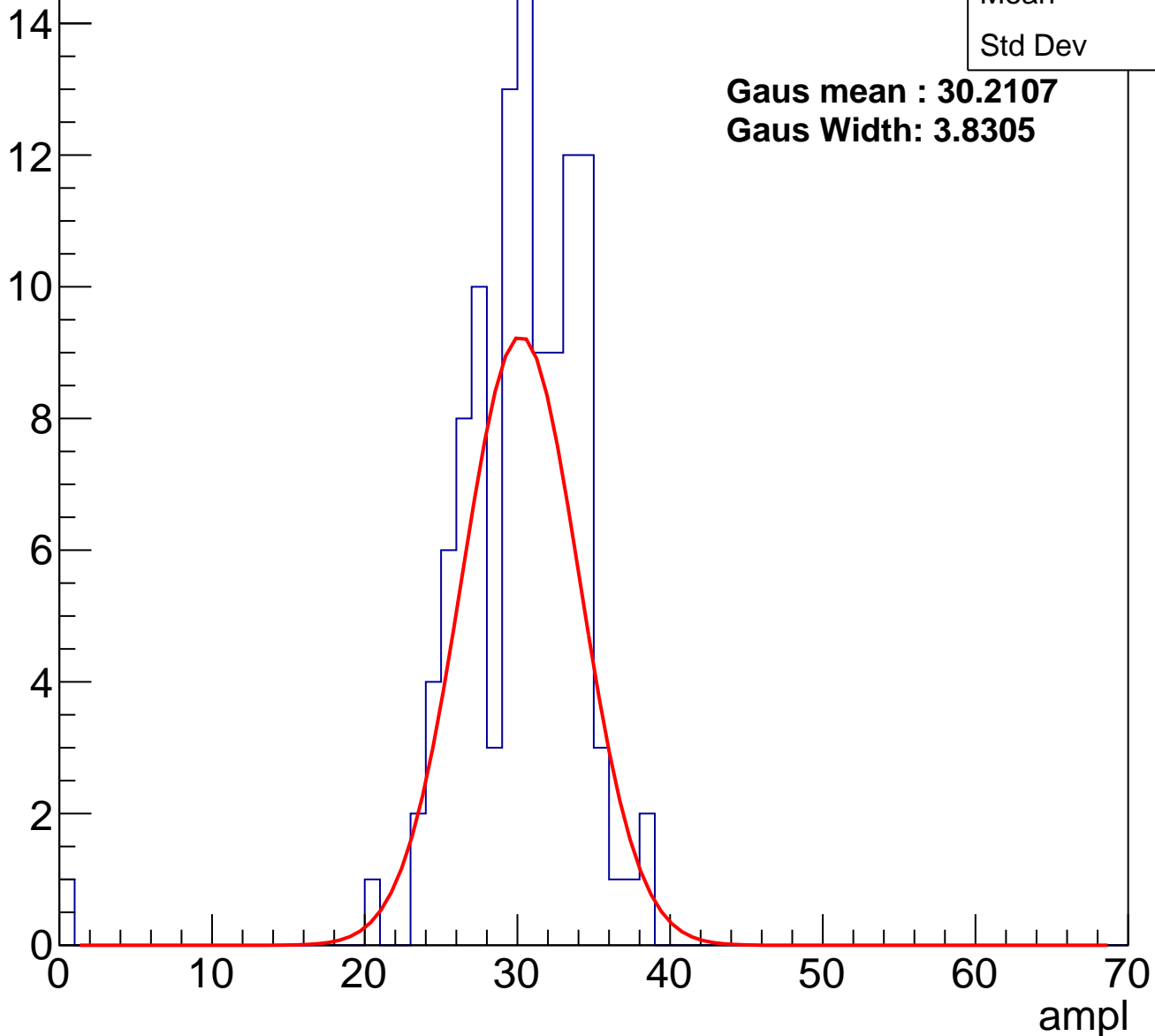
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	29.7
Std Dev	4.49

Gaus mean : 30.2107

Gaus Width: 3.8305

Entry



B1L001S, U20-ch111, adc1

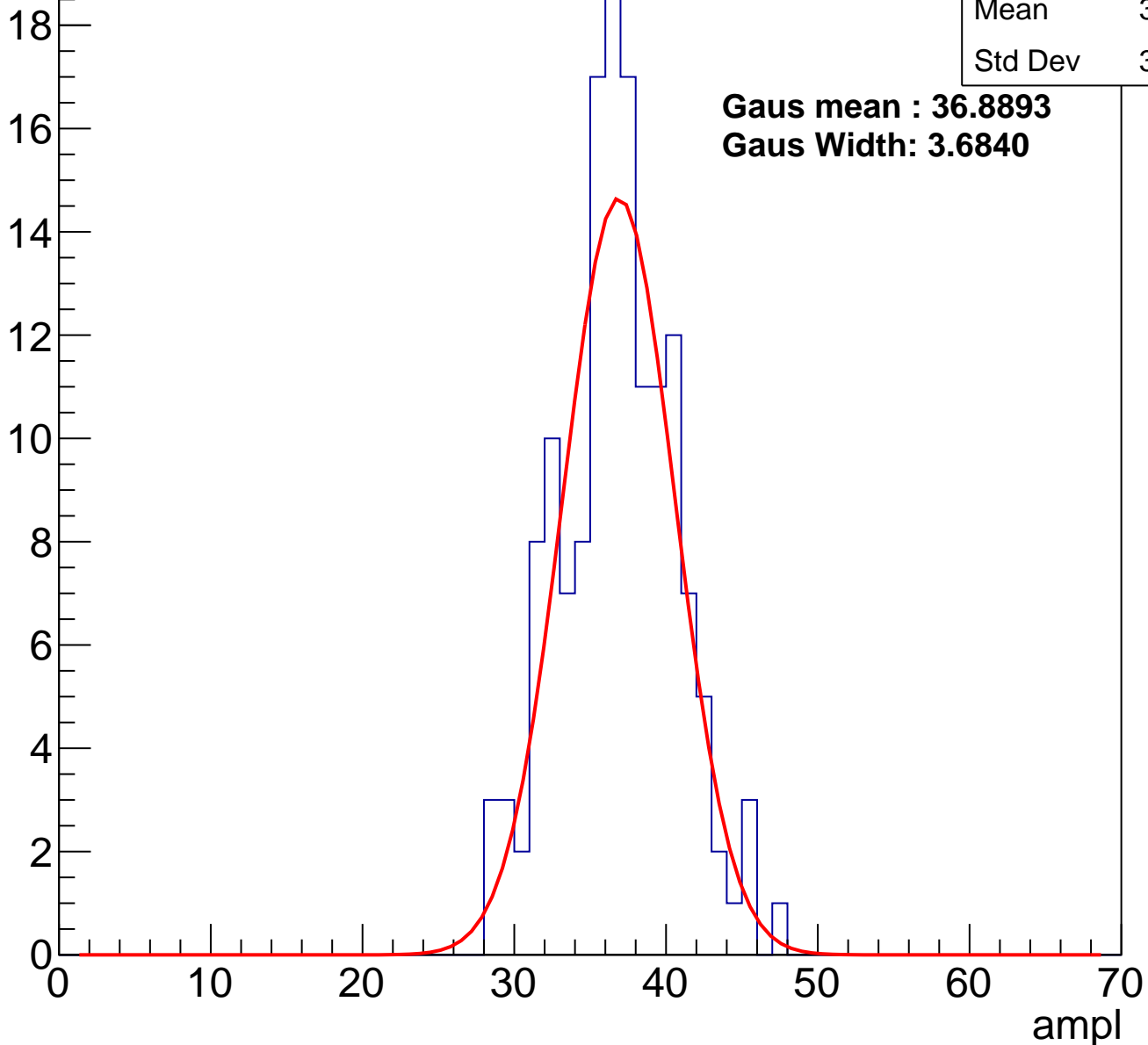
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	36.37
Std Dev	3.776

Gaus mean : 36.8893

Gaus Width: 3.6840

Entry



B1L001S, U20-ch111, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

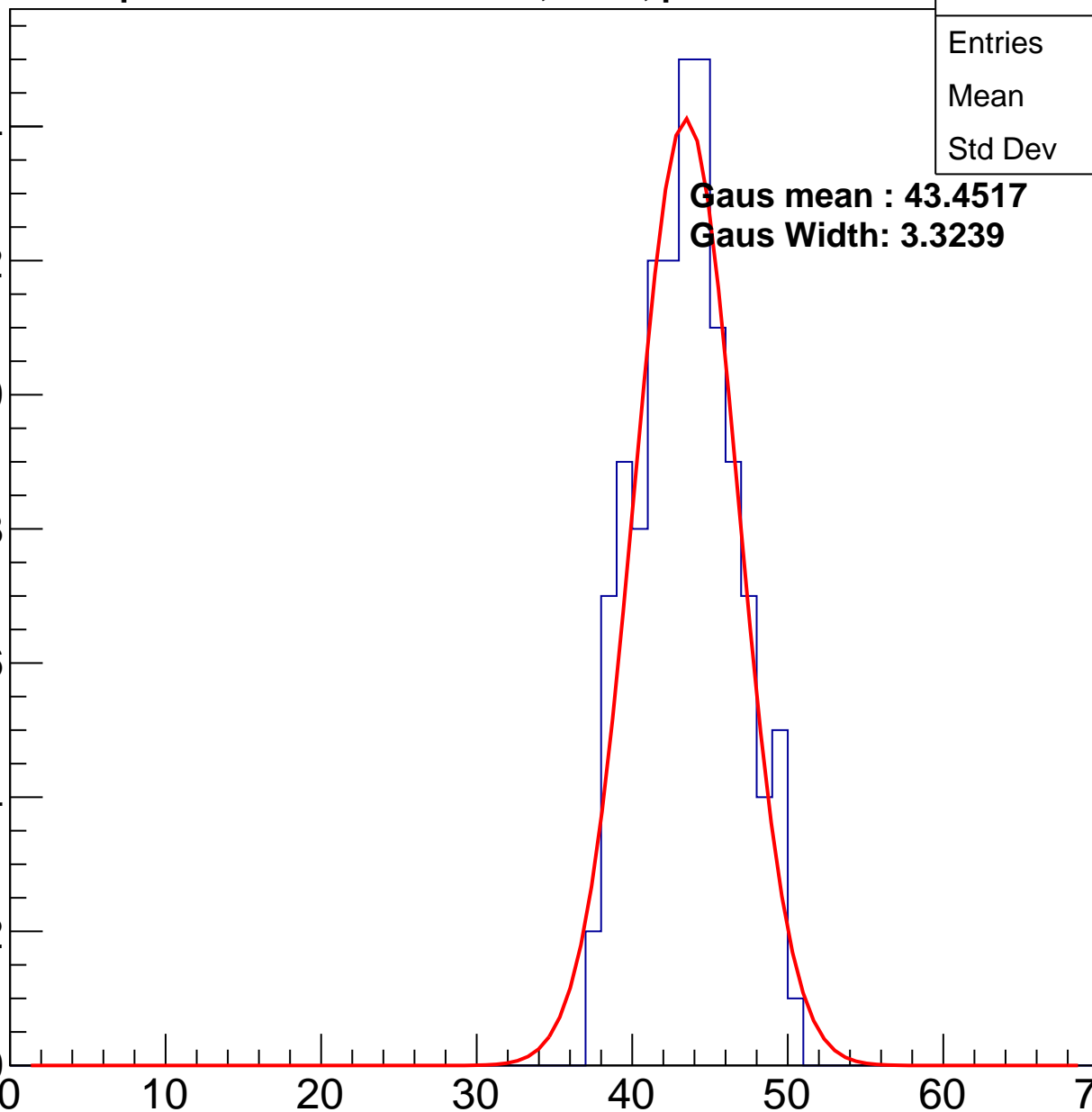
70

ampl

Entries	117
Mean	43.05
Std Dev	3.071

Gaus mean : 43.4517

Gaus Width: 3.3239



B1L001S, U20-ch111, adc3

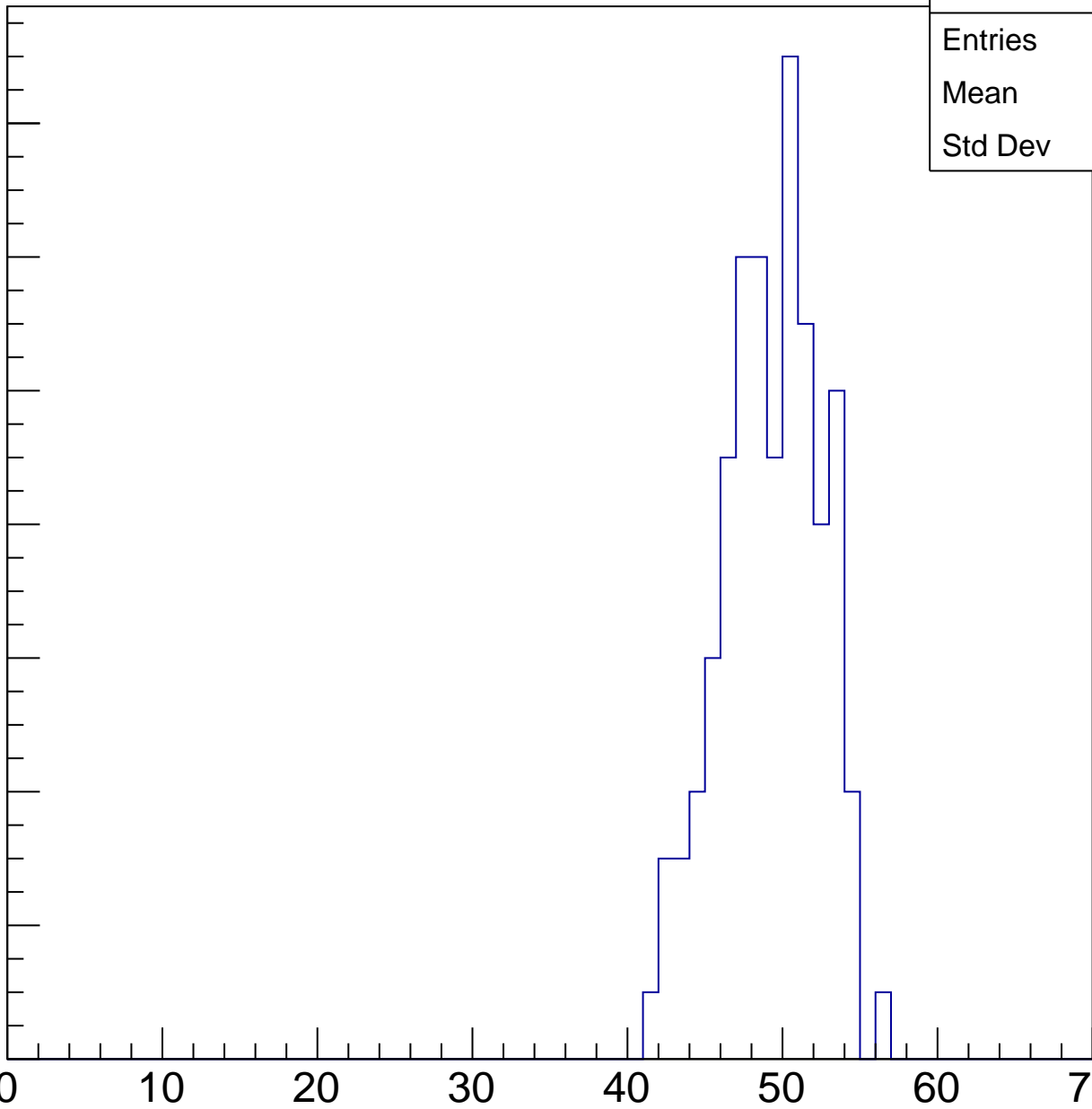
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	108
Mean	48.76
Std Dev	3.177

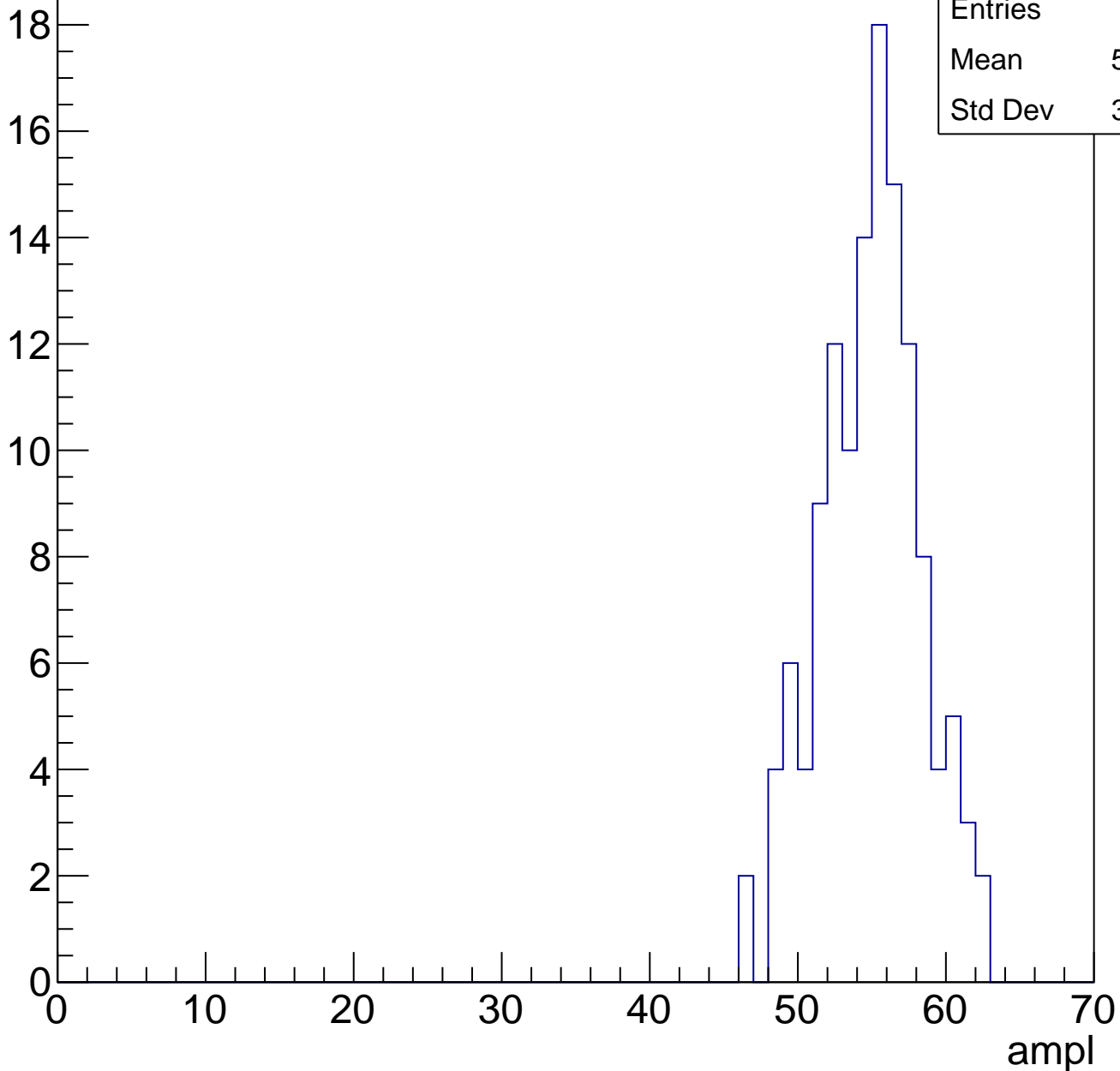
ampl



B1L001S, U20-ch111, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch111, adc5

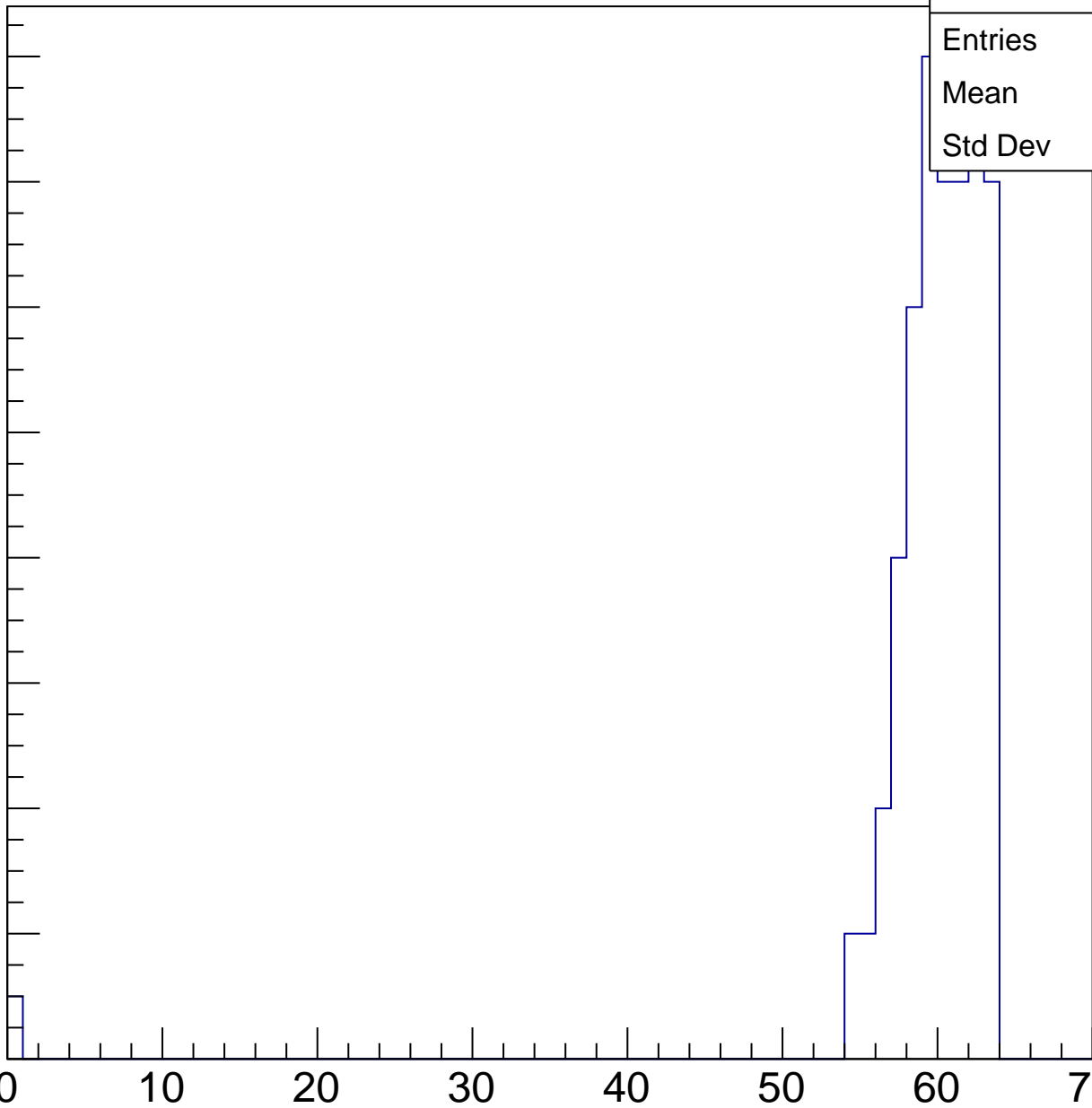
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	59.25
Std Dev	6.313

ampl

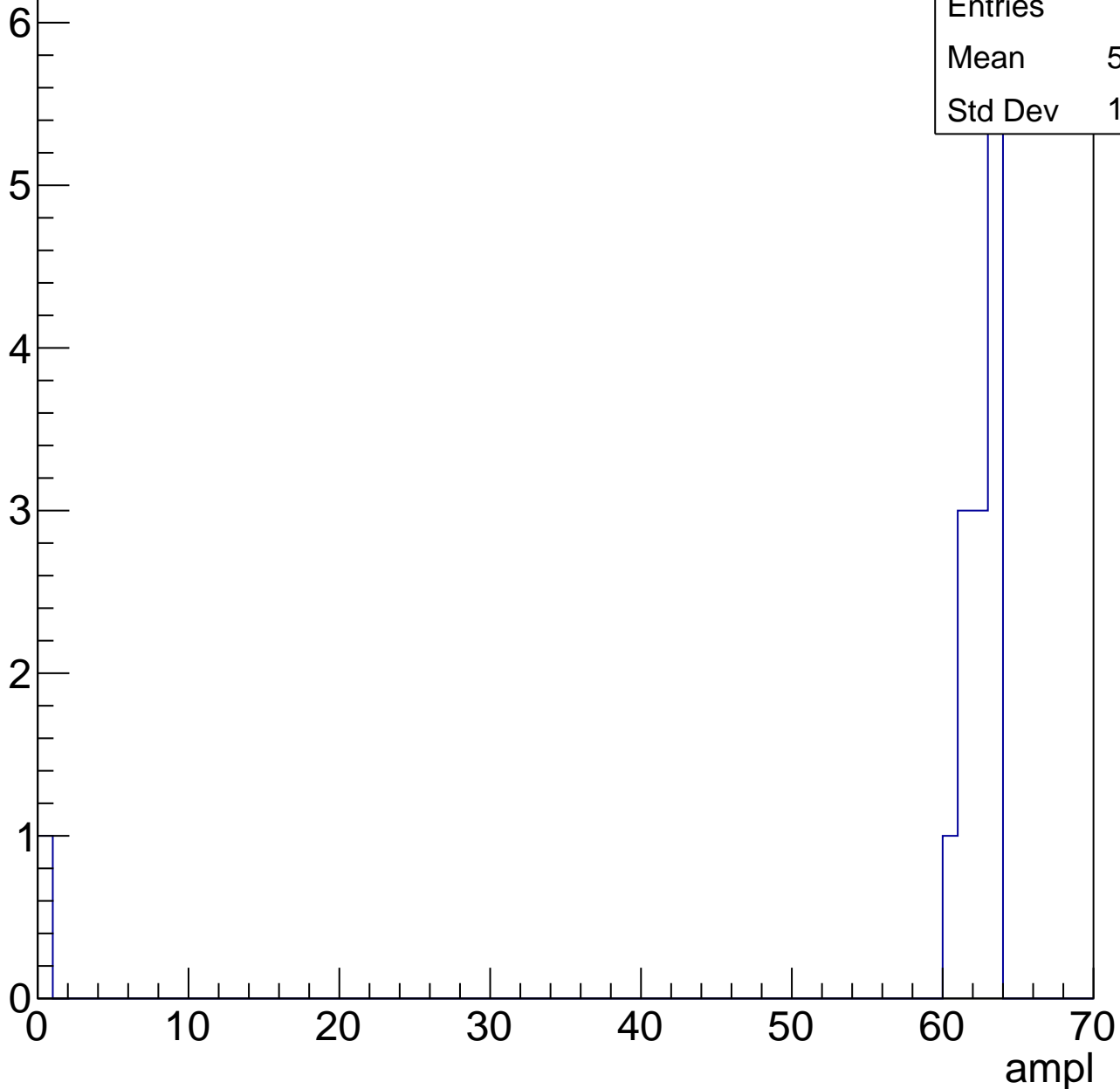


B1L001S, U20-ch111, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.64
Std Dev	16.02



B1L001S, U20-ch111, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



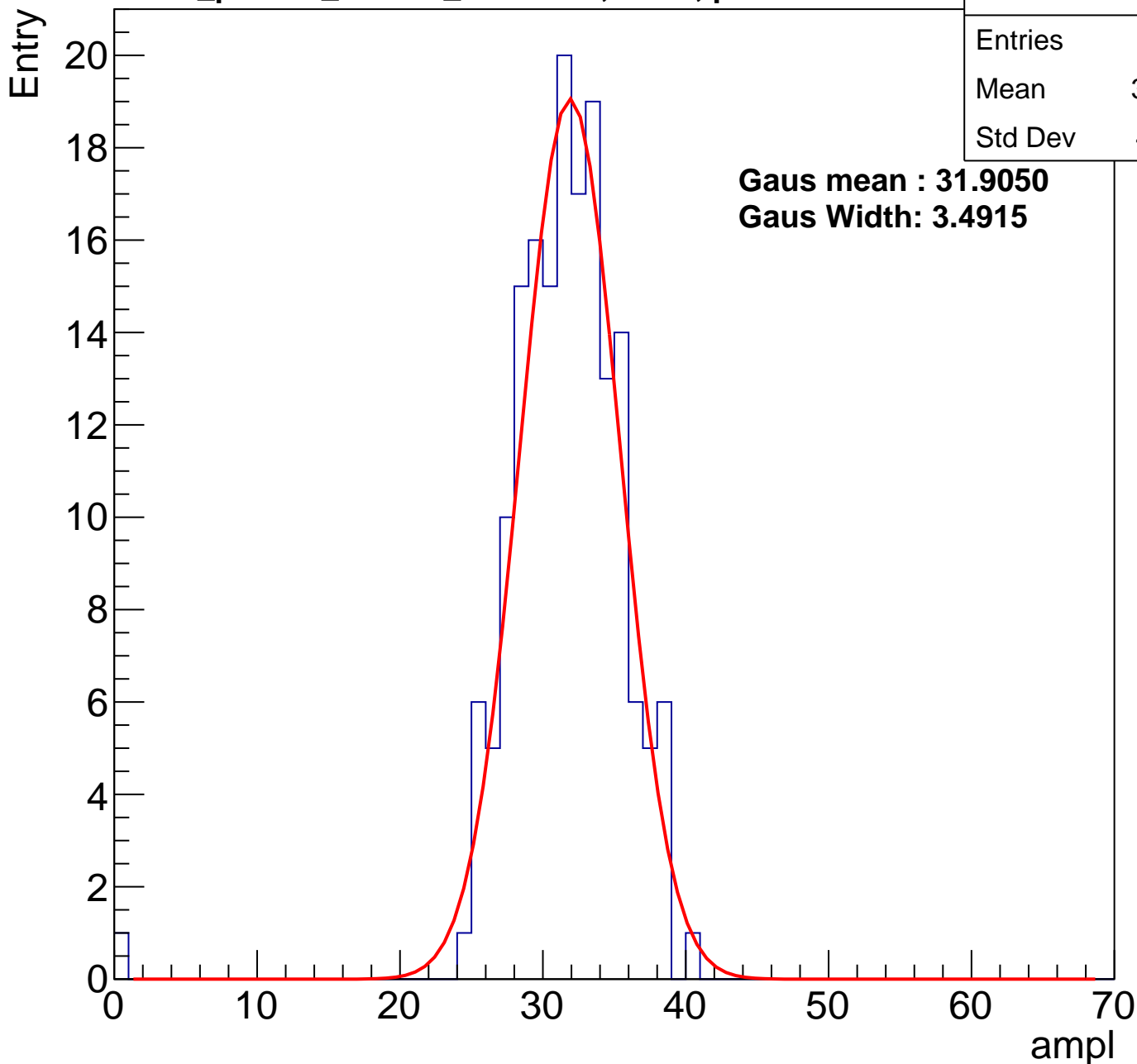
B1L001S, U20-ch112, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	170
Mean	31.18
Std Dev	4.101

Gaus mean : 31.9050

Gaus Width: 3.4915



B1L001S, U20-ch112, adc1

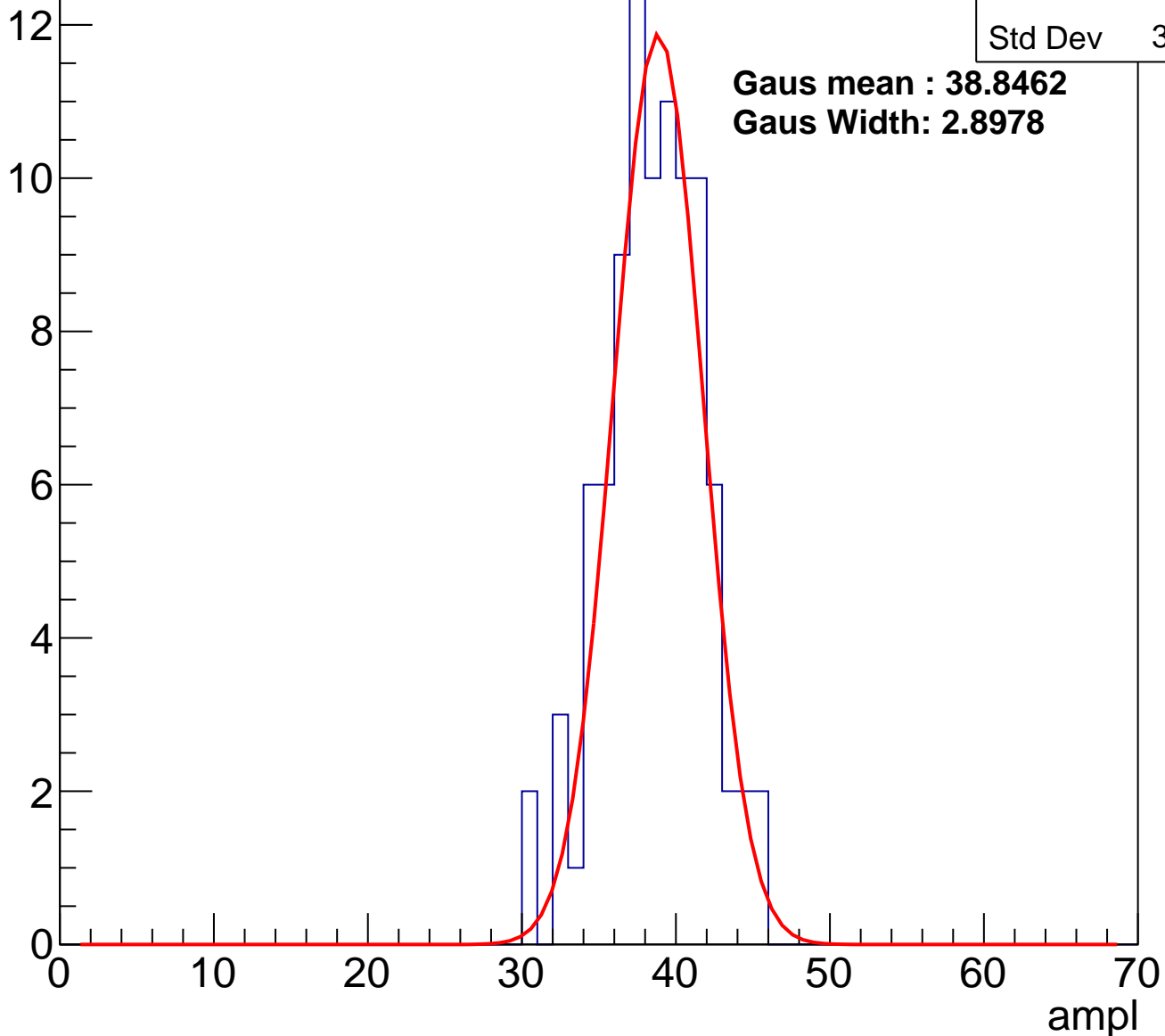
calib_packv5_042523_0143.root, FC#2, port C2

Entries	93
Mean	38.1
Std Dev	3.139

Gaus mean : 38.8462

Gaus Width: 2.8978

Entry



B1L001S, U20-ch112, adc2

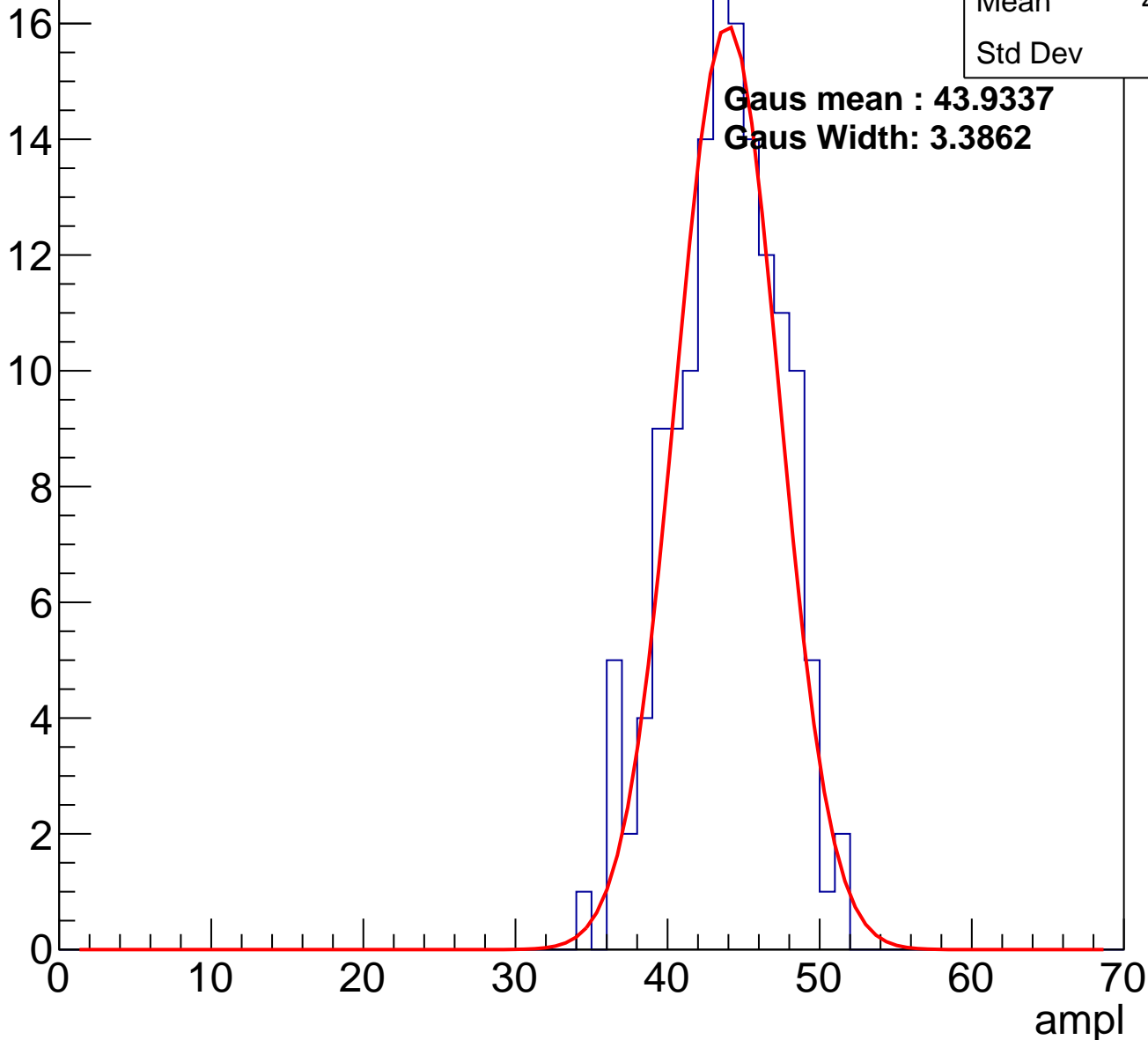
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	142
Mean	43.38
Std Dev	3.48

Gaus mean : 43.9337

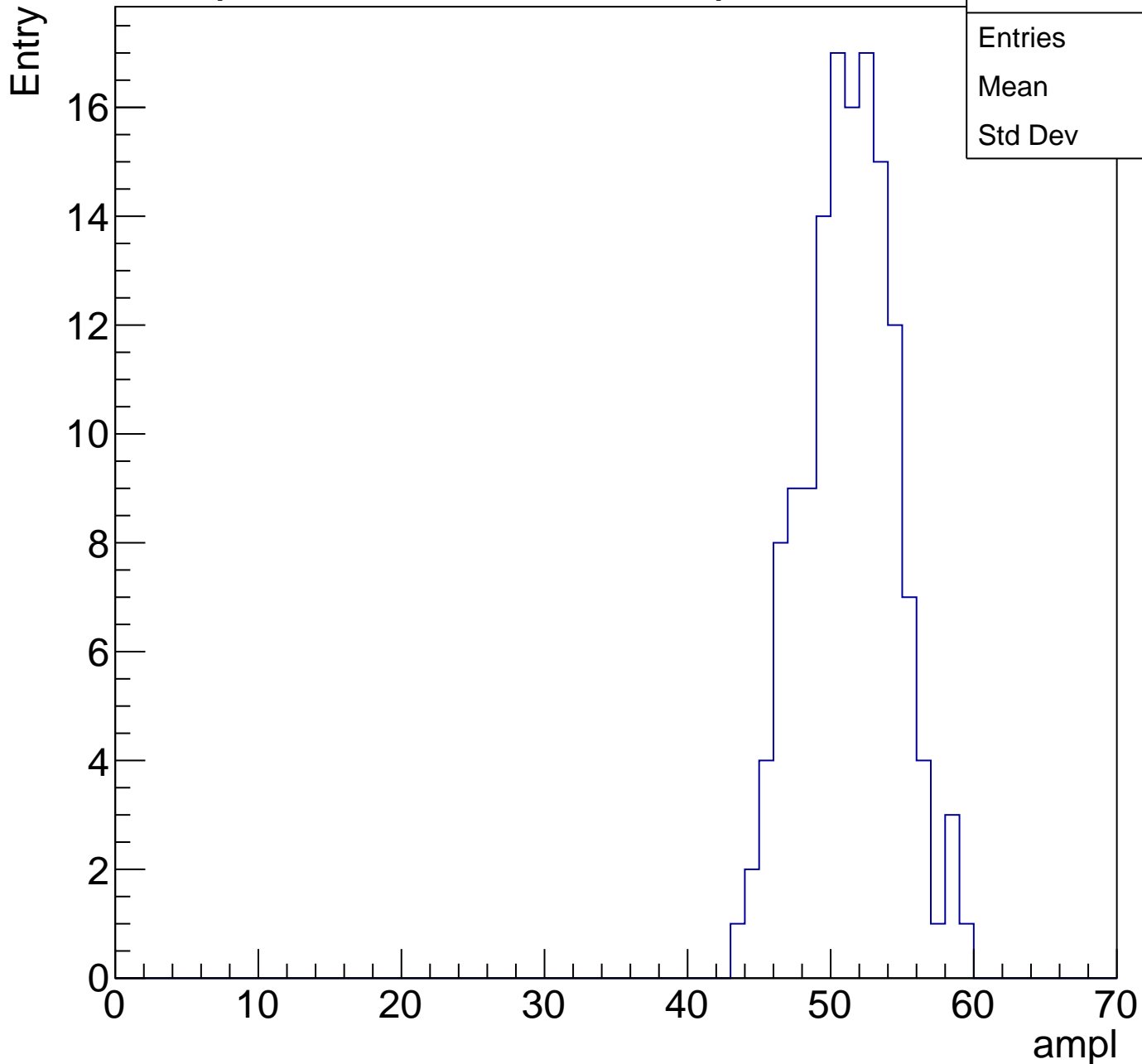
Gaus Width: 3.3862



B1L001S, U20-ch112, adc3

calib_packv5_042523_0143.root, FC#2, port C2

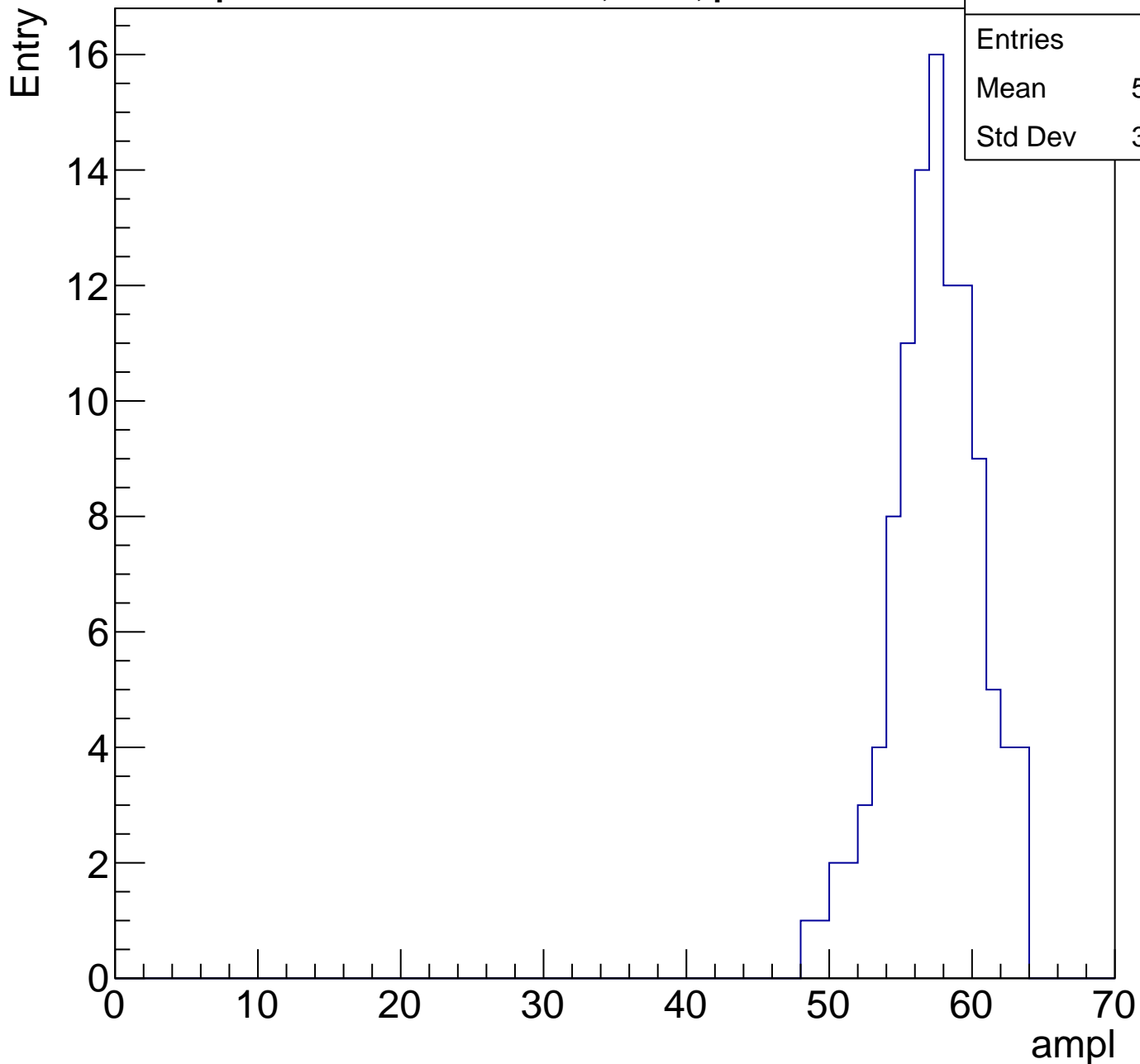
Entries	140
Mean	50.8
Std Dev	3.21



B1L001S, U20-ch112, adc4

calib_packv5_042523_0143.root, FC#2, port C2

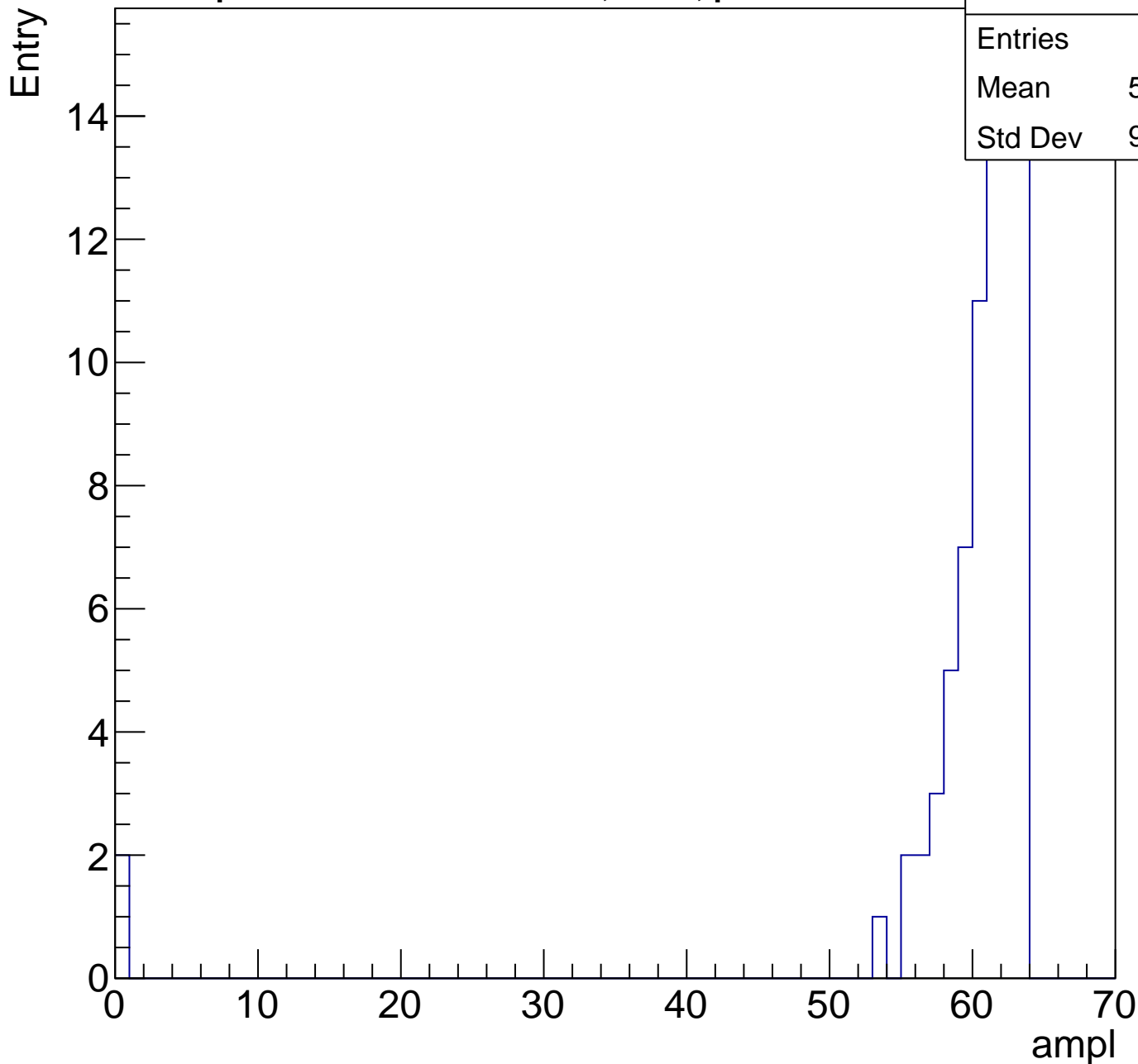
Entries	108
Mean	56.94
Std Dev	3.116



B1L001S, U20-ch112, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	76
Mean	58.89
Std Dev	9.932



B1L001S, U20-ch112, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

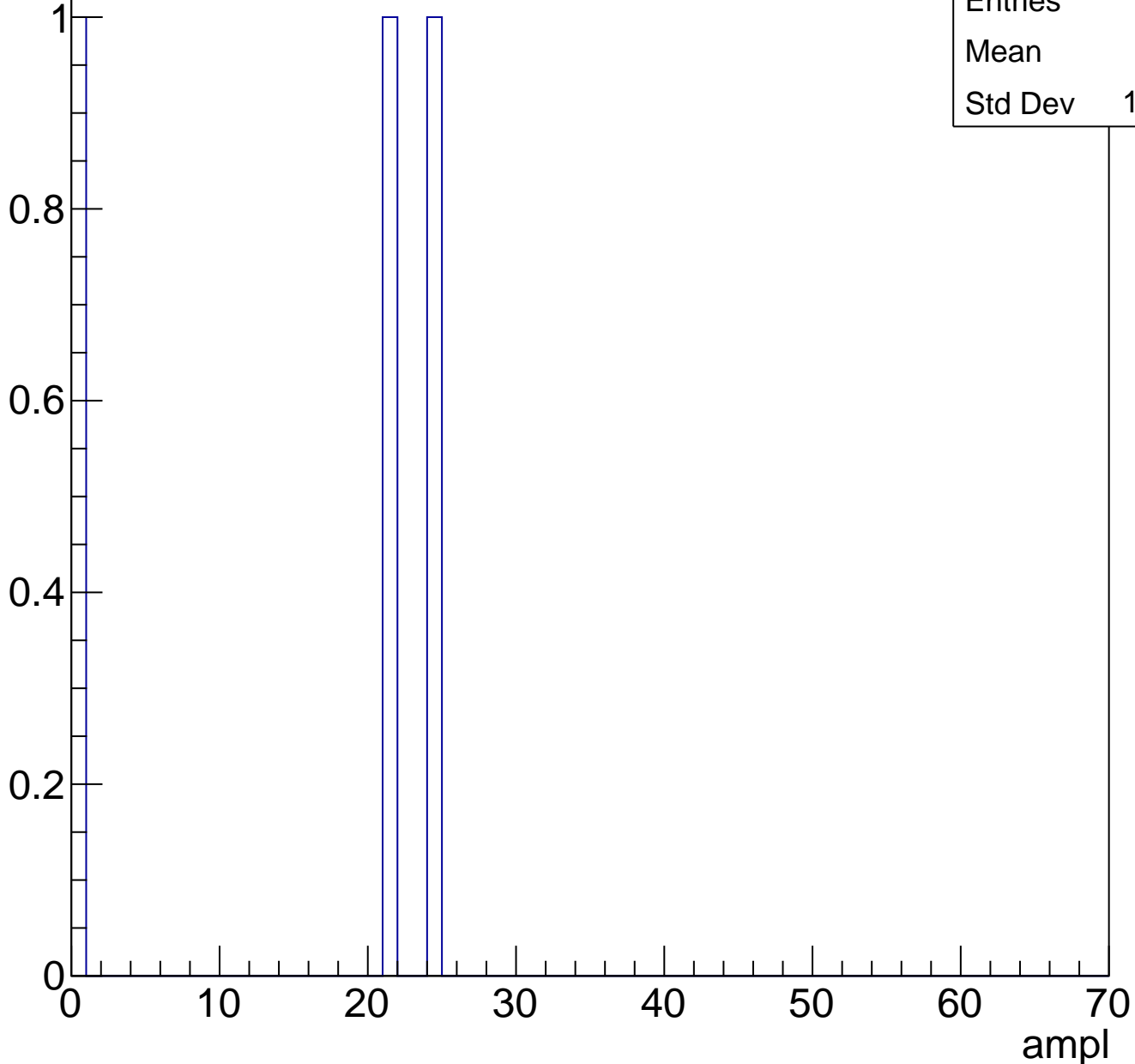


Entries	4
Mean	62
Std Dev	0.7071

B1L001S, U20-ch112, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch113, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	29.64
Std Dev	4.016

Gaus mean : 30.3629

Gaus Width: 3.2456

Entry

18
16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U20-ch113, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	36.35
Std Dev	2.926

Gaus mean : 36.5184

Gaus Width: 2.6922

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

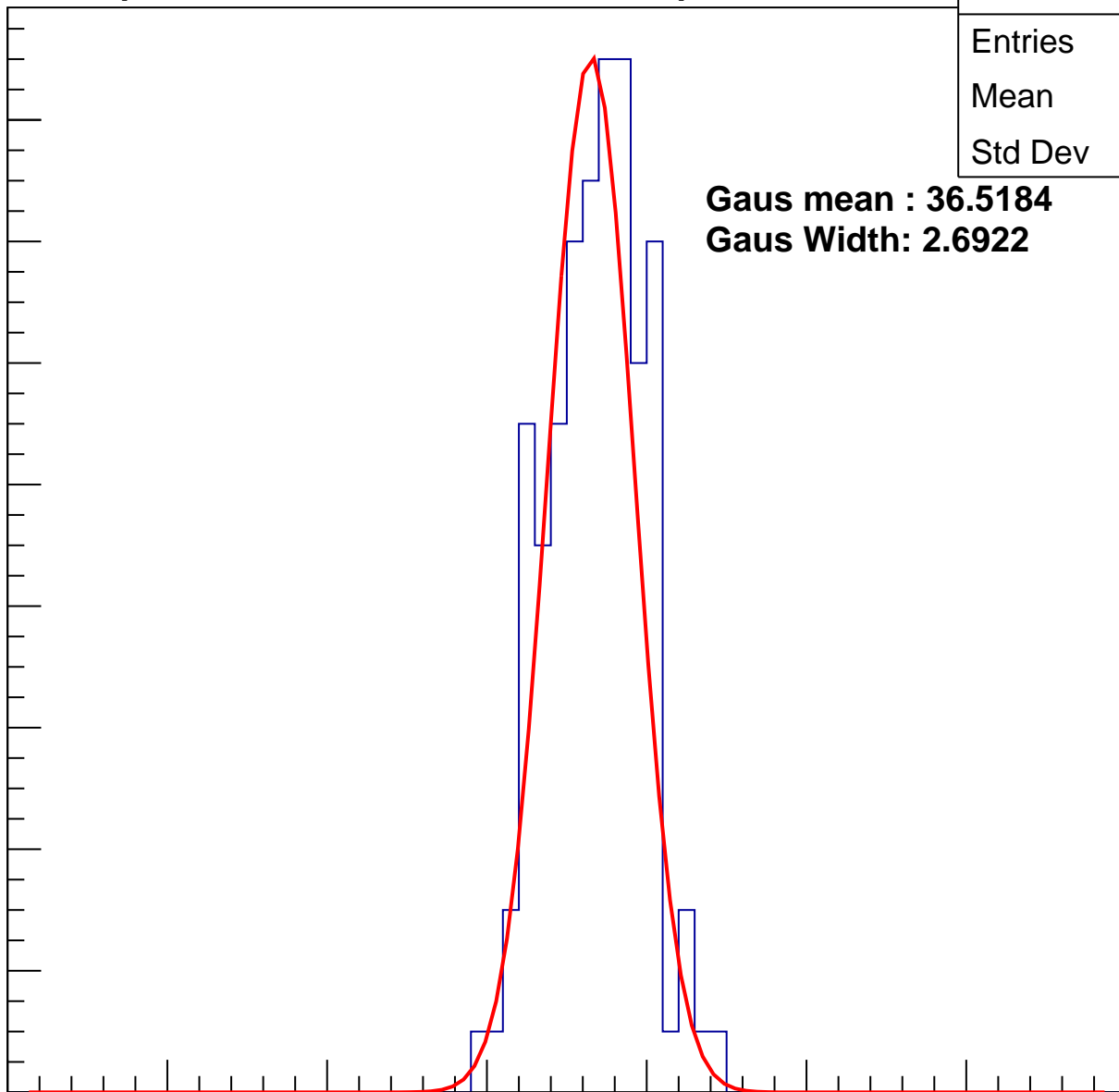
40

50

60

70

ampl



B1L001S, U20-ch113, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

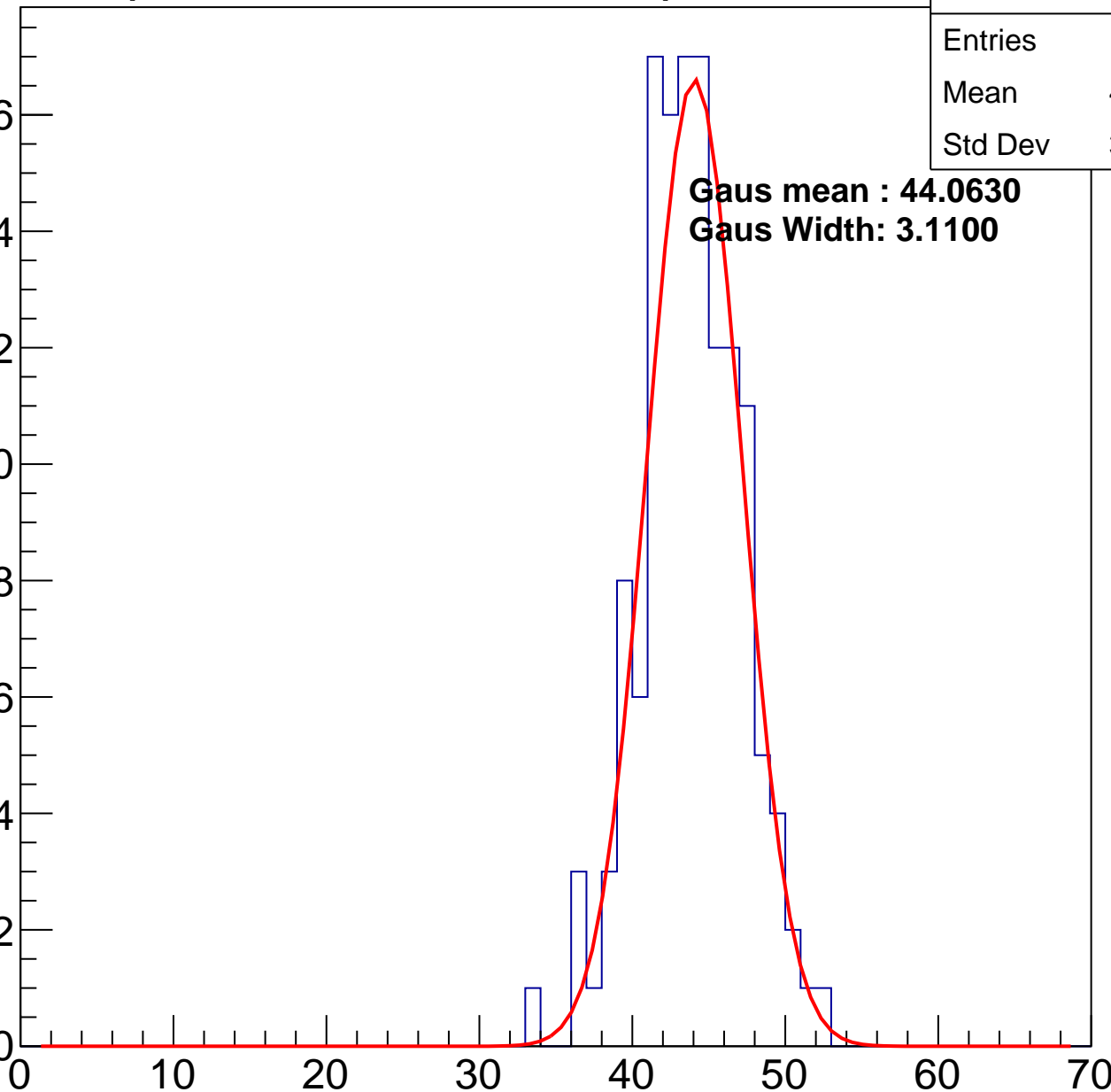
Entries	137
Mean	43.36
Std Dev	3.273

Gaus mean : 44.0630

Gaus Width: 3.1100

16
14
12
10
8
6
4
2
0

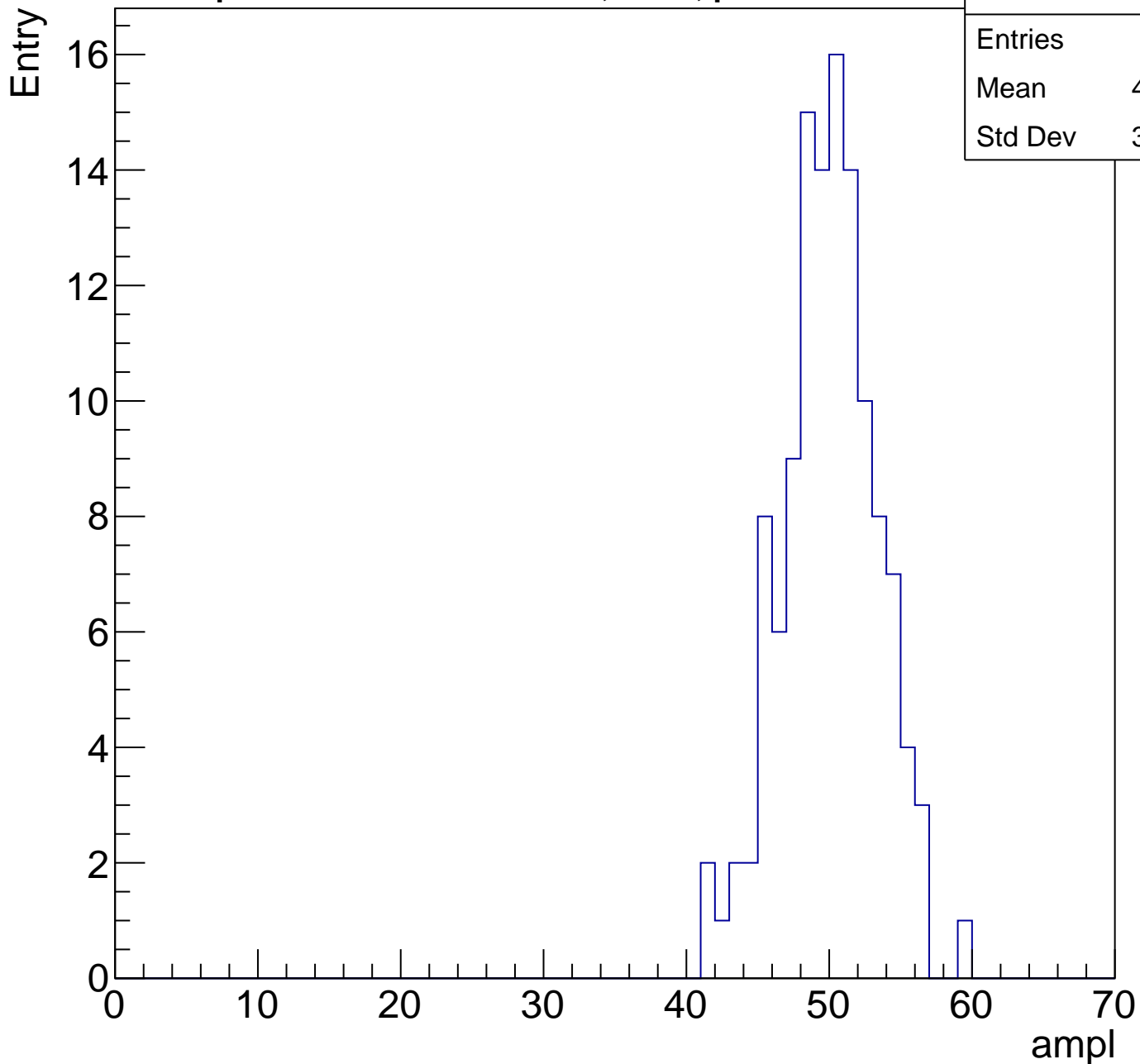
ampl



B1L001S, U20-ch113, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	49.56
Std Dev	3.327



B1L001S, U20-ch113, adc4

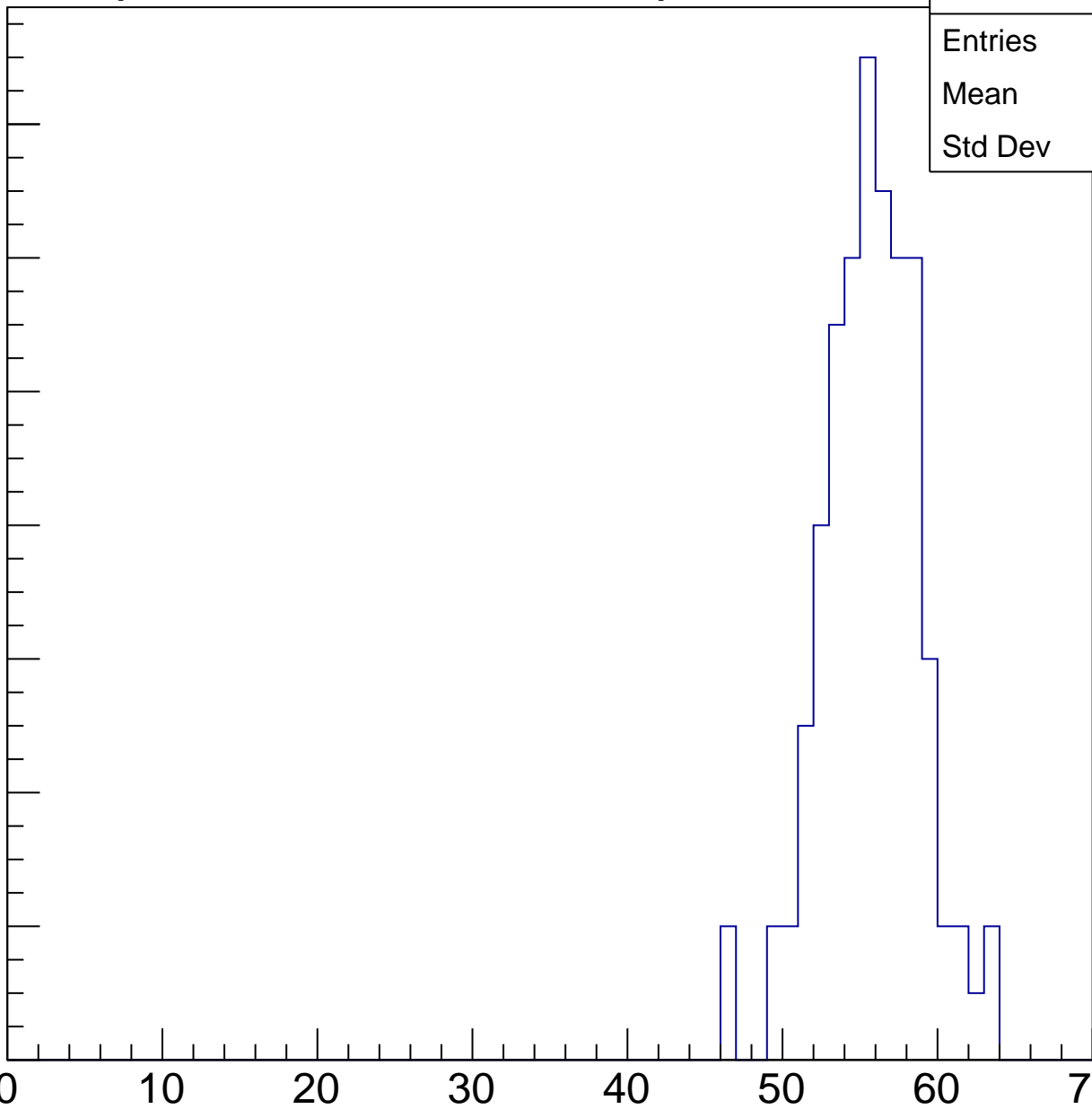
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	107
Mean	55.22
Std Dev	3.134

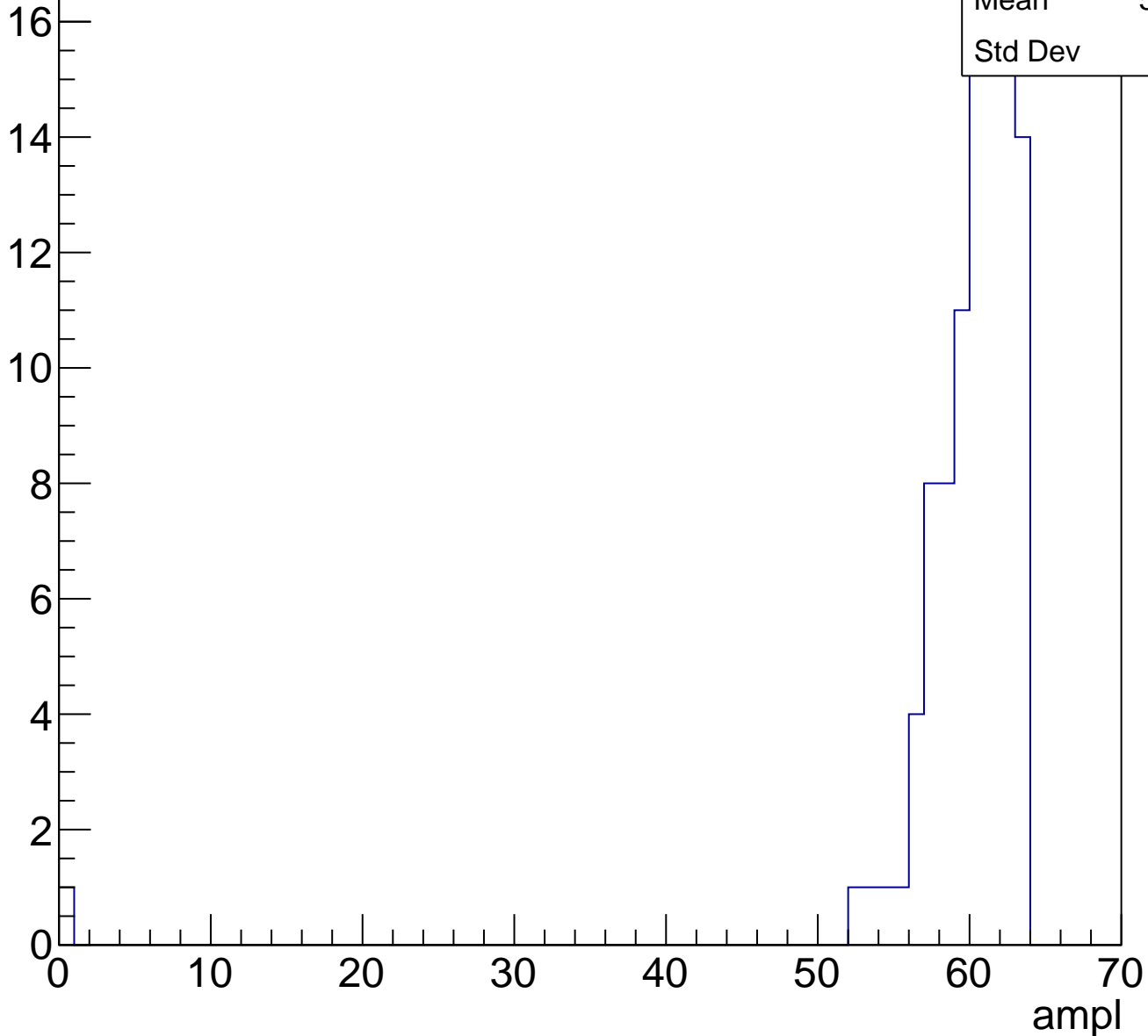
ampl



B1L001S, U20-ch113, adc5

calib_packv5_042523_0143.root, FC#2, port C2

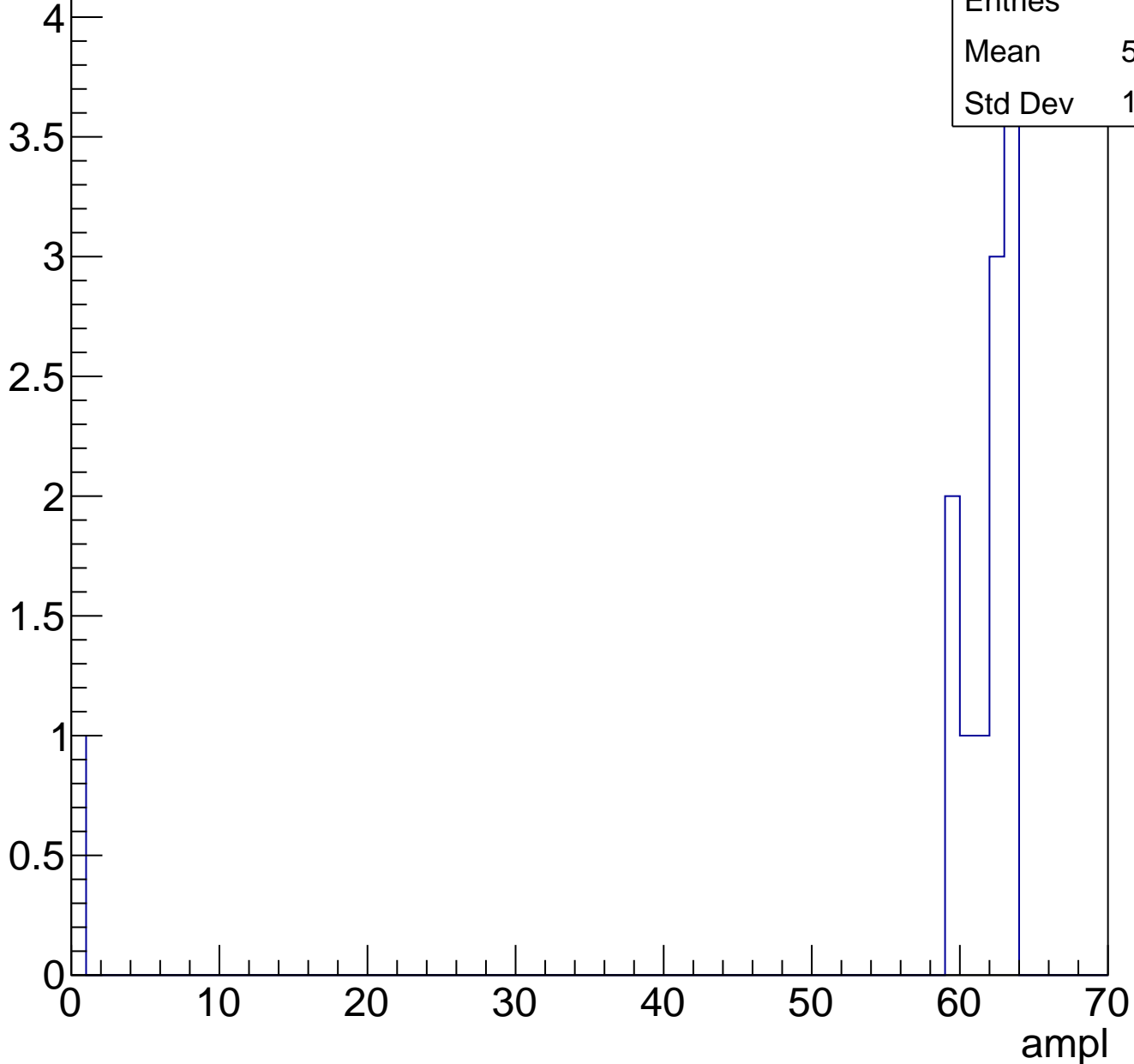
Entry



B1L001S, U20-ch113, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	12
Mean	56.42
Std Dev	17.07

B1L001S, U20-ch113, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L001S, U20-ch114, adc0

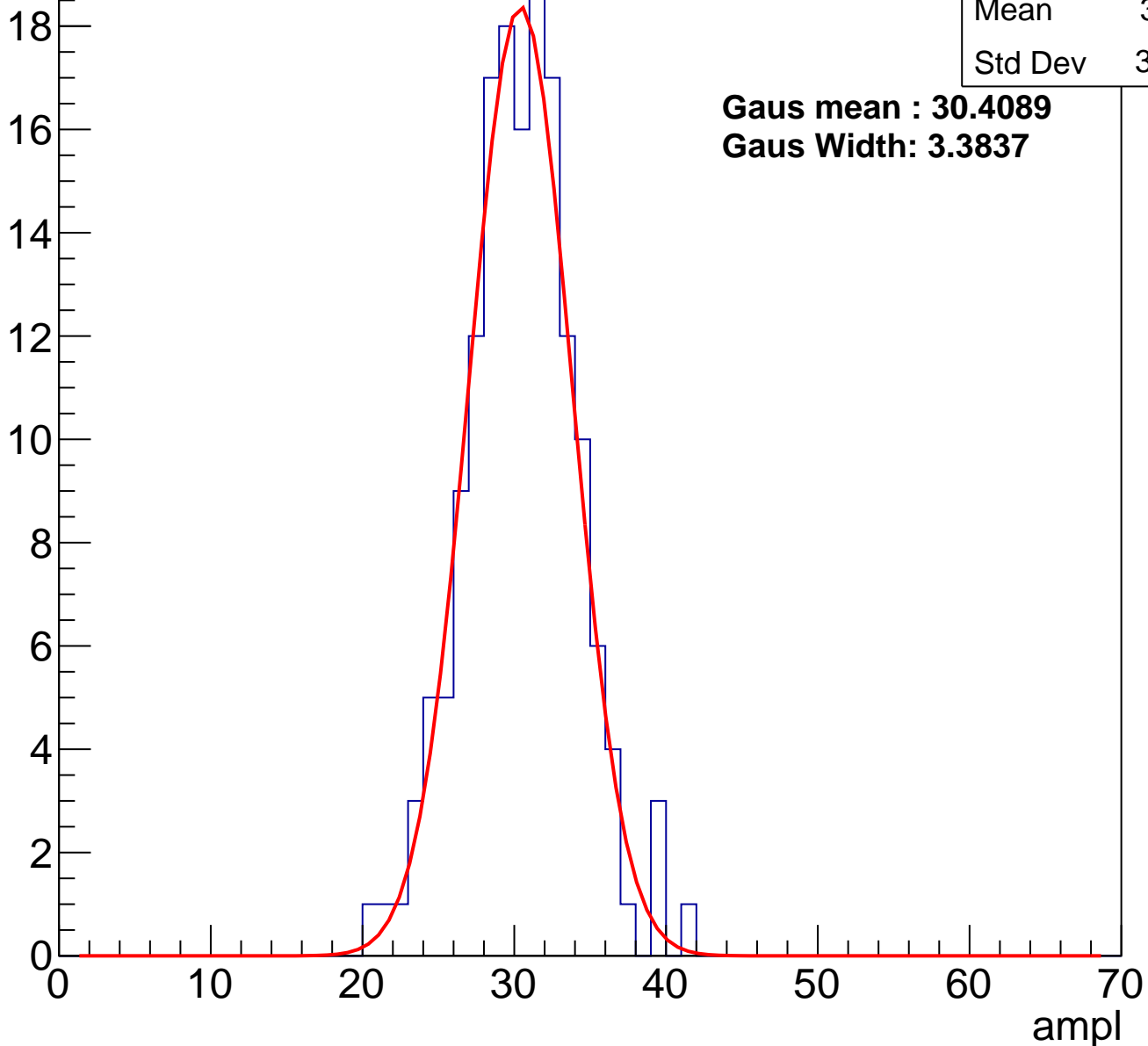
calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	30.01
Std Dev	3.609

Gaus mean : 30.4089

Gaus Width: 3.3837

Entry



B1L001S, U20-ch114, adc1

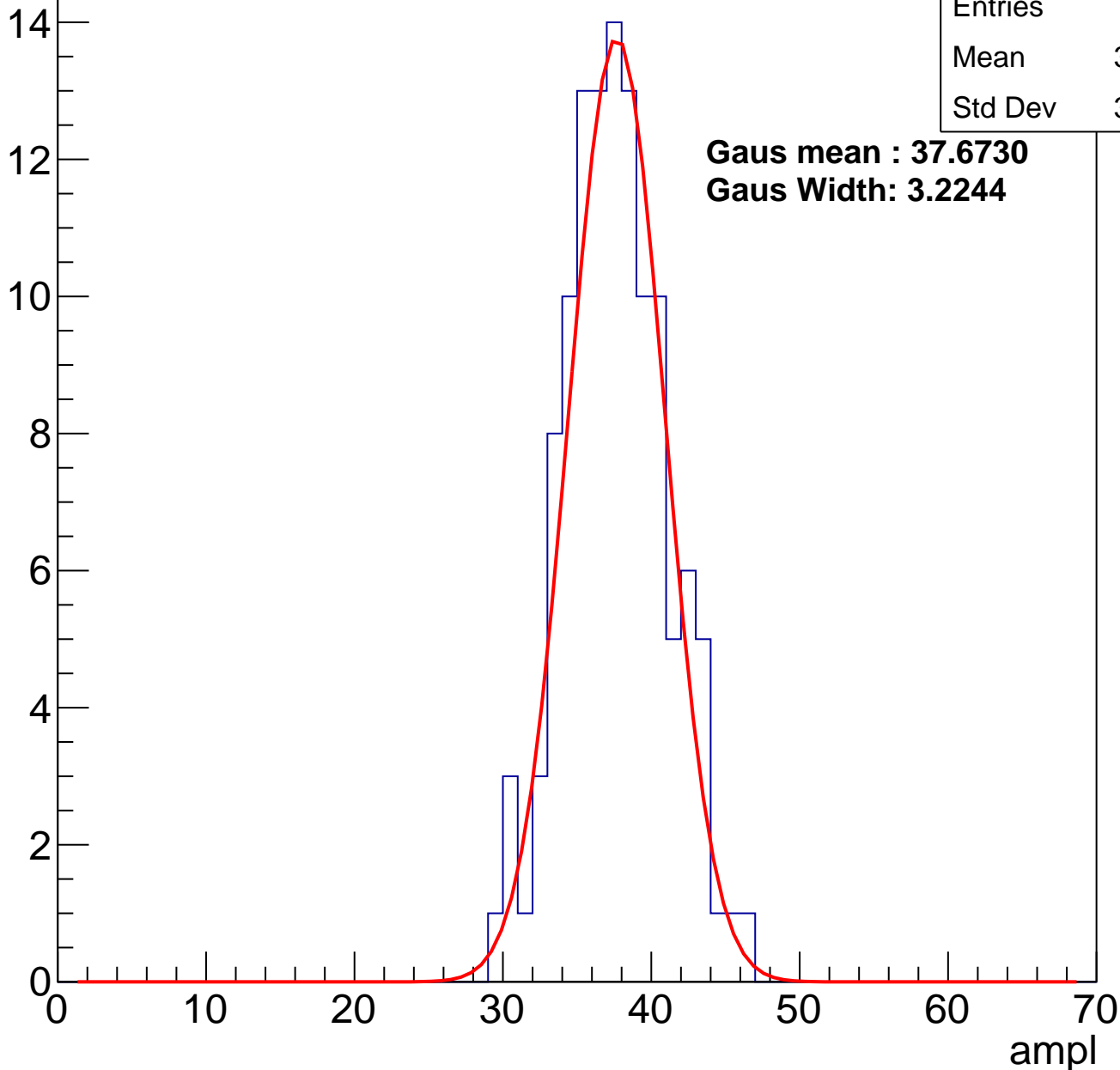
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	37.14
Std Dev	3.382

Gaus mean : 37.6730

Gaus Width: 3.2244

Entry



B1L001S, U20-ch114, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

102

Mean

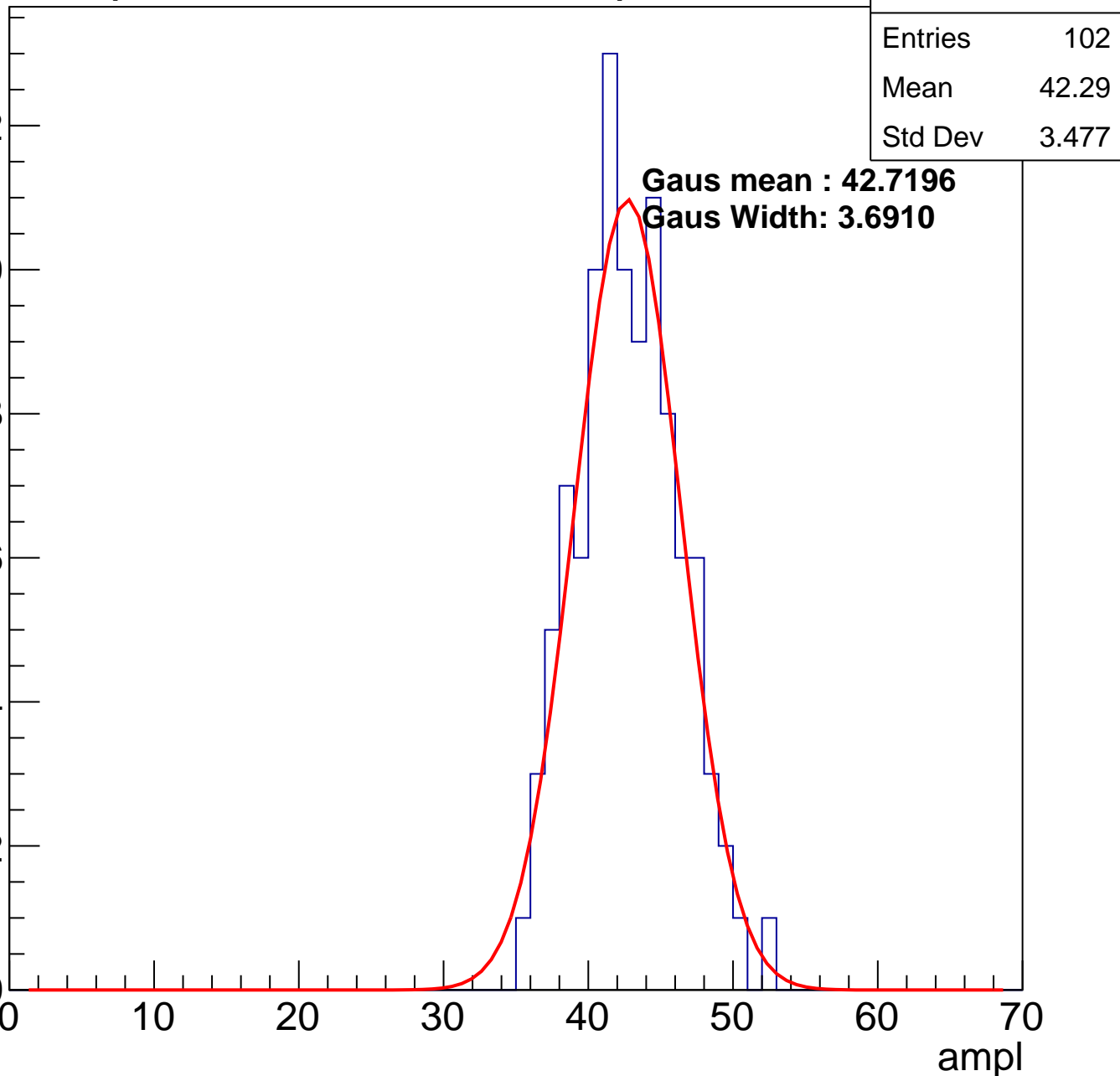
42.29

Std Dev

3.477

Gaus mean : 42.7196

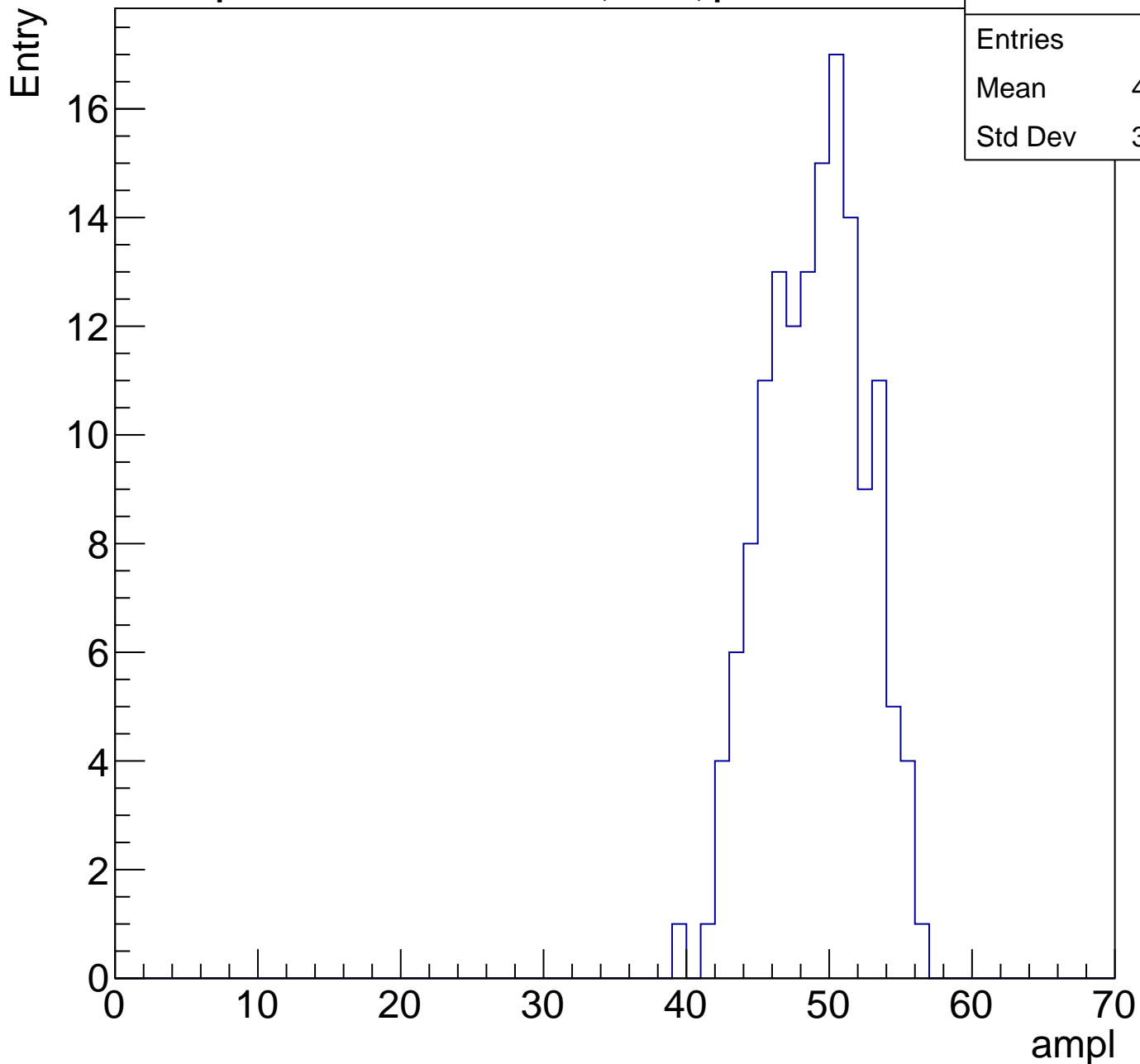
Gaus Width: 3.6910



B1L001S, U20-ch114, adc3

calib_packv5_042523_0143.root, FC#2, port C2

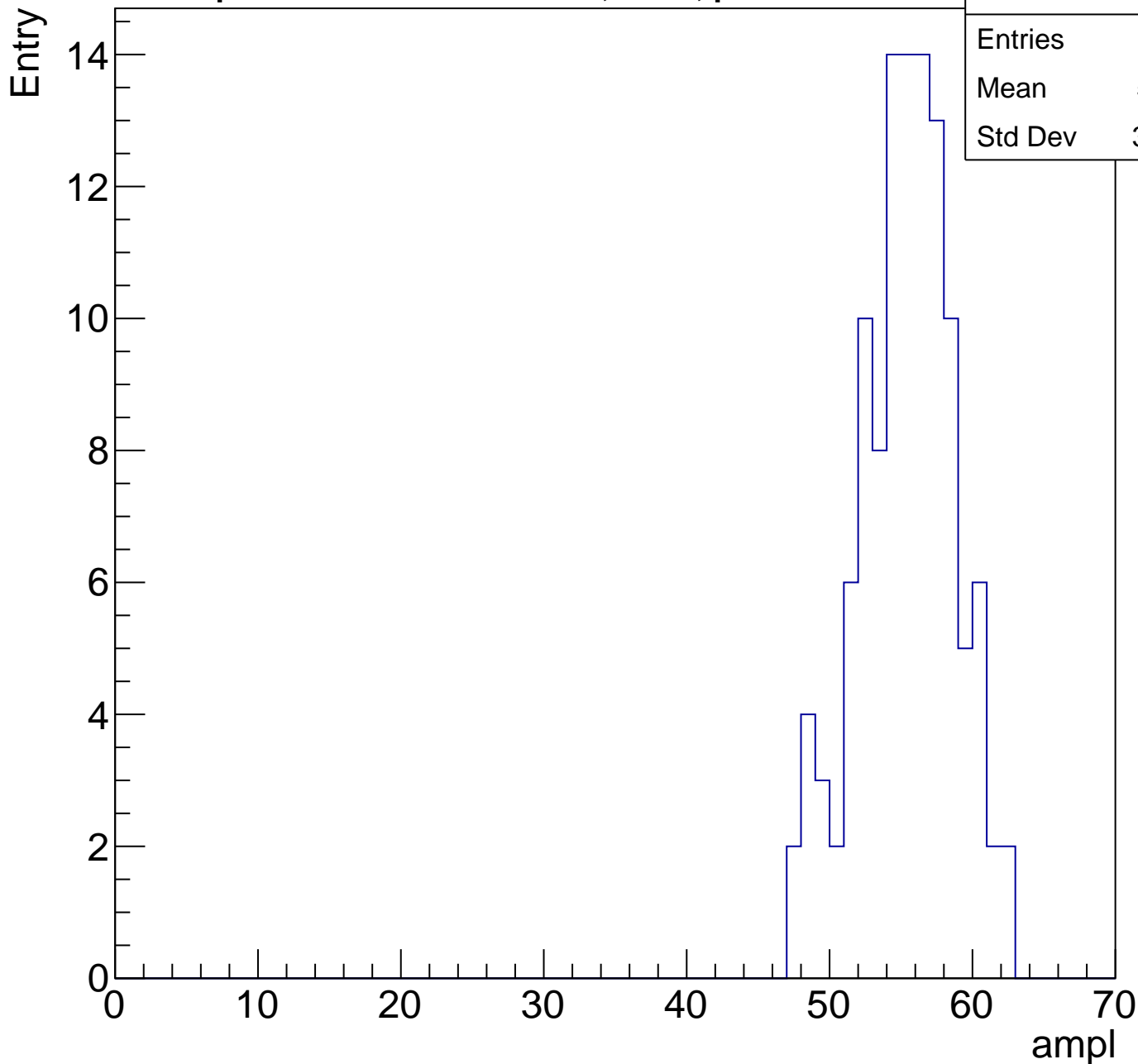
Entries	145
Mean	48.52
Std Dev	3.468



B1L001S, U20-ch114, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	54.91
Std Dev	3.322



B1L001S, U20-ch114, adc5

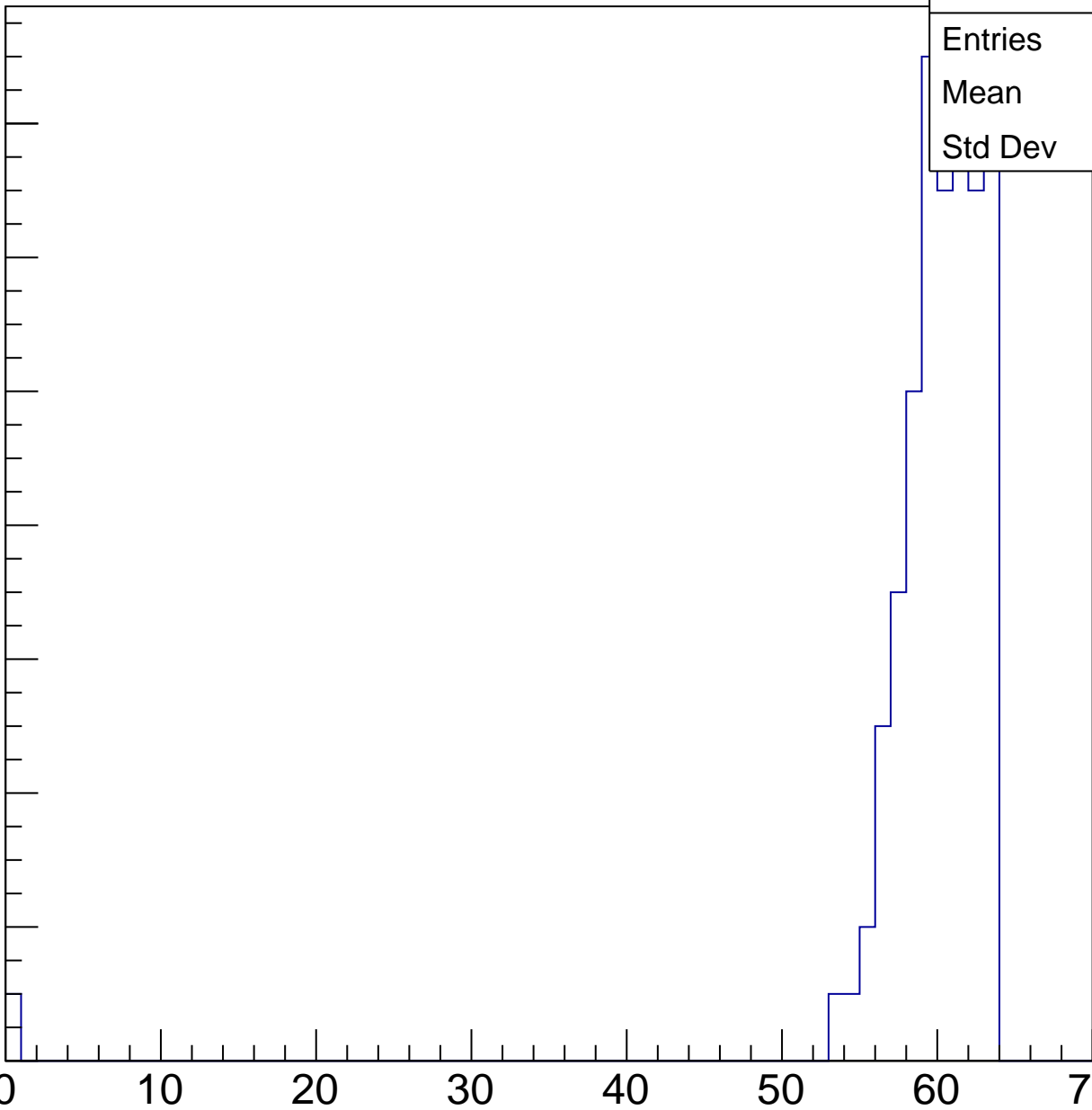
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	59.22
Std Dev	6.475

ampl

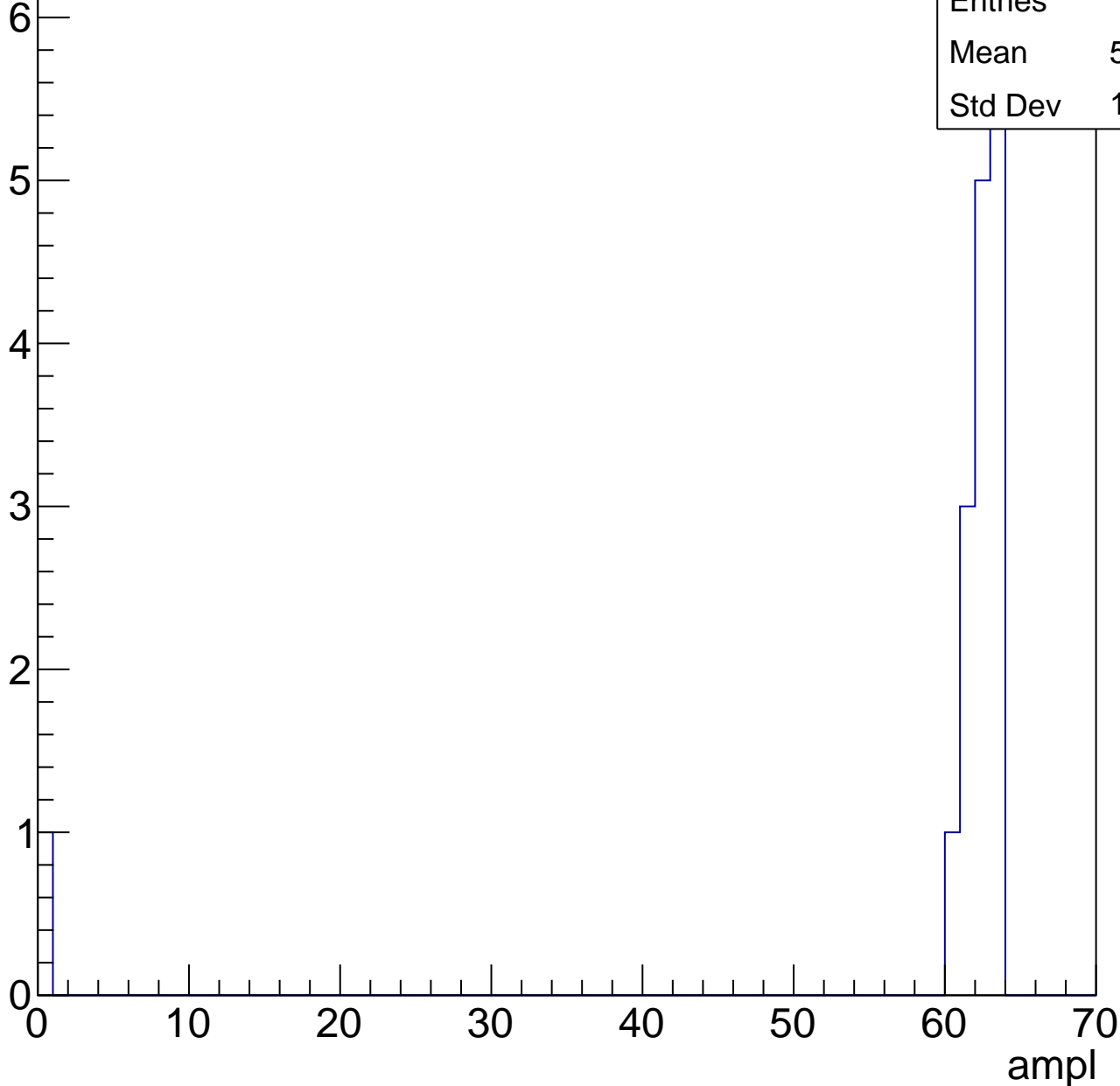


B1L001S, U20-ch114, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

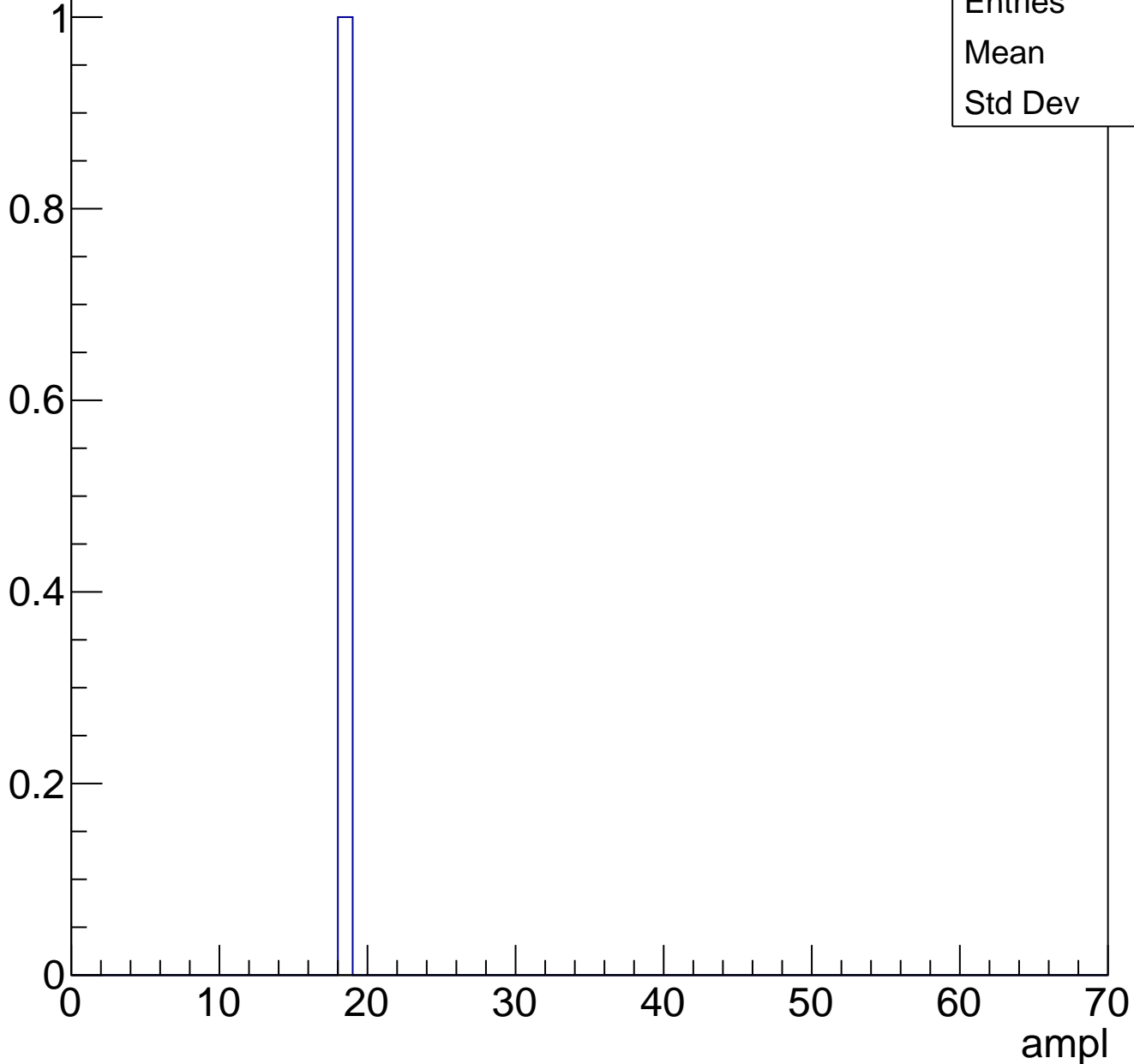
Entries	16
Mean	58.19
Std Dev	15.05



B1L001S, U20-ch114, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch115, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	31.13
Std Dev	3.593

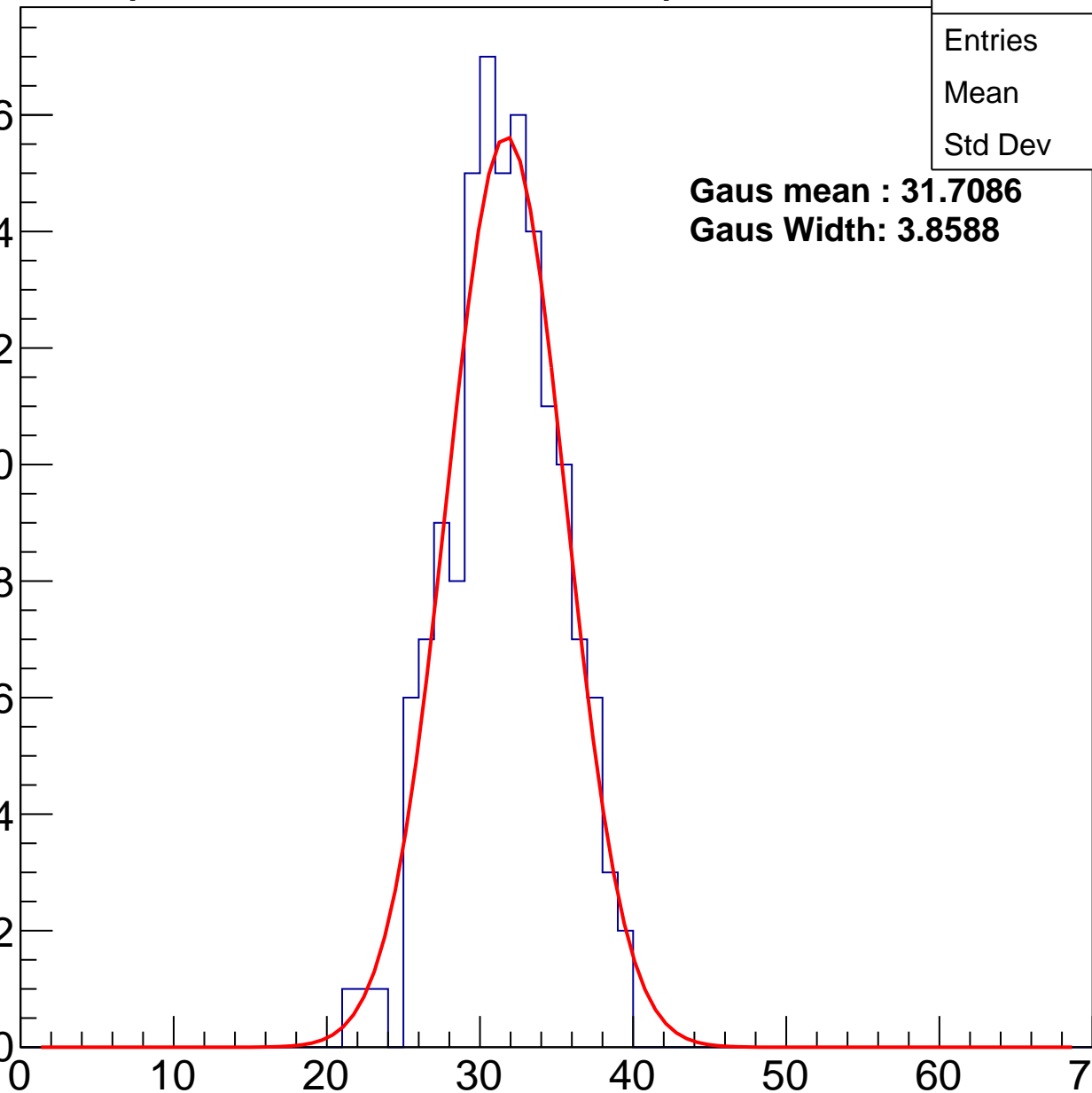
Gaus mean : 31.7086

Gaus Width: 3.8588

Entry

16
14
12
10
8
6
4
2
0

ampl



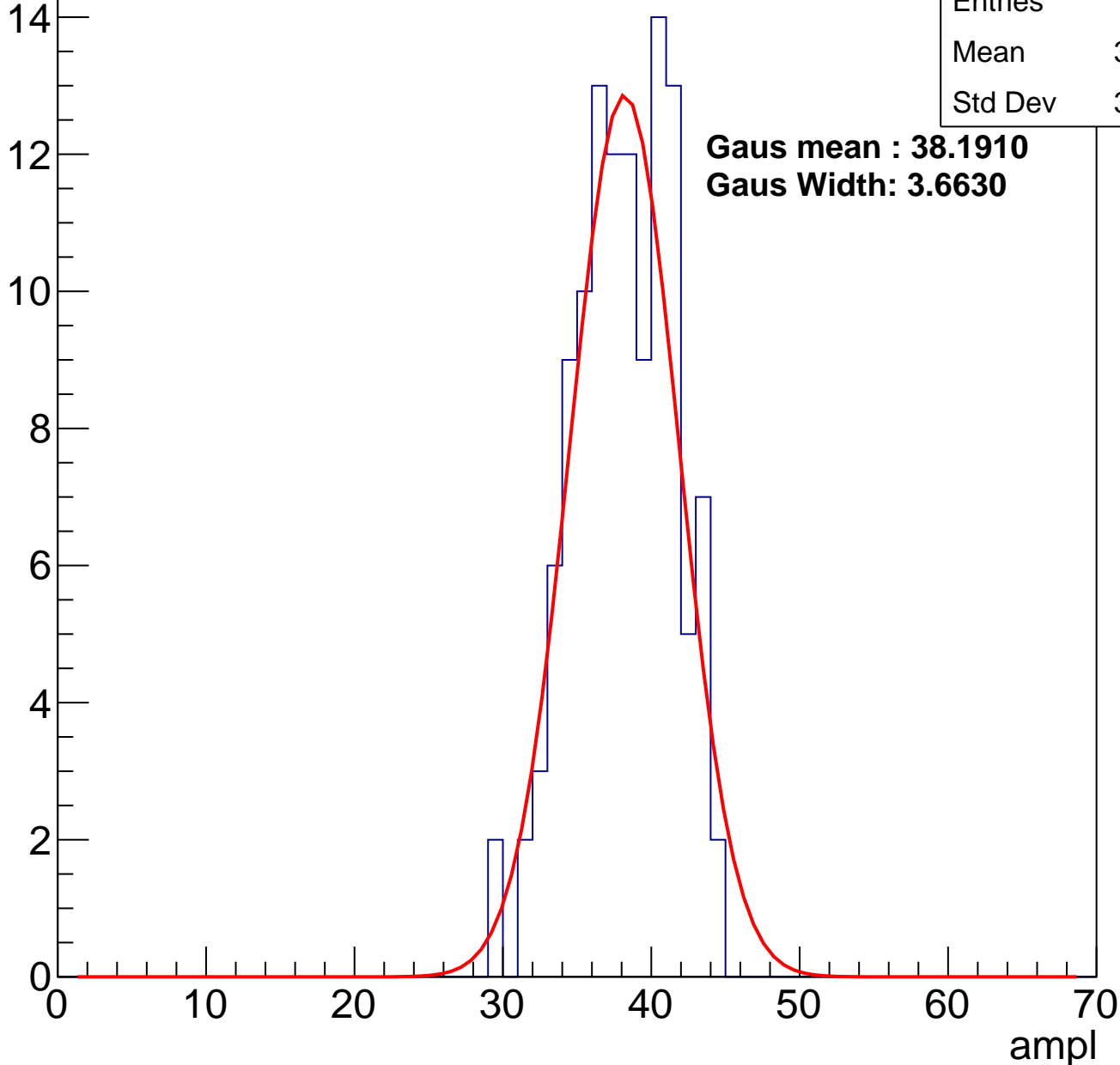
B1L001S, U20-ch115, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	37.66
Std Dev	3.319

Gaus mean : 38.1910
Gaus Width: 3.6630

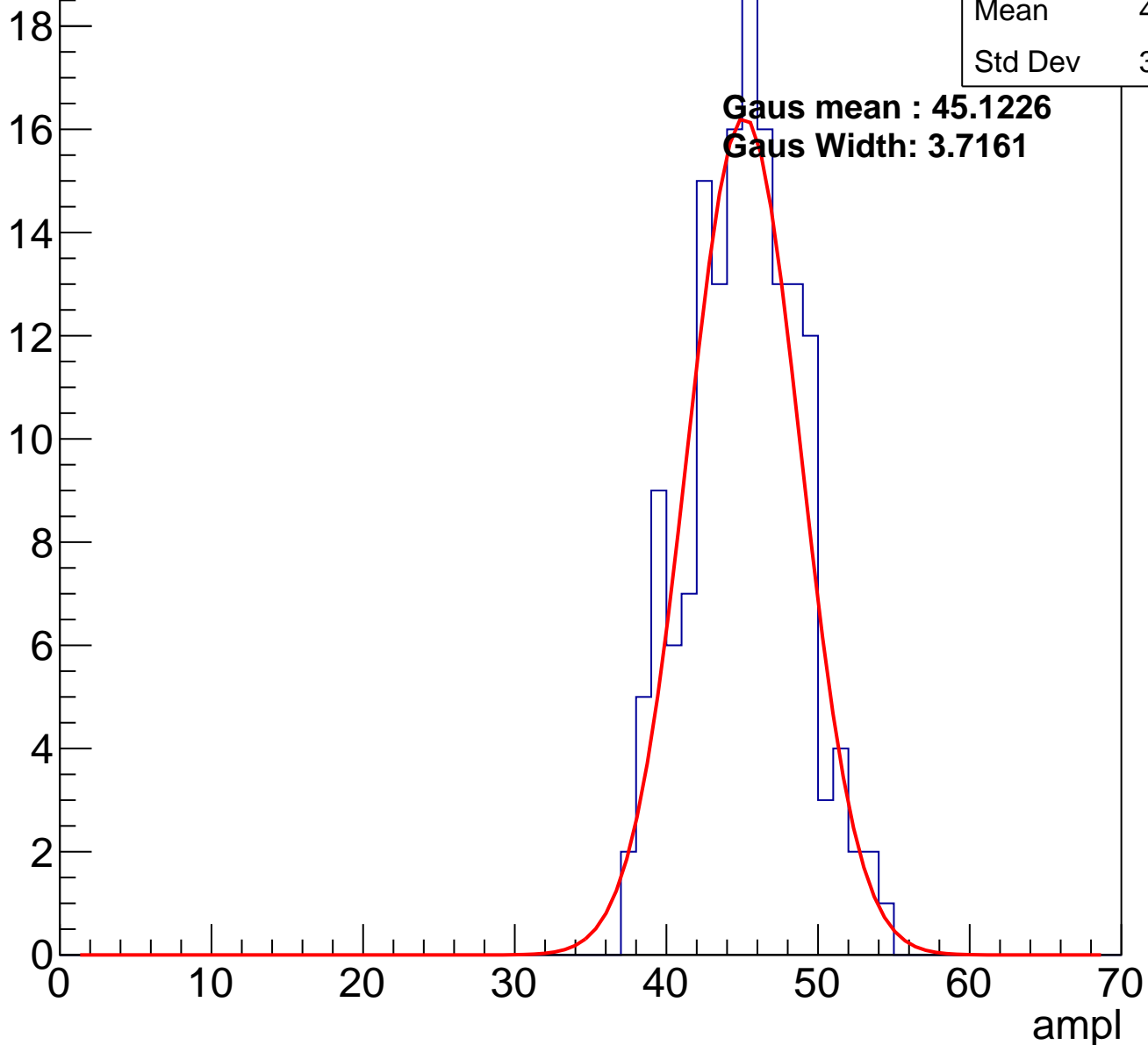
Entry



B1L001S, U20-ch115, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch115, adc3

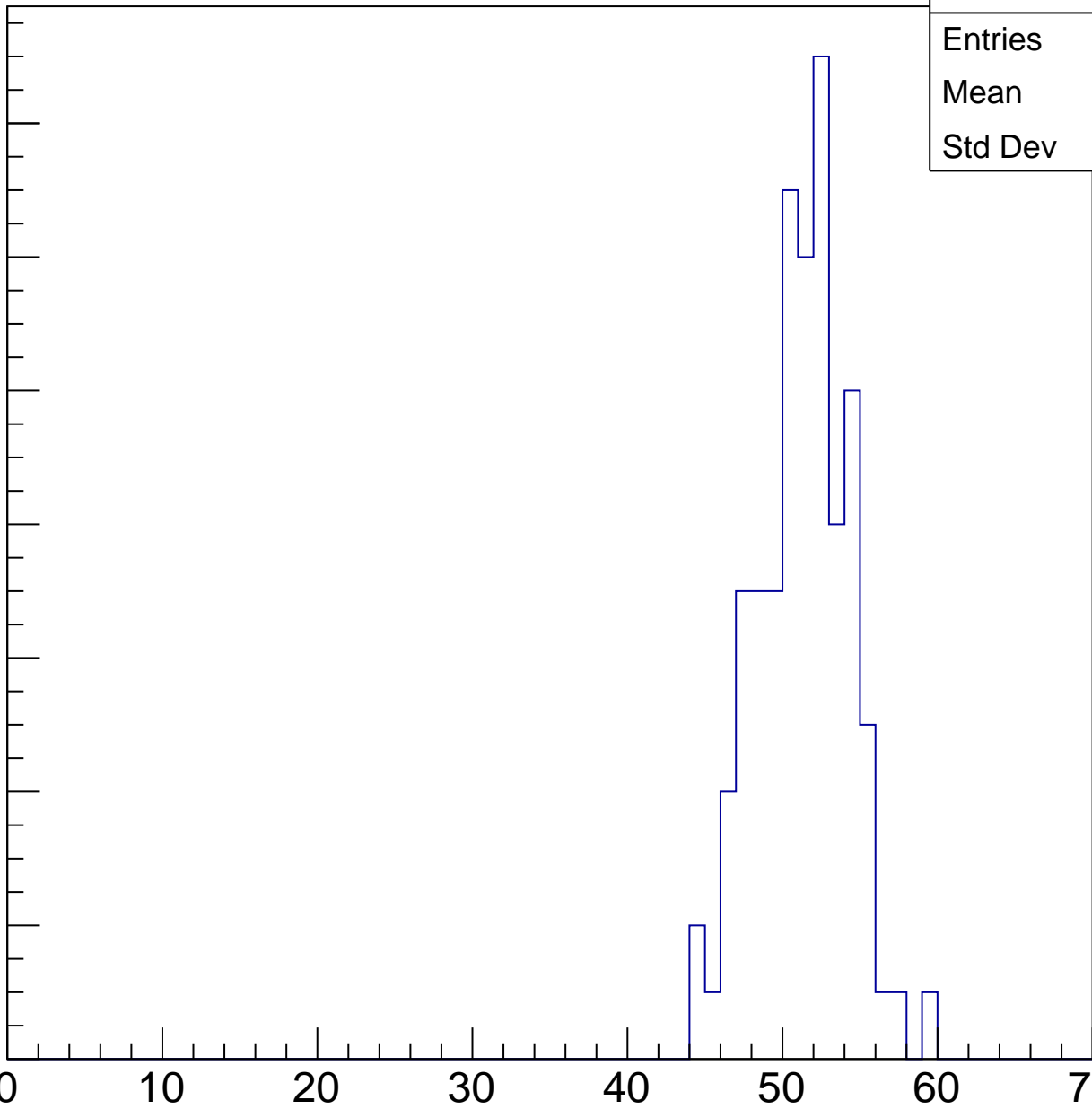
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	50.83
Std Dev	2.898

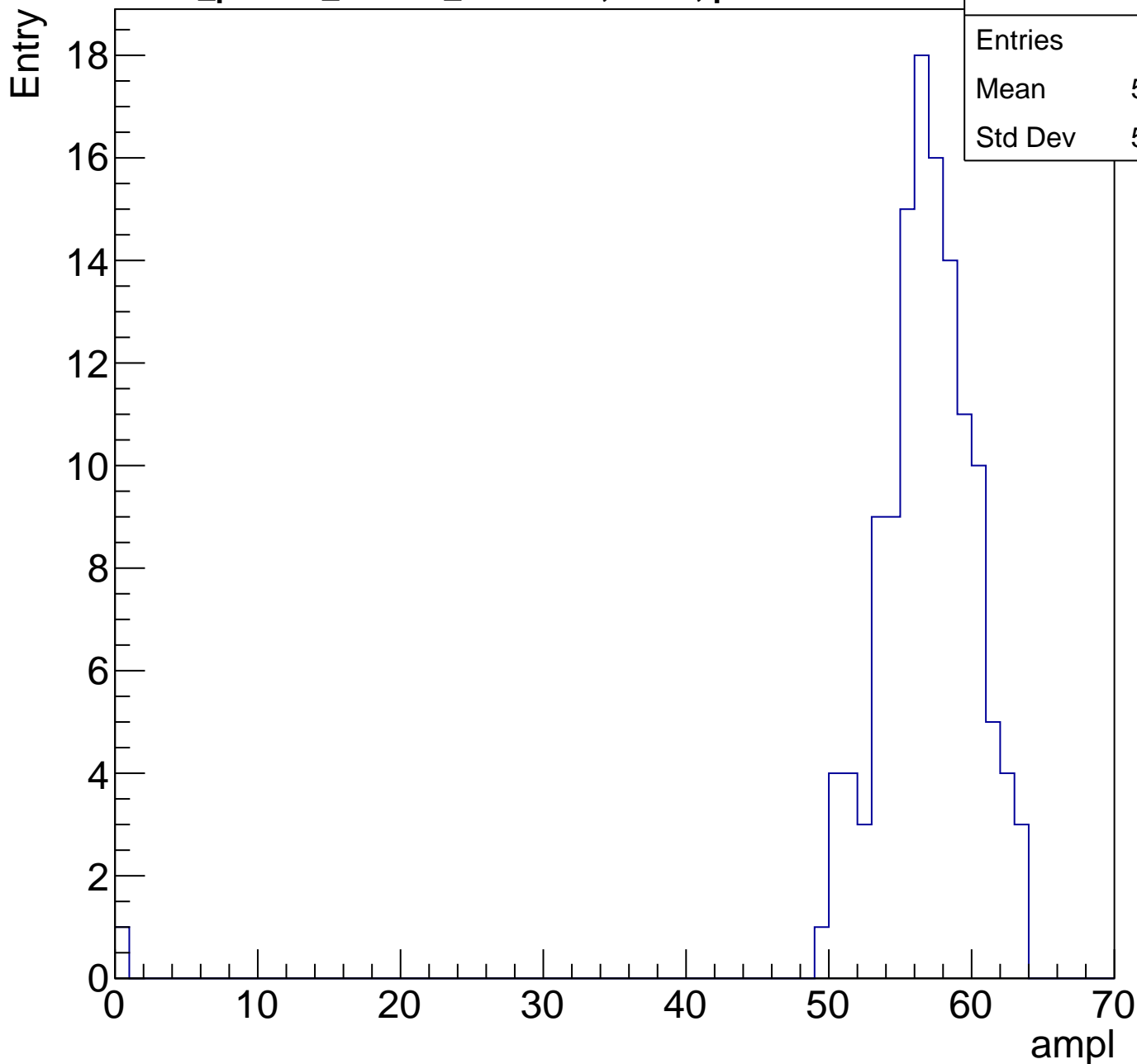
ampl



B1L001S, U20-ch115, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	56.06
Std Dev	5.856



B1L001S, U20-ch115, adc5

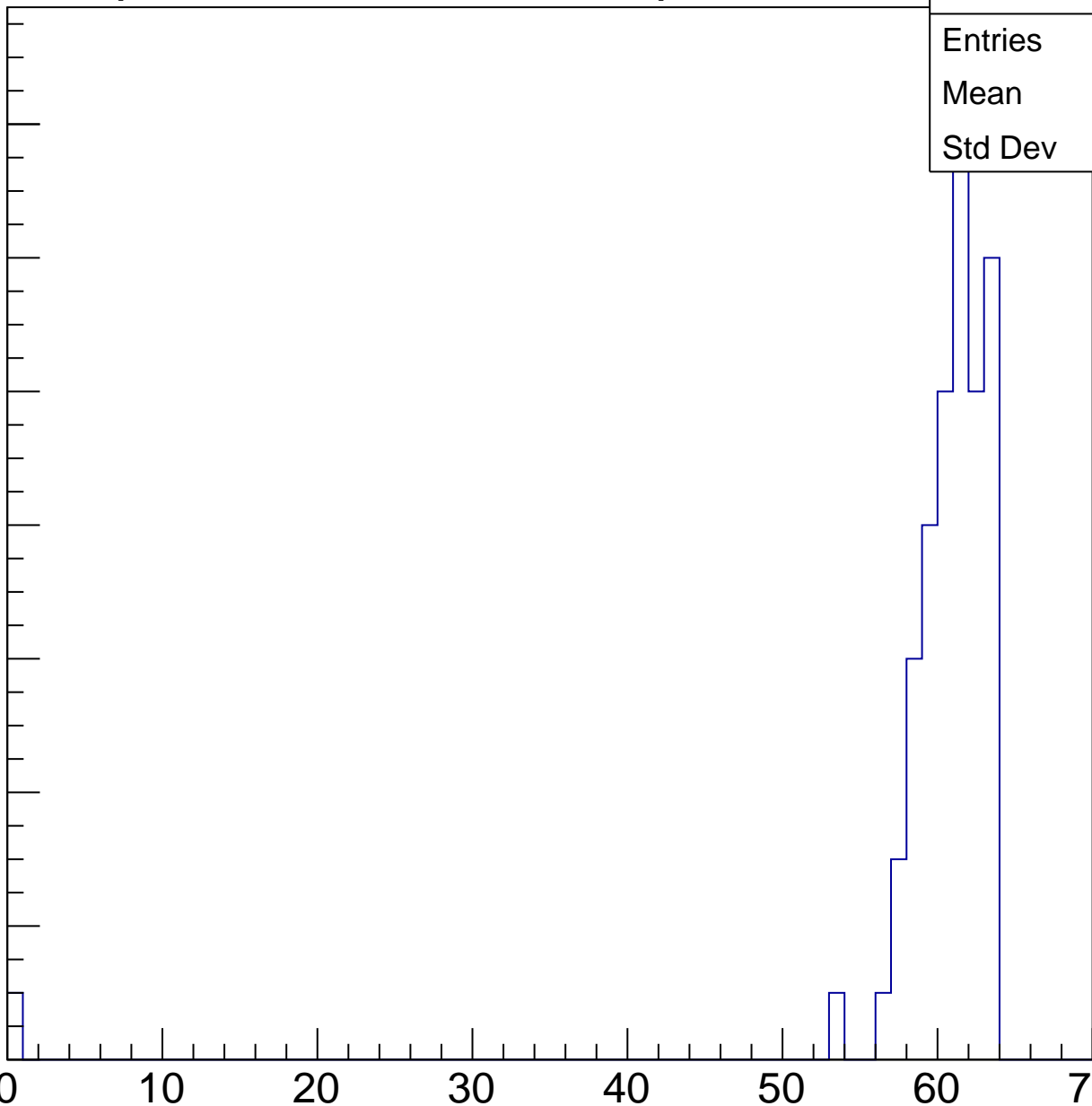
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	67
Mean	59.57
Std Dev	7.604

ampl

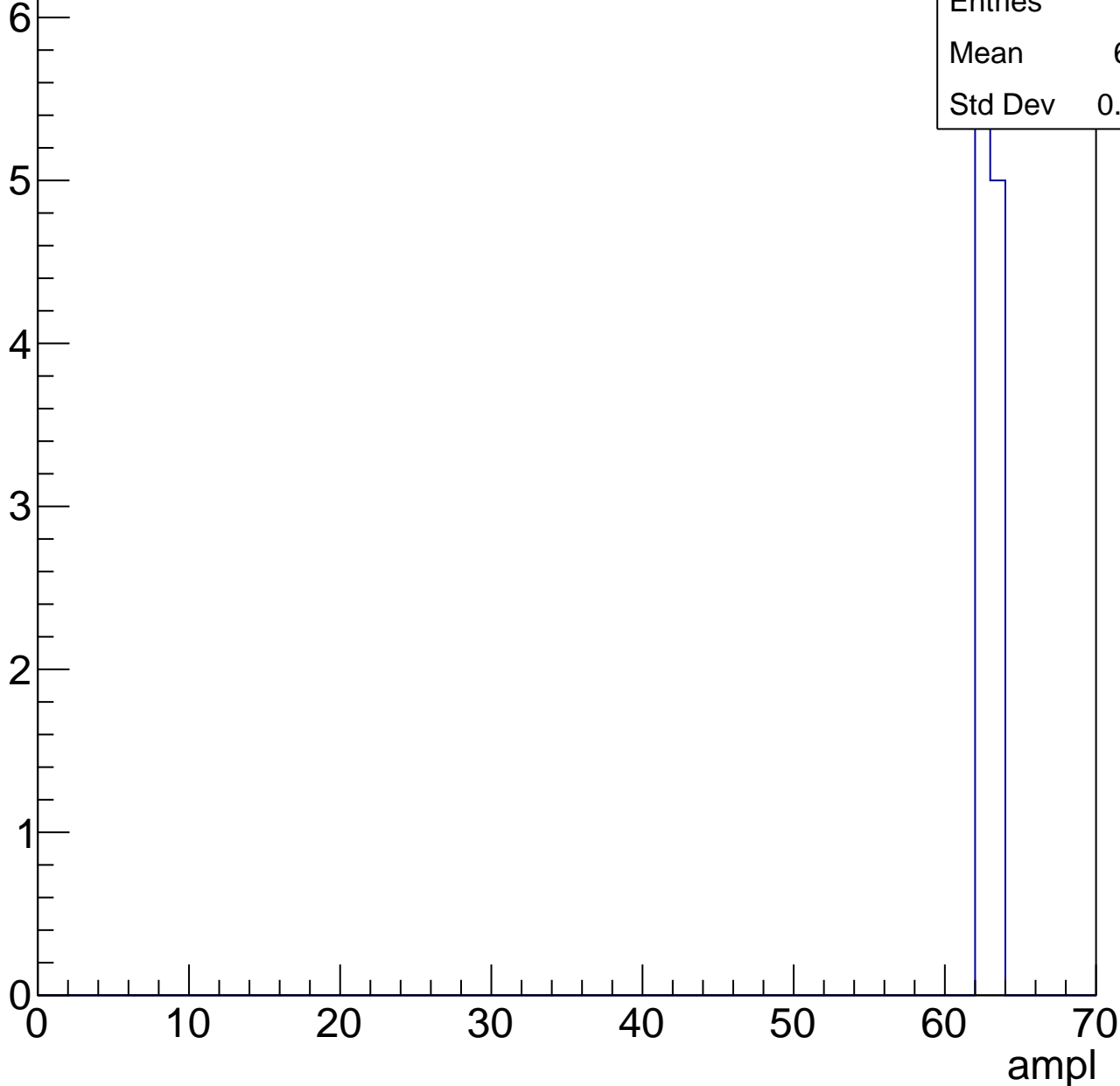


B1L001S, U20-ch115, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.45
Std Dev	0.4979



B1L001S, U20-ch115, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L001S, U20-ch116, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	31.42
Std Dev	3.315

Gaus mean : 31.8926

Gaus Width: 3.5180

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

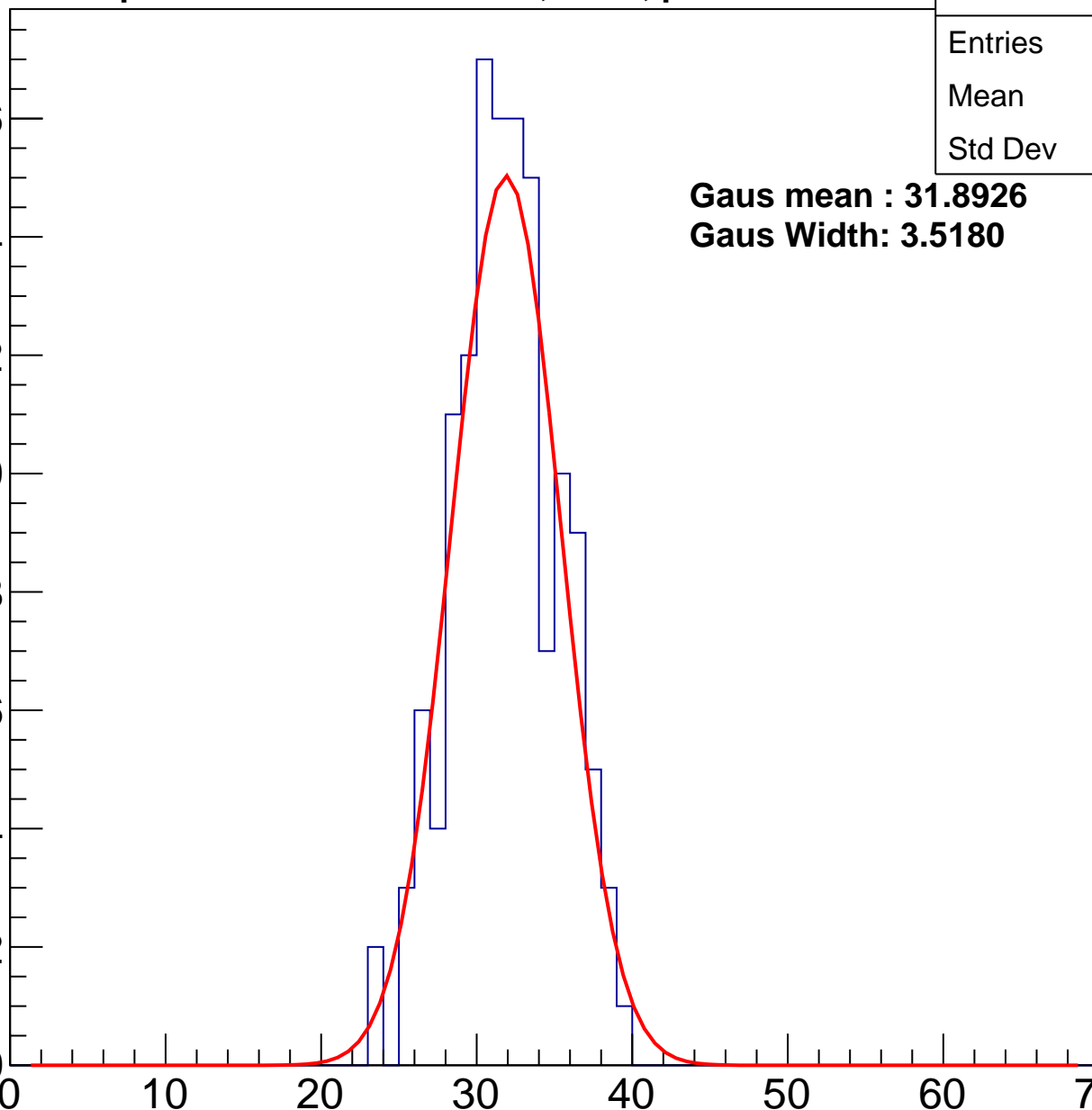
40

50

60

70

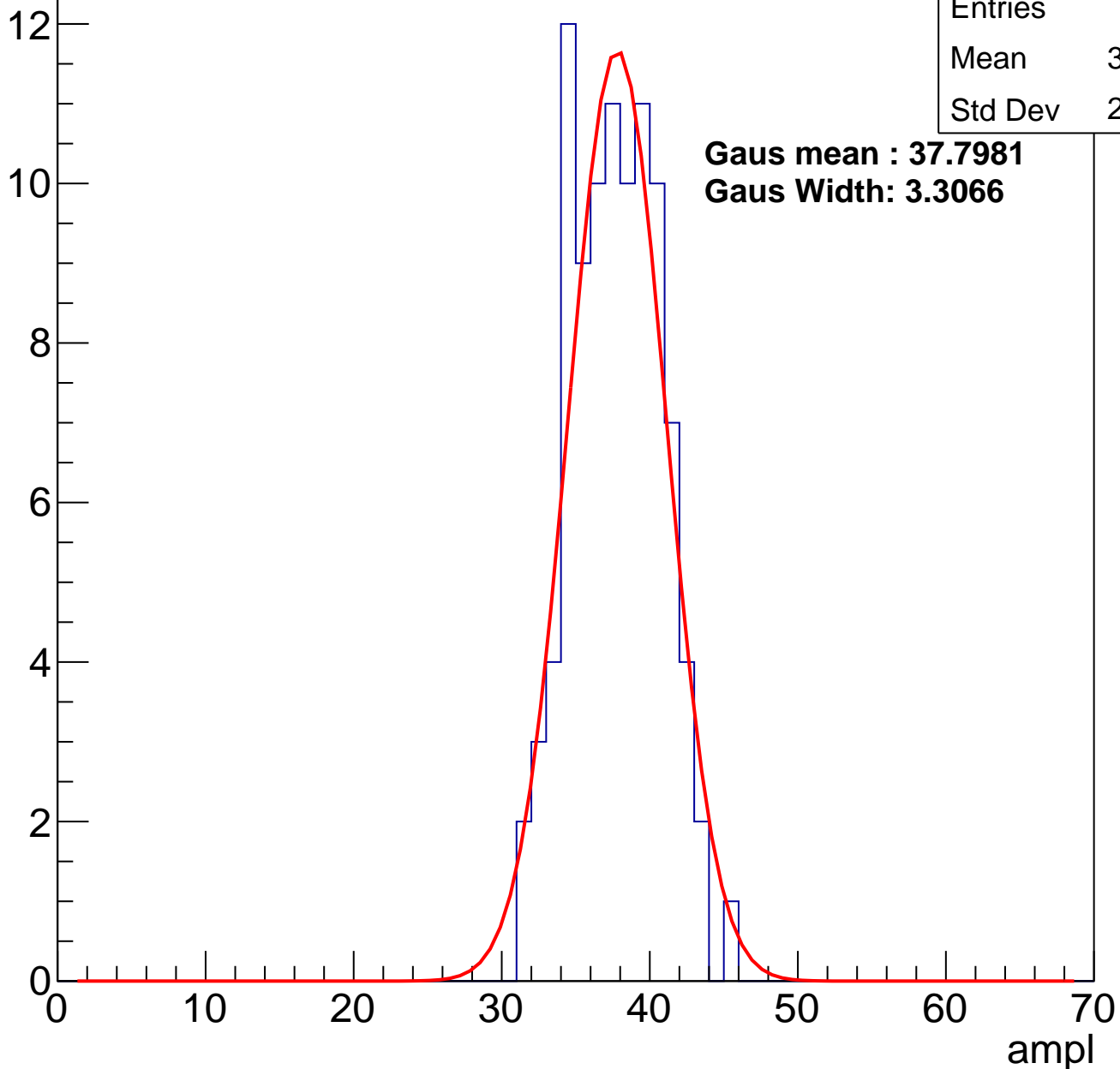
ampl



B1L001S, U20-ch116, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch116, adc2

calib_packv5_042523_0143.root, FC#2, port C2

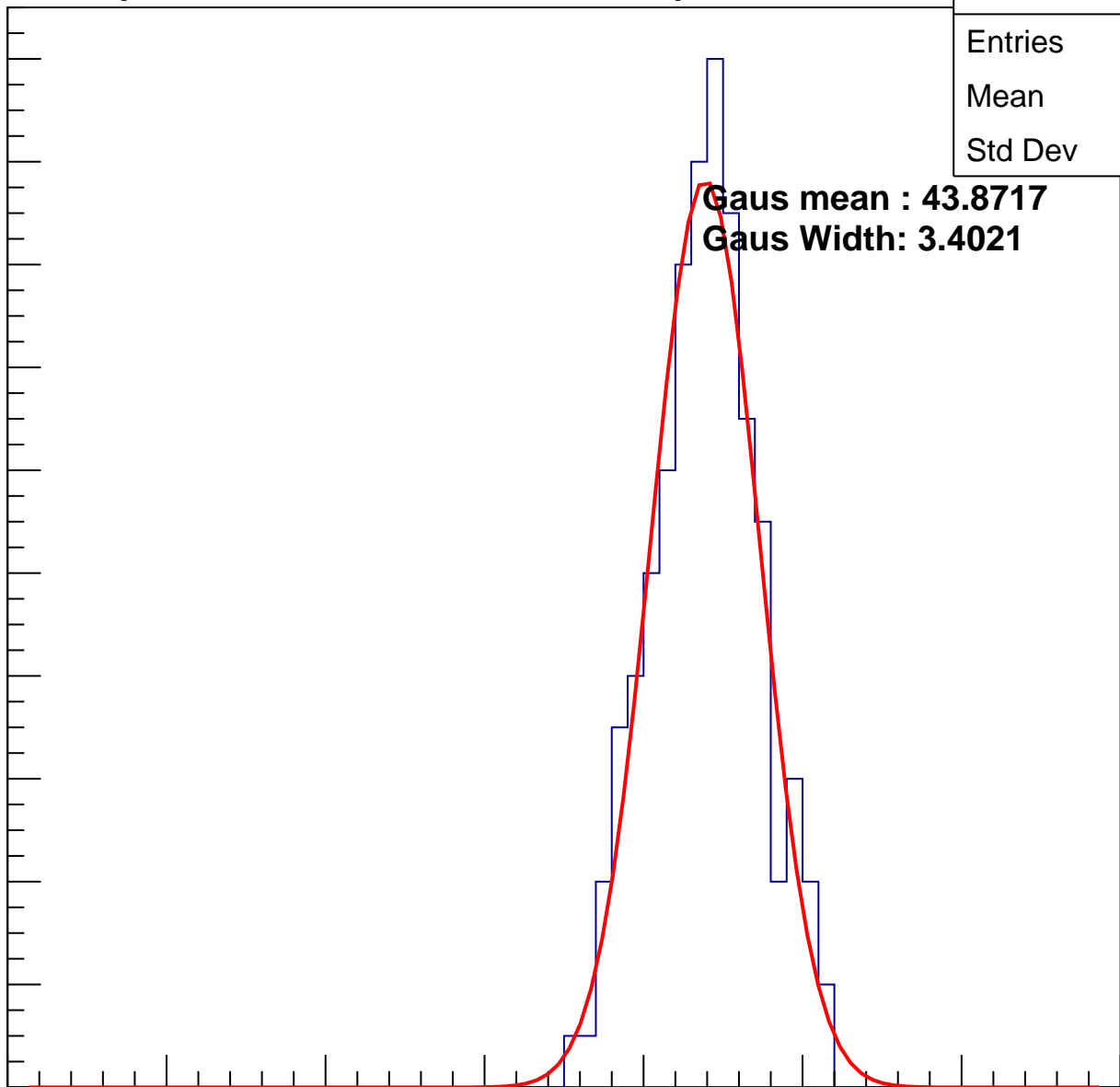
Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	154
Mean	43.4
Std Dev	3.333

Gaus mean : 43.8717
Gaus Width: 3.4021

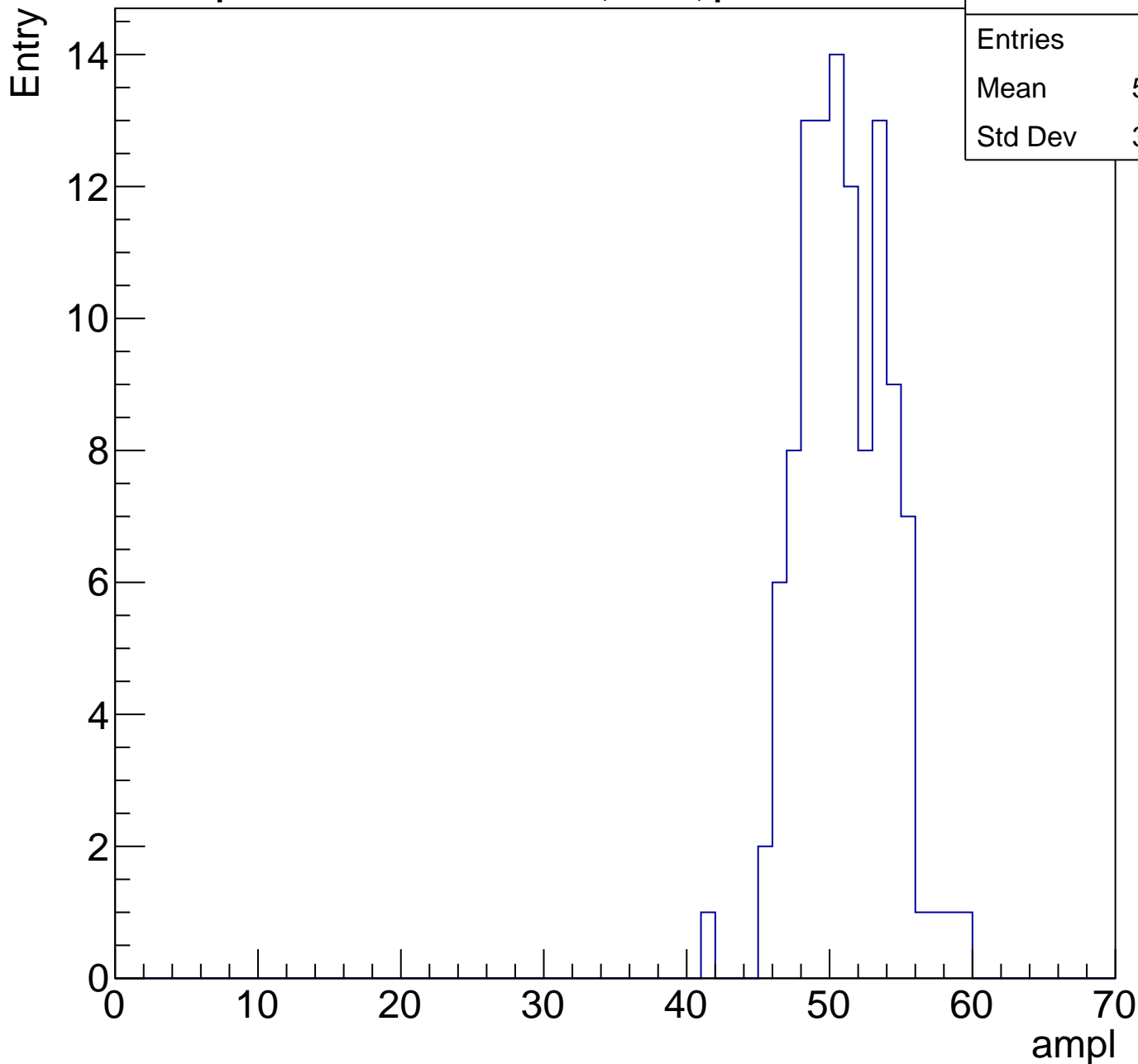
0 10 20 30 40 50 60 70
ampl



B1L001S, U20-ch116, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	50.56
Std Dev	3.073



B1L001S, U20-ch116, adc4

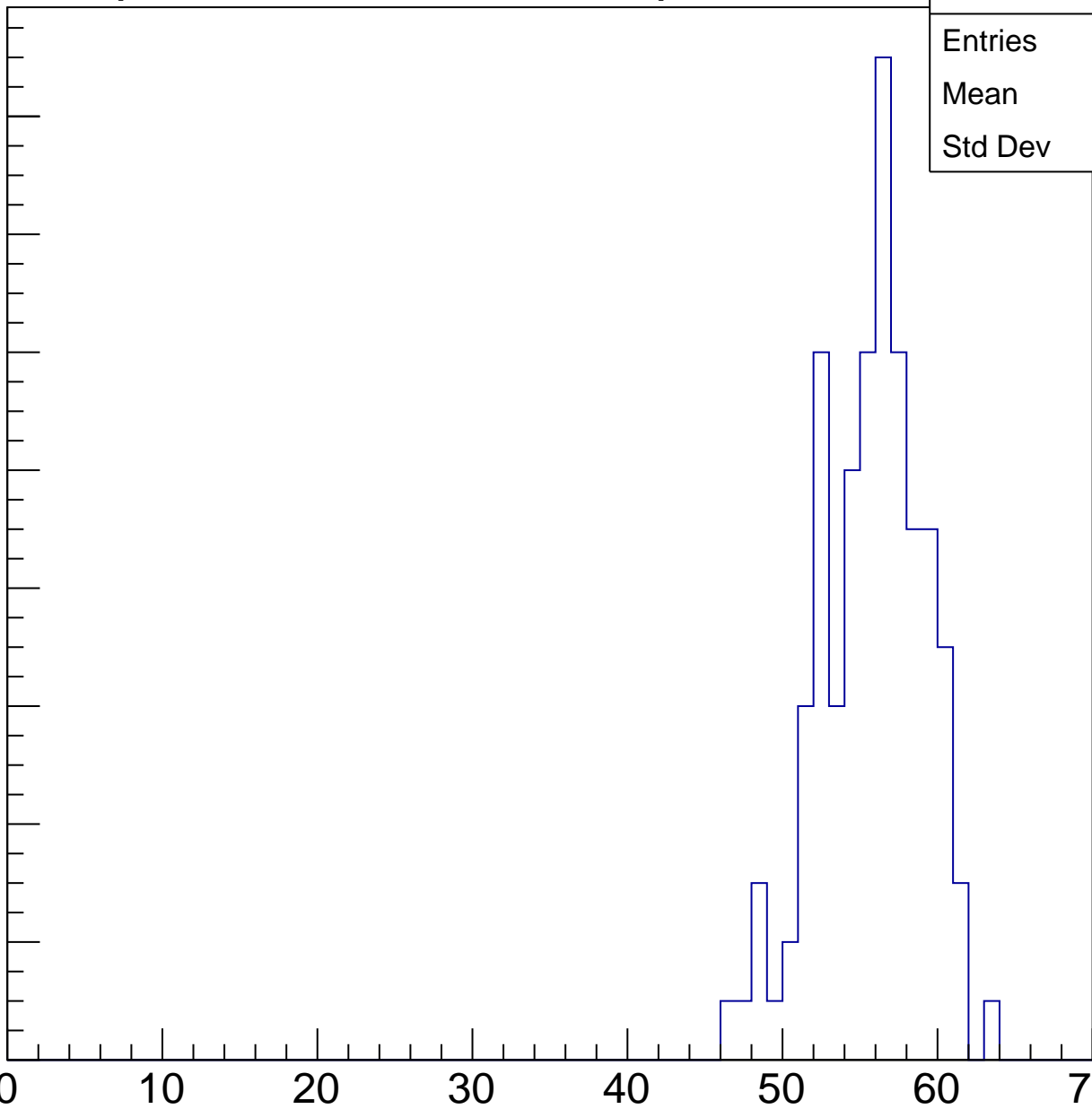
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	55.26
Std Dev	3.335

ampl



B1L001S, U20-ch116, adc5

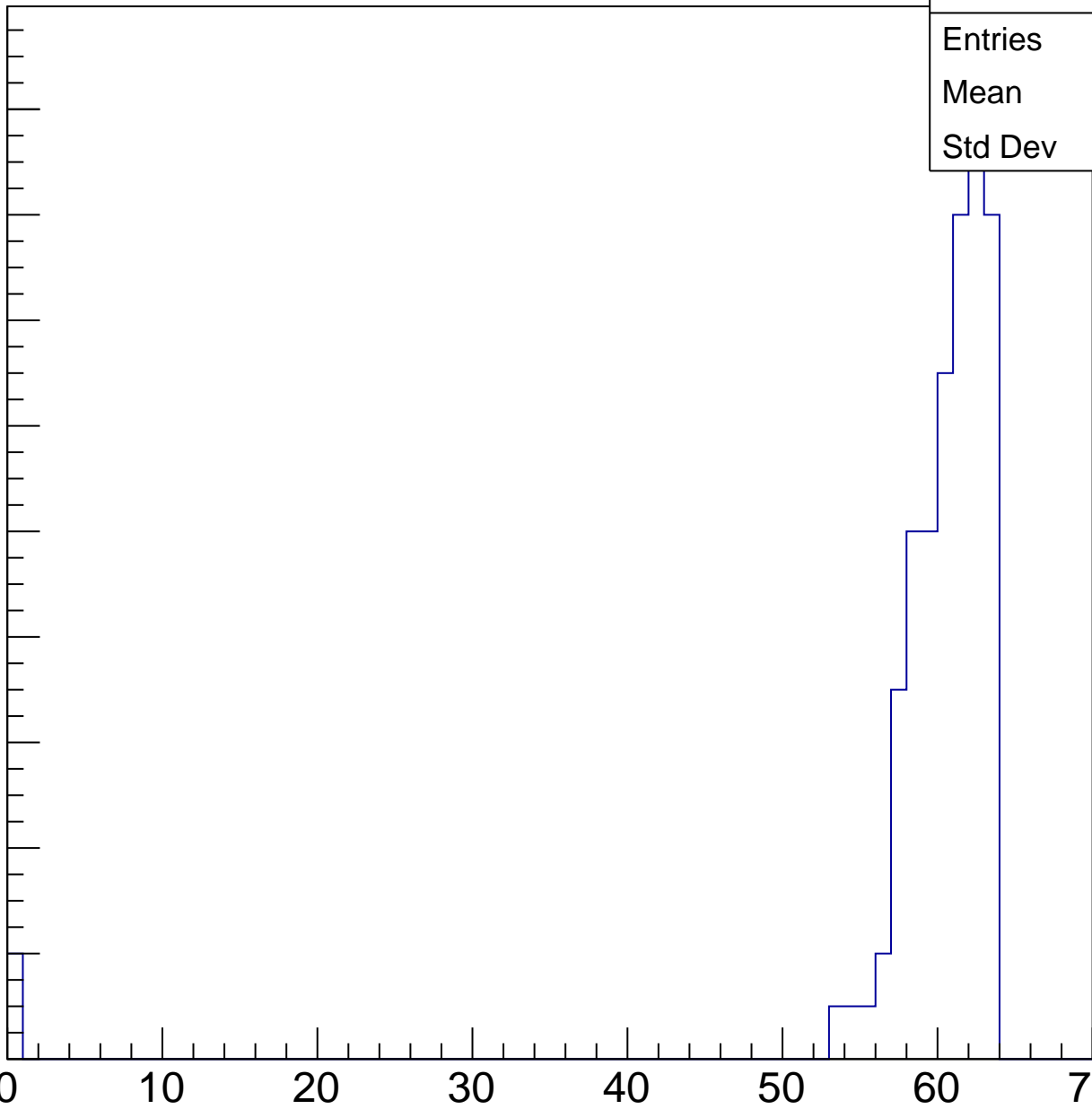
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.03
Std Dev	8.806

ampl



B1L001S, U20-ch116, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch116, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L001S, U20-ch117, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	30.15
Std Dev	3.223

Gaus mean : 30.6064

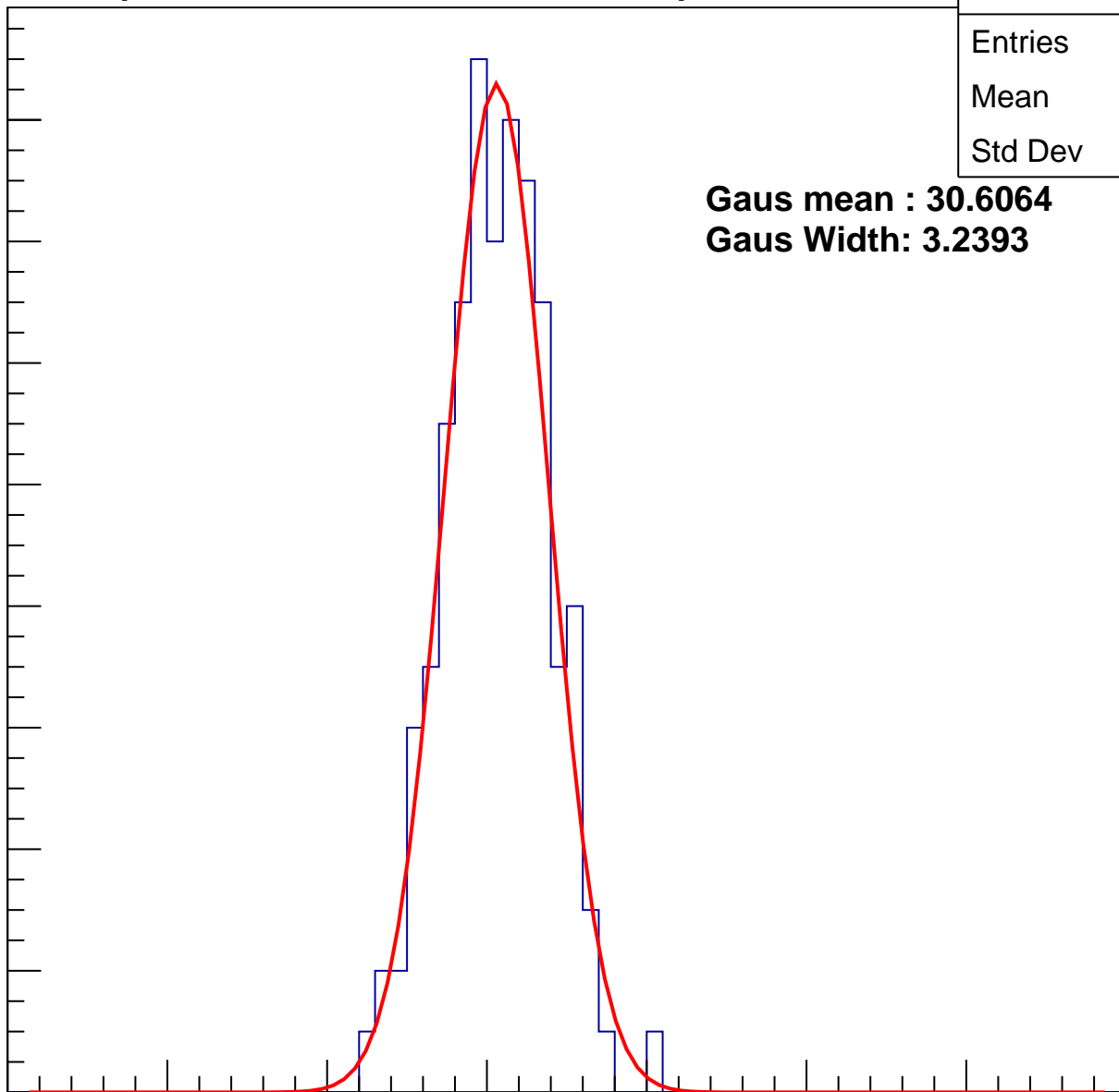
Gaus Width: 3.2393

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch117, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

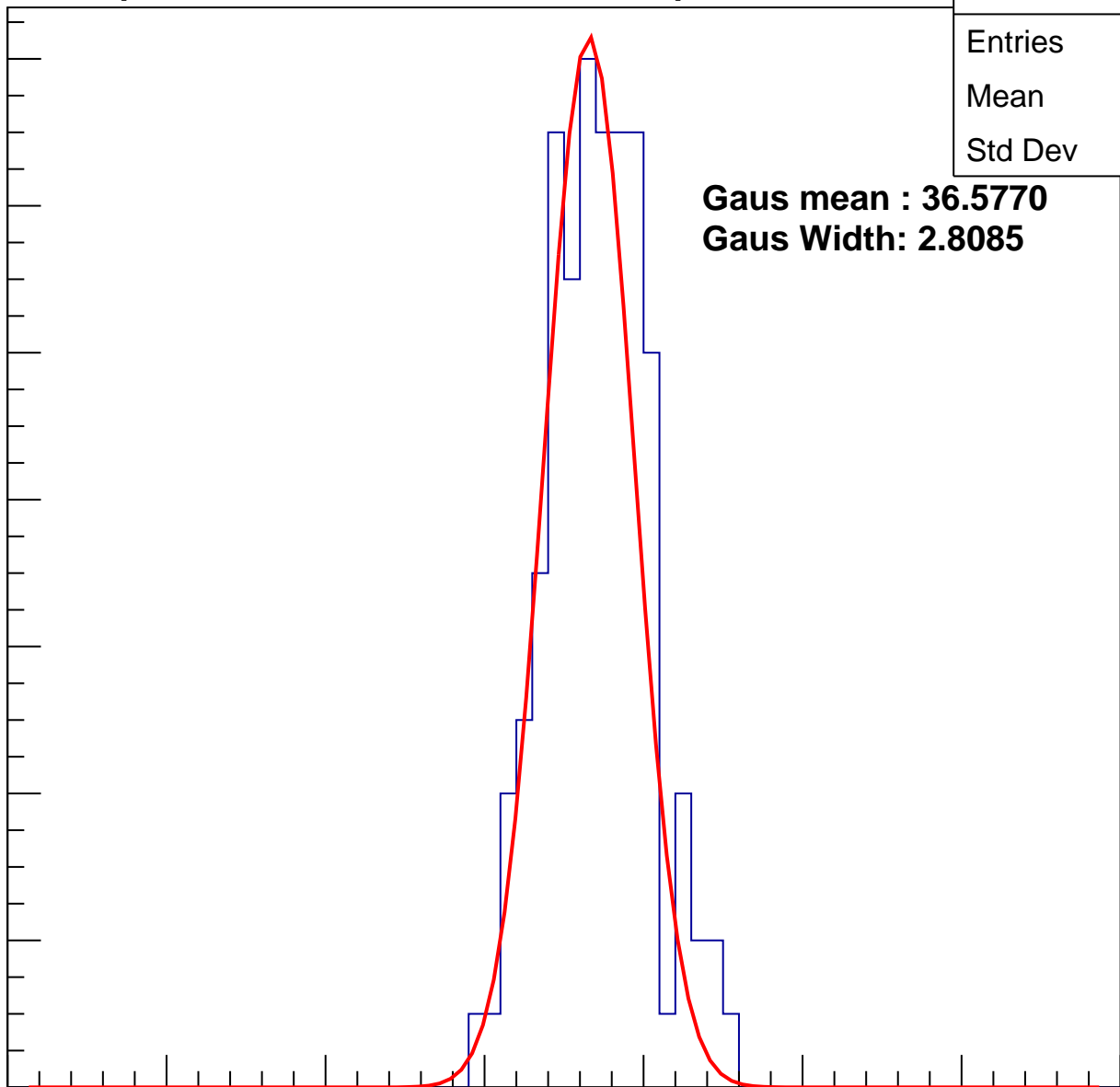
14
12
10
8
6
4
2
0

Entries	115
Mean	36.65
Std Dev	3.176

Gaus mean : 36.5770
Gaus Width: 2.8085

0 10 20 30 40 50 60 70

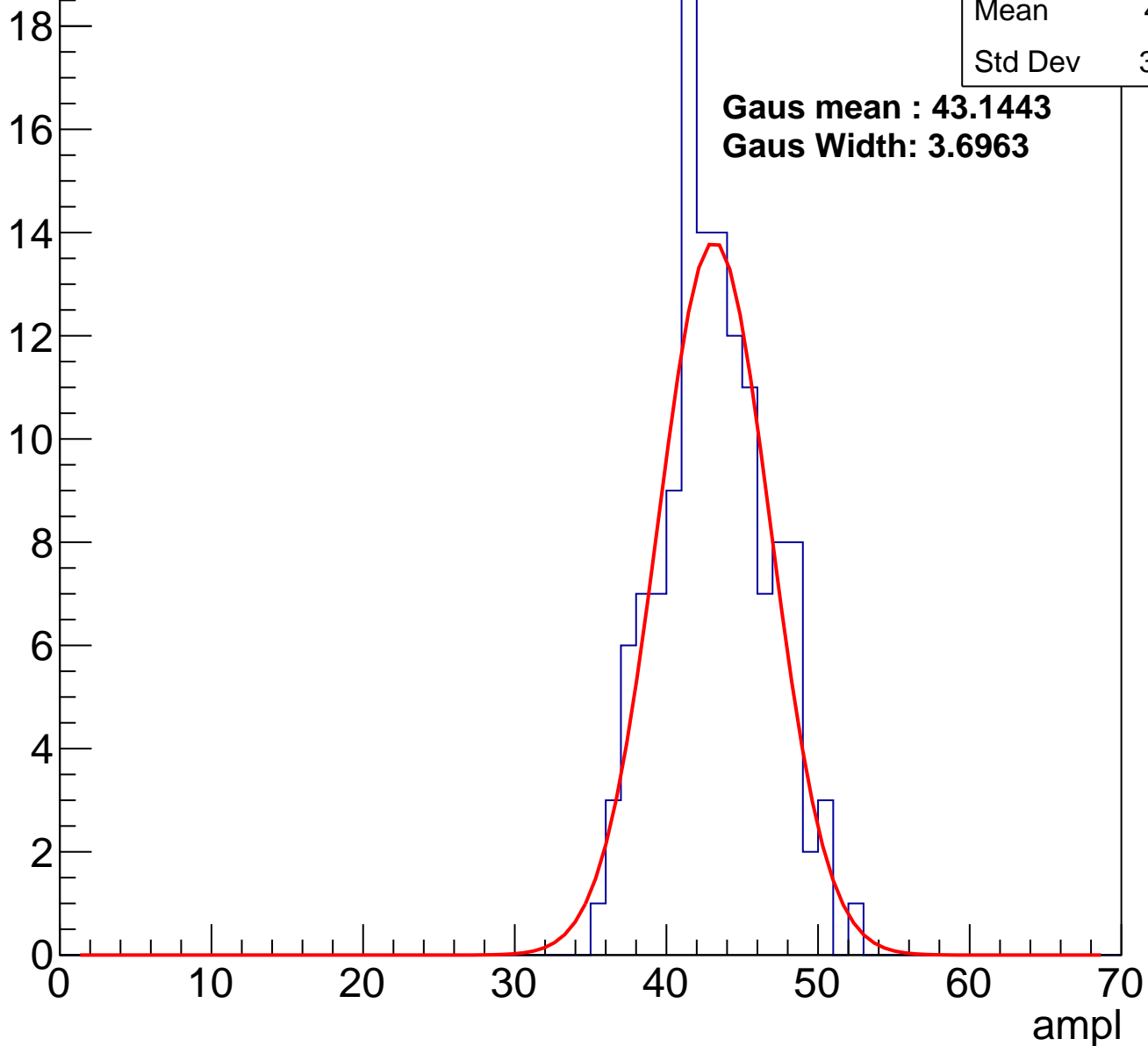
ampl



B1L001S, U20-ch117, adc2

calib_packv5_042523_0143.root, FC#2, port C2

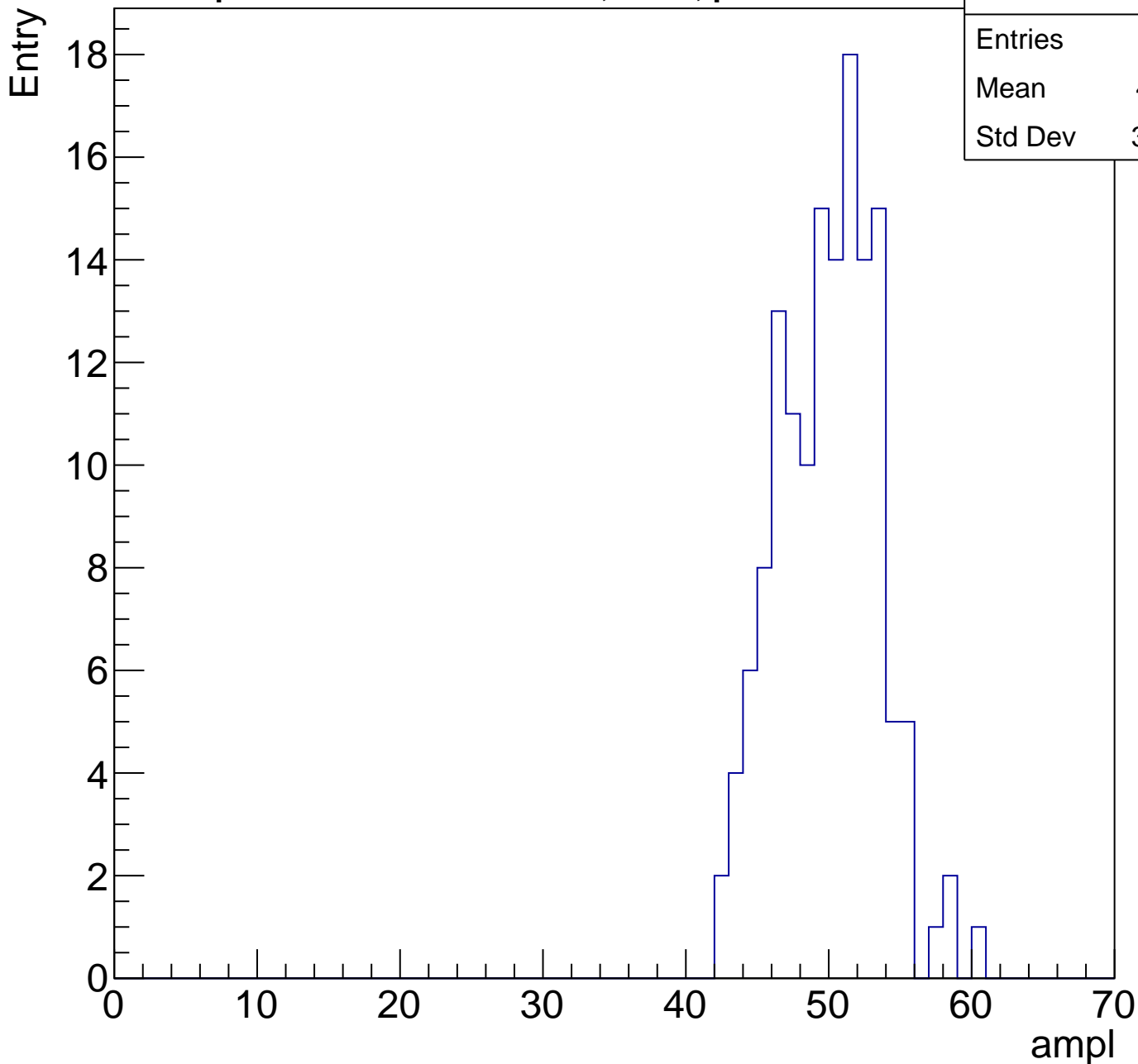
Entry



B1L001S, U20-ch117, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	49.51
Std Dev	3.494



B1L001S, U20-ch117, adc4

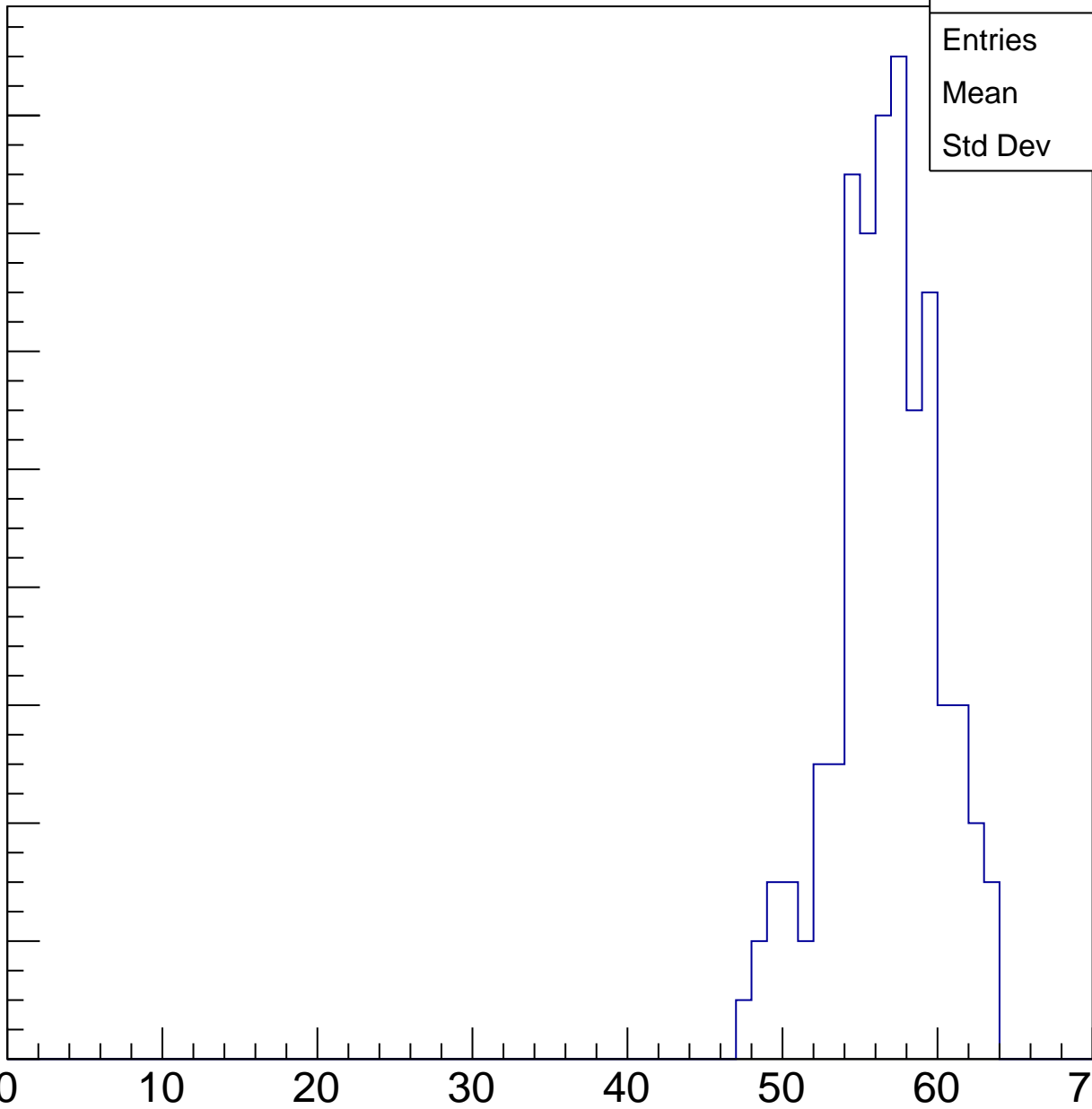
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	56.19
Std Dev	3.361

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U20-ch117, adc5

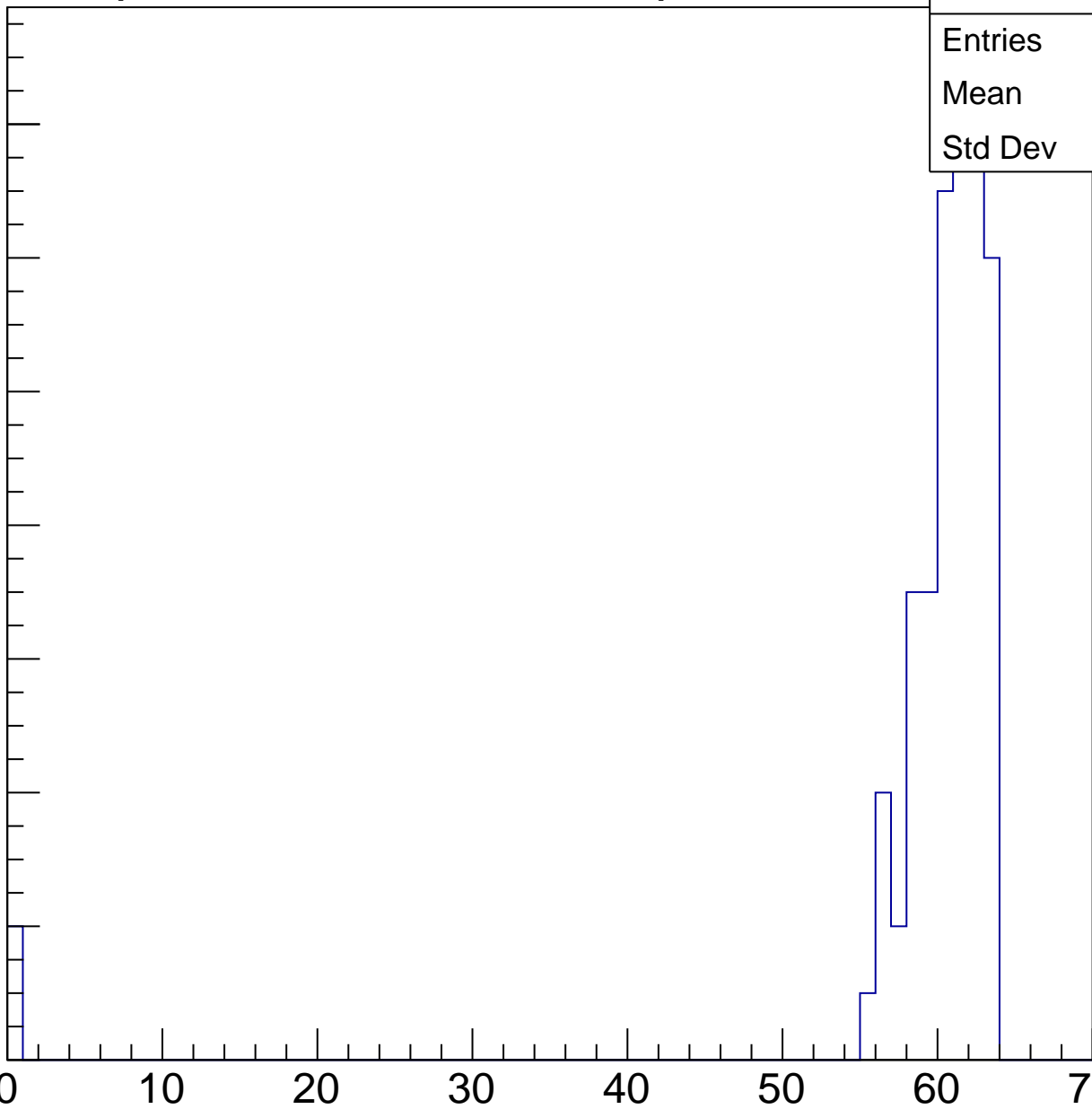
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	58.86
Std Dev	9.819

ampl

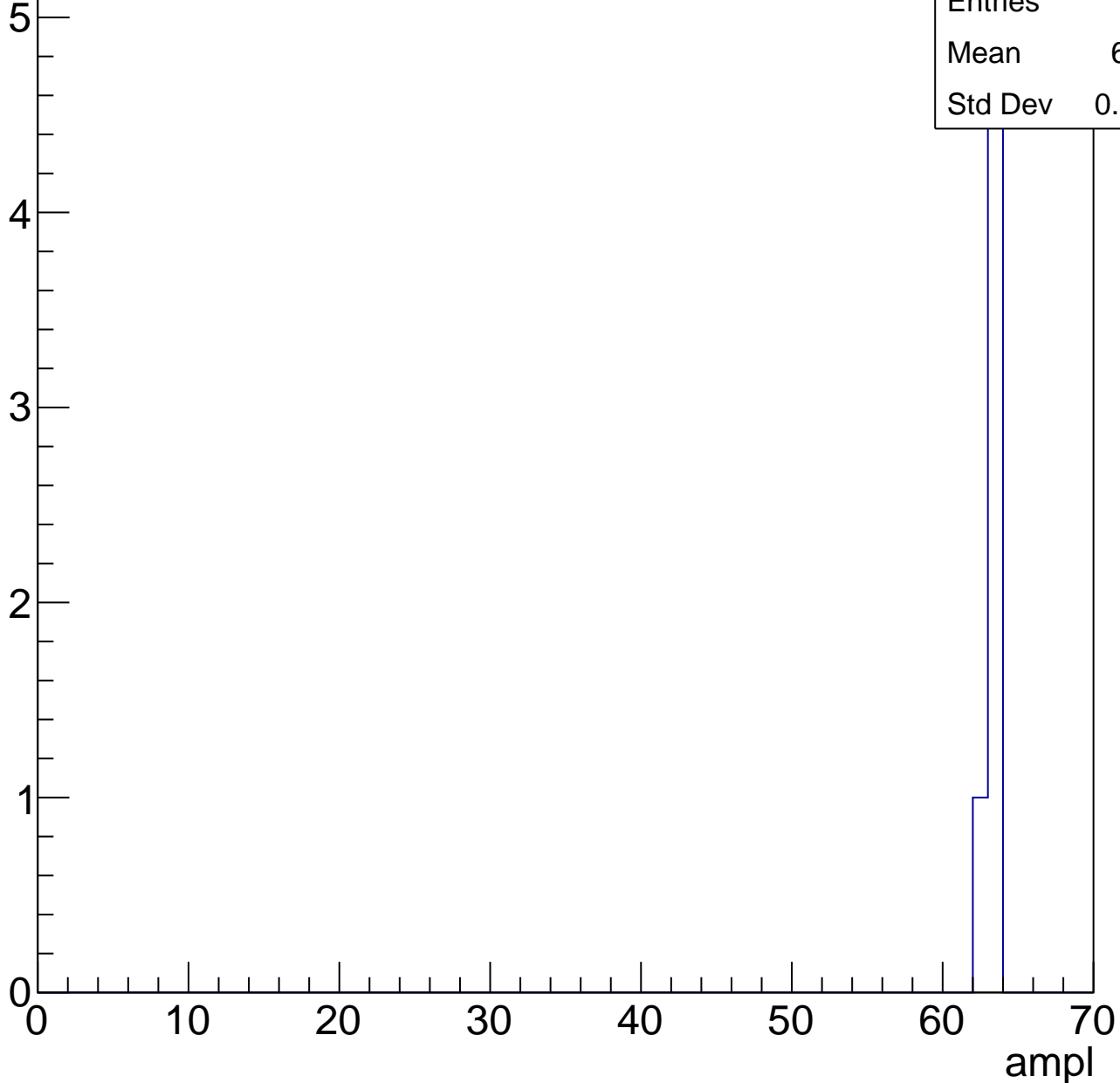


B1L001S, U20-ch117, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B1L001S, U20-ch117, adc7

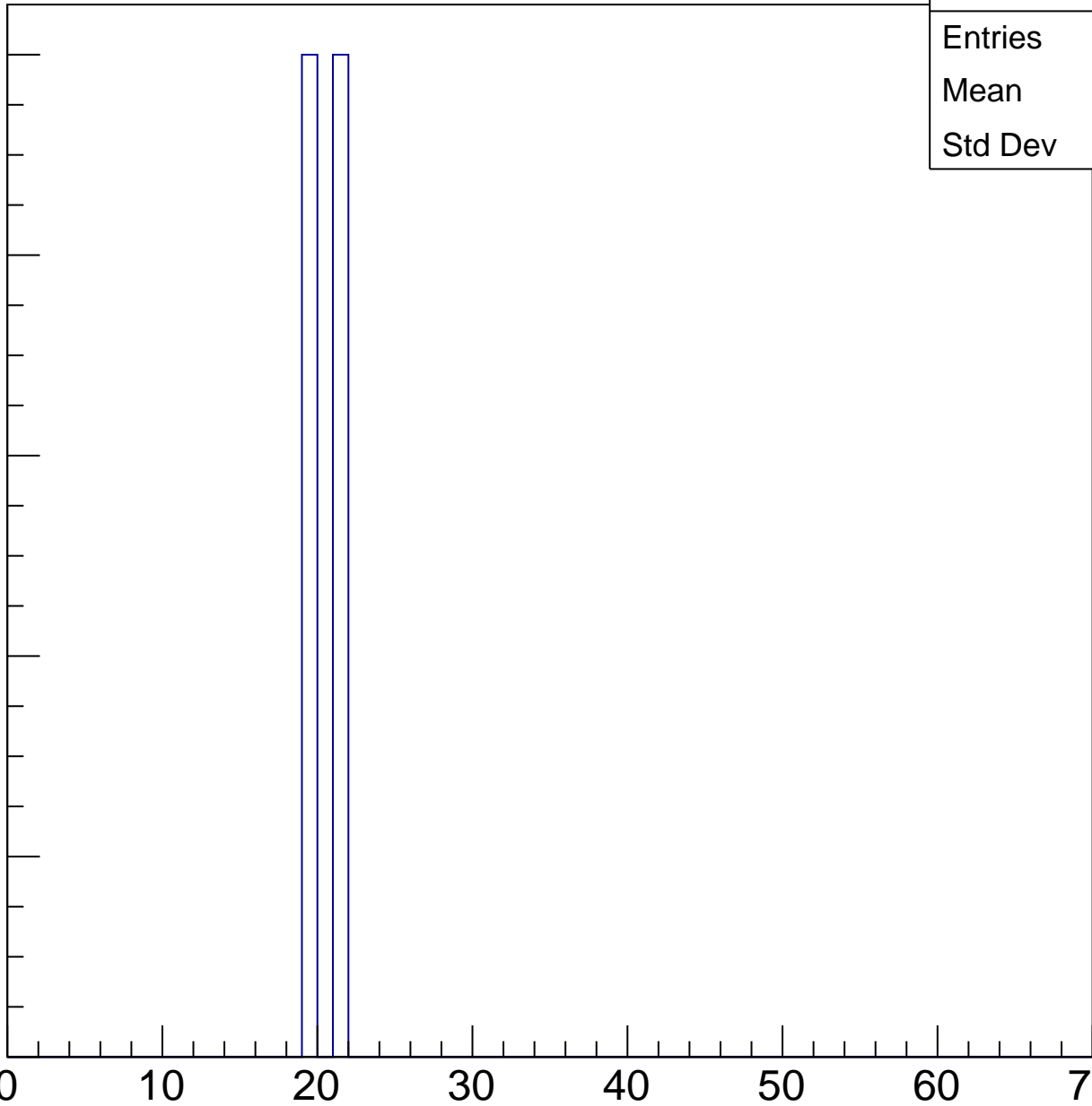
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	20
Std Dev	1

ampl



B1L001S, U20-ch118, adc0

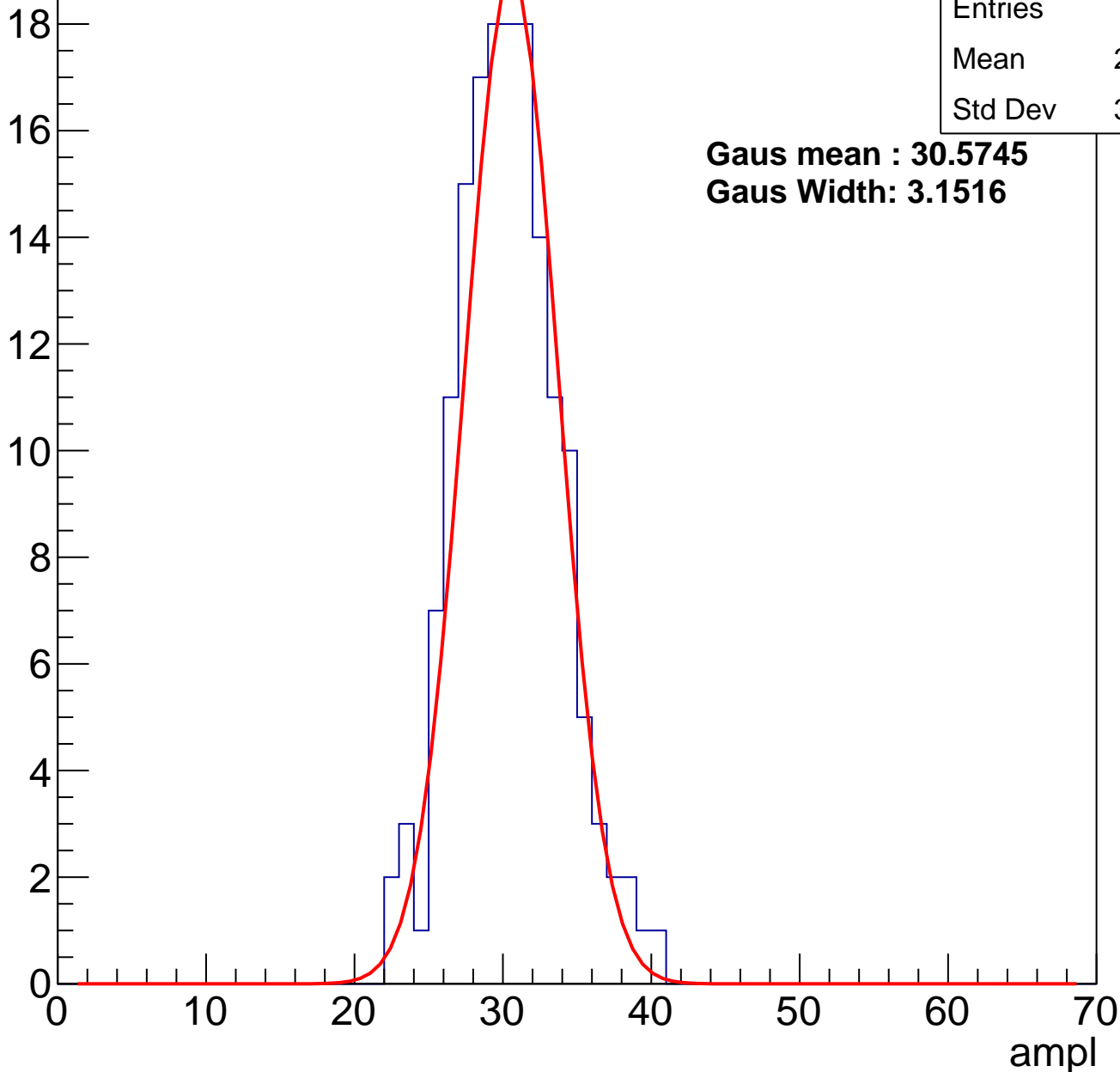
calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	29.95
Std Dev	3.416

Gaus mean : 30.5745

Gaus Width: 3.1516

Entry



B1L001S, U20-ch118, adc1

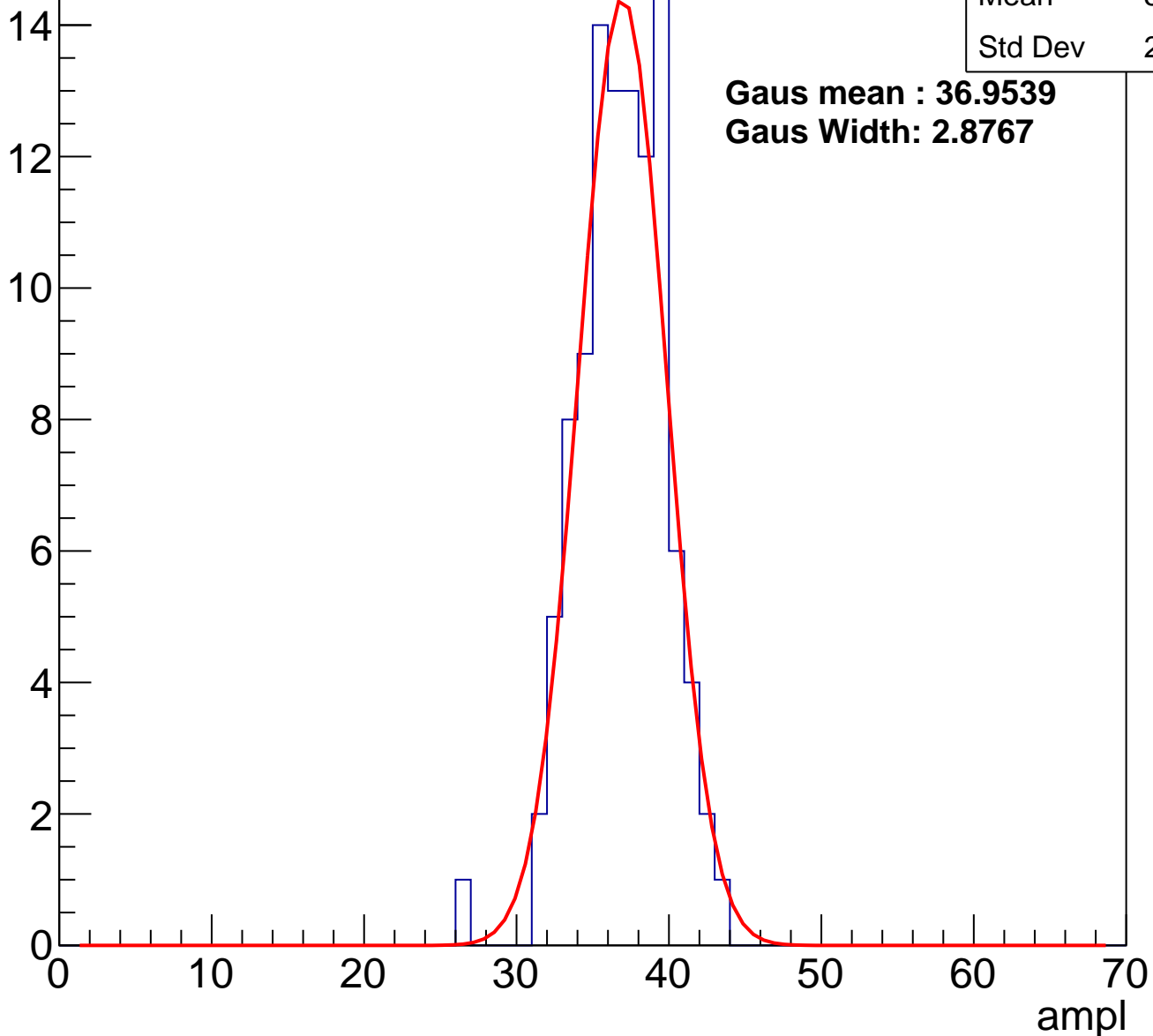
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	36.47
Std Dev	2.832

Gaus mean : 36.9539

Gaus Width: 2.8767

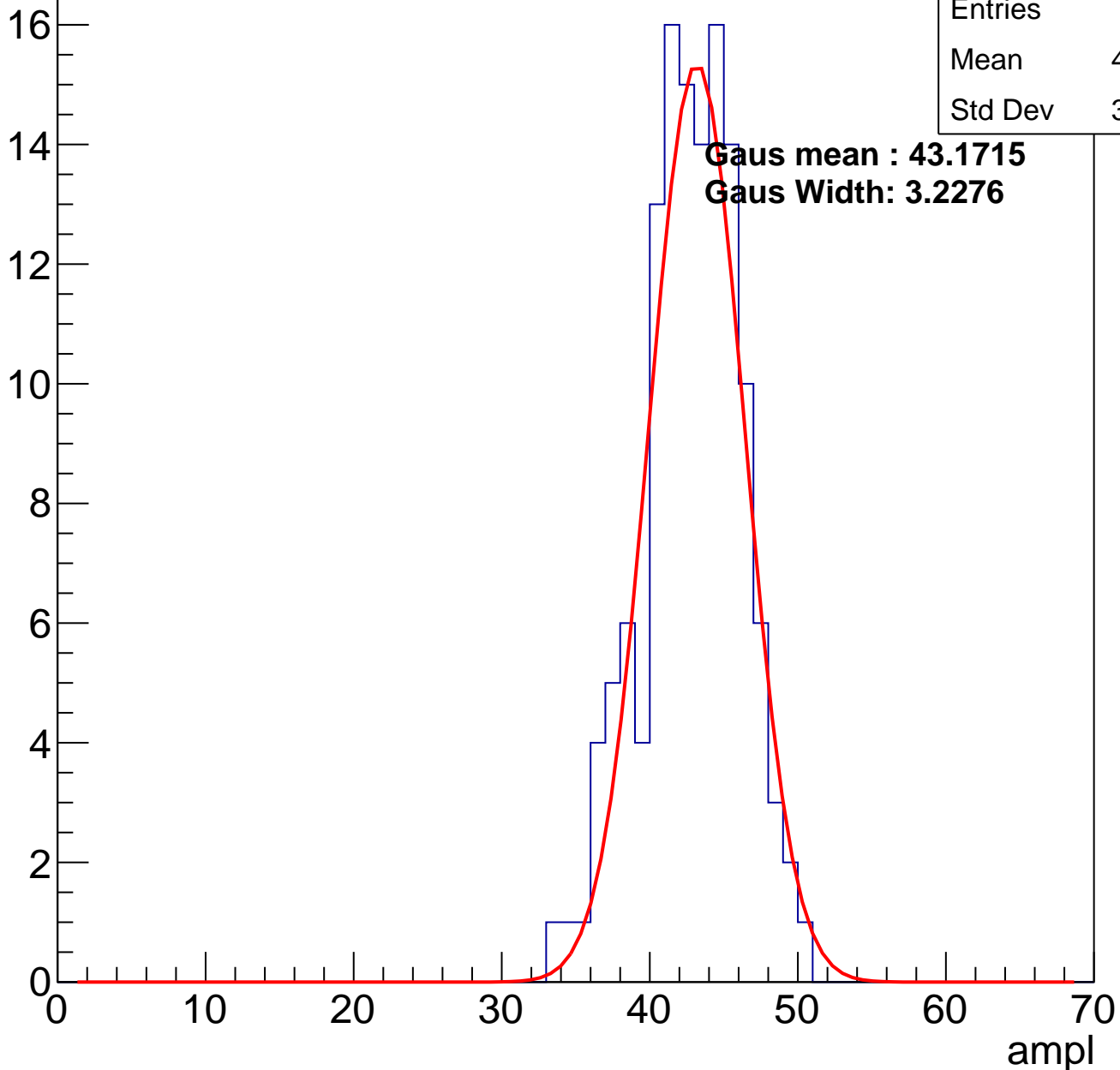
Entry



B1L001S, U20-ch118, adc2

calib_packv5_042523_0143.root, FC#2, port C2

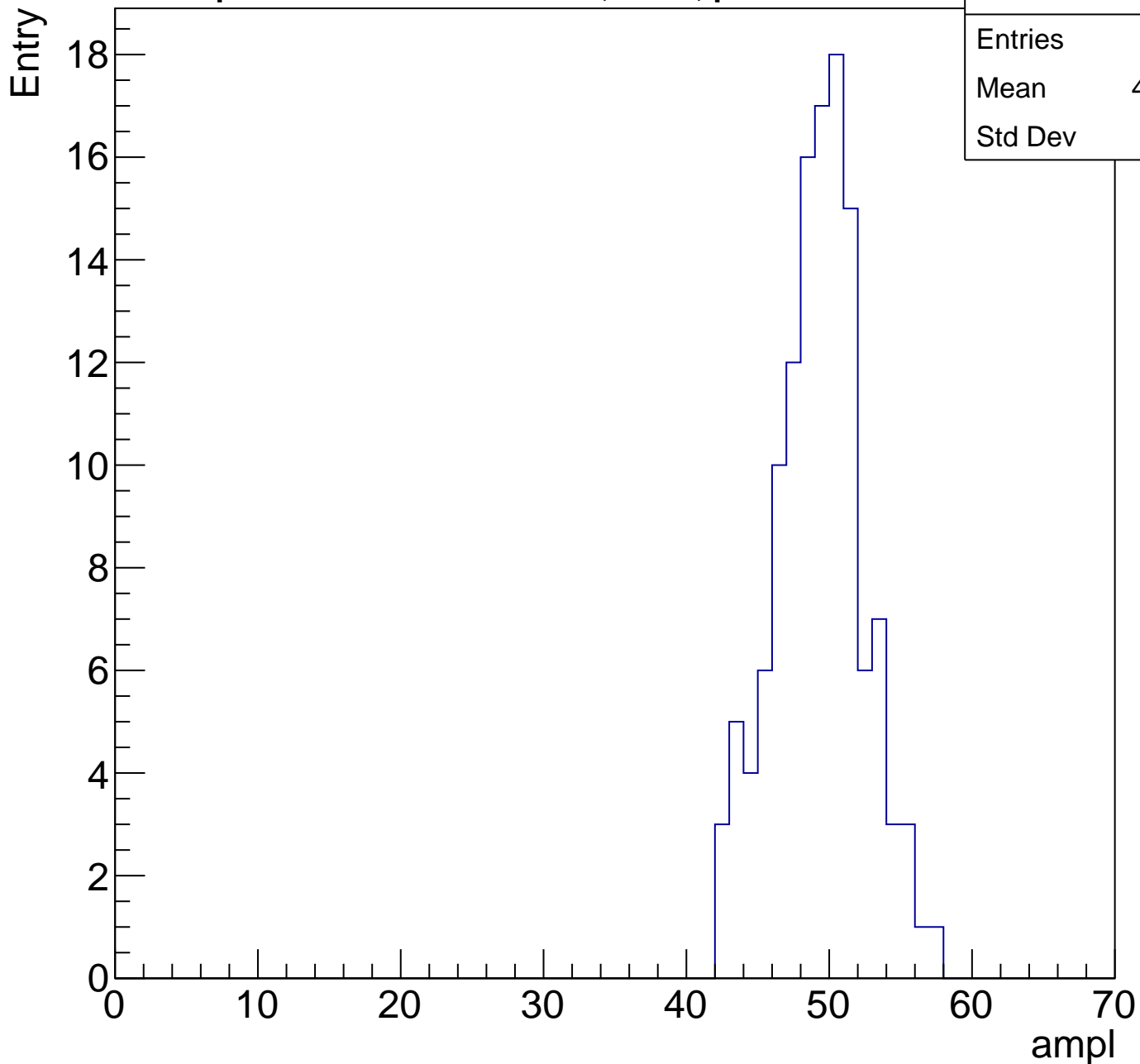
Entry



B1L001S, U20-ch118, adc3

calib_packv5_042523_0143.root, FC#2, port C2

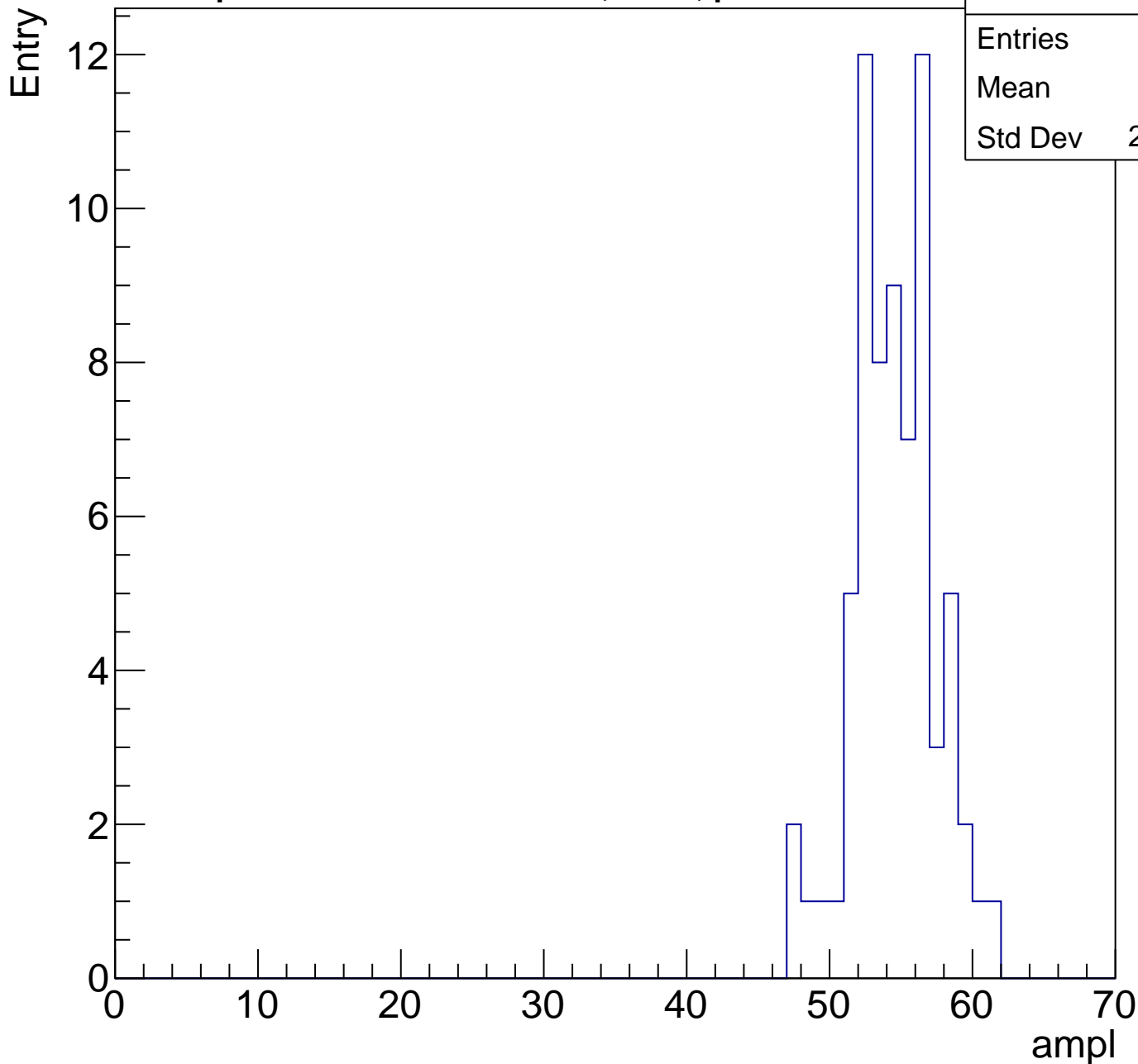
Entries	127
Mean	48.82
Std Dev	3.09



B1L001S, U20-ch118, adc4

calib_packv5_042523_0143.root, FC#2, port C2

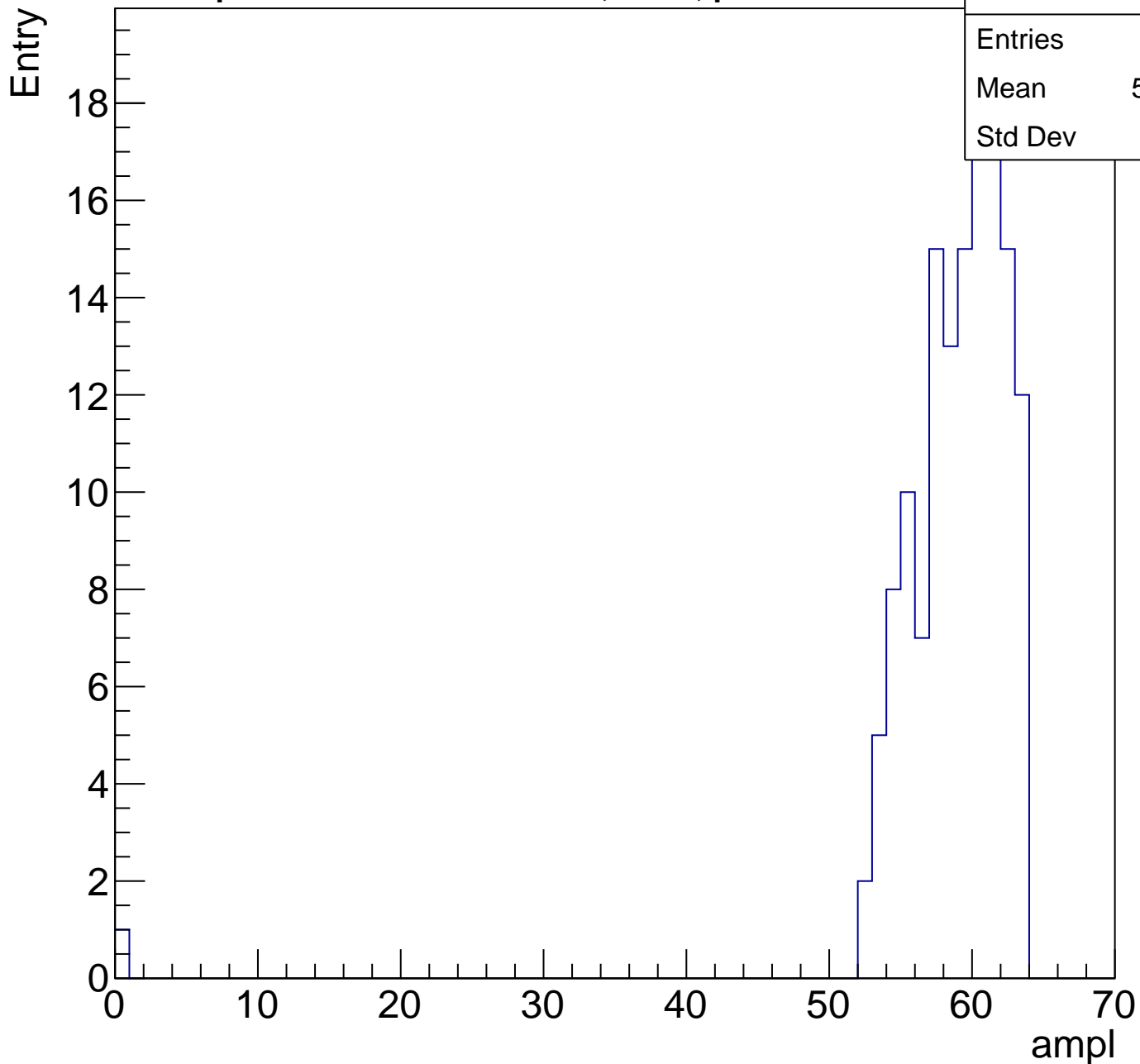
Entries	70
Mean	54.1
Std Dev	2.859



B1L001S, U20-ch118, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	58.27
Std Dev	5.75

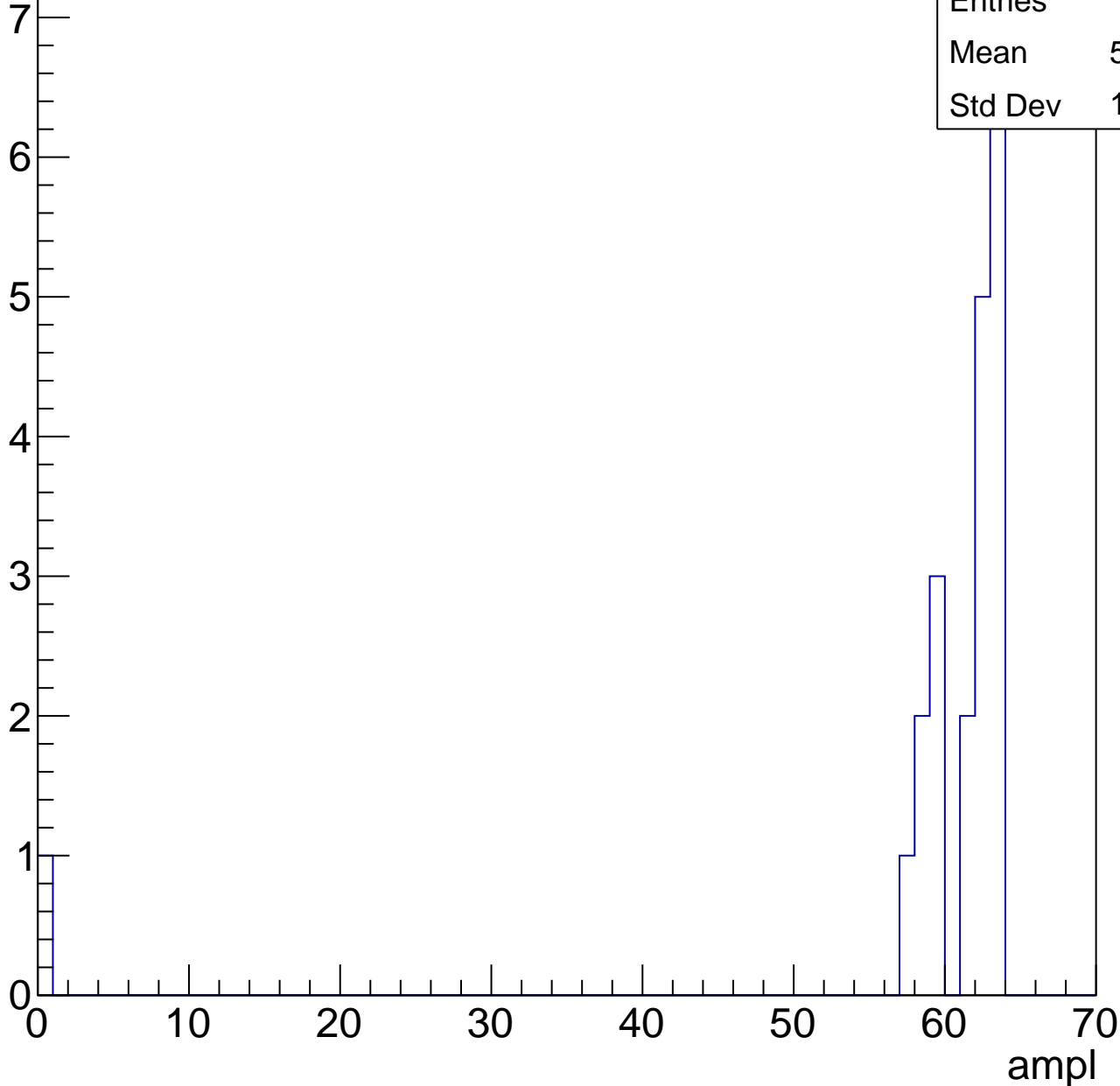


B1L001S, U20-ch118, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	21
Mean	58.24
Std Dev	13.17



B1L001S, U20-ch118, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	34.67
Std Dev	20.04

ampl

0 10 20 30 40 50 60 70

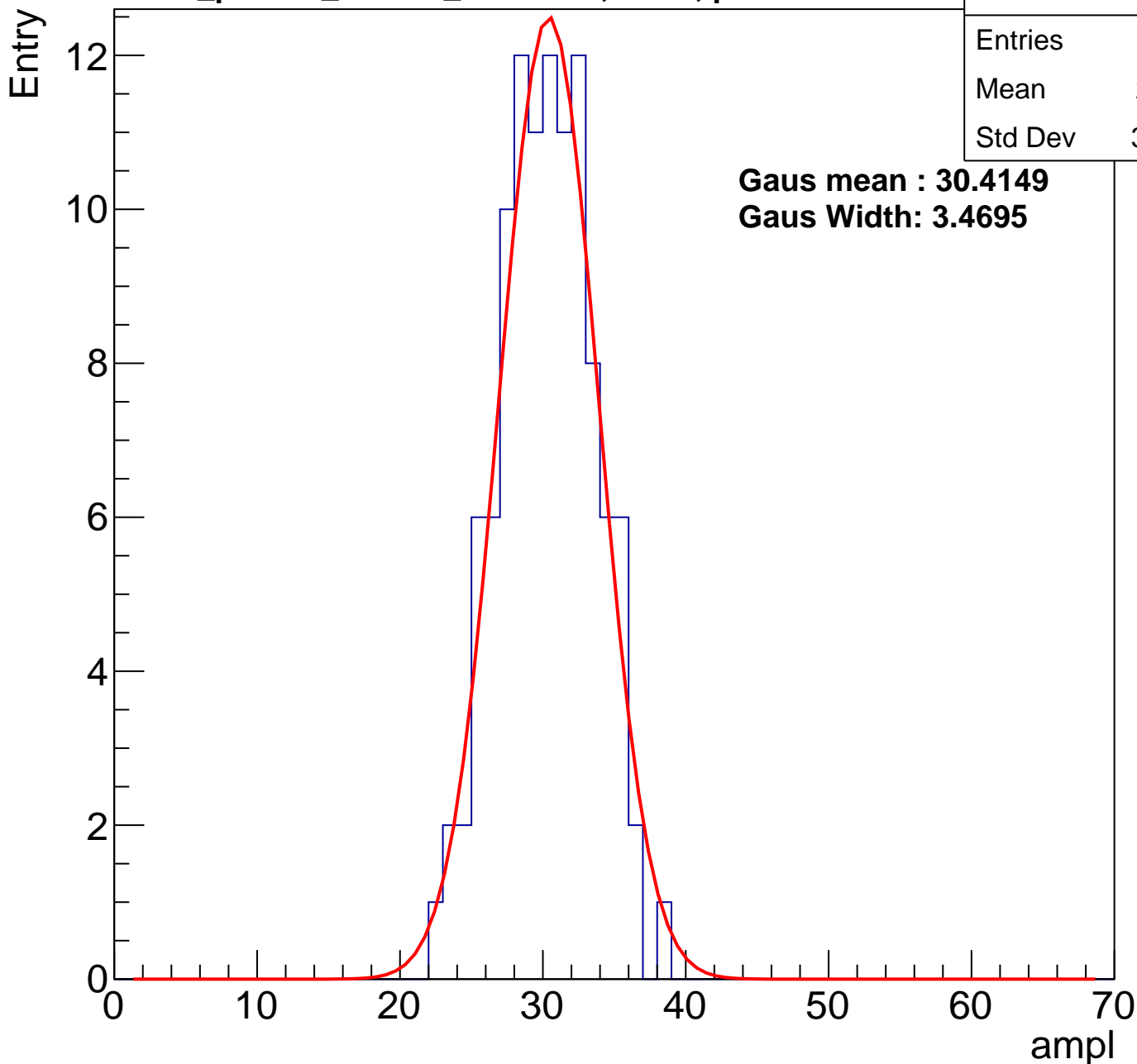
B1L001S, U20-ch119, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	29.81
Std Dev	3.247

Gaus mean : 30.4149

Gaus Width: 3.4695



B1L001S, U20-ch119, adc1

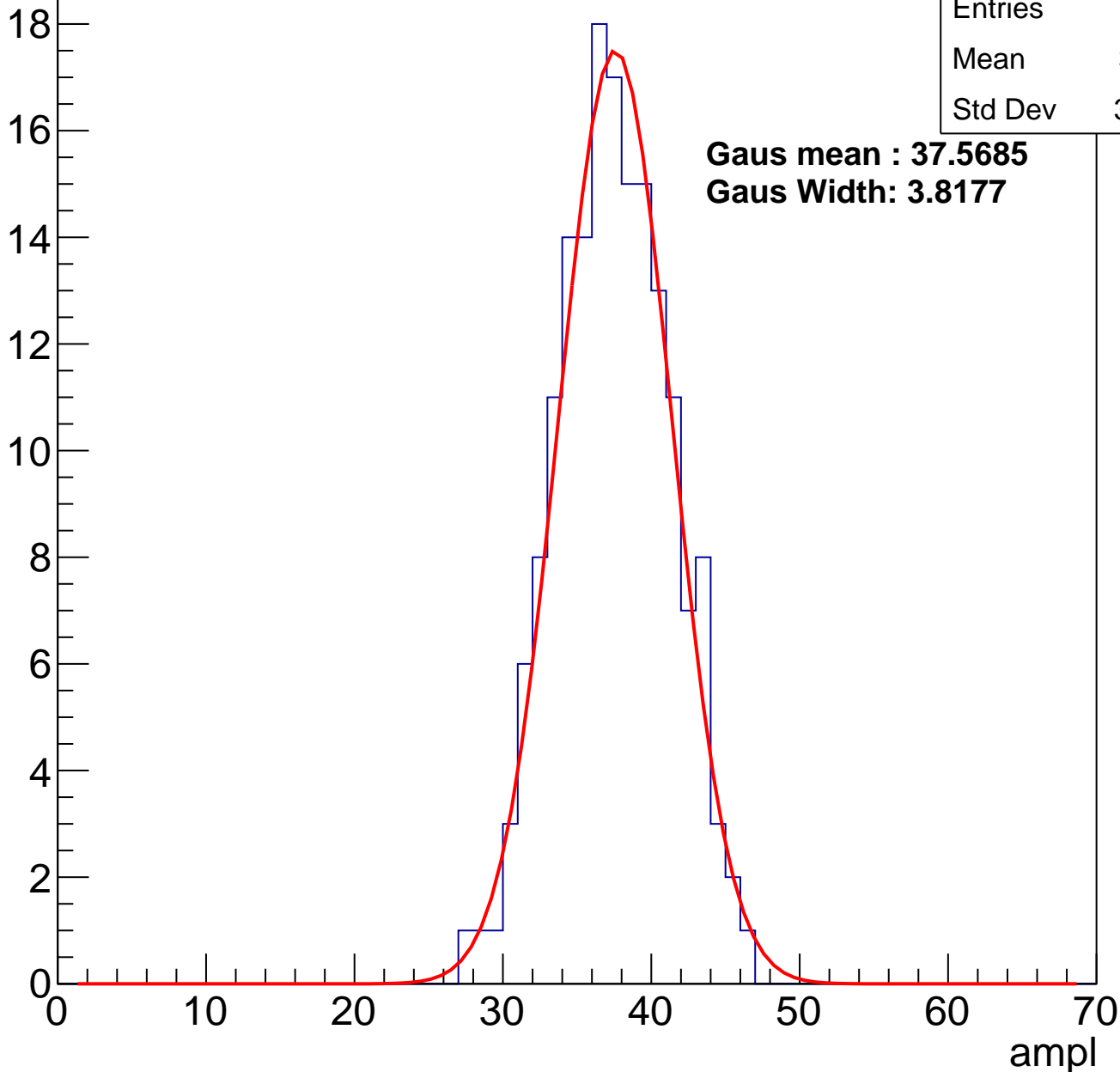
calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	37.01
Std Dev	3.746

Gaus mean : 37.5685

Gaus Width: 3.8177

Entry



B1L001S, U20-ch119, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

138

Mean

44.35

Std Dev

3.287

Gaus mean : 44.7938

Gaus Width: 3.4886

0

10

20

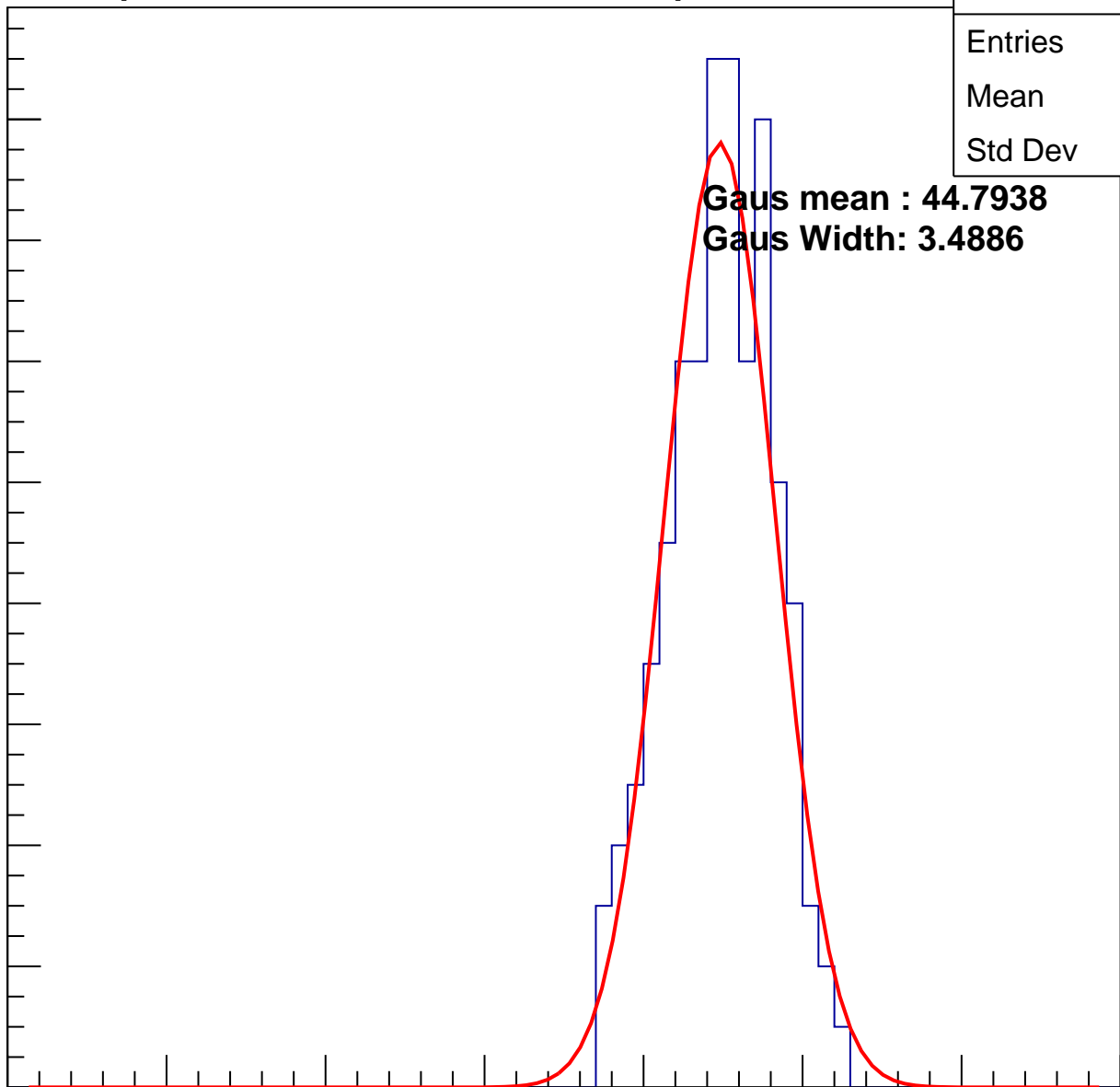
30

40

50

60

ampl



B1L001S, U20-ch119, adc3

calib_packv5_042523_0143.root, FC#2, port C2

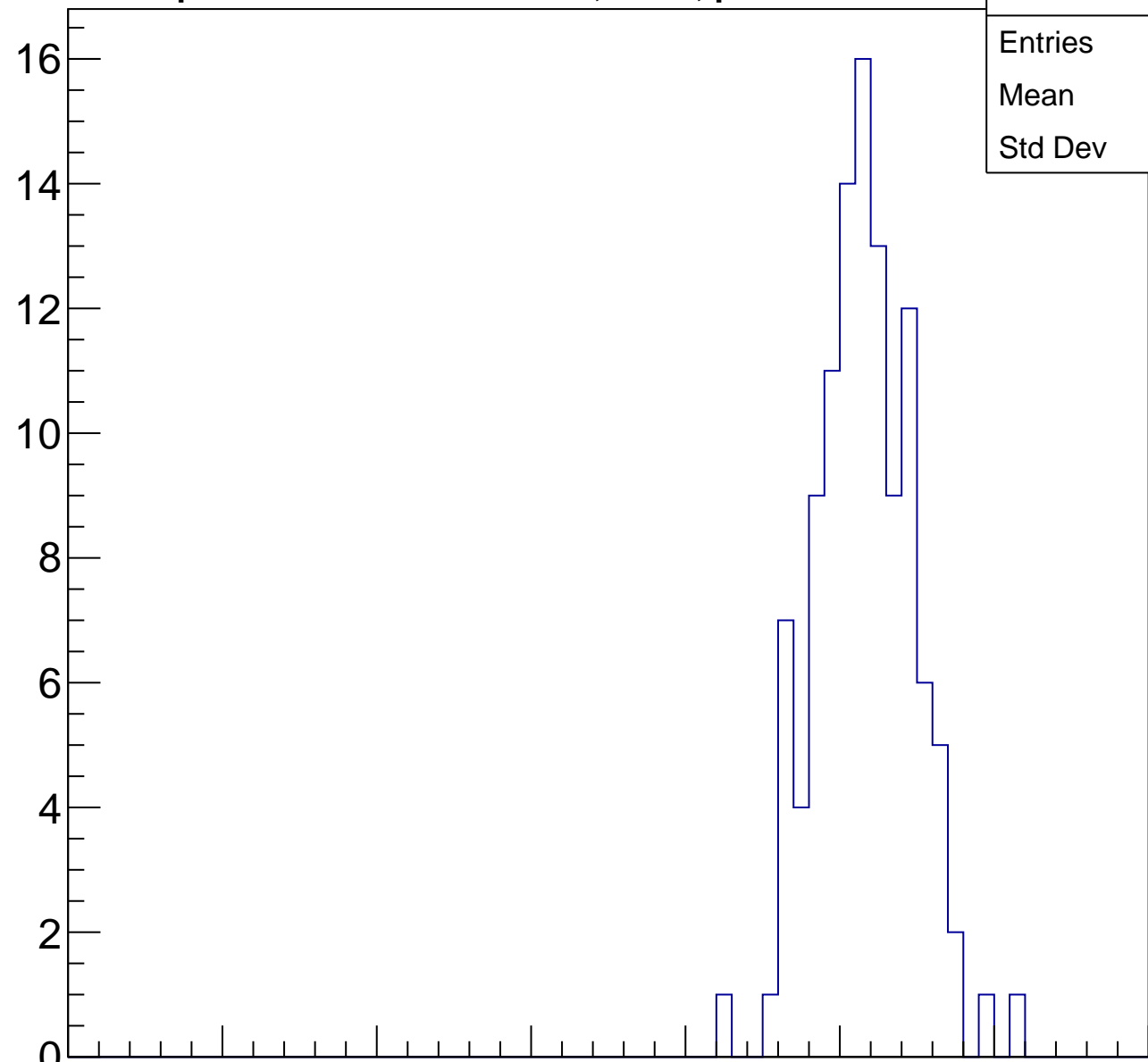
Entries	112
Mean	51.15
Std Dev	3.143

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

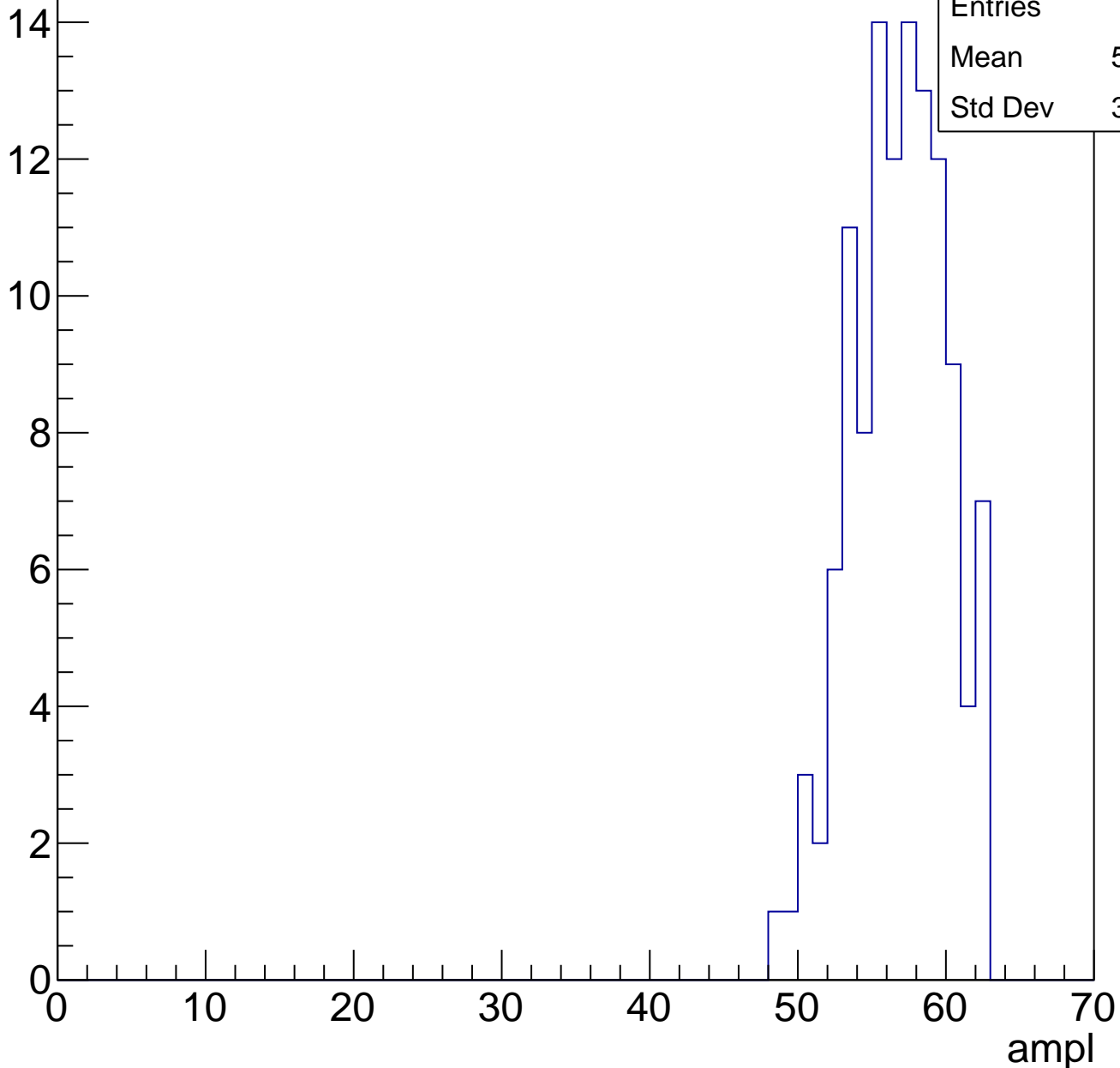


B1L001S, U20-ch119, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	56.38
Std Dev	3.156

Entry



B1L001S, U20-ch119, adc5

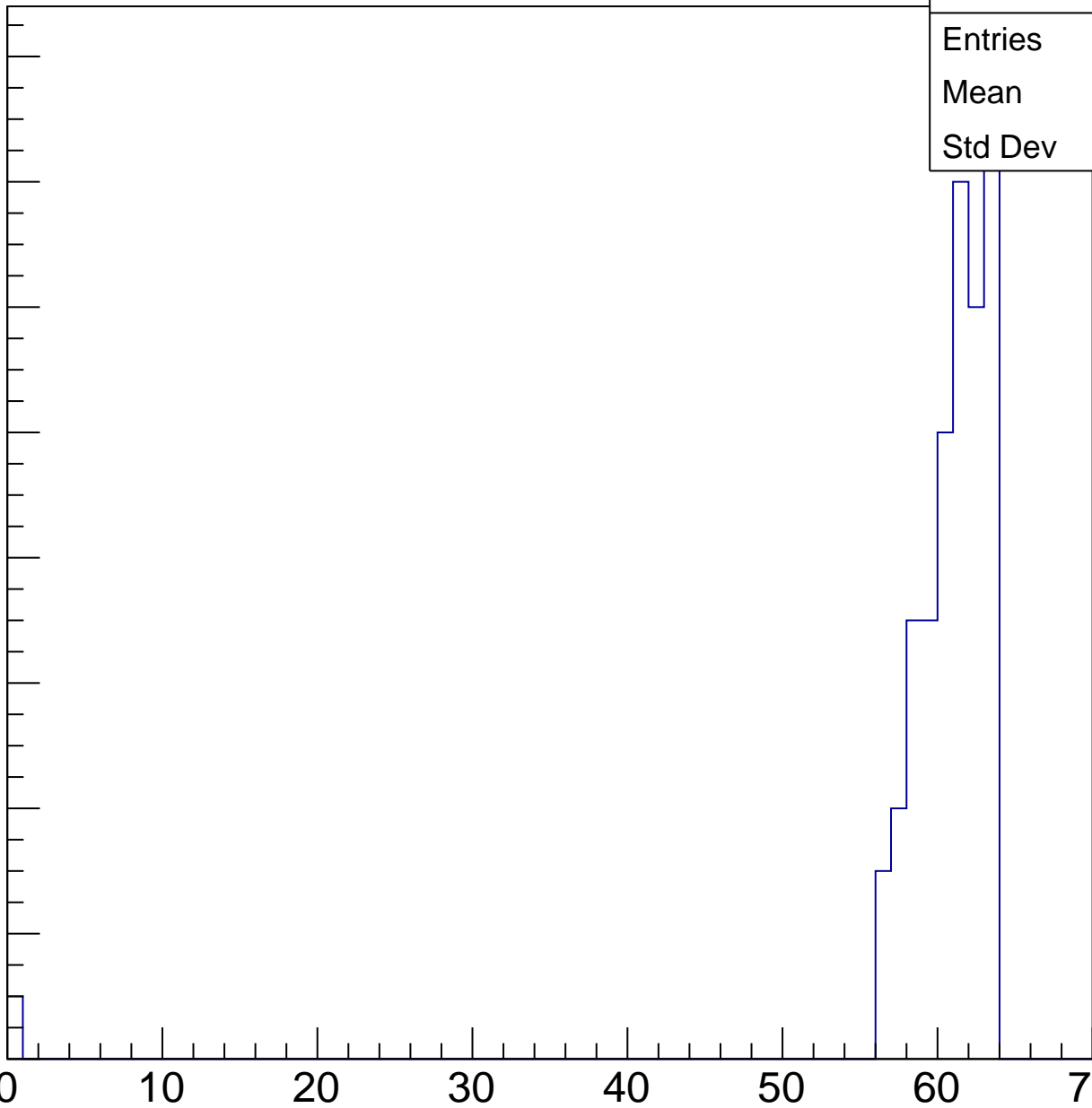
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	74
Mean	59.74
Std Dev	7.278

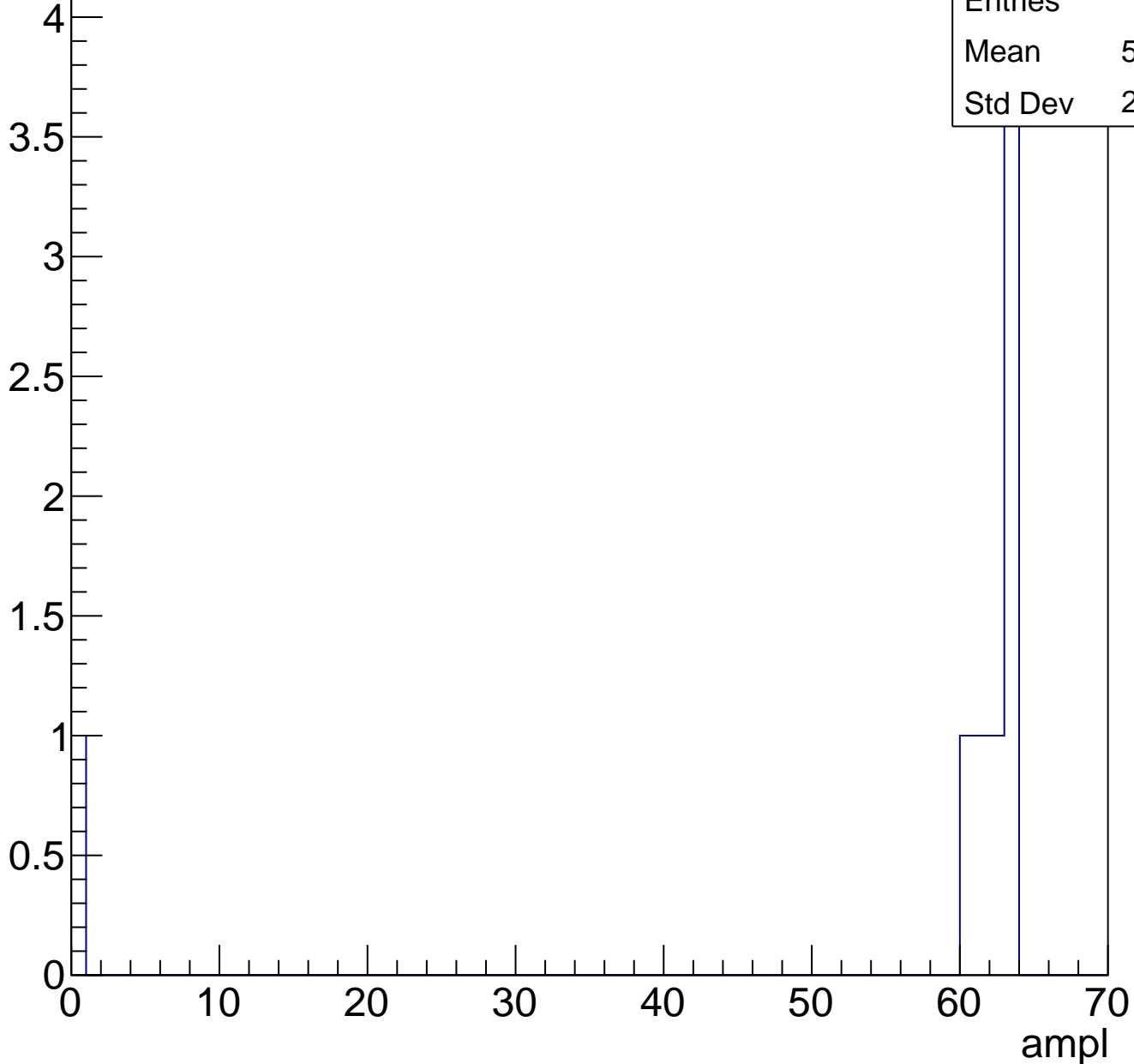
ampl



B1L001S, U20-ch119, adc6

calib_packv5_042523_0143.root, FC#2, port C2

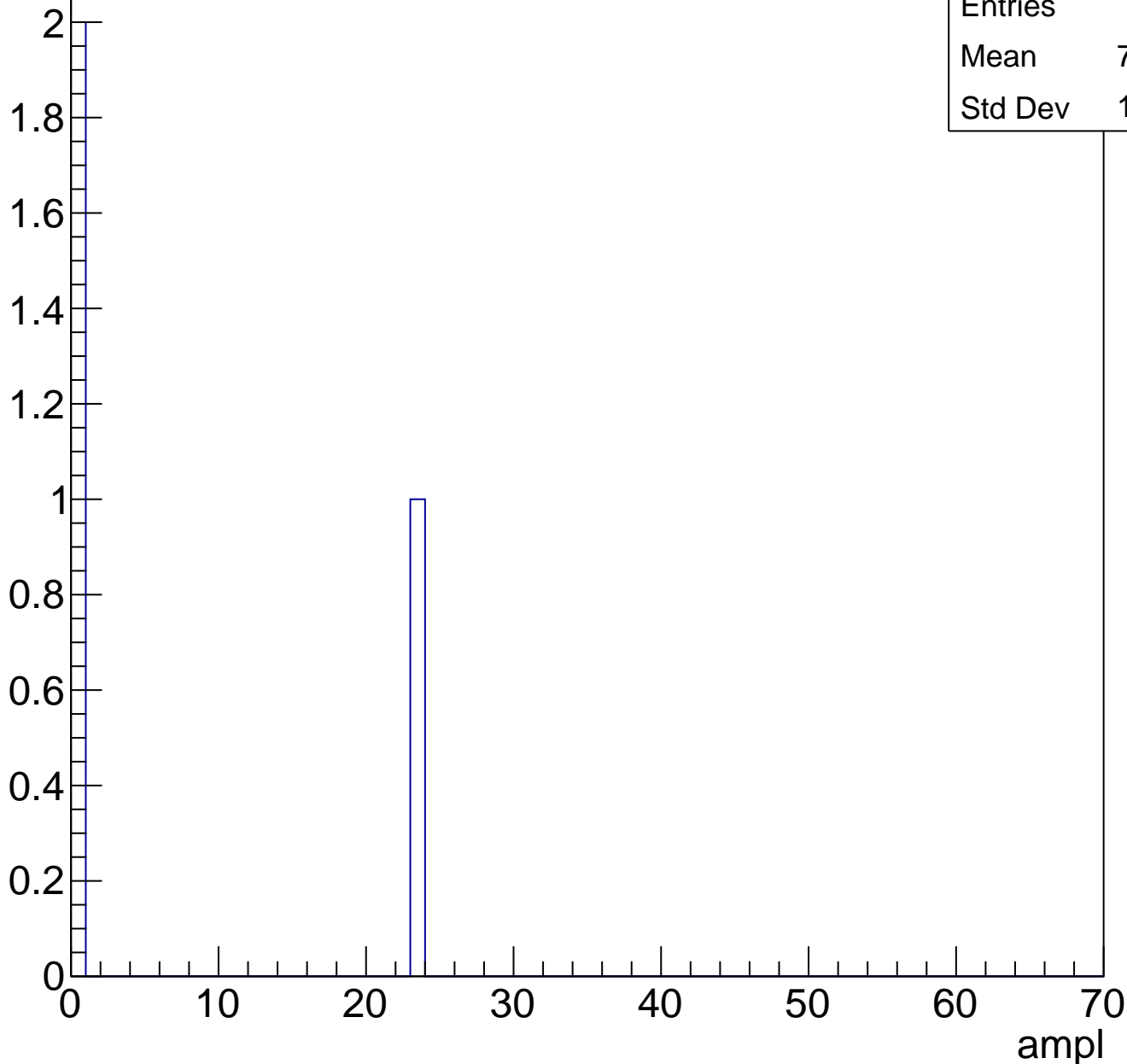
Entry



B1L001S, U20-ch119, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L001S, U20-ch120, adc0

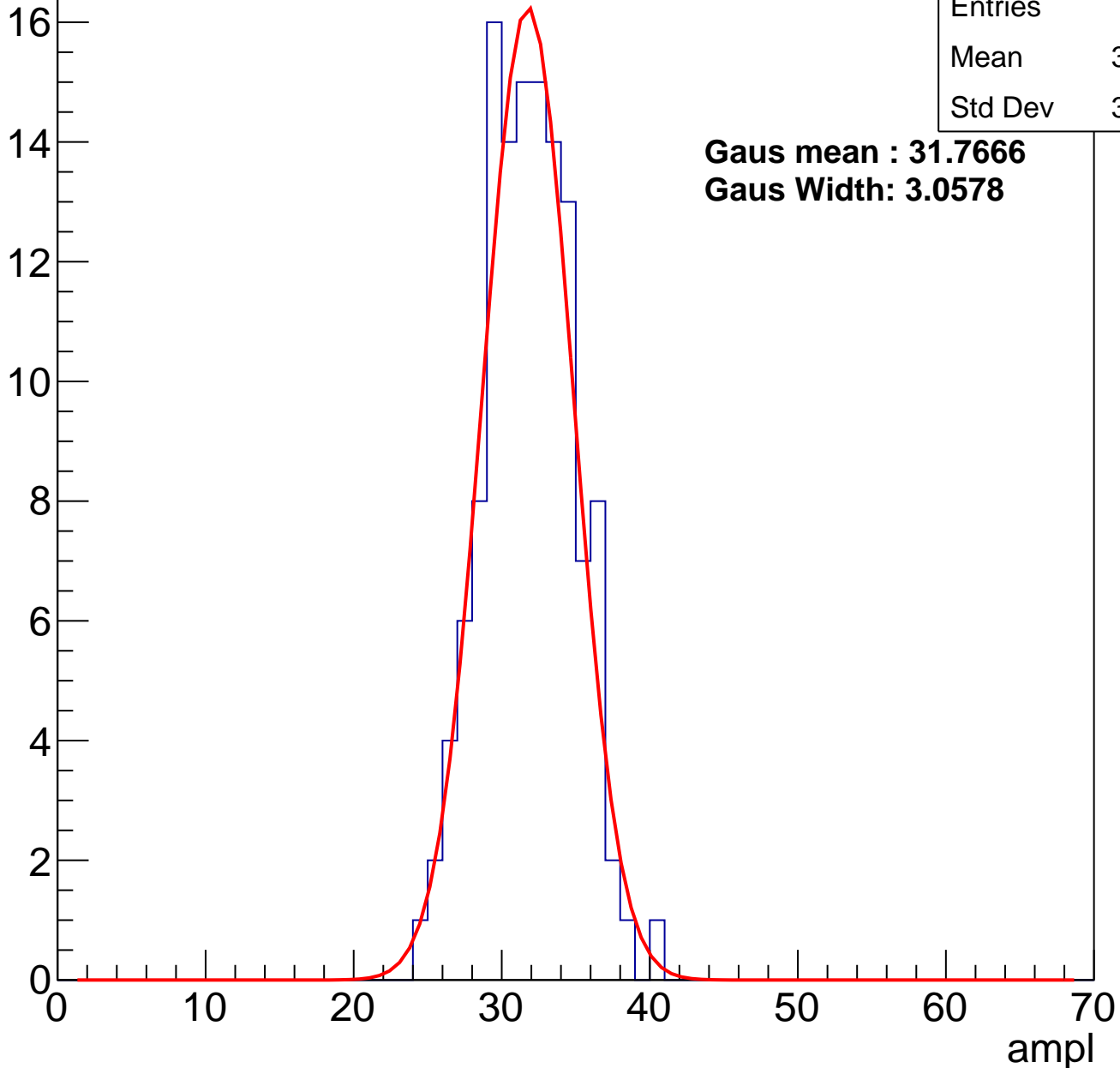
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	31.35
Std Dev	3.013

Gaus mean : 31.7666

Gaus Width: 3.0578

Entry



B1L001S, U20-ch120, adc1

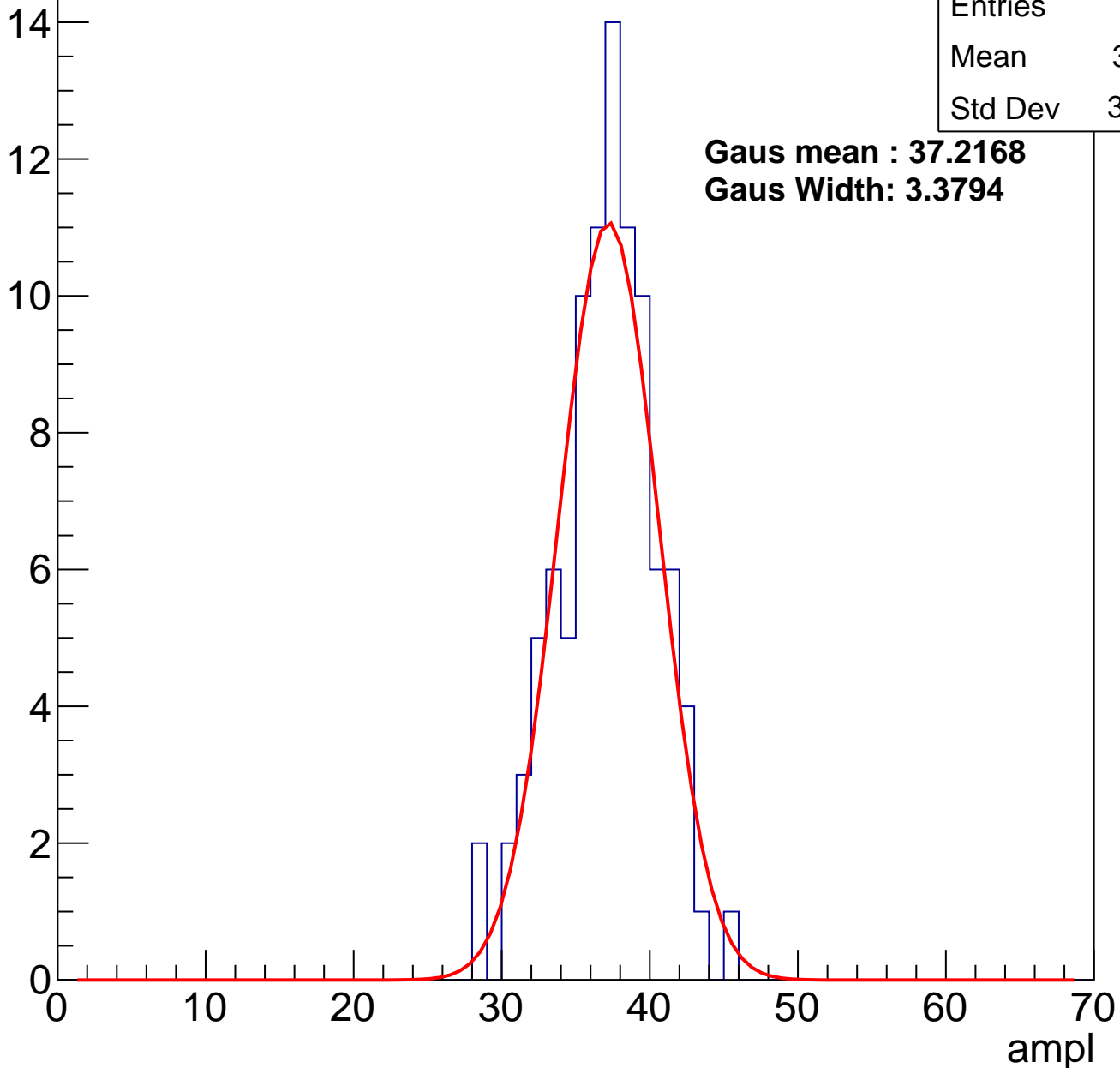
calib_packv5_042523_0143.root, FC#2, port C2

Entries	97
Mean	36.61
Std Dev	3.323

Gaus mean : 37.2168

Gaus Width: 3.3794

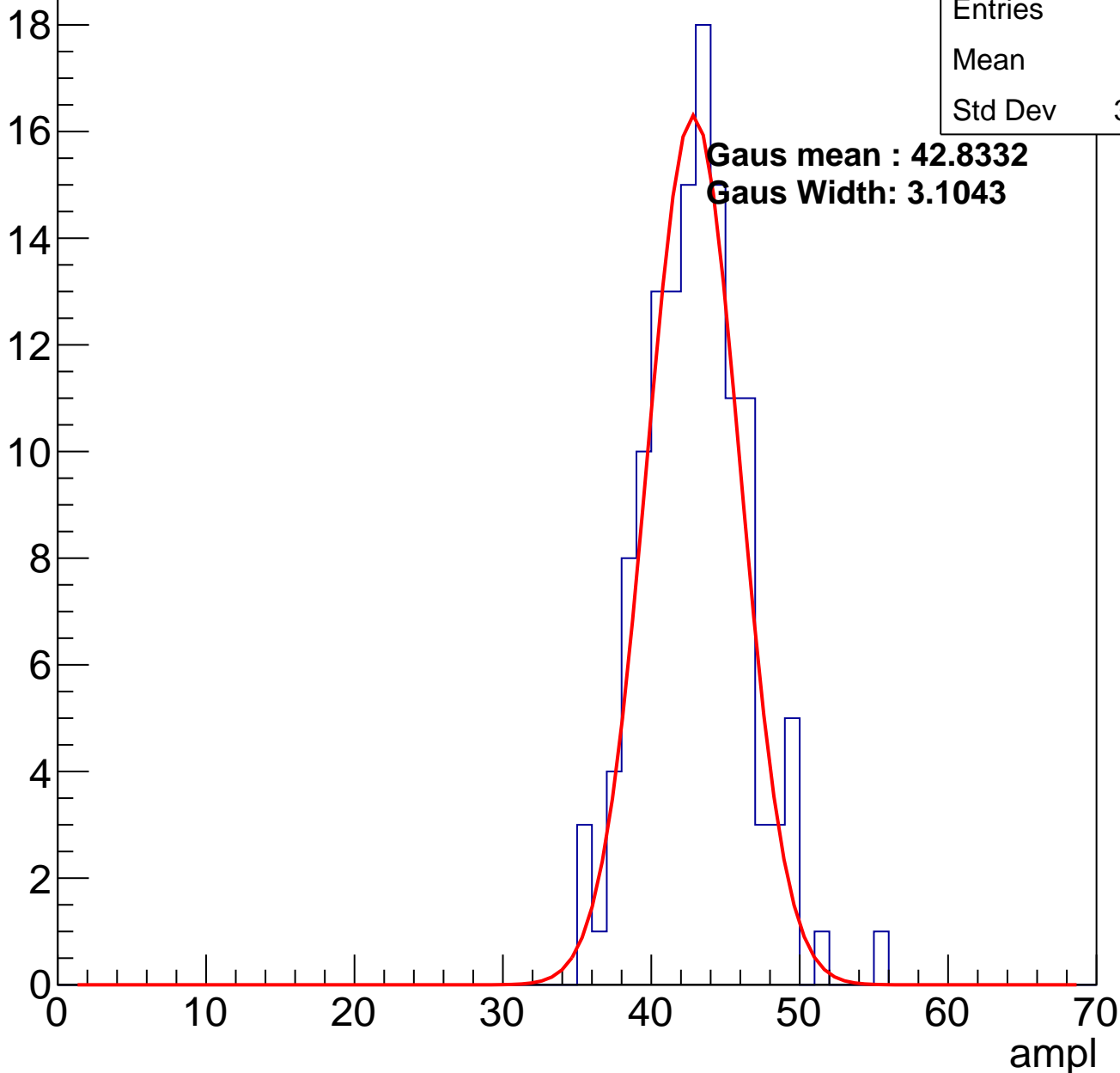
Entry



B1L001S, U20-ch120, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch120, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	48.89
Std Dev	2.904

Entry

16

14

12

10

8

6

4

2

0

0

10

20

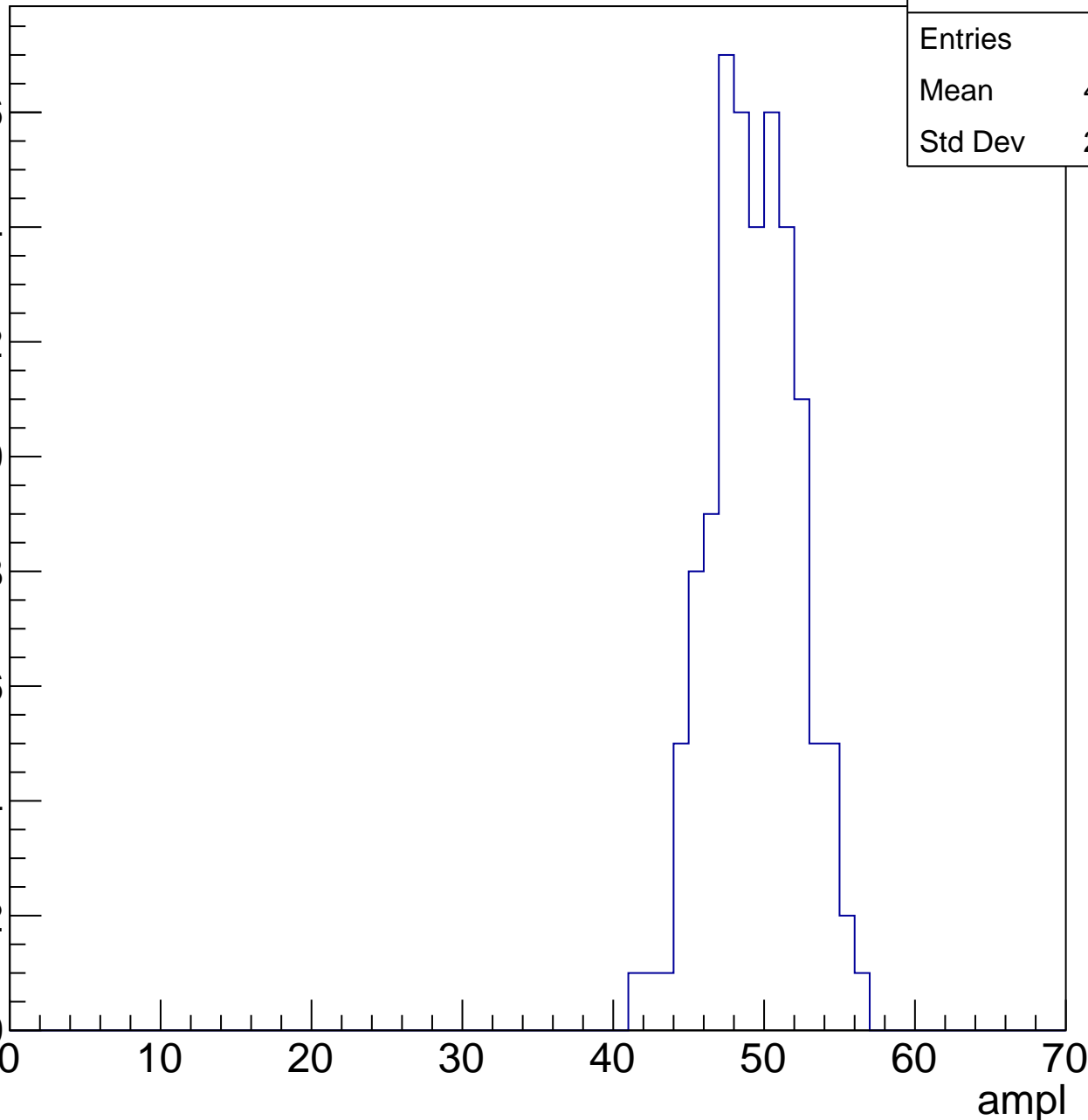
30

40

50

60

ampl



B1L001S, U20-ch120, adc4

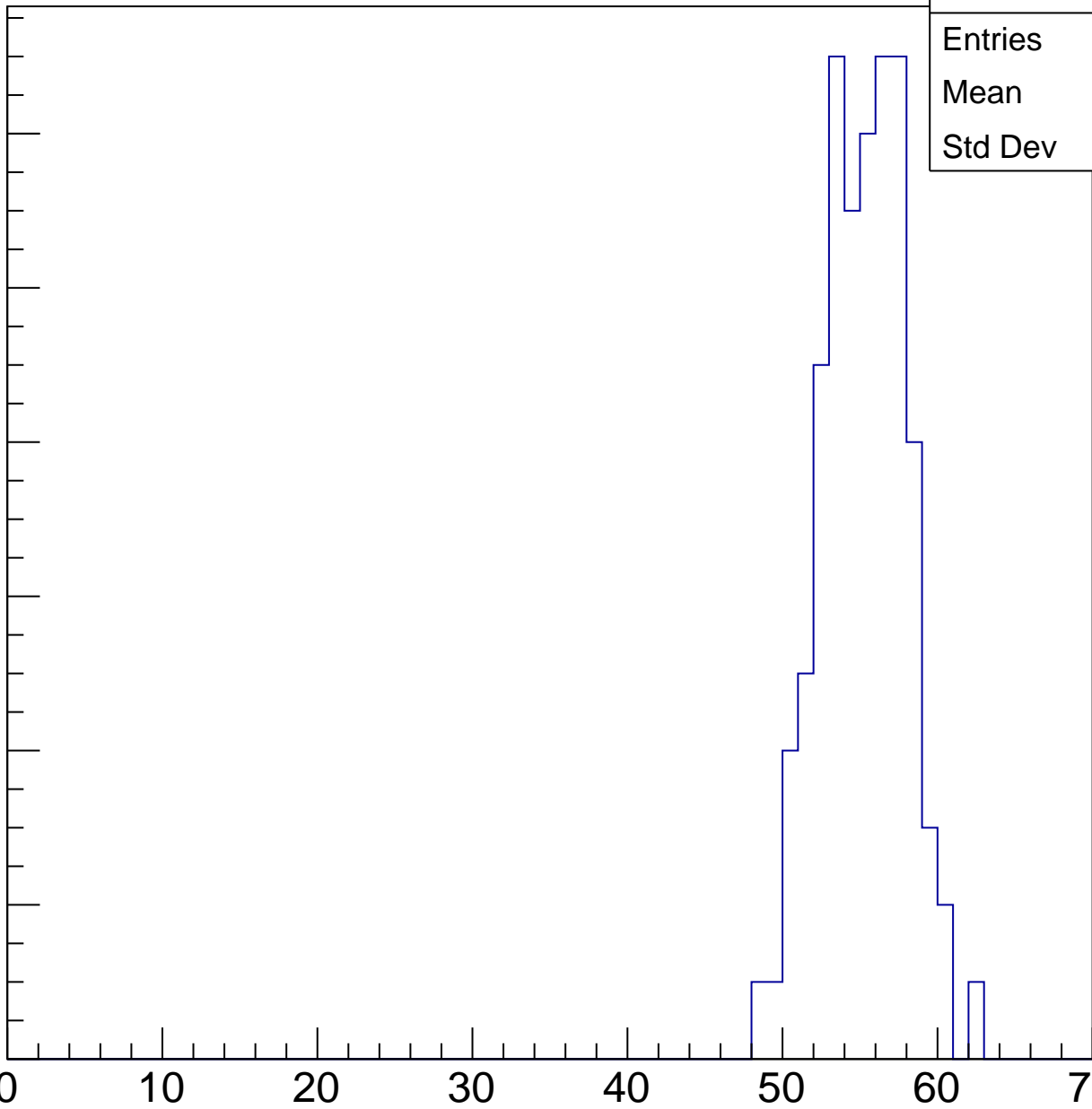
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	96
Mean	54.74
Std Dev	2.674

ampl



B1L001S, U20-ch120, adc5

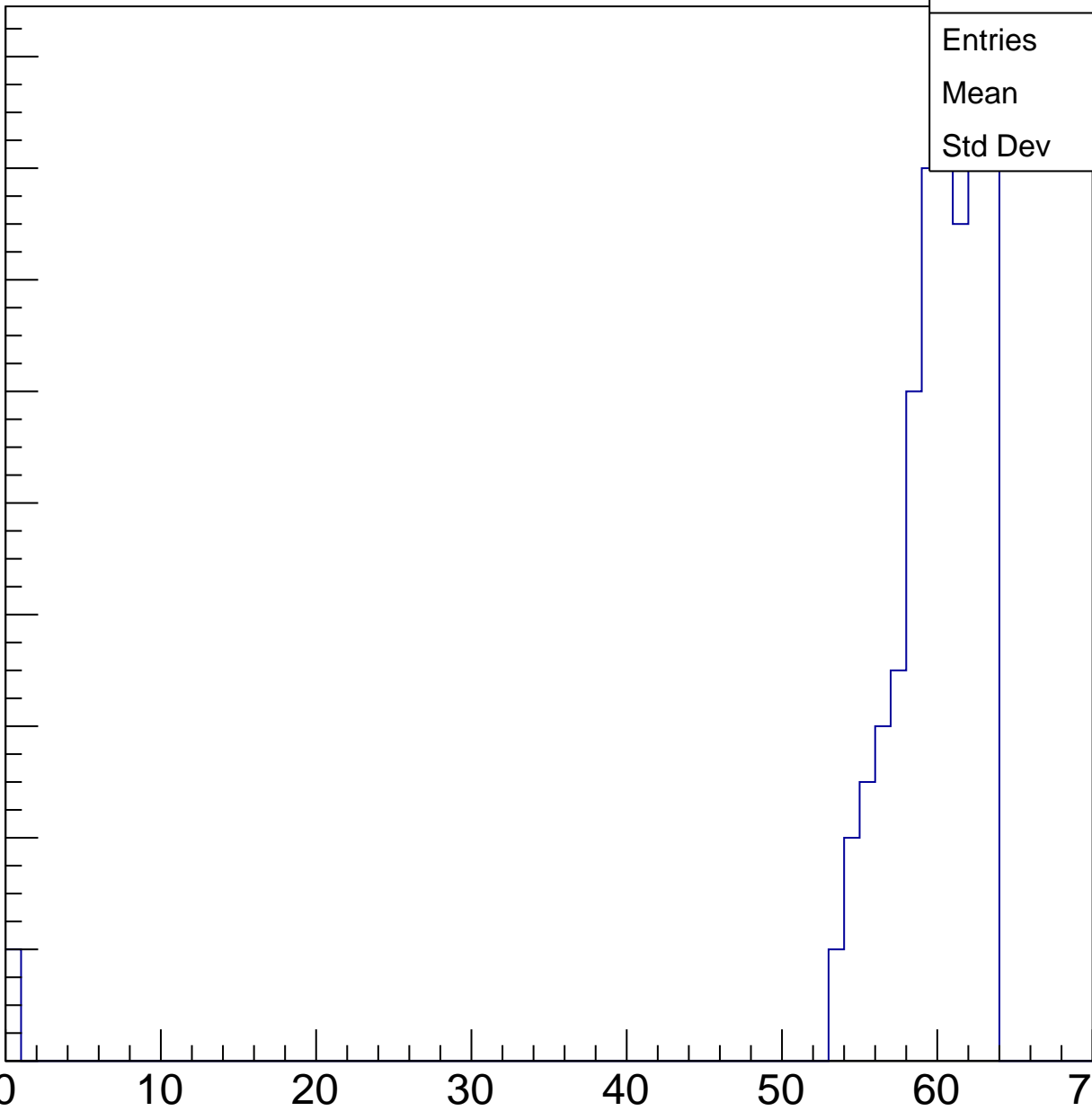
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	122
Mean	58.66
Std Dev	8.007

ampl

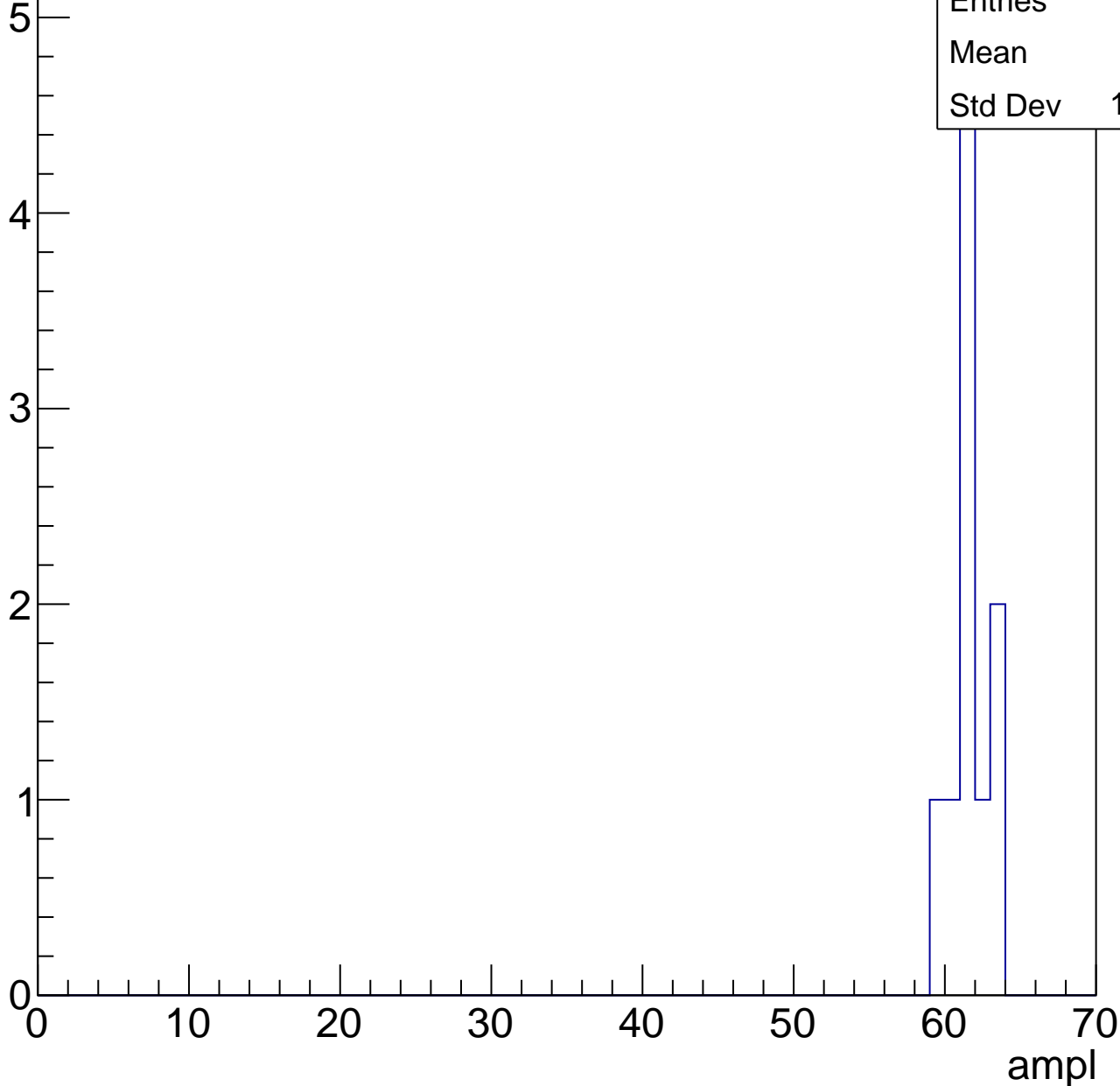


B1L001S, U20-ch120, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

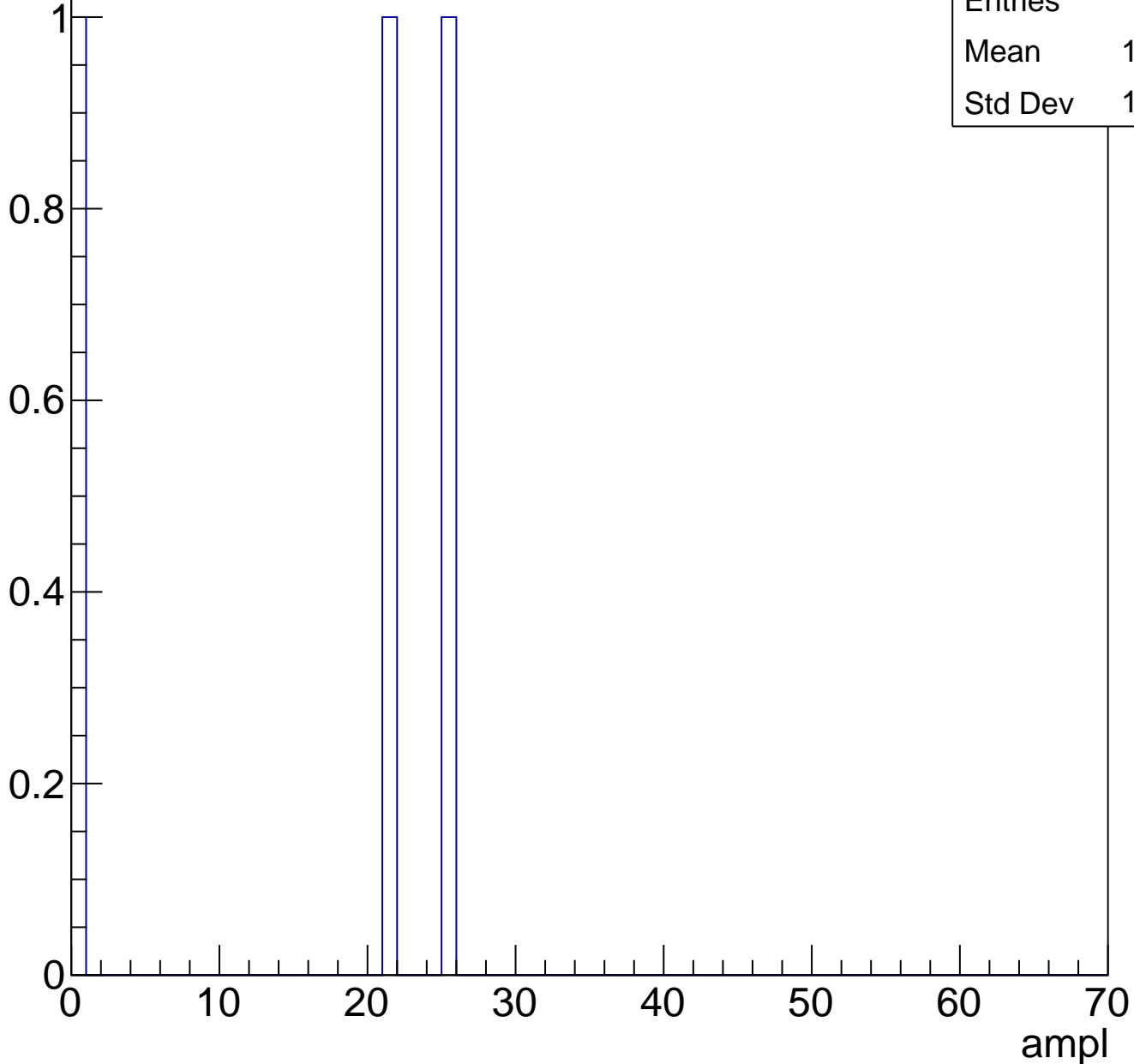
Entries	10
Mean	61.2
Std Dev	1.166



B1L001S, U20-ch120, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



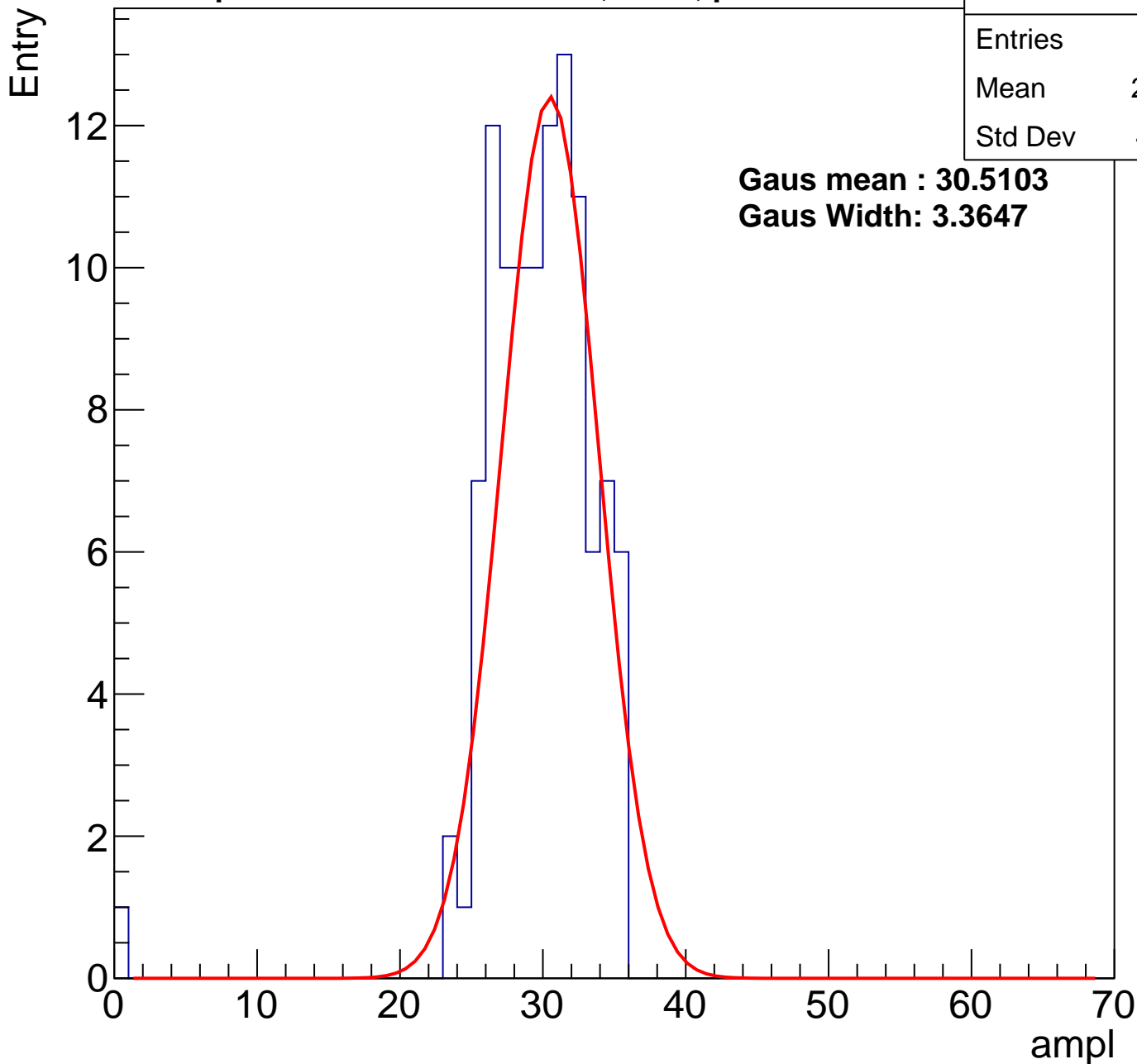
B1L001S, U20-ch121, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	29.24
Std Dev	4.141

Gaus mean : 30.5103

Gaus Width: 3.3647



B1L001S, U20-ch121, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	35.39
Std Dev	3.204

Gaus mean : 36.0728

Gaus Width: 3.4559

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

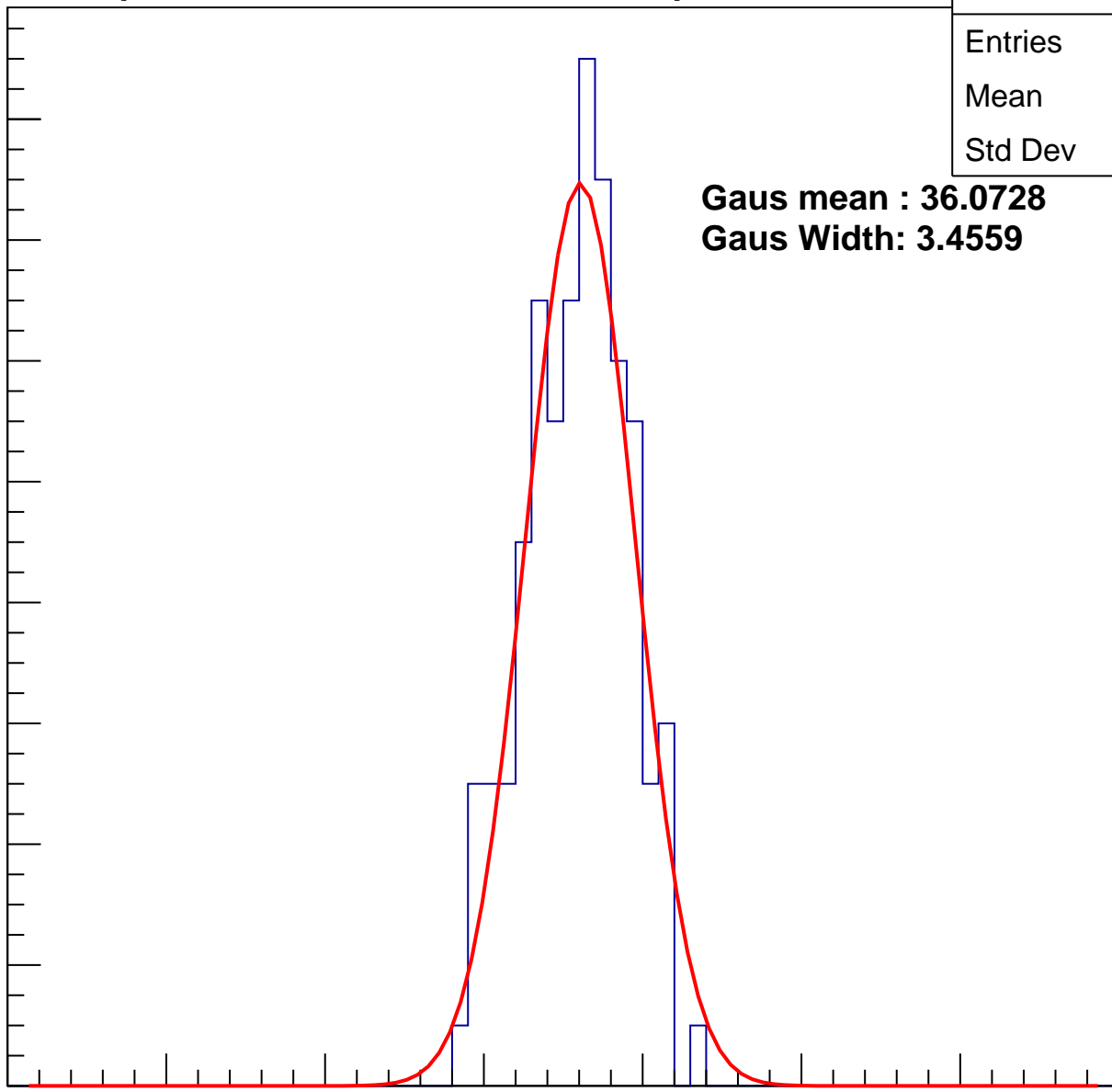
40

50

60

70

ampl



B1L001S, U20-ch121, adc2

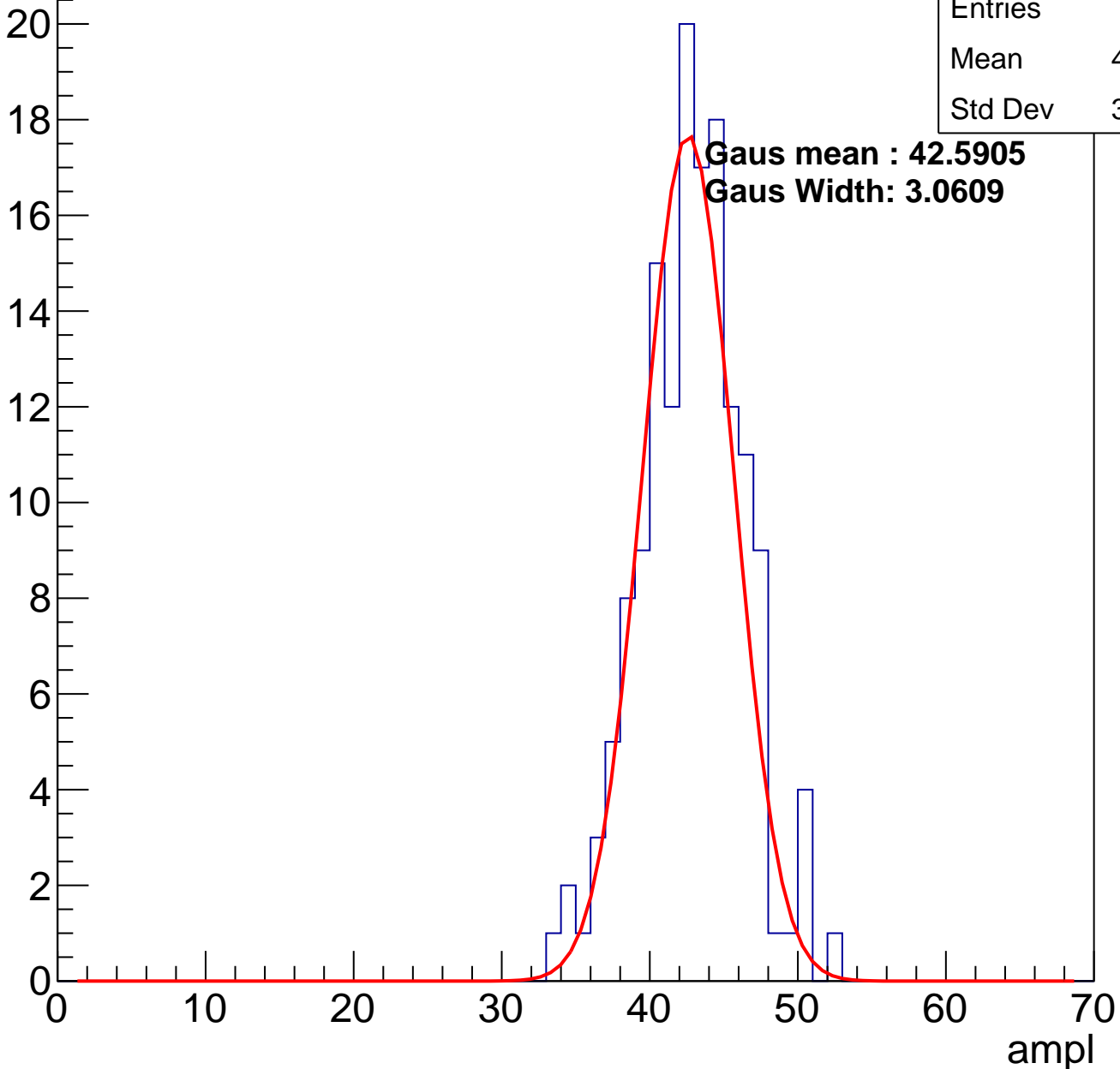
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	150
Mean	42.38
Std Dev	3.454

Gaus mean : 42.5905

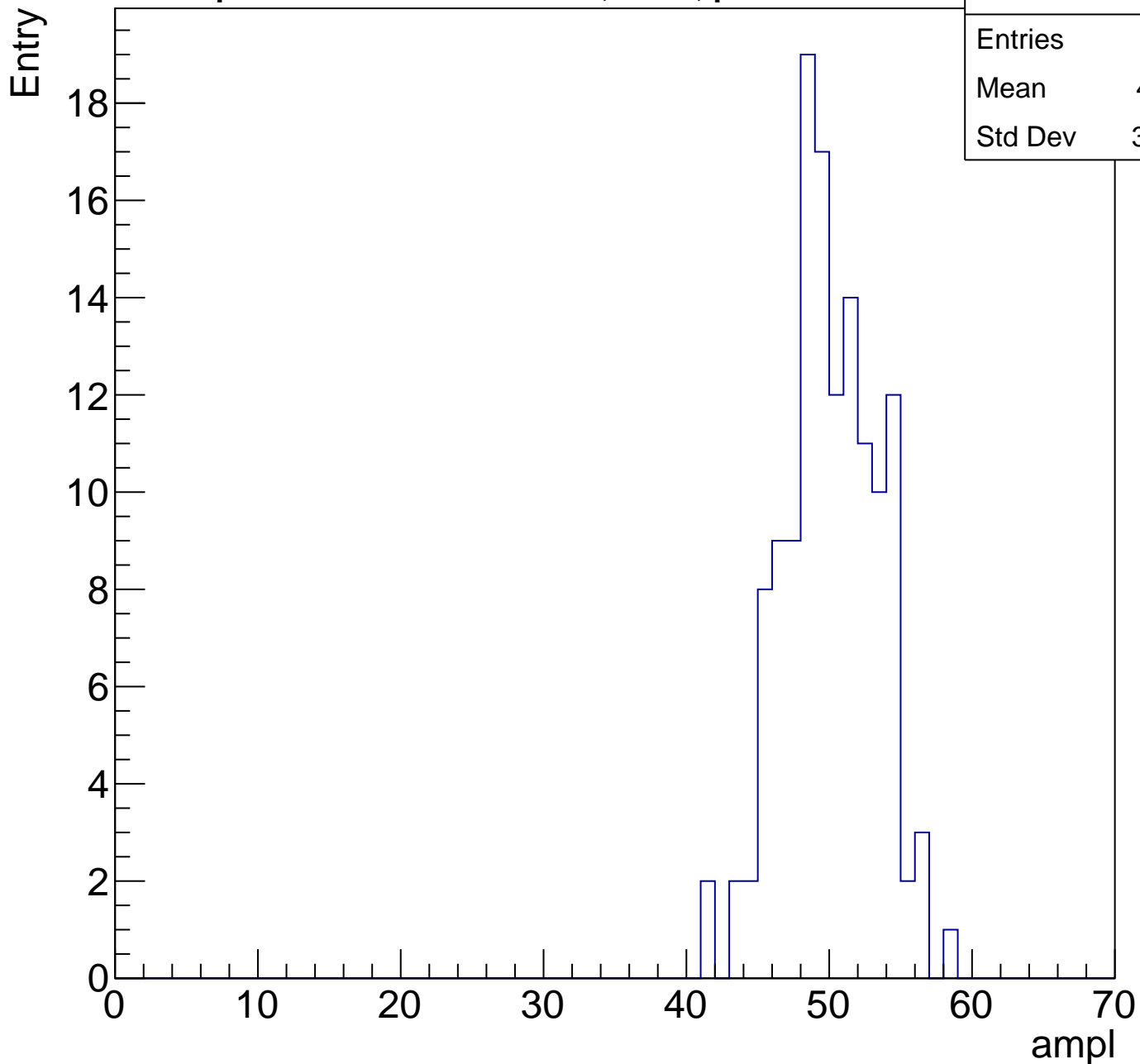
Gaus Width: 3.0609



B1L001S, U20-ch121, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	49.61
Std Dev	3.235

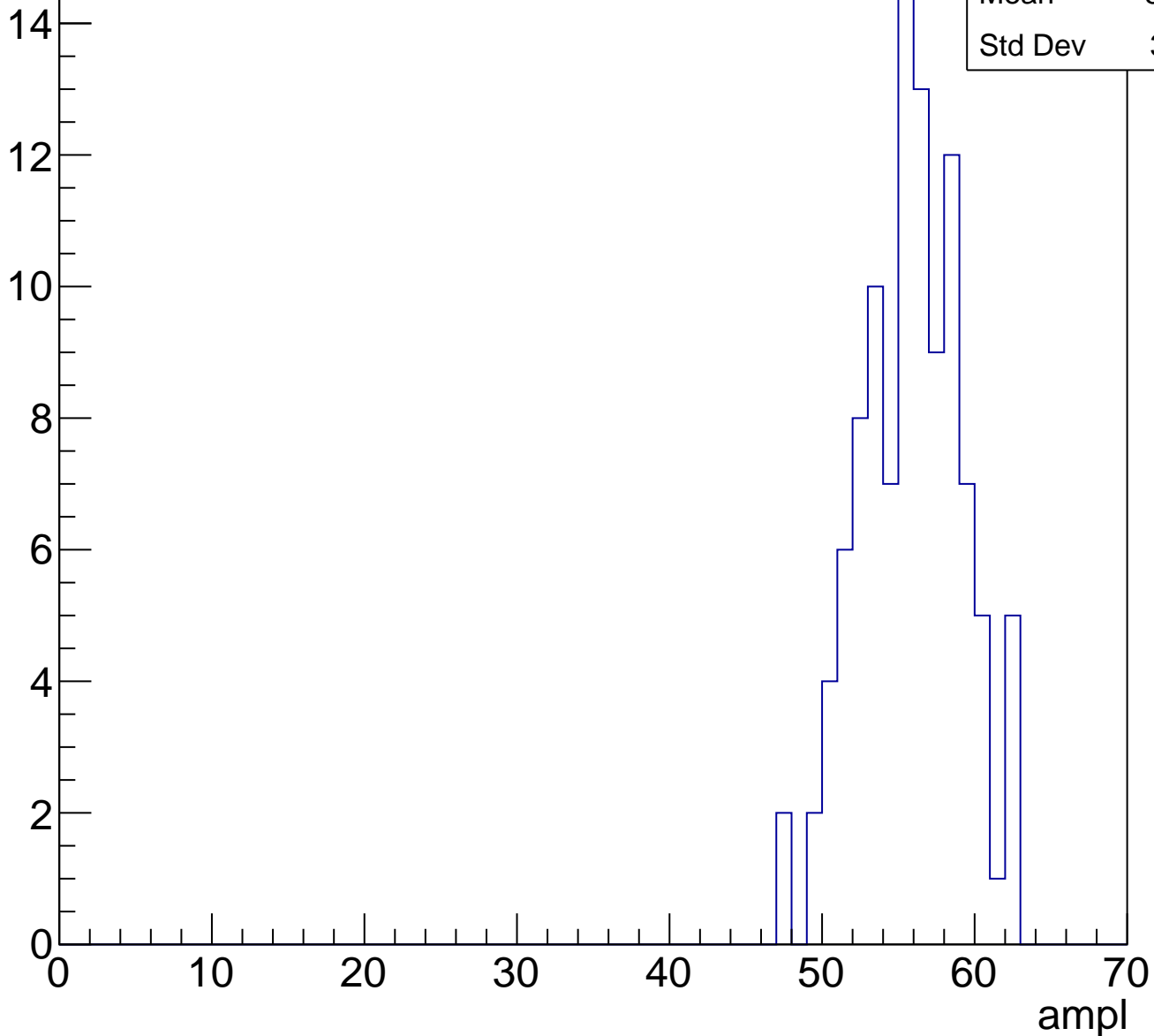


B1L001S, U20-ch121, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	55.36
Std Dev	3.331

Entry



B1L001S, U20-ch121, adc5

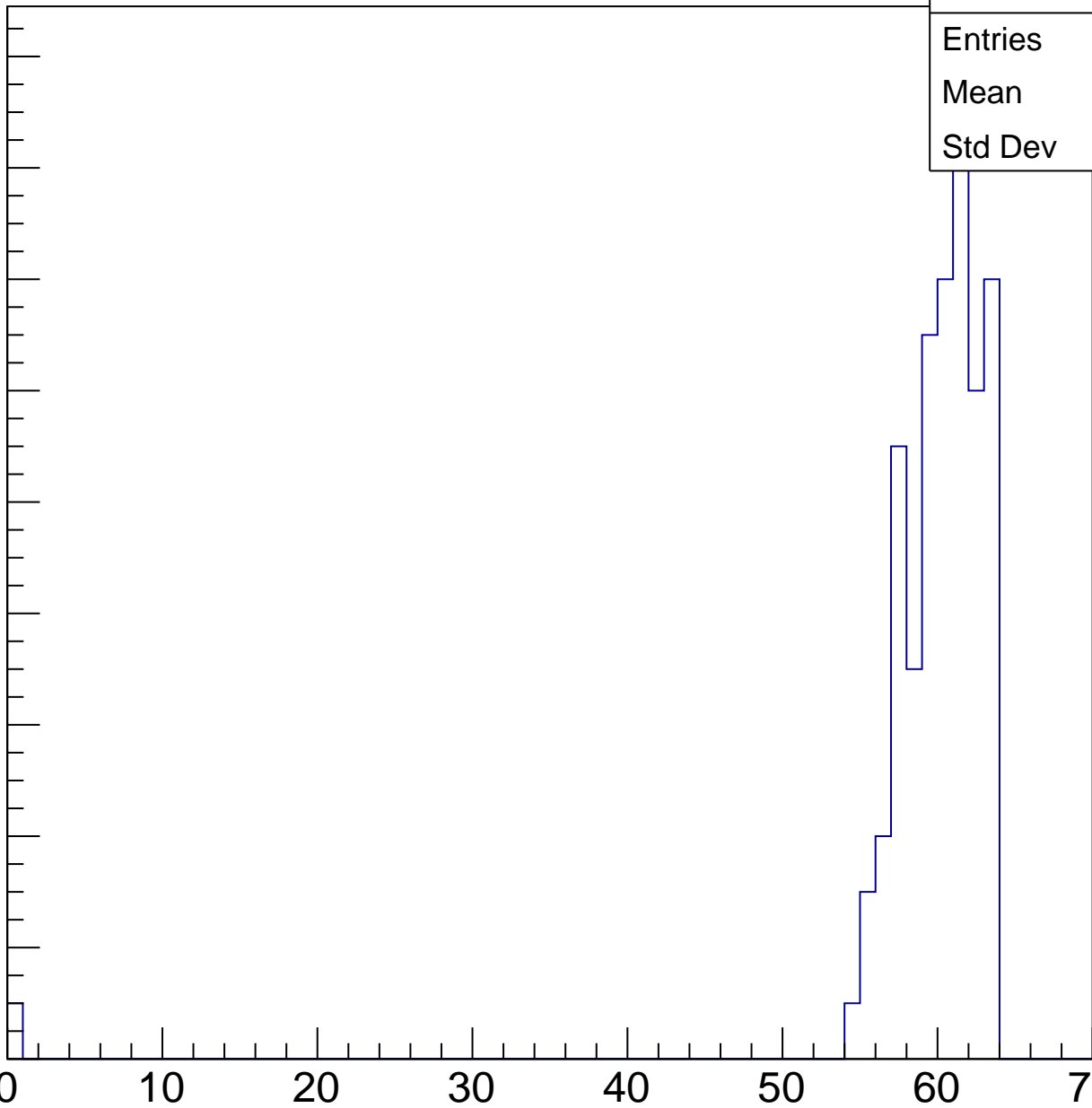
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.26
Std Dev	6.431

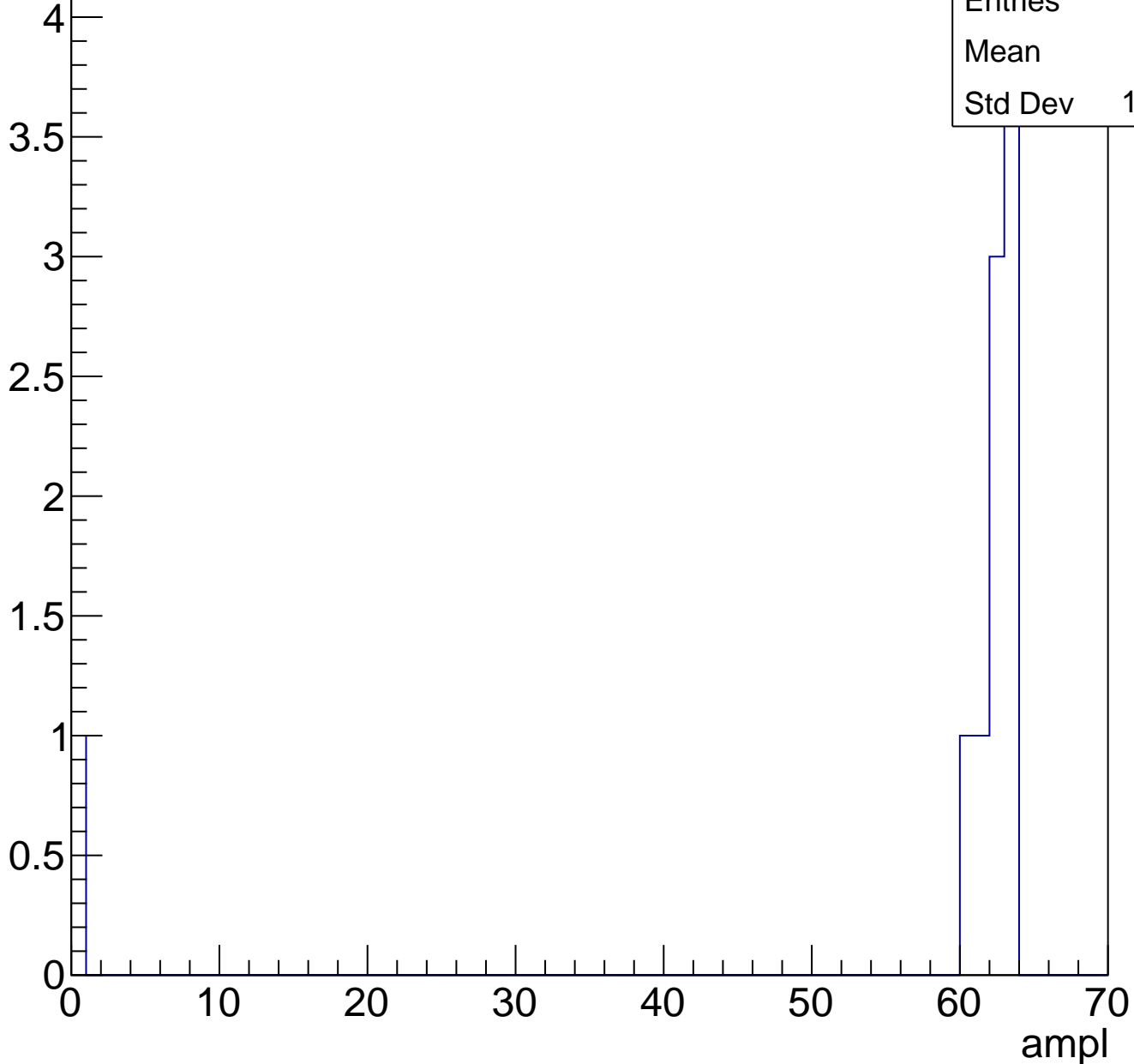
ampl



B1L001S, U20-ch121, adc6

calib_packv5_042523_0143.root, FC#2, port C2

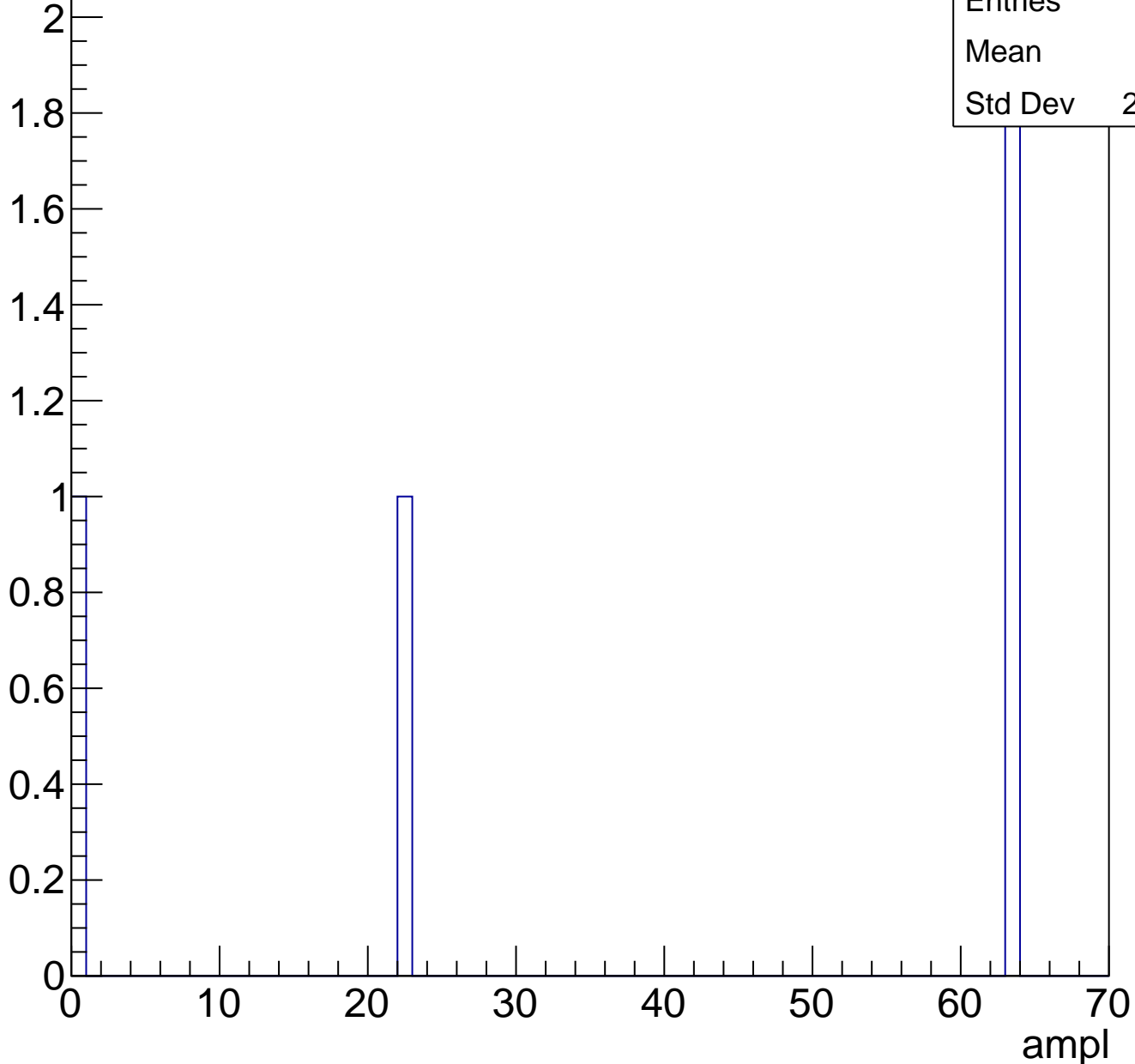
Entry



B1L001S, U20-ch121, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch122, adc0

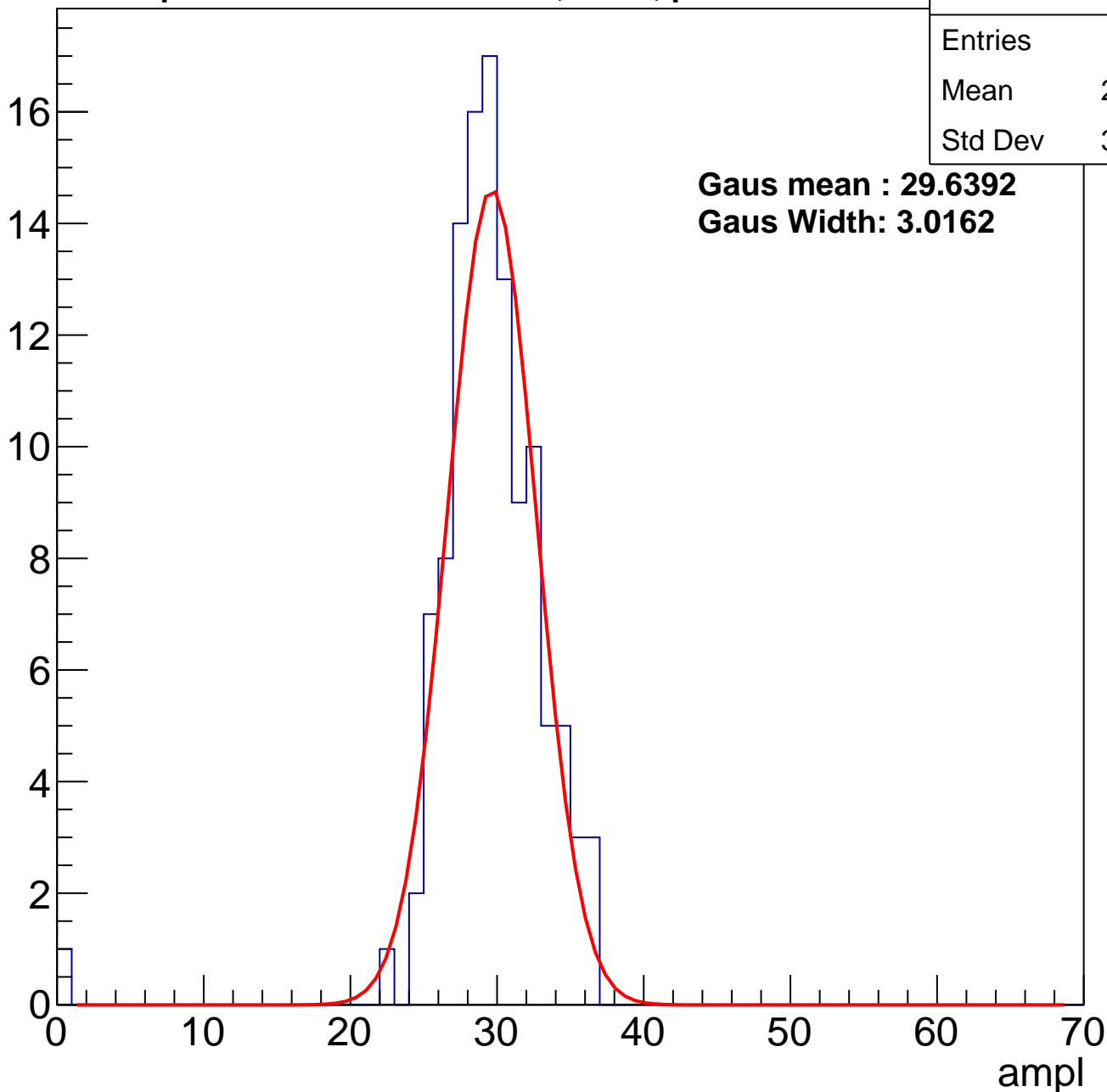
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	29.03
Std Dev	3.968

Gaus mean : 29.6392

Gaus Width: 3.0162

Entry



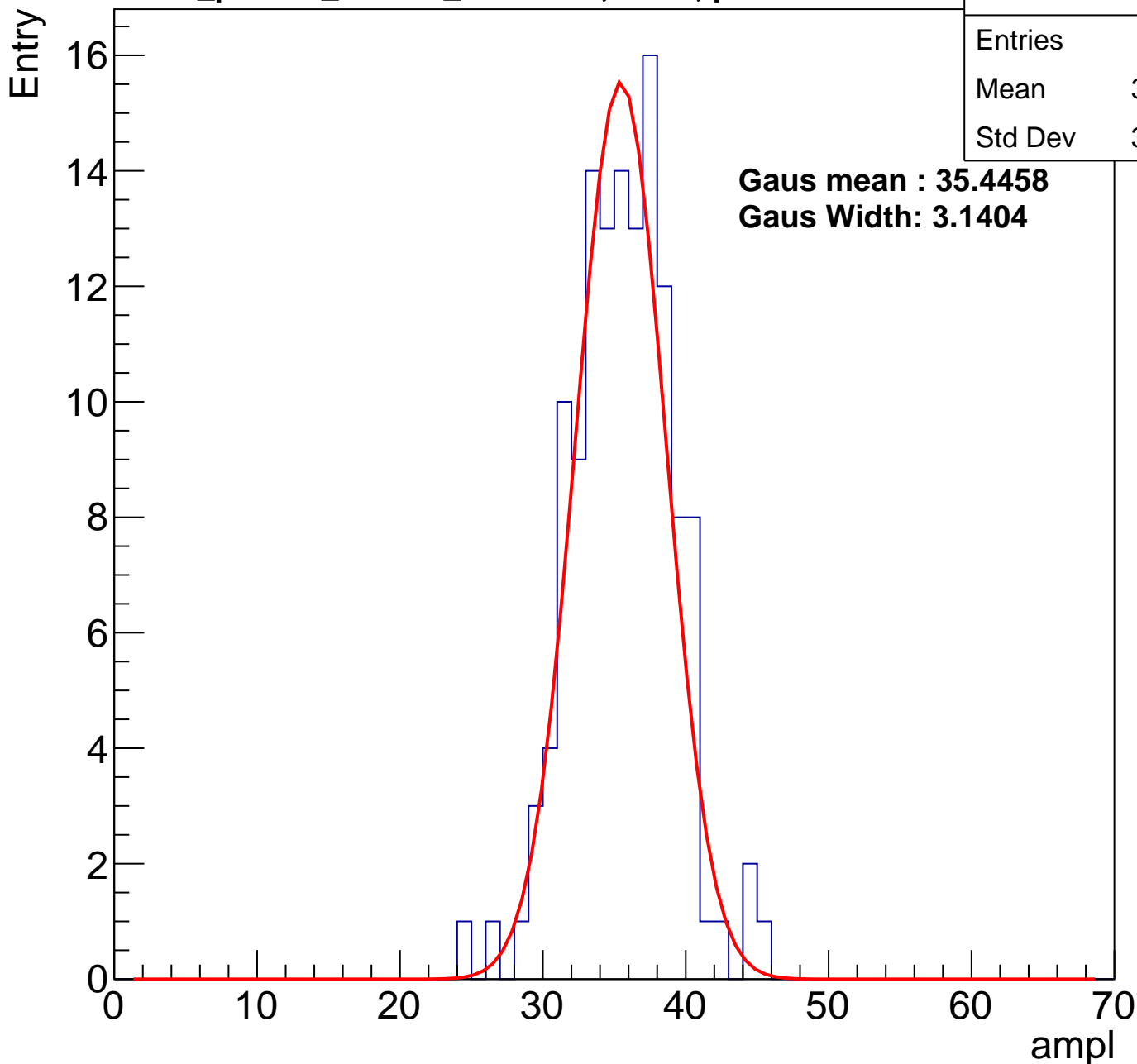
B1L001S, U20-ch122, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	35.16
Std Dev	3.509

Gaus mean : 35.4458

Gaus Width: 3.1404



B1L001S, U20-ch122, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

138

Mean

41.99

Std Dev

3.341

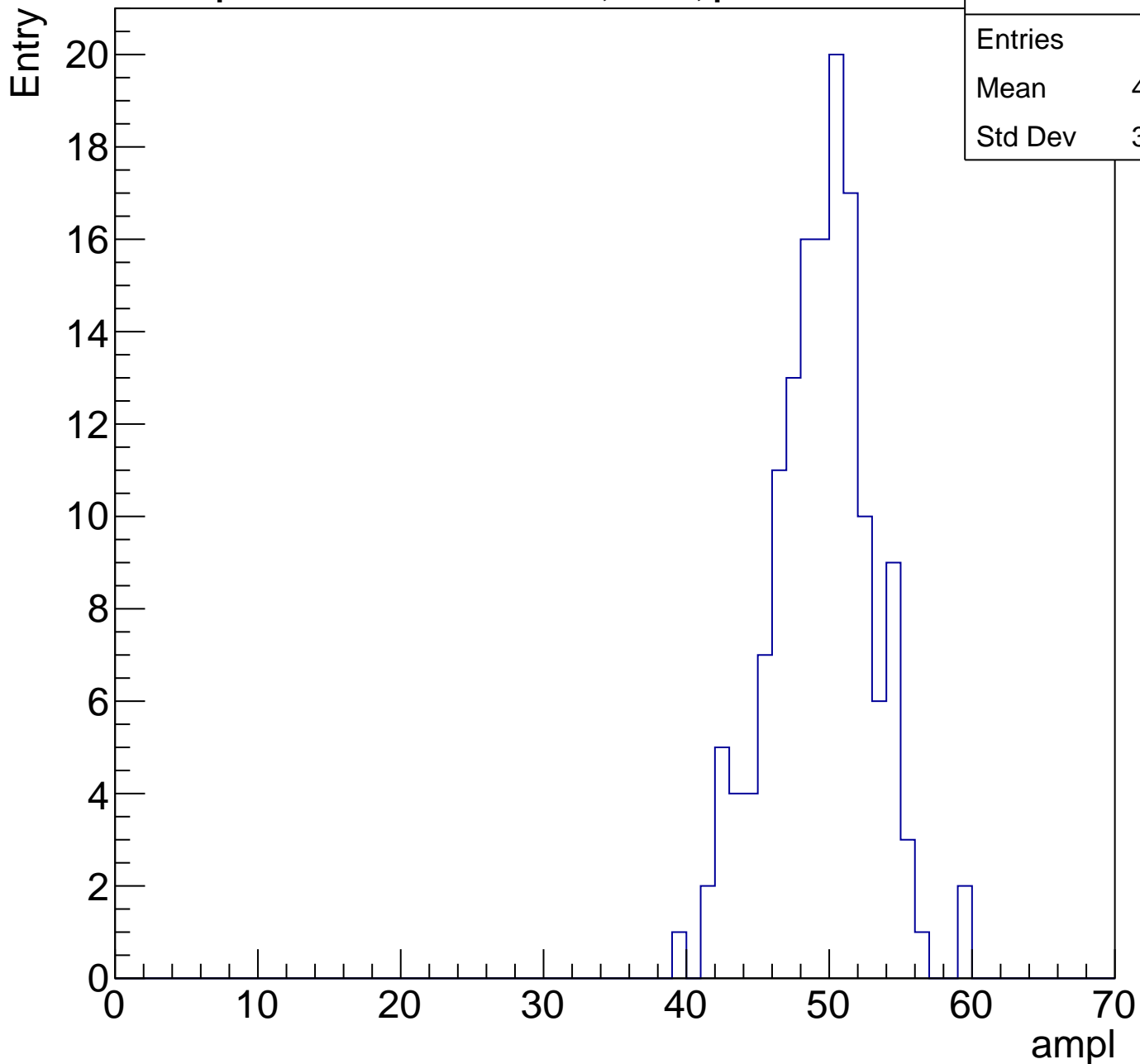
Gaus mean : 42.6159

Gaus Width: 3.5849

B1L001S, U20-ch122, adc3

calib_packv5_042523_0143.root, FC#2, port C2

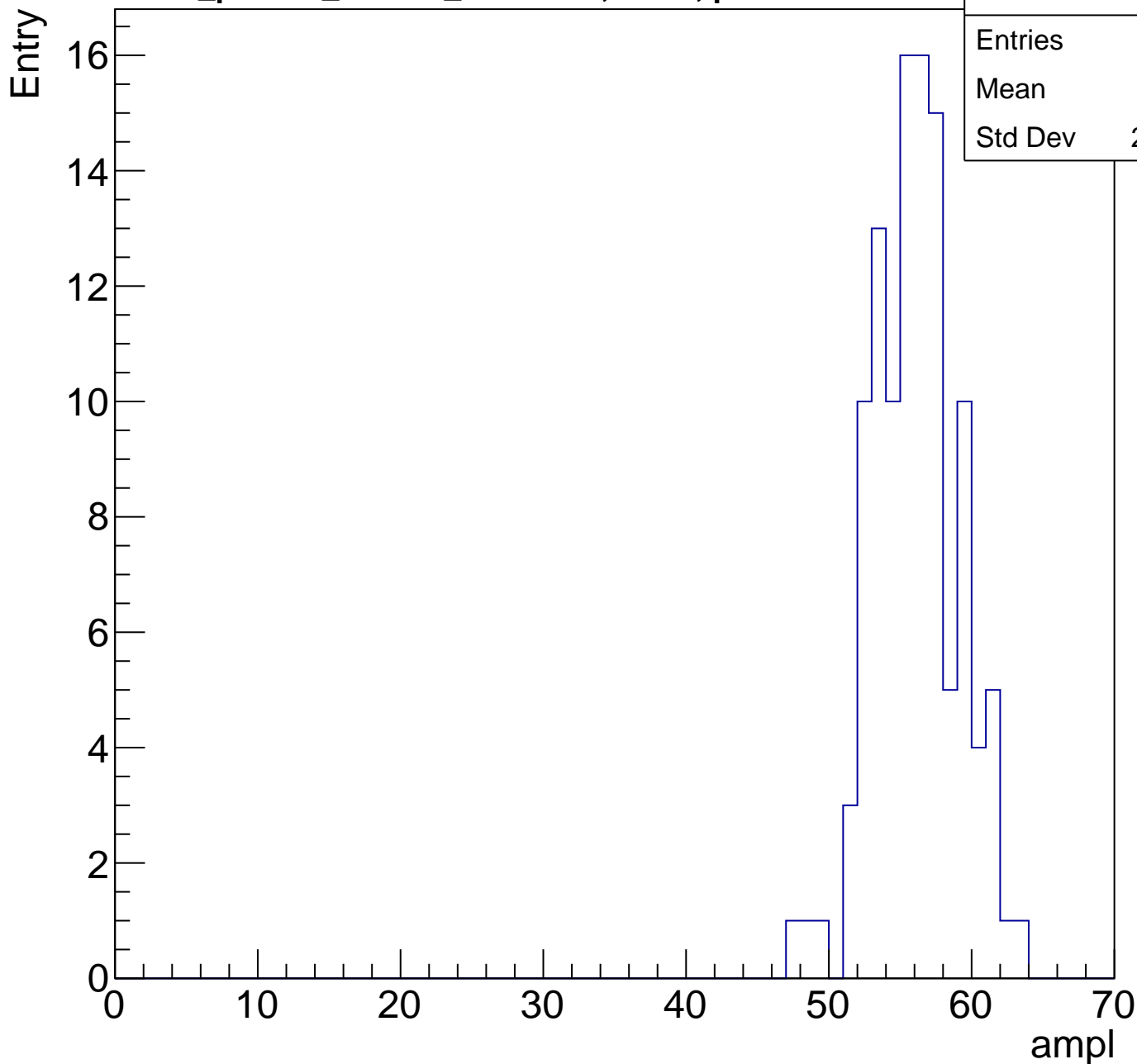
Entries	147
Mean	48.93
Std Dev	3.549



B1L001S, U20-ch122, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	55.6
Std Dev	2.962



B1L001S, U20-ch122, adc5

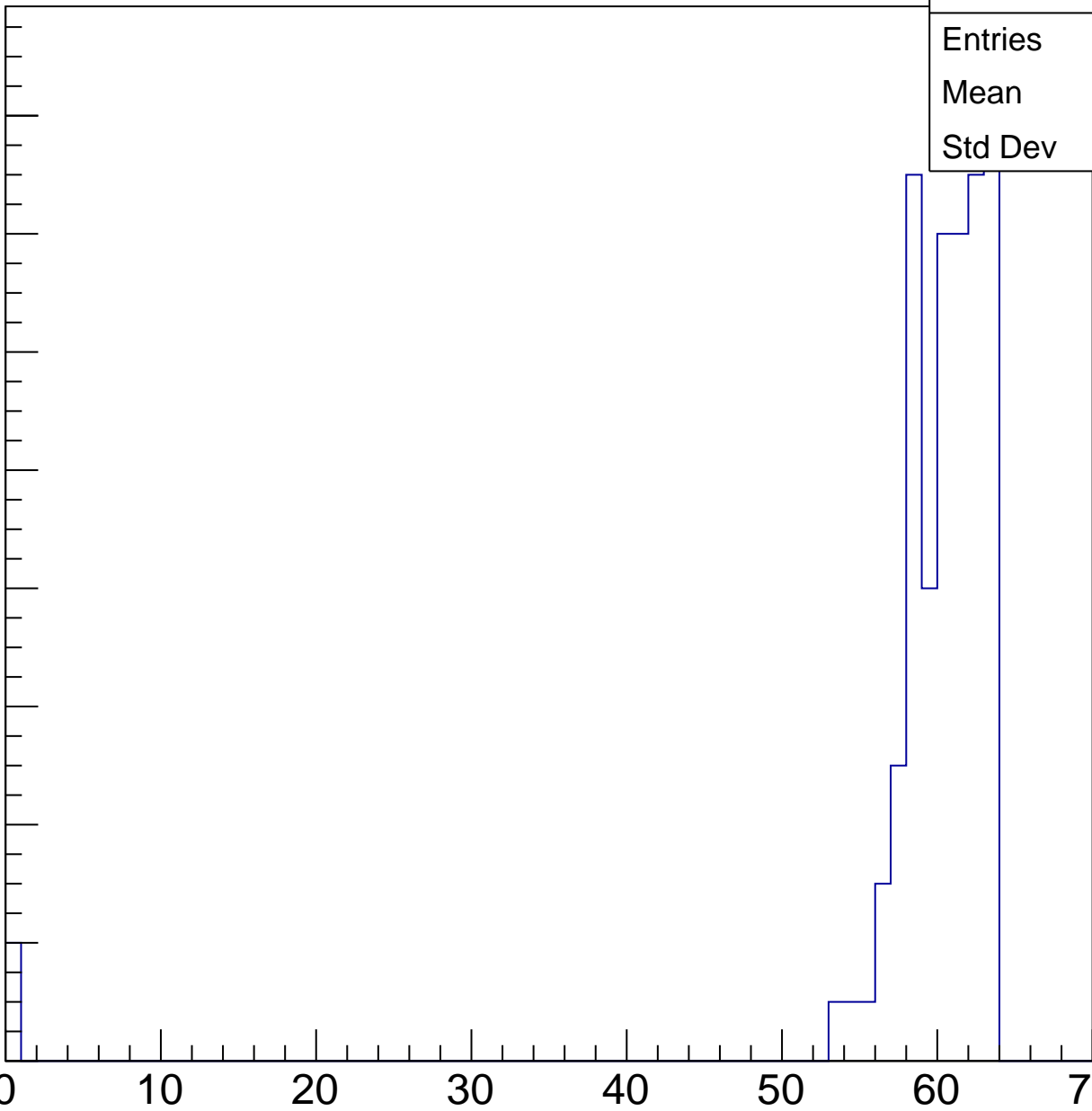
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	58.88
Std Dev	8.884

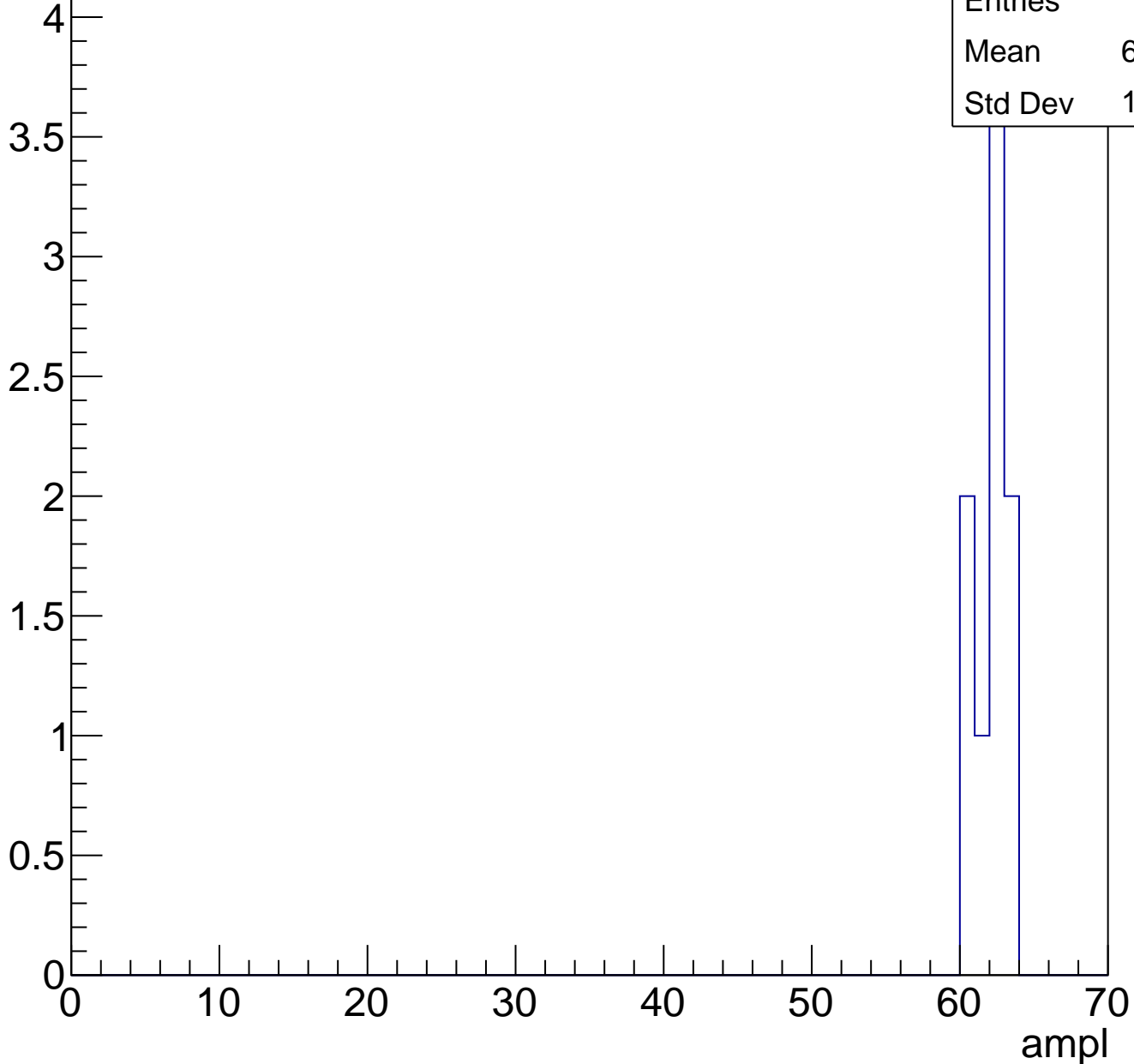
ampl



B1L001S, U20-ch122, adc6

calib_packv5_042523_0143.root, FC#2, port C2

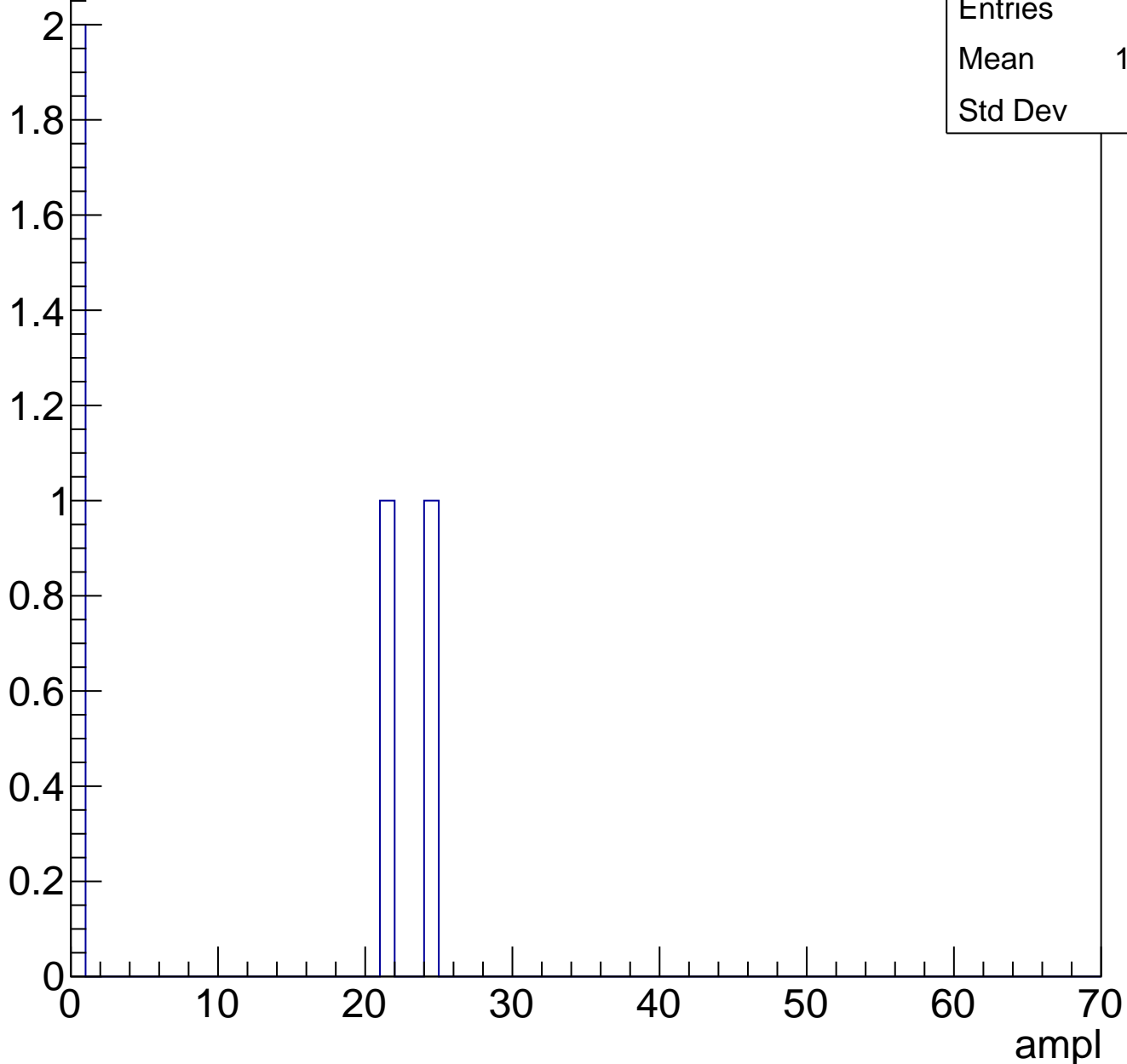
Entry



B1L001S, U20-ch122, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	11.25
Std Dev	11.3

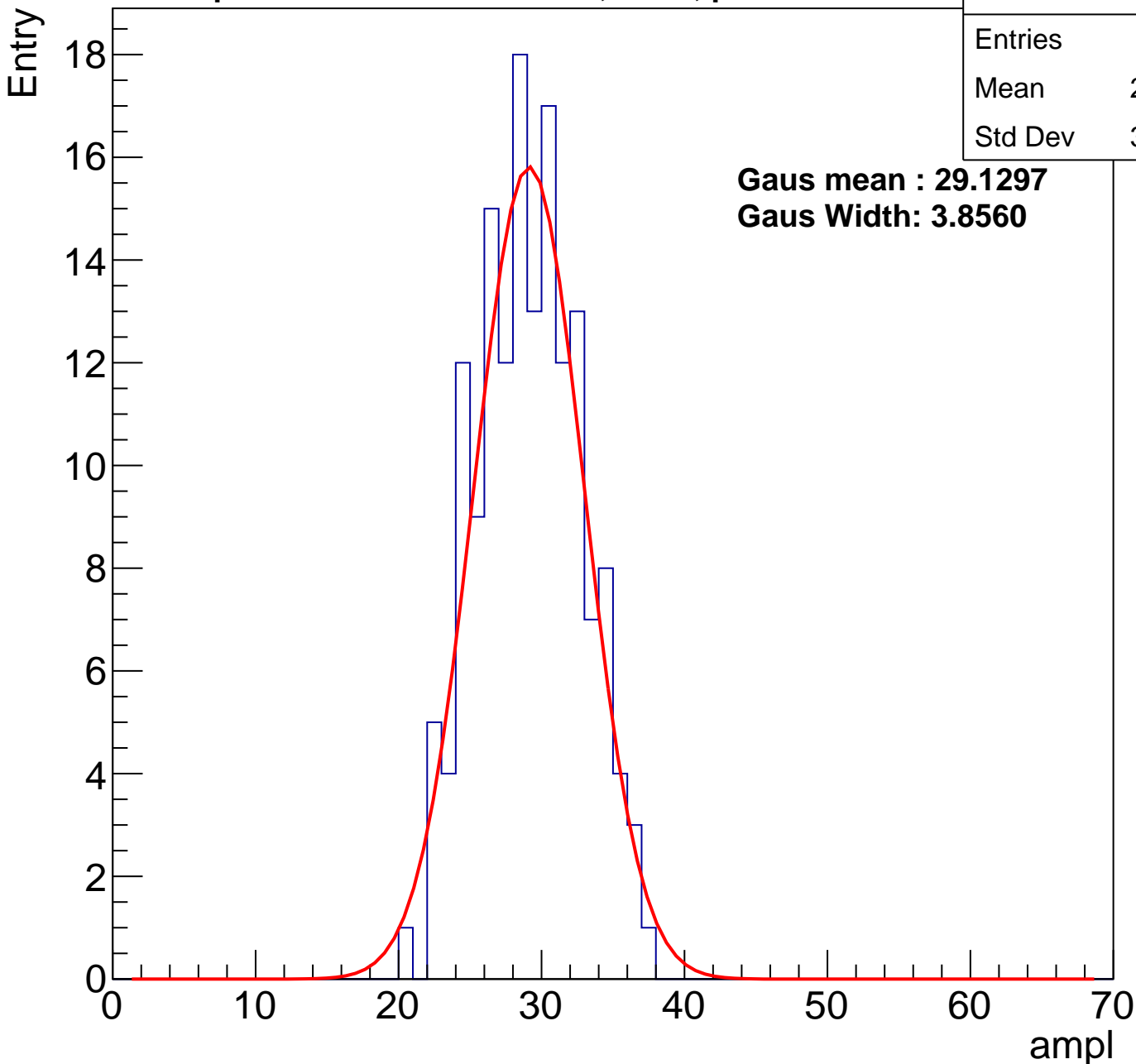
B1L001S, U20-ch123, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	28.68
Std Dev	3.556

Gaus mean : 29.1297

Gaus Width: 3.8560



B1L001S, U20-ch123, adc1

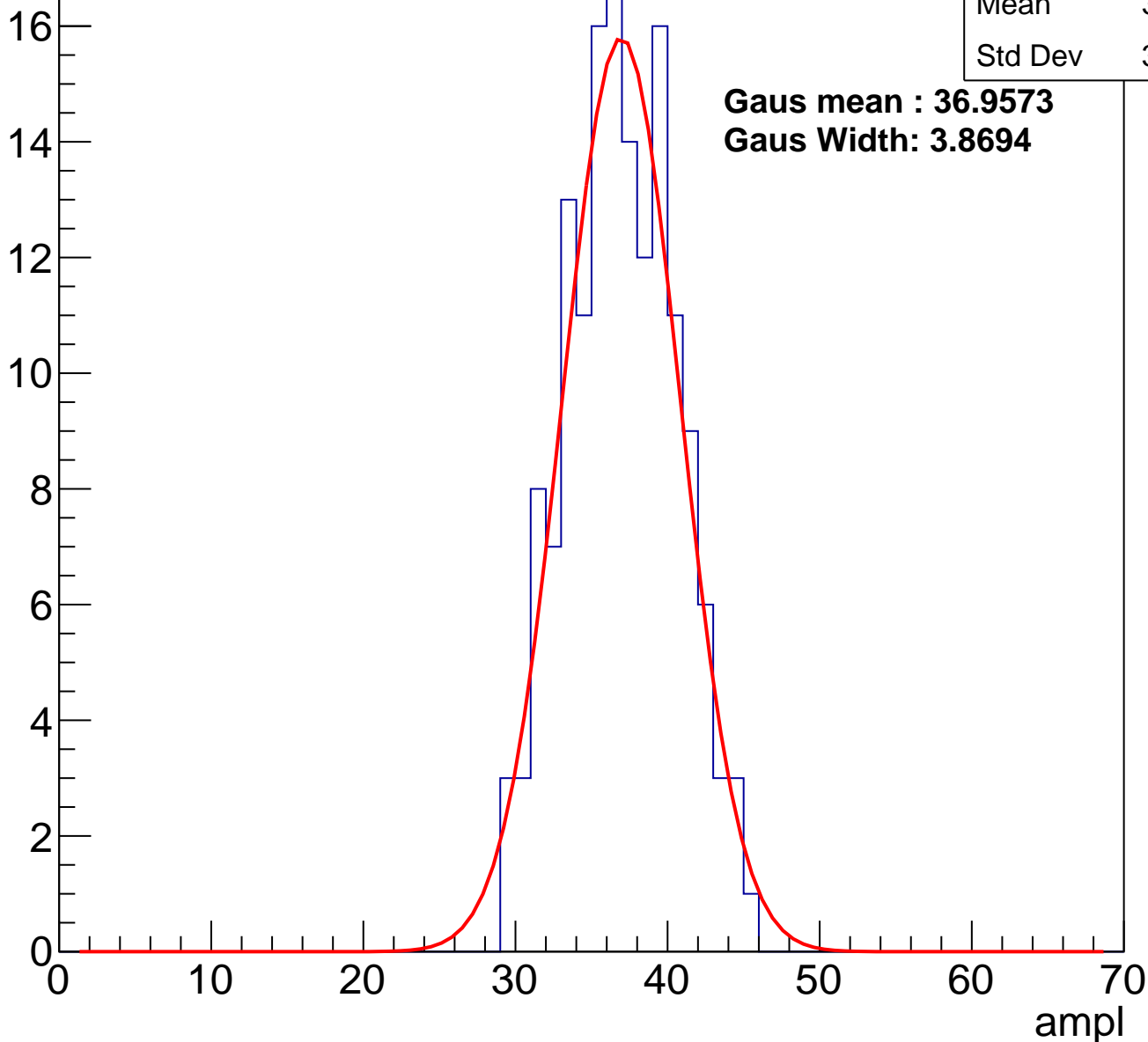
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	153
Mean	36.53
Std Dev	3.565

Gaus mean : 36.9573

Gaus Width: 3.8694



B1L001S, U20-ch123, adc2

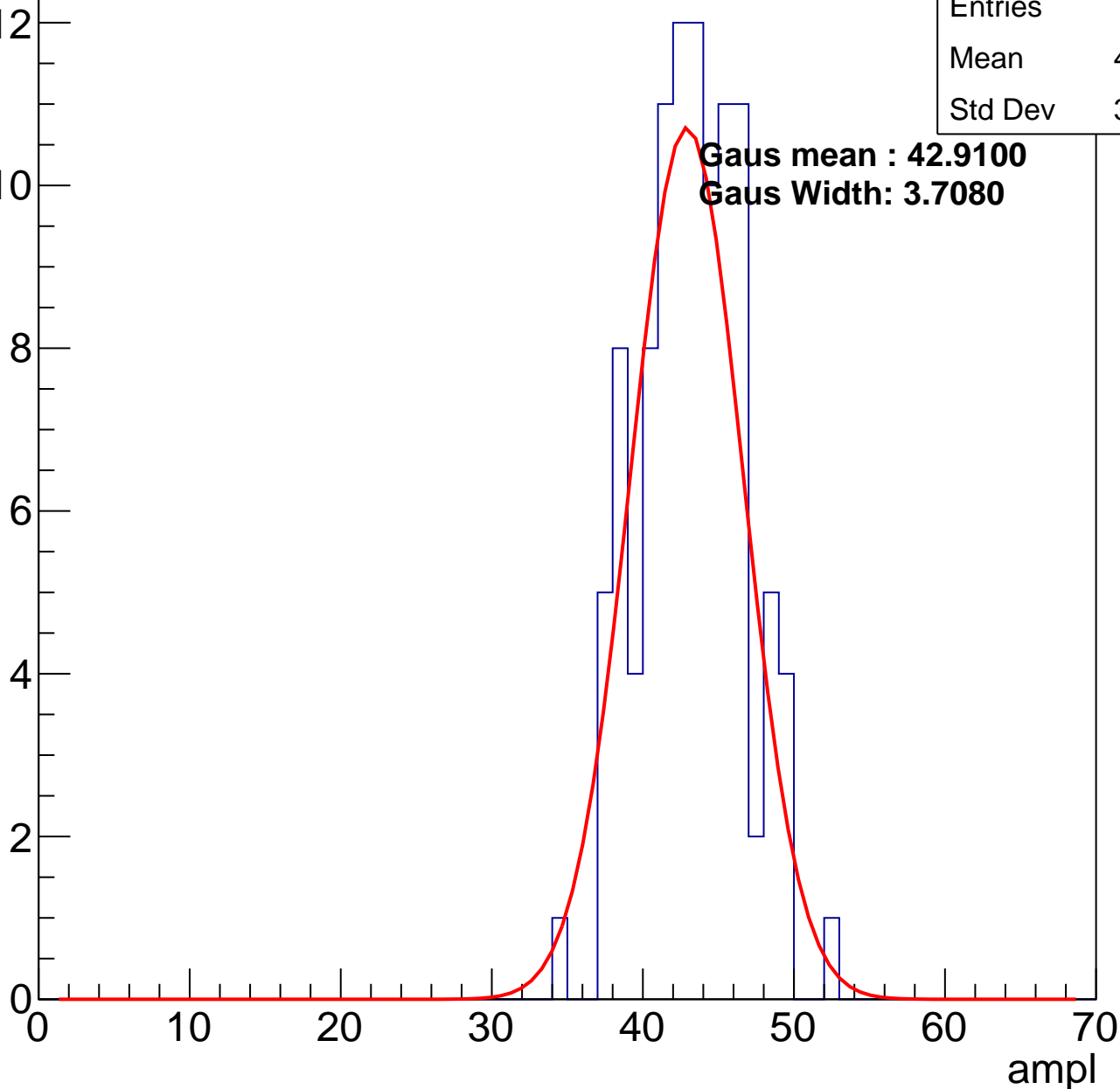
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	105
Mean	42.79
Std Dev	3.357

Gaus mean : 42.9100

Gaus Width: 3.7080

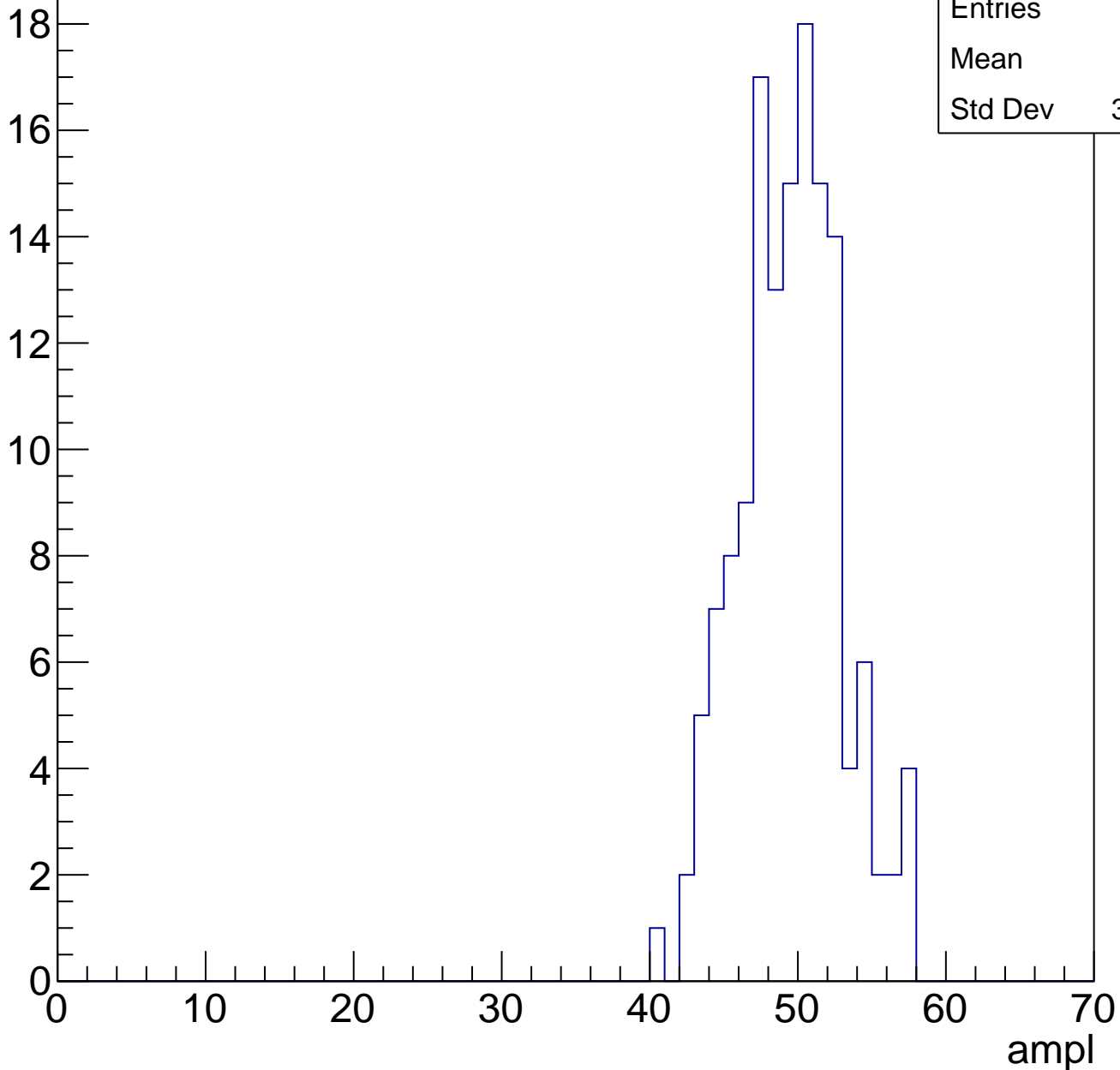


B1L001S, U20-ch123, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	49
Std Dev	3.427

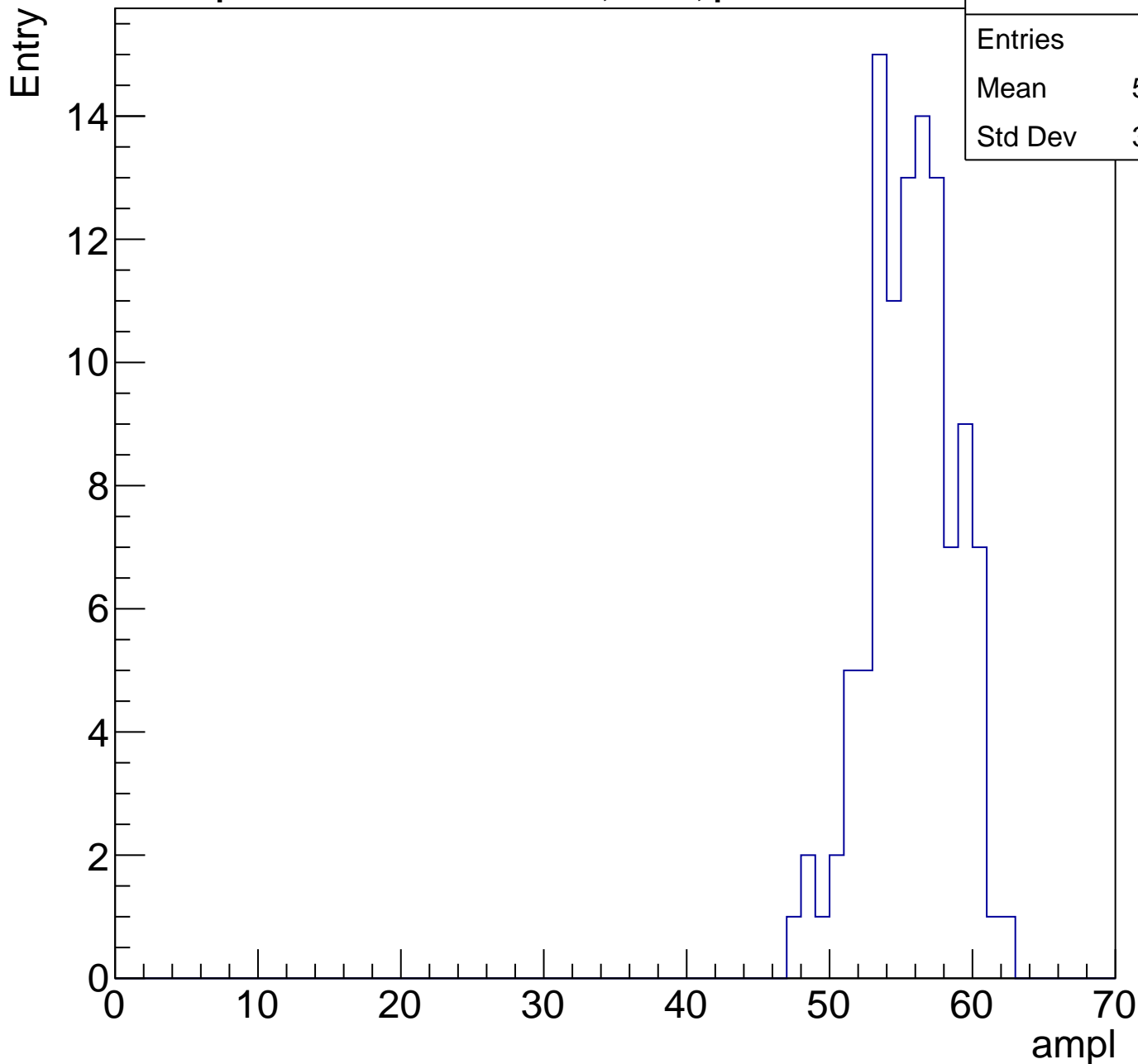
Entry



B1L001S, U20-ch123, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	55.29
Std Dev	3.008



B1L001S, U20-ch123, adc5

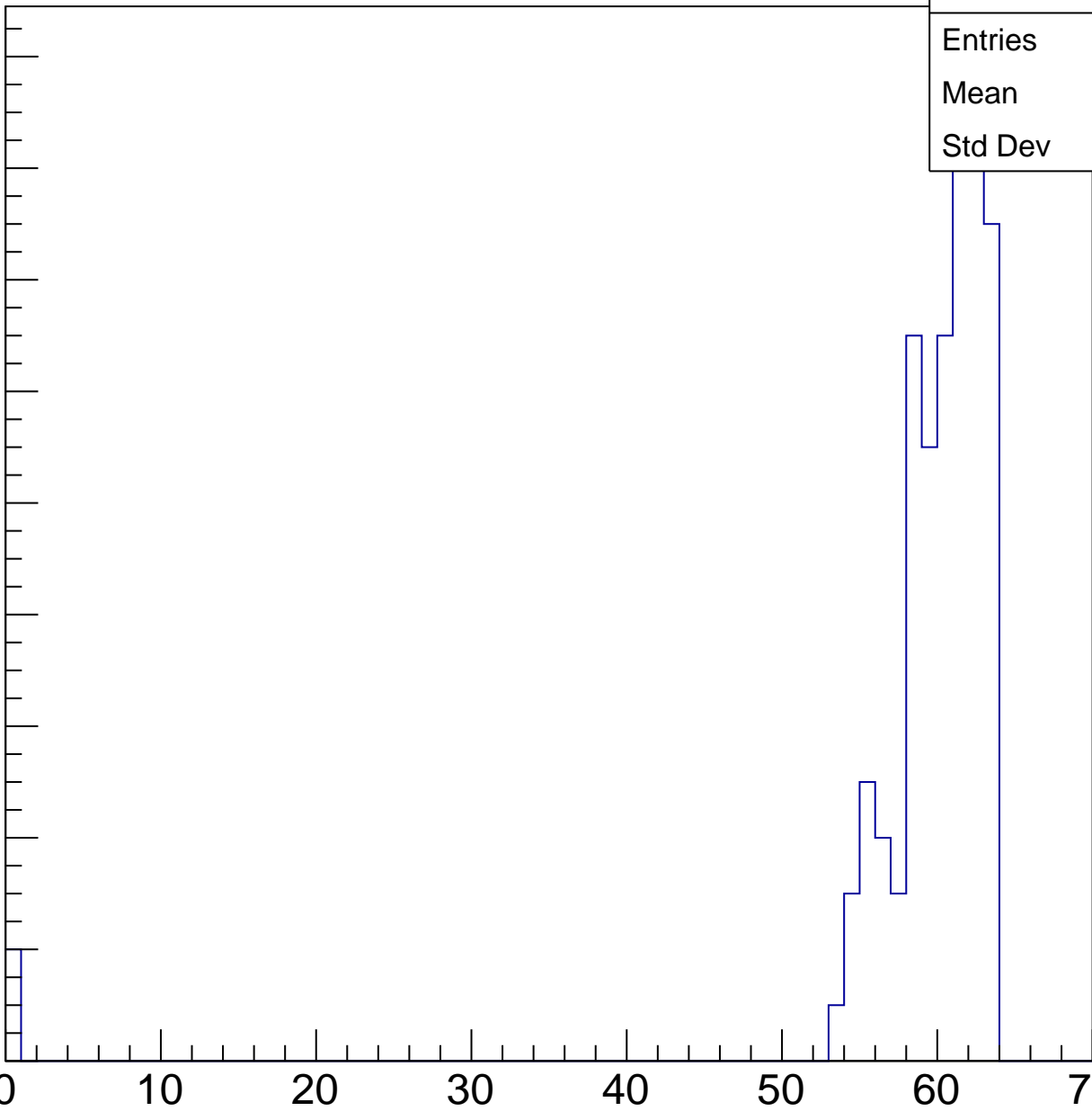
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	105
Mean	58.71
Std Dev	8.554

ampl

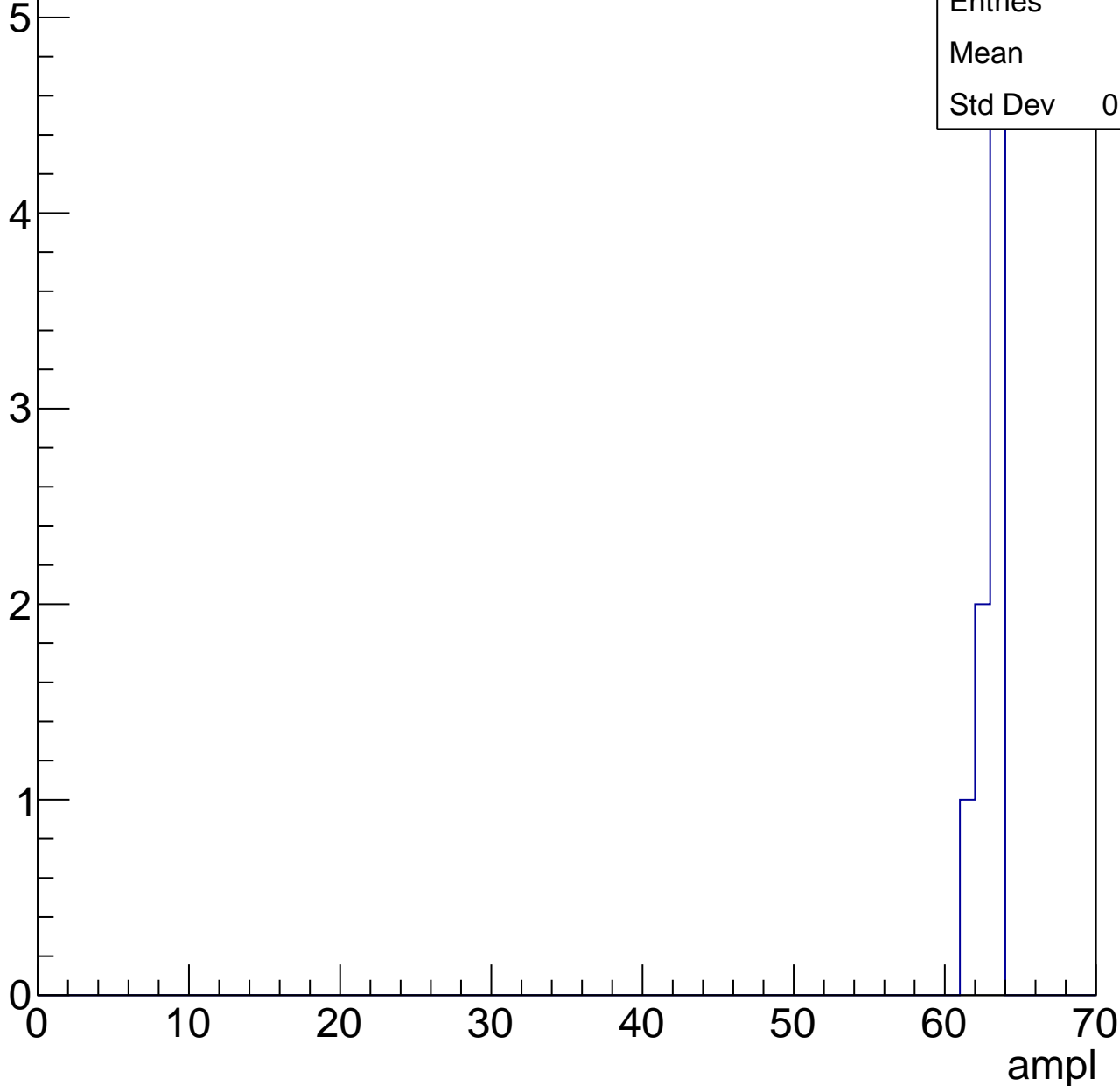


B1L001S, U20-ch123, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	62.5
Std Dev	0.7071



B1L001S, U20-ch123, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	19
Std Dev	0

ampl

B1L001S, U20-ch124, adc0

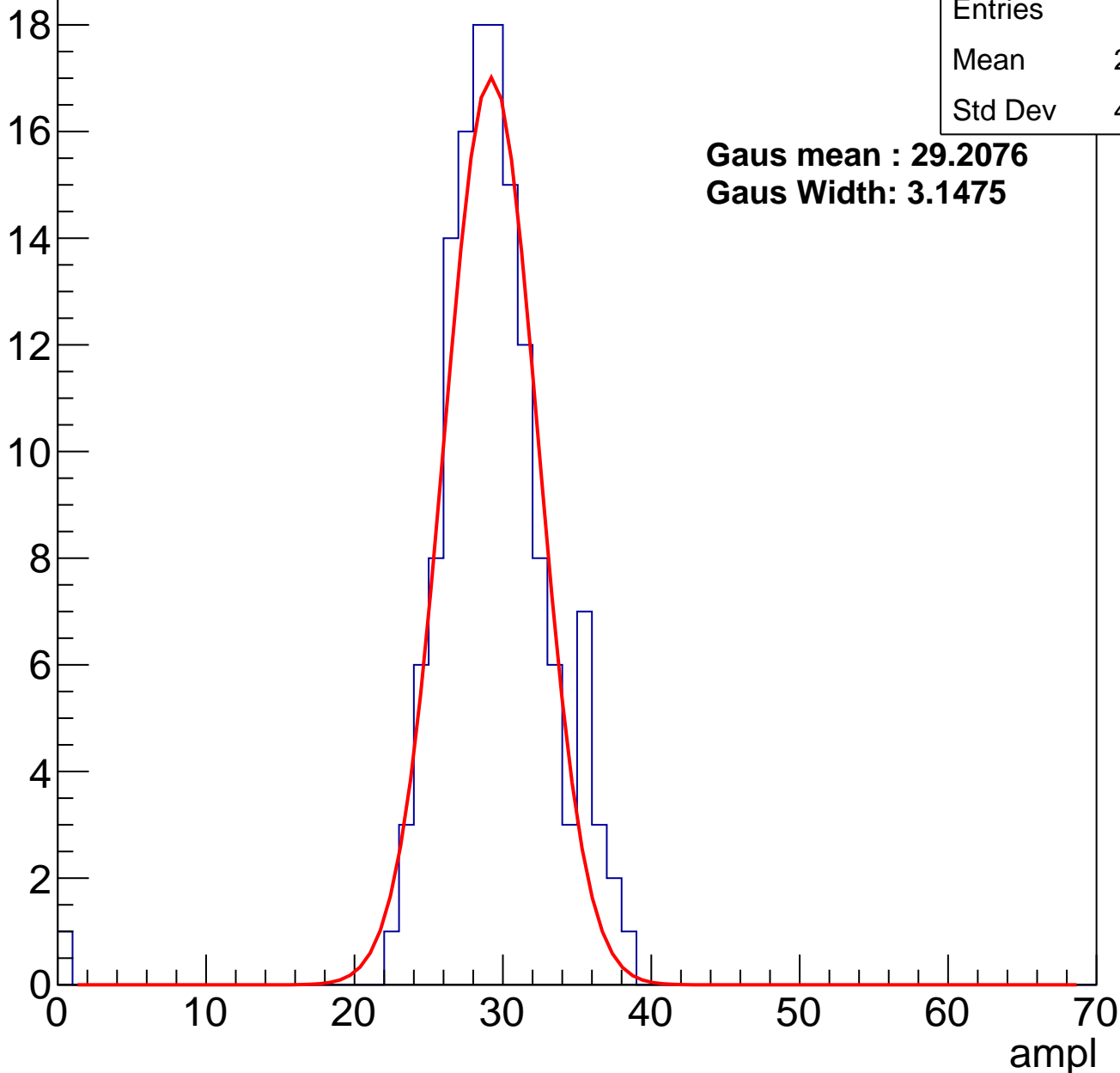
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	28.87
Std Dev	4.107

Gaus mean : 29.2076

Gaus Width: 3.1475

Entry



B1L001S, U20-ch124, adc1

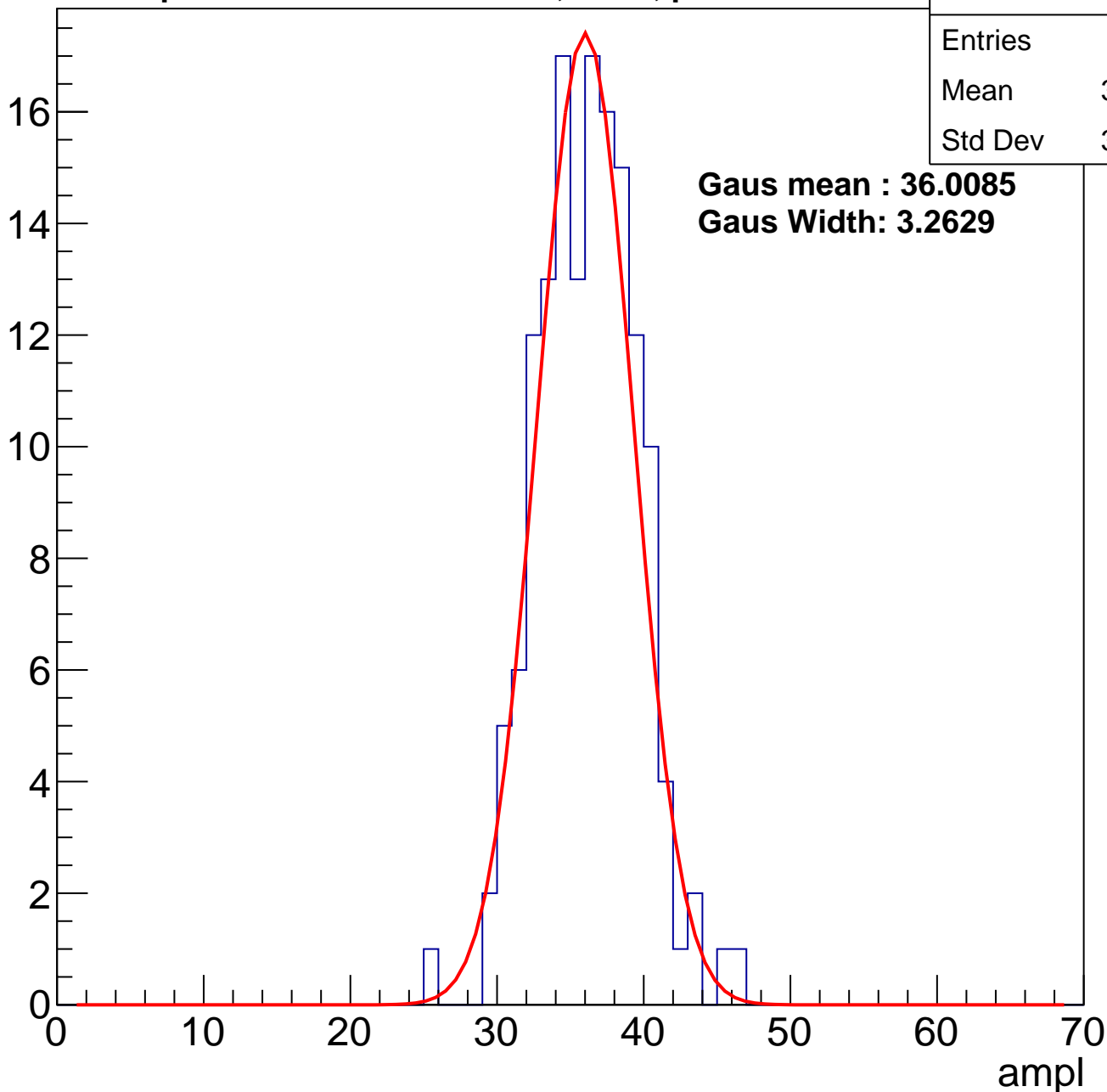
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	35.74
Std Dev	3.377

Gaus mean : 36.0085

Gaus Width: 3.2629

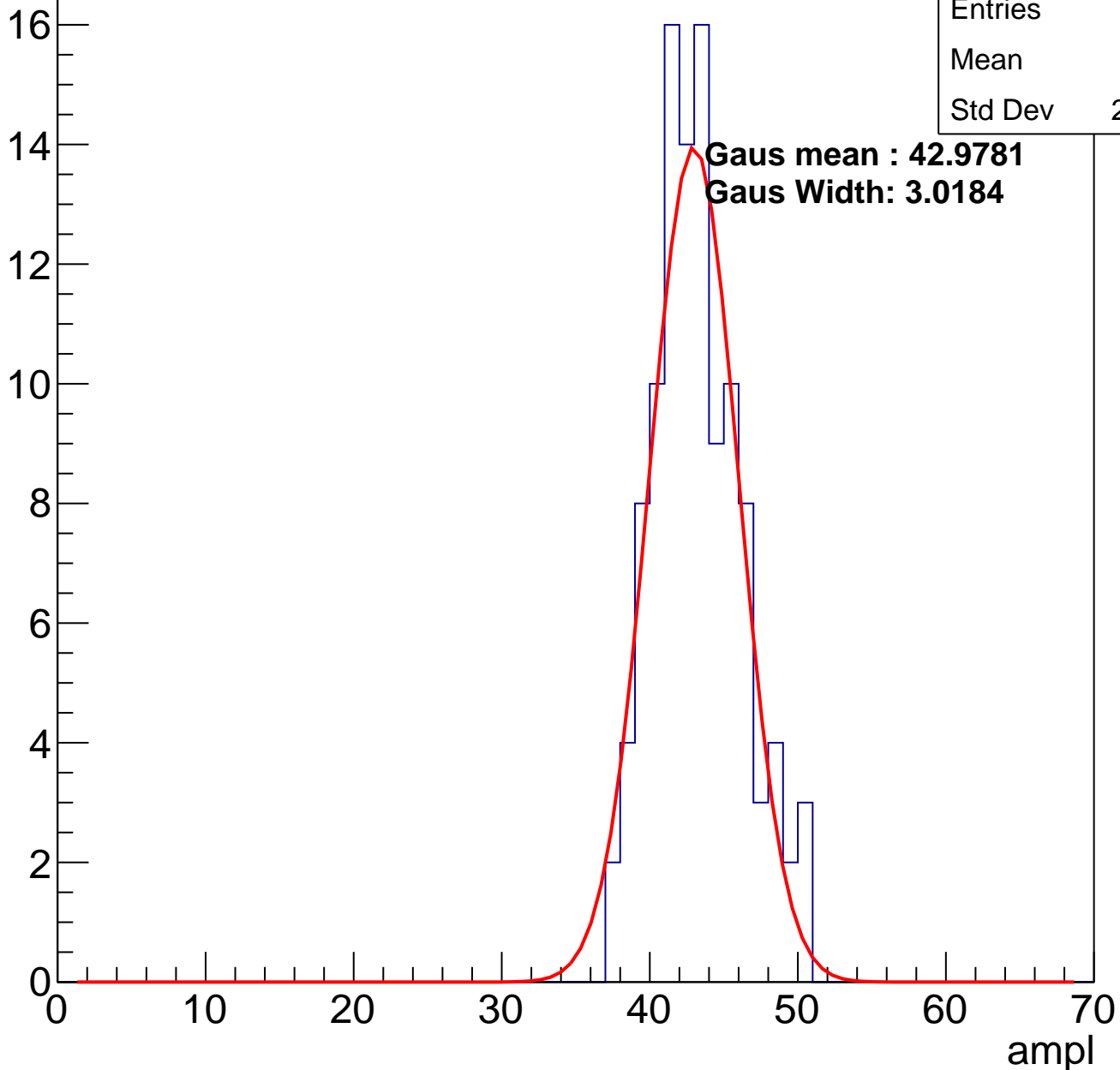
Entry



B1L001S, U20-ch124, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch124, adc3

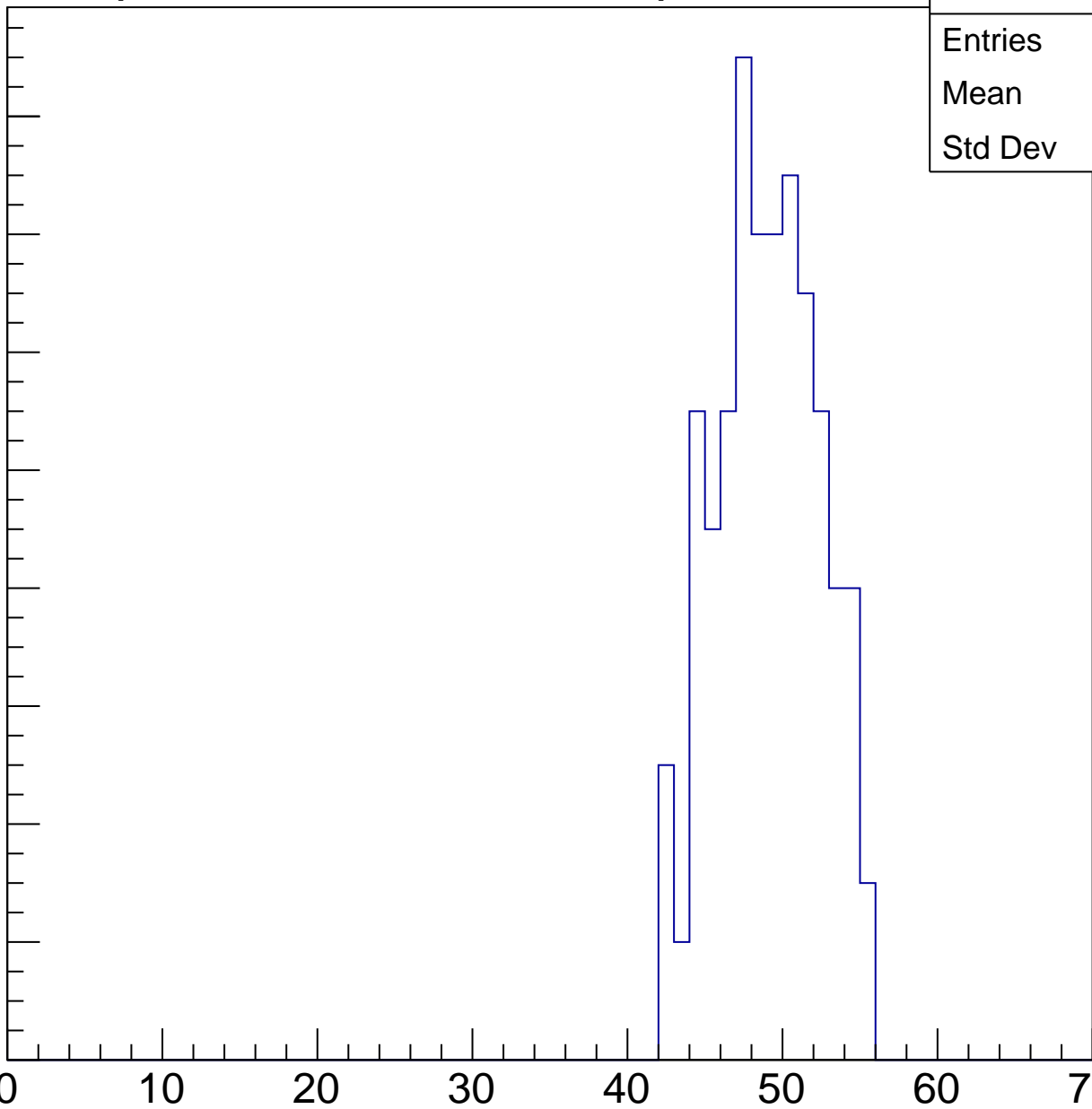
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	141
Mean	48.61
Std Dev	3.259

ampl



B1L001S, U20-ch124, adc4

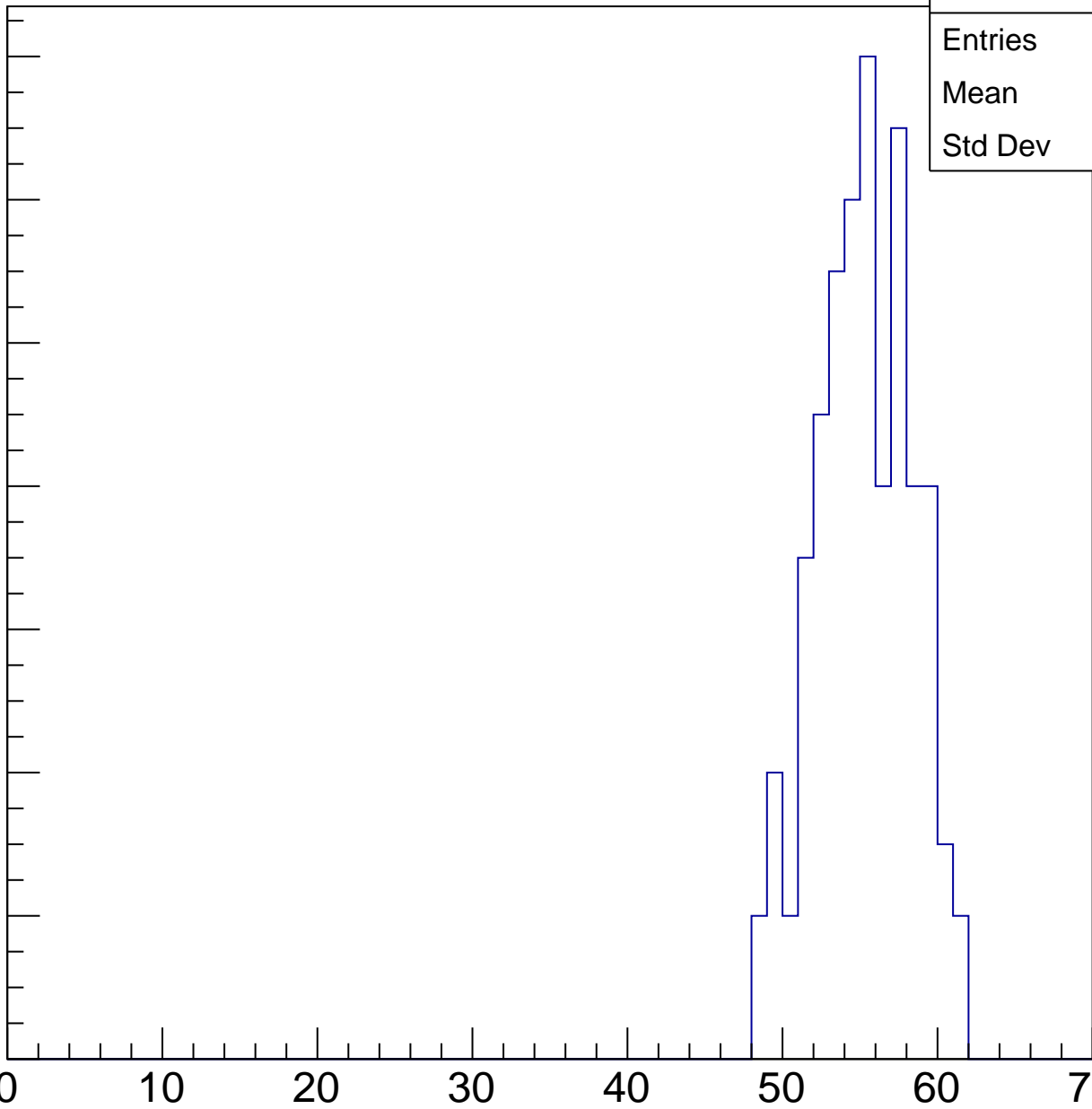
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	103
Mean	54.81
Std Dev	3.028

ampl



B1L001S, U20-ch124, adc5

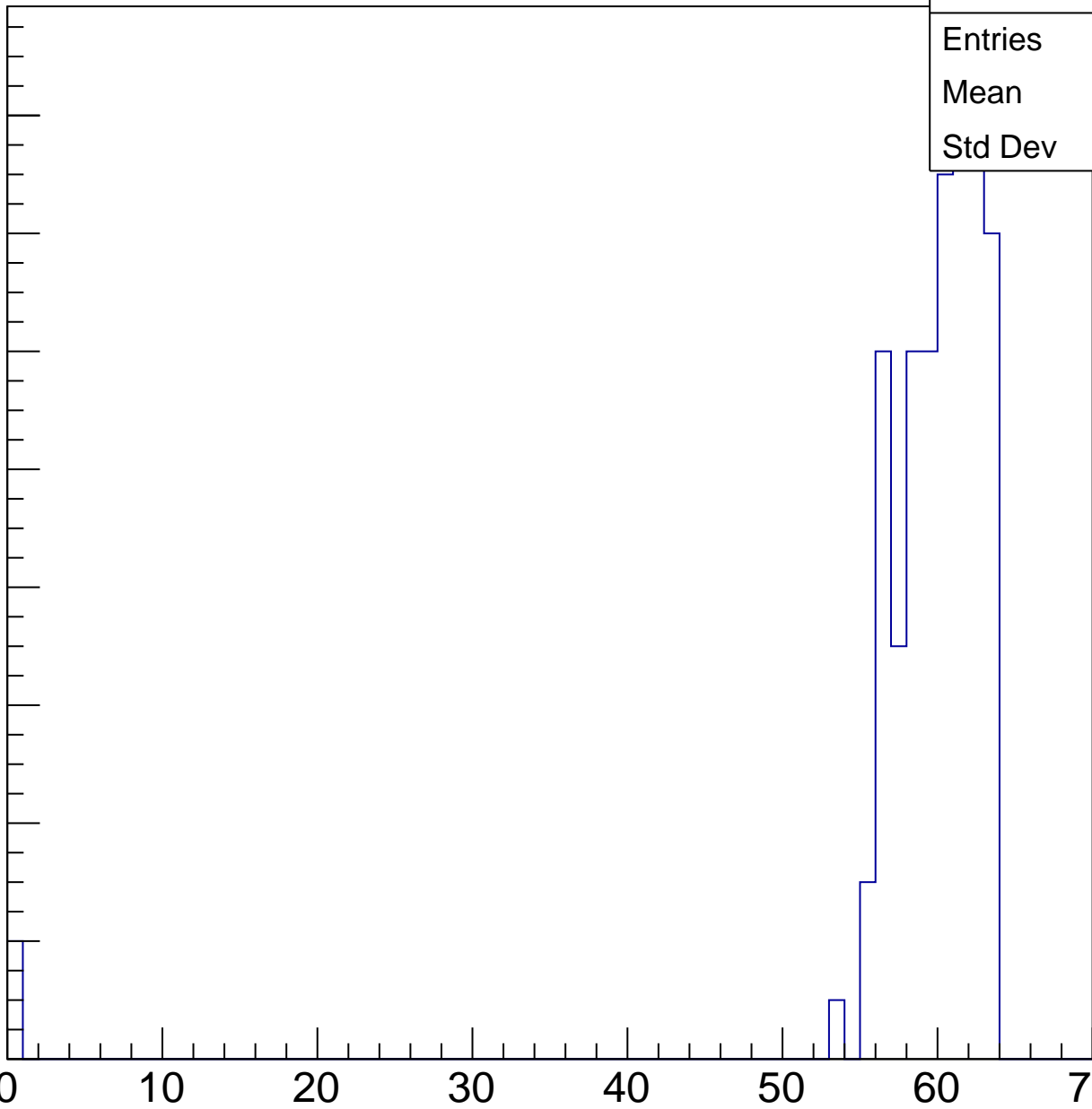
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	58.6
Std Dev	8.292

ampl

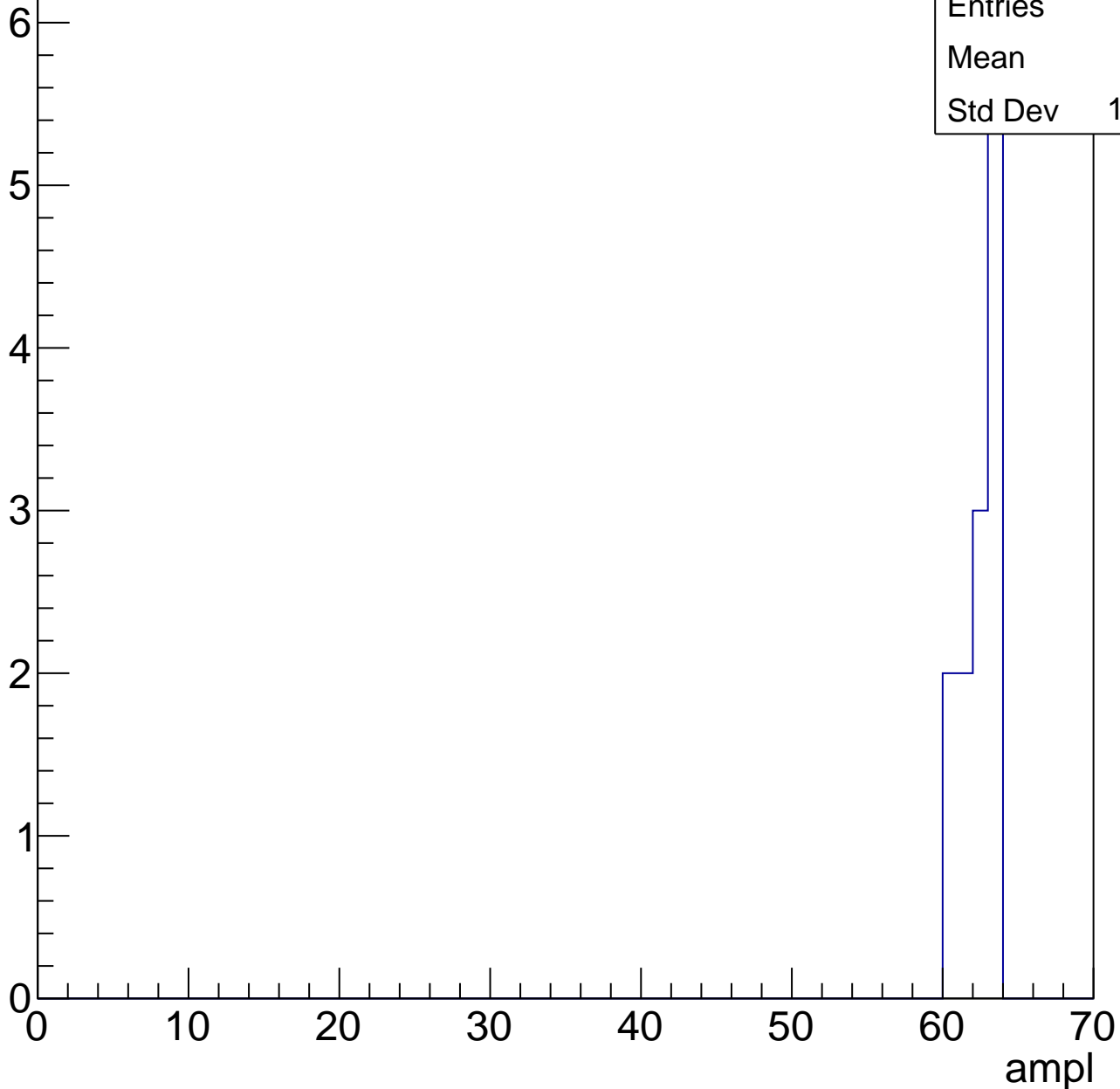


B1L001S, U20-ch124, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	62
Std Dev	1.109



B1L001S, U20-ch124, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch125, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

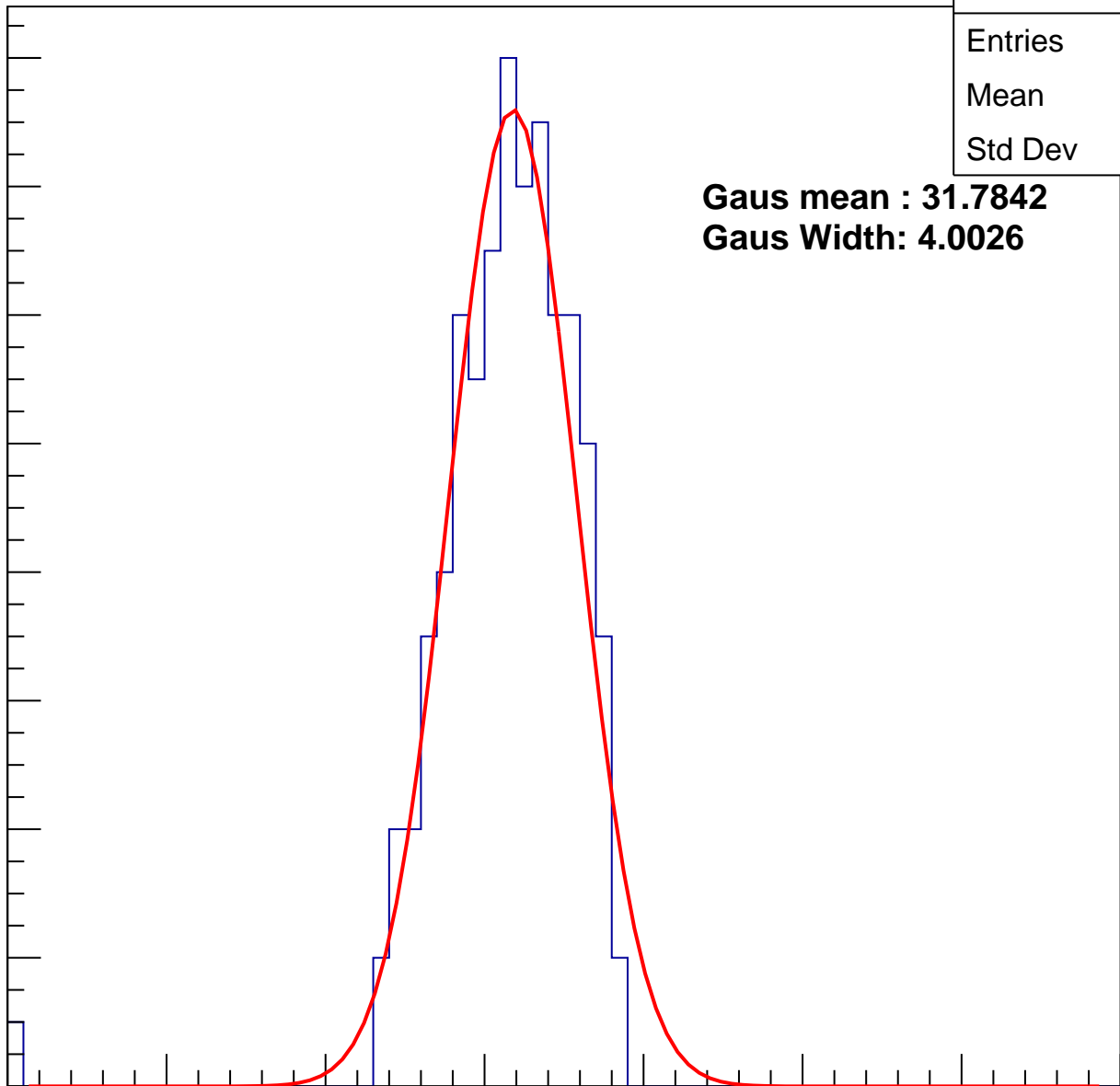
16
14
12
10
8
6
4
2
0

Entries	150
Mean	30.98
Std Dev	4.375

Gaus mean : 31.7842
Gaus Width: 4.0026

ampl

0 10 20 30 40 50 60 70



B1L001S, U20-ch125, adc1

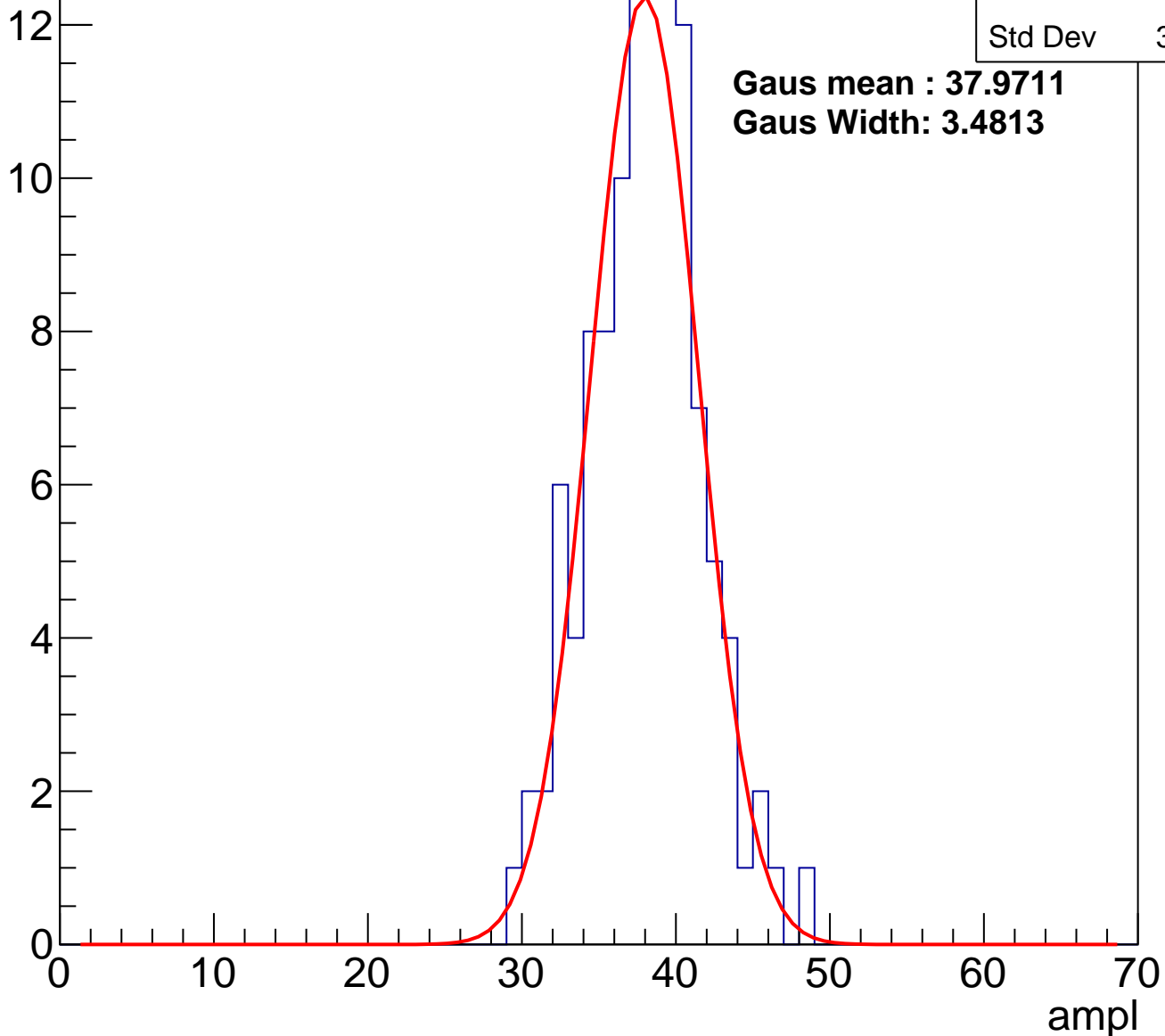
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	37.58
Std Dev	3.592

Gaus mean : 37.9711

Gaus Width: 3.4813

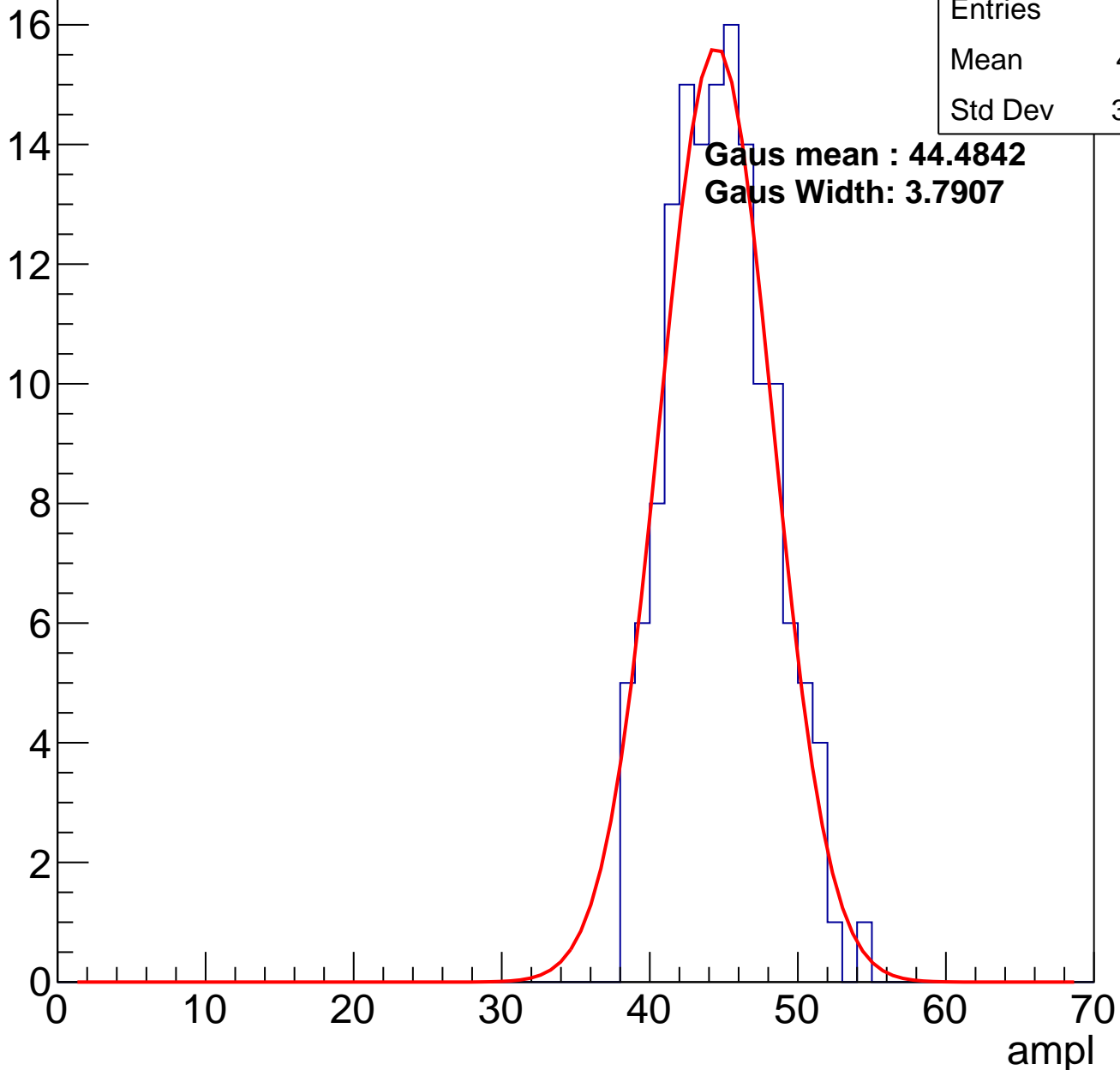
Entry



B1L001S, U20-ch125, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

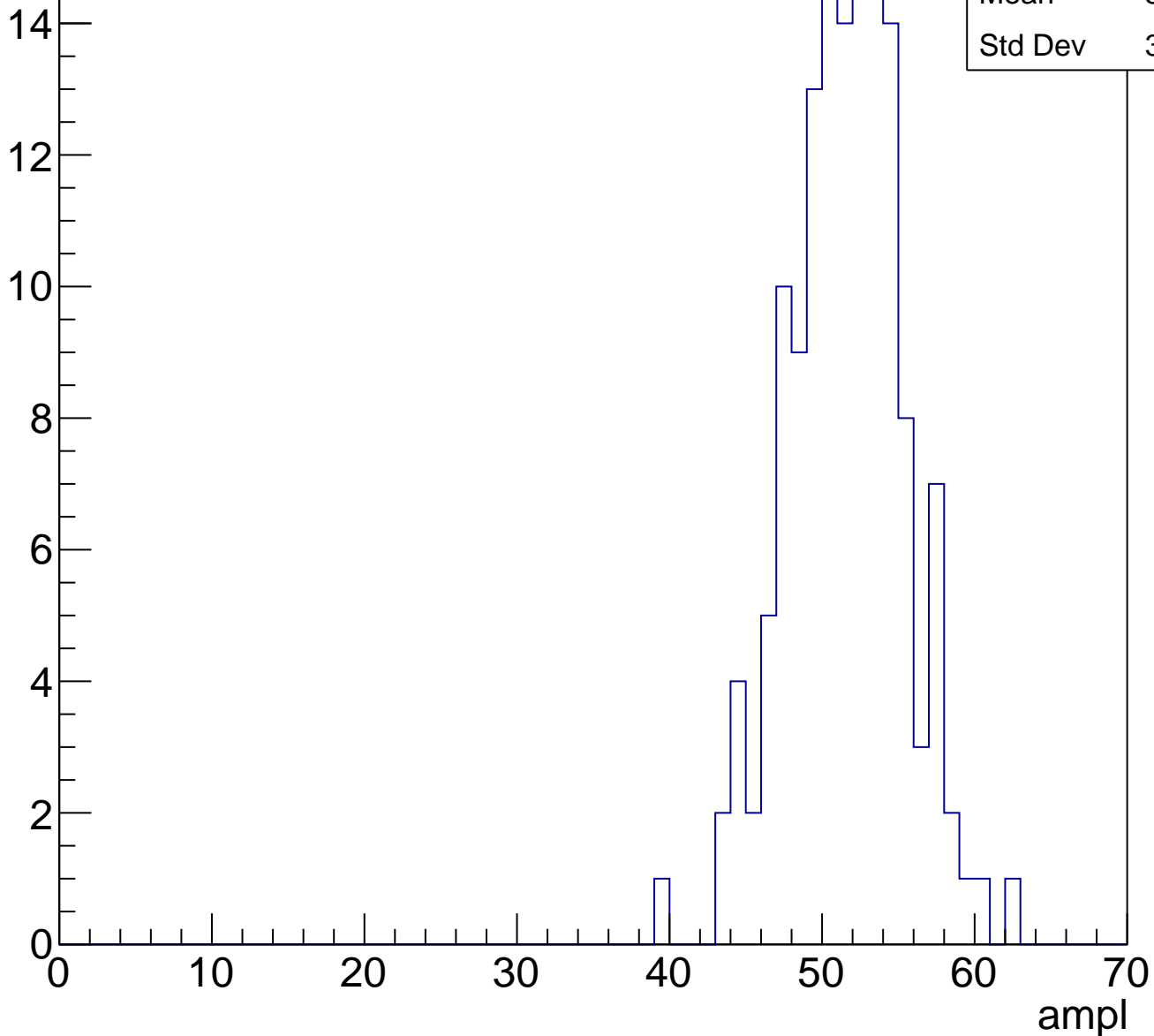


B1L001S, U20-ch125, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	51.12
Std Dev	3.744

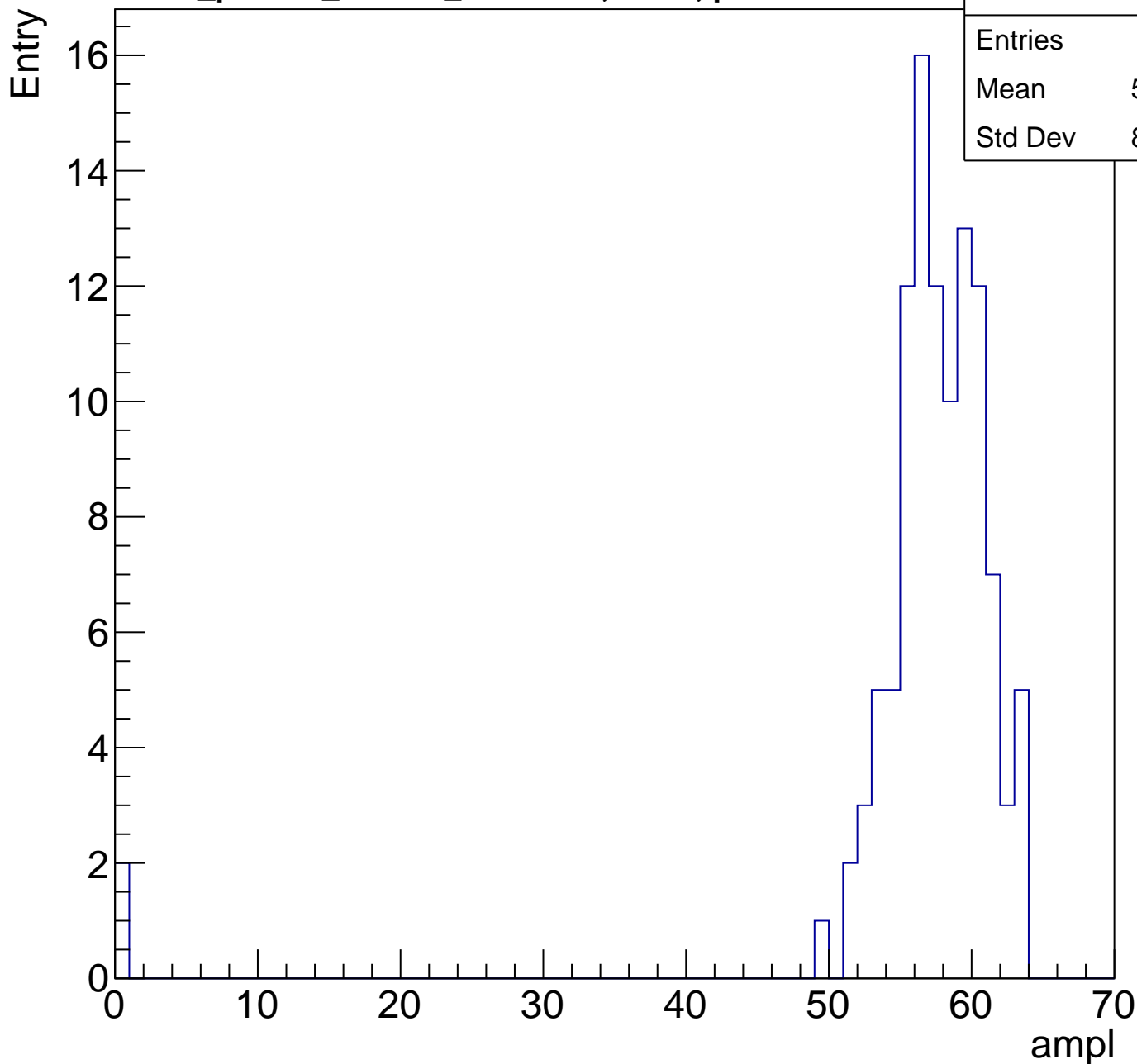
Entry



B1L001S, U20-ch125, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	56.27
Std Dev	8.265



B1L001S, U20-ch125, adc5

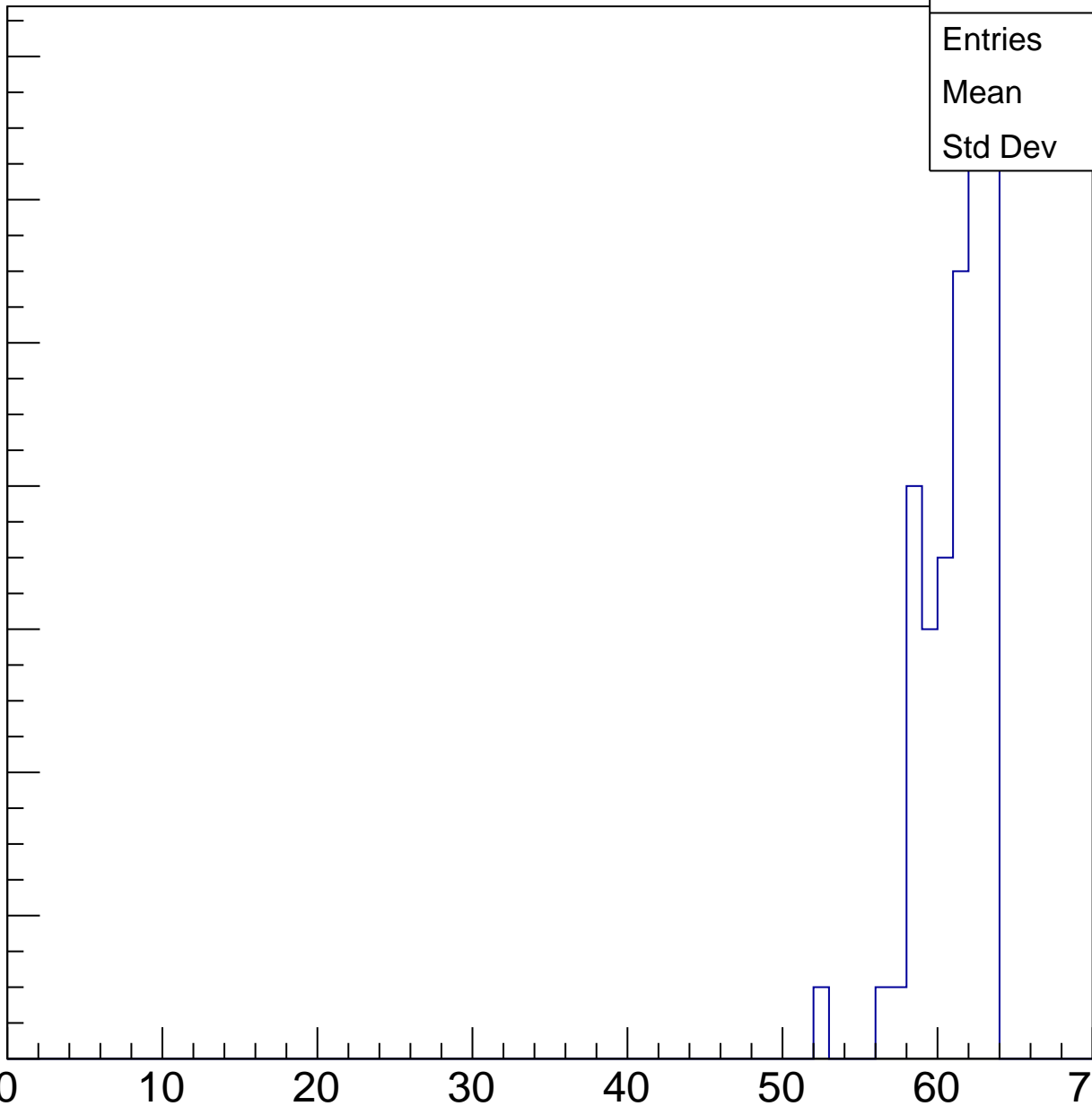
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	63
Mean	60.7
Std Dev	2.143

ampl



B1L001S, U20-ch125, adc6

calib_packv5_042523_0143.root, FC#2, port C2

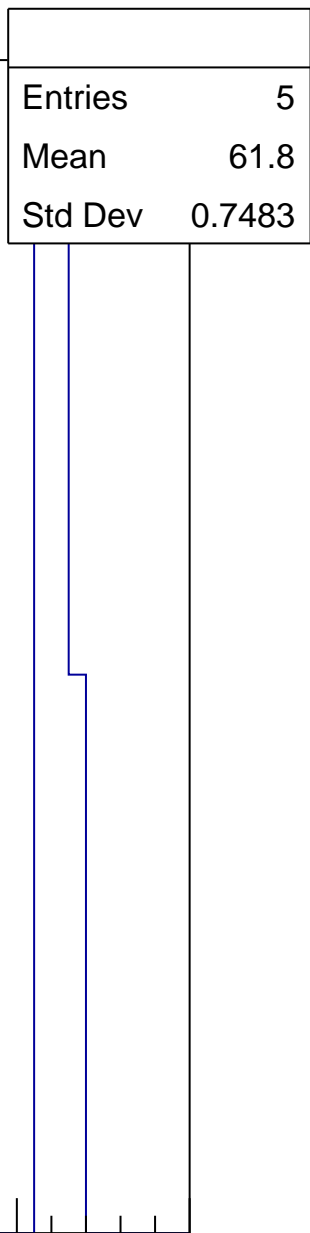
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl



B1L001S, U20-ch125, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U20-ch126, adc0

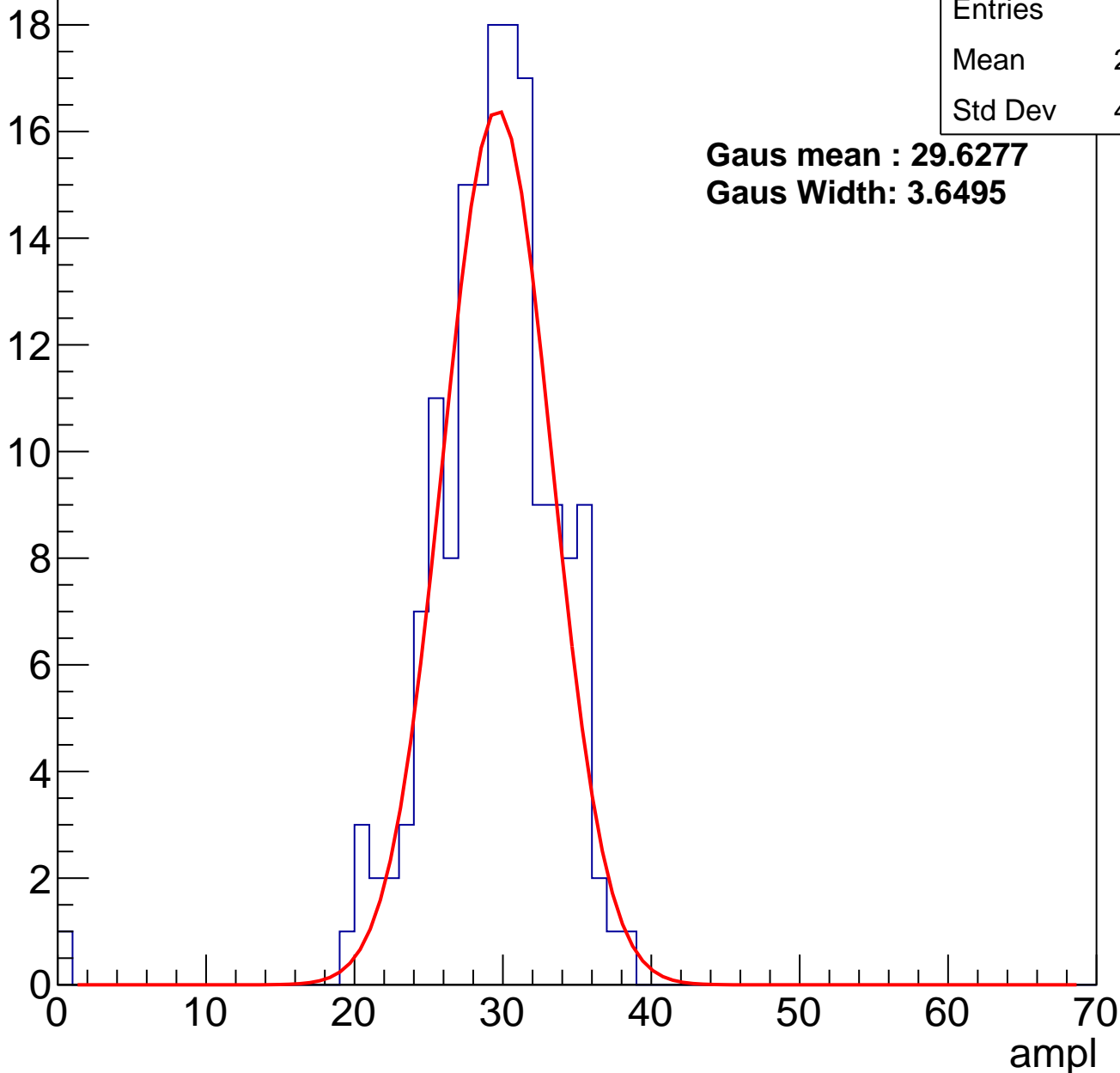
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	28.86
Std Dev	4.395

Gaus mean : 29.6277

Gaus Width: 3.6495

Entry



B1L001S, U20-ch126, adc1

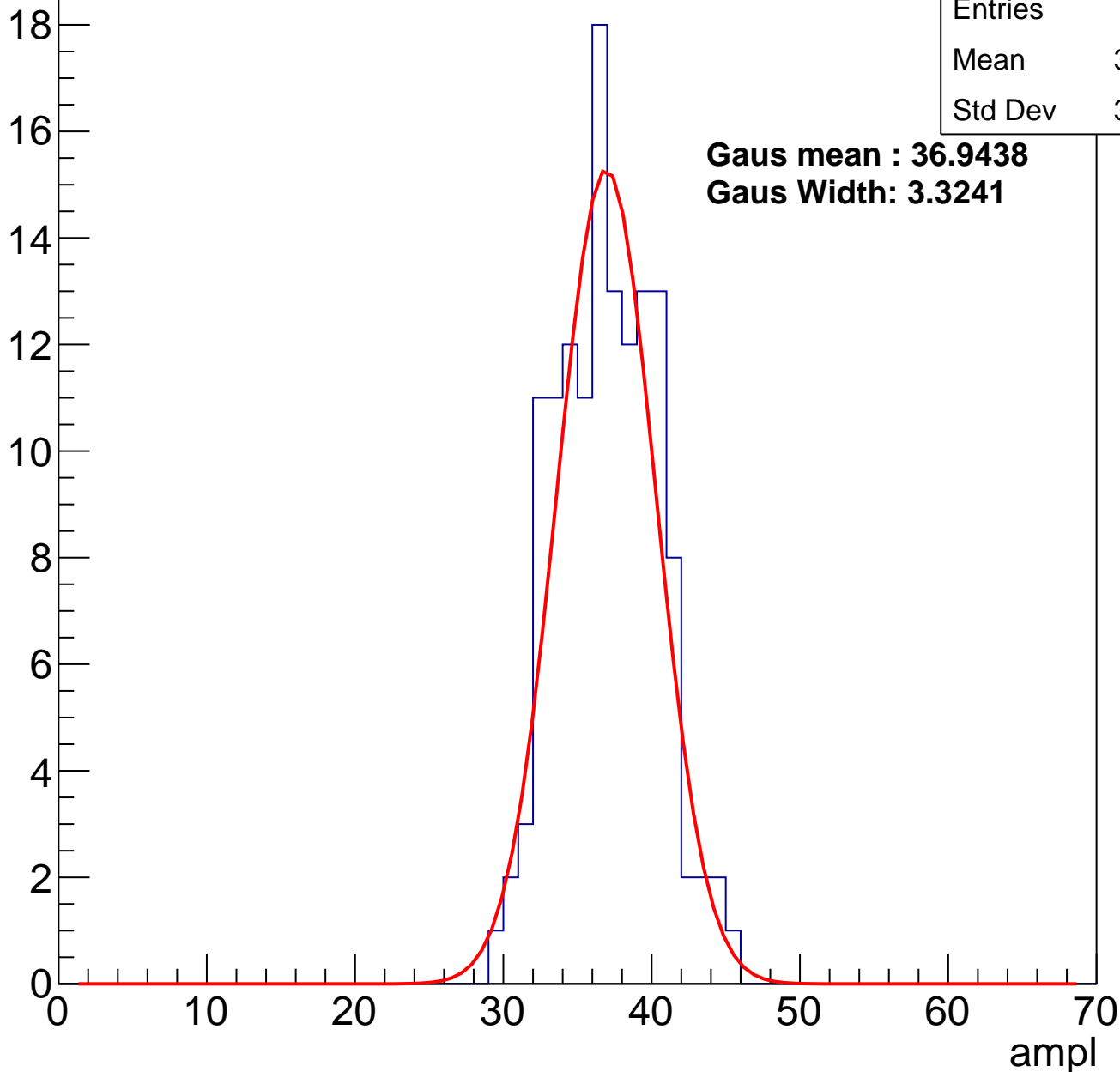
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	36.54
Std Dev	3.284

Gaus mean : 36.9438

Gaus Width: 3.3241

Entry



B1L001S, U20-ch126, adc2

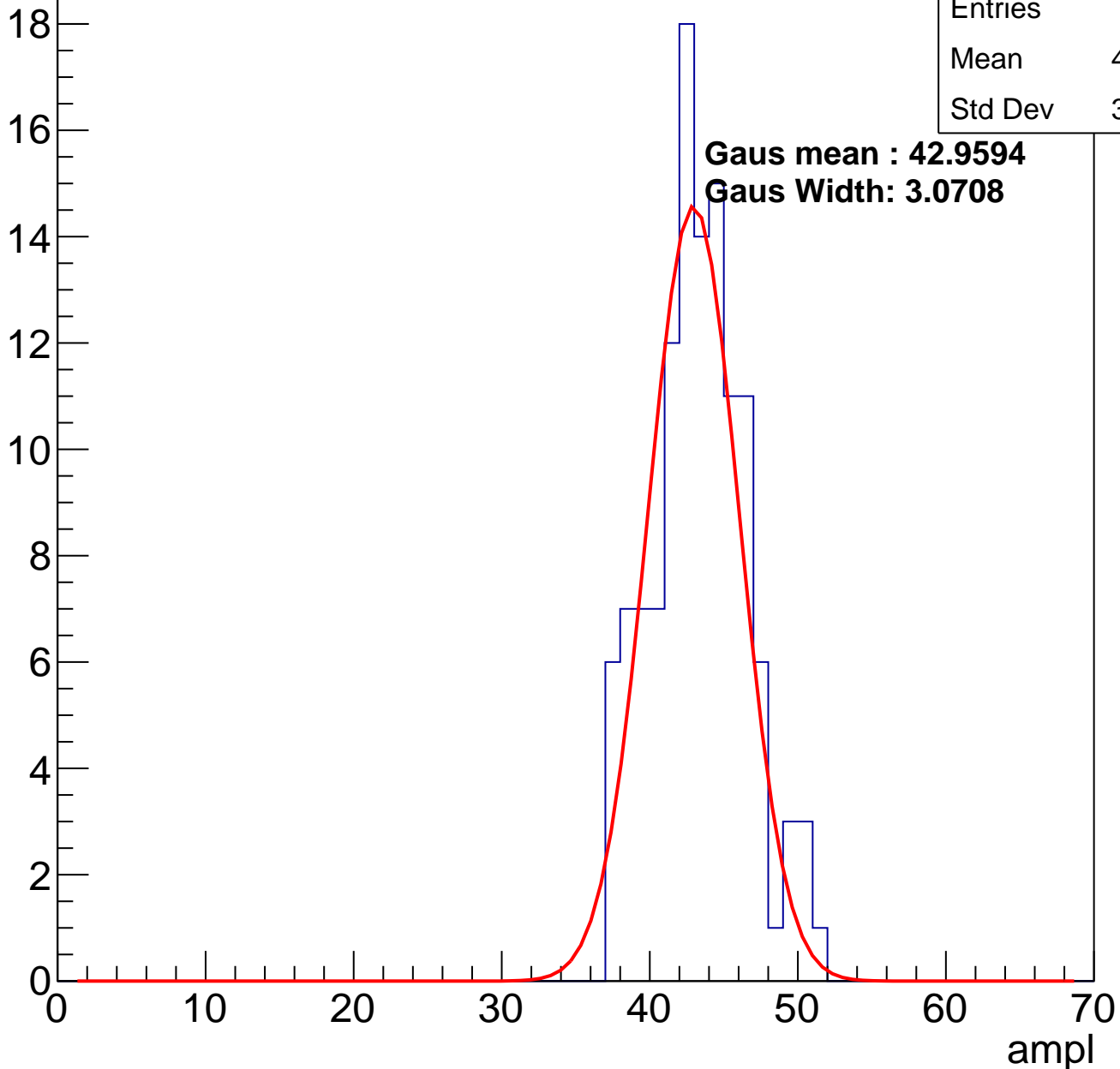
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	42.87
Std Dev	3.173

Gaus mean : 42.9594

Gaus Width: 3.0708

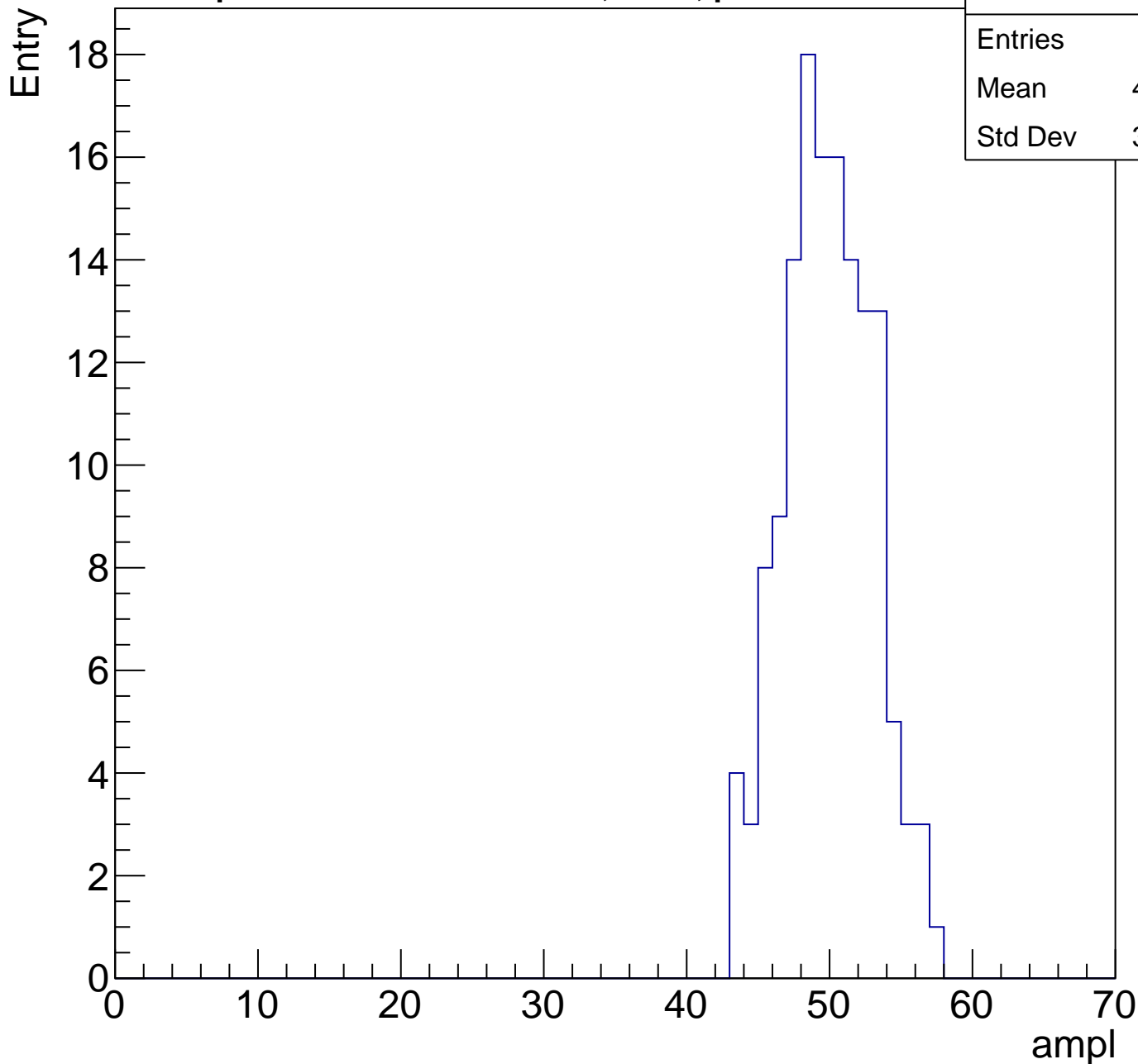
Entry



B1L001S, U20-ch126, adc3

calib_packv5_042523_0143.root, FC#2, port C2

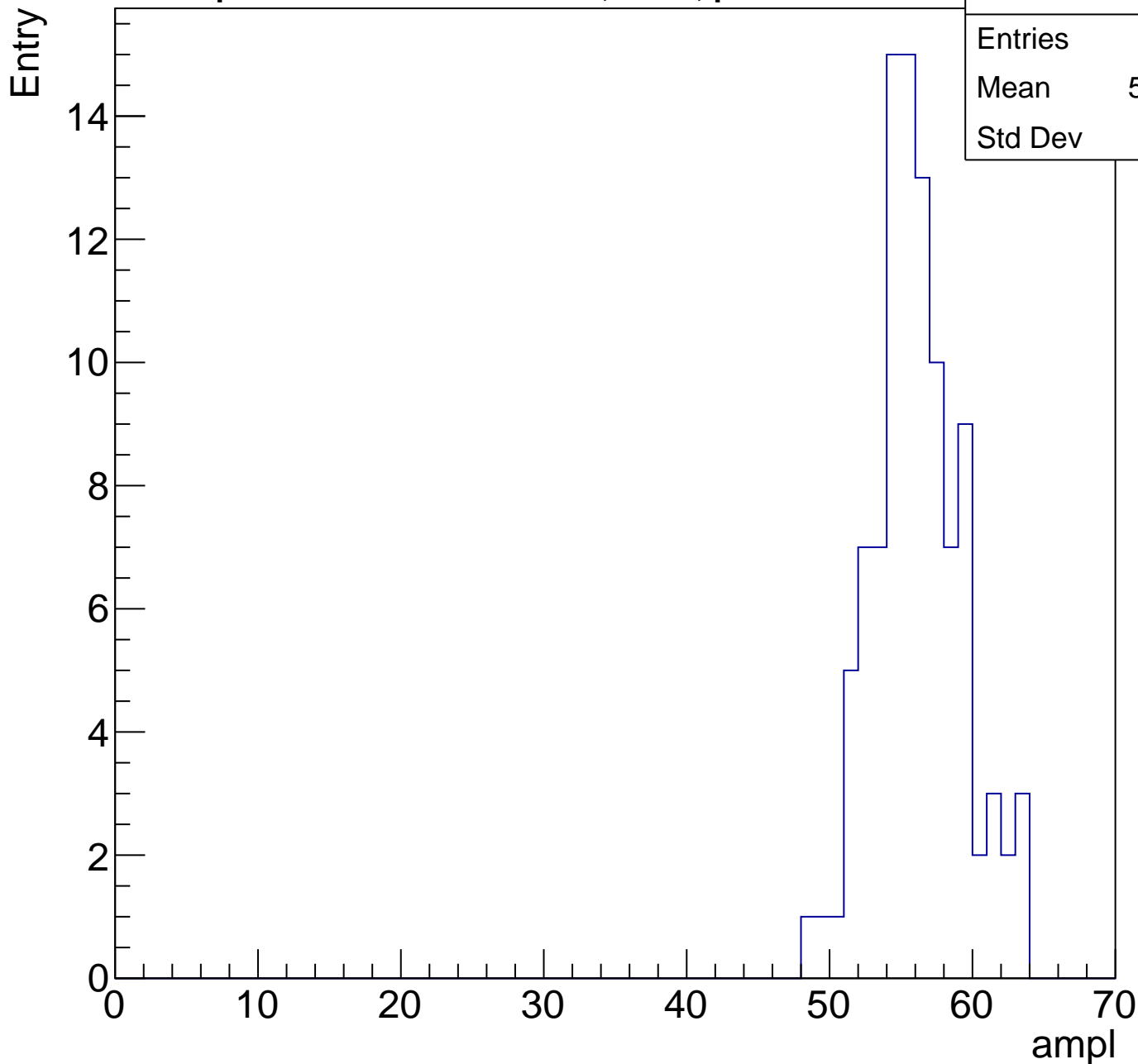
Entries	140
Mean	49.45
Std Dev	3.064



B1L001S, U20-ch126, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	55.67
Std Dev	3.08



B1L001S, U20-ch126, adc5

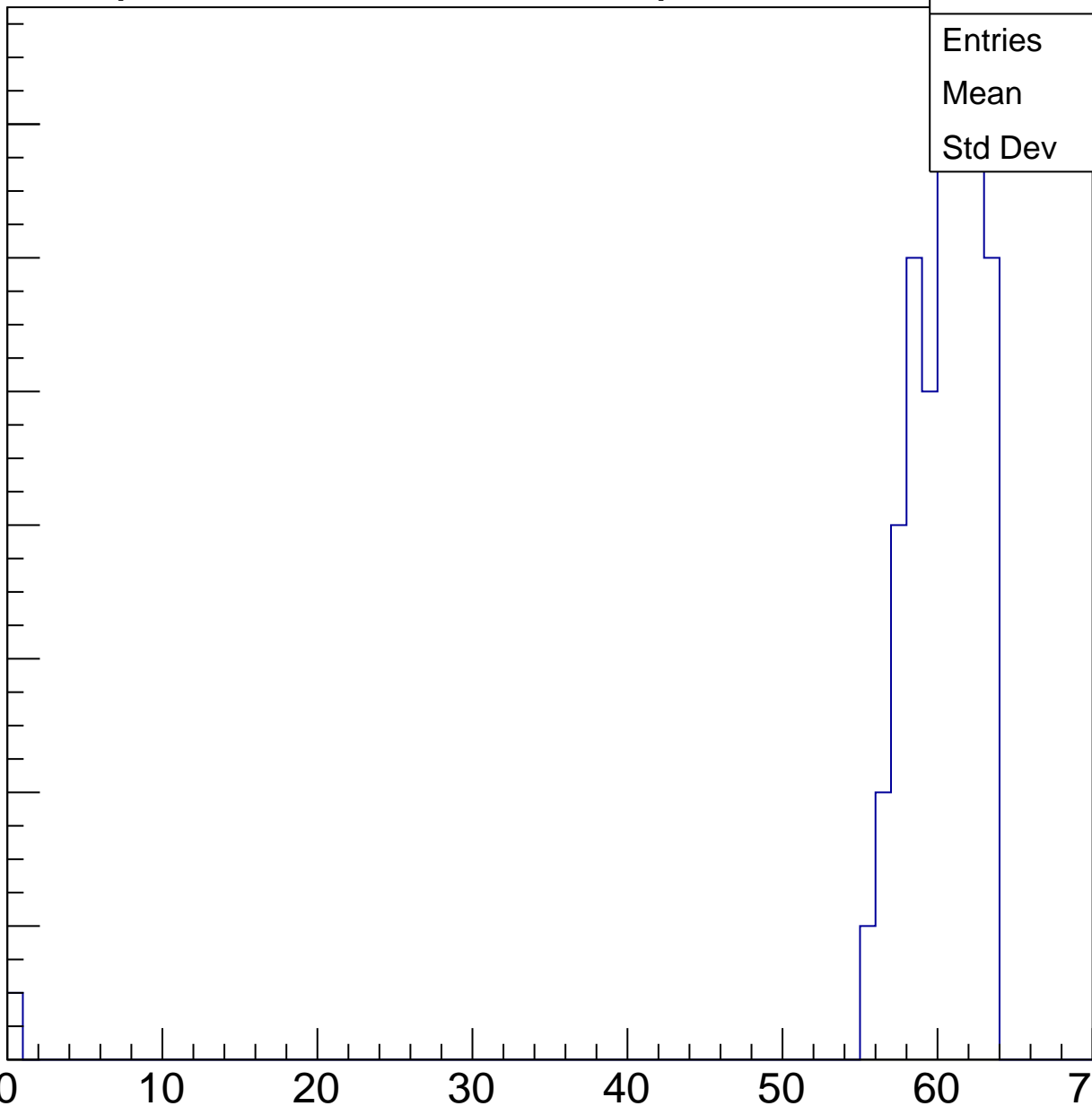
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	59.29
Std Dev	6.575

ampl

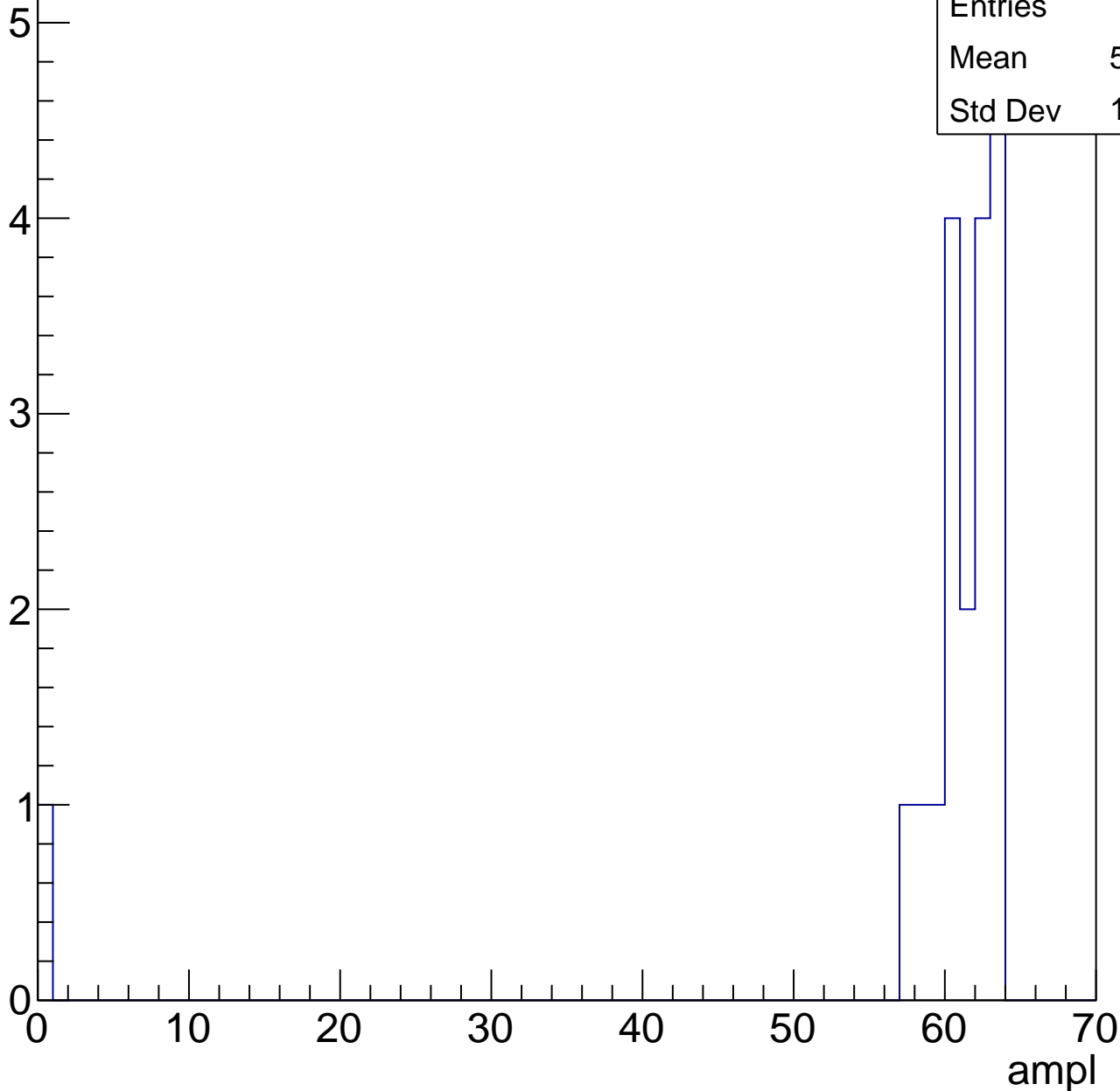


B1L001S, U20-ch126, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	57.84
Std Dev	13.74



B1L001S, U20-ch126, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U20-ch127, adc0

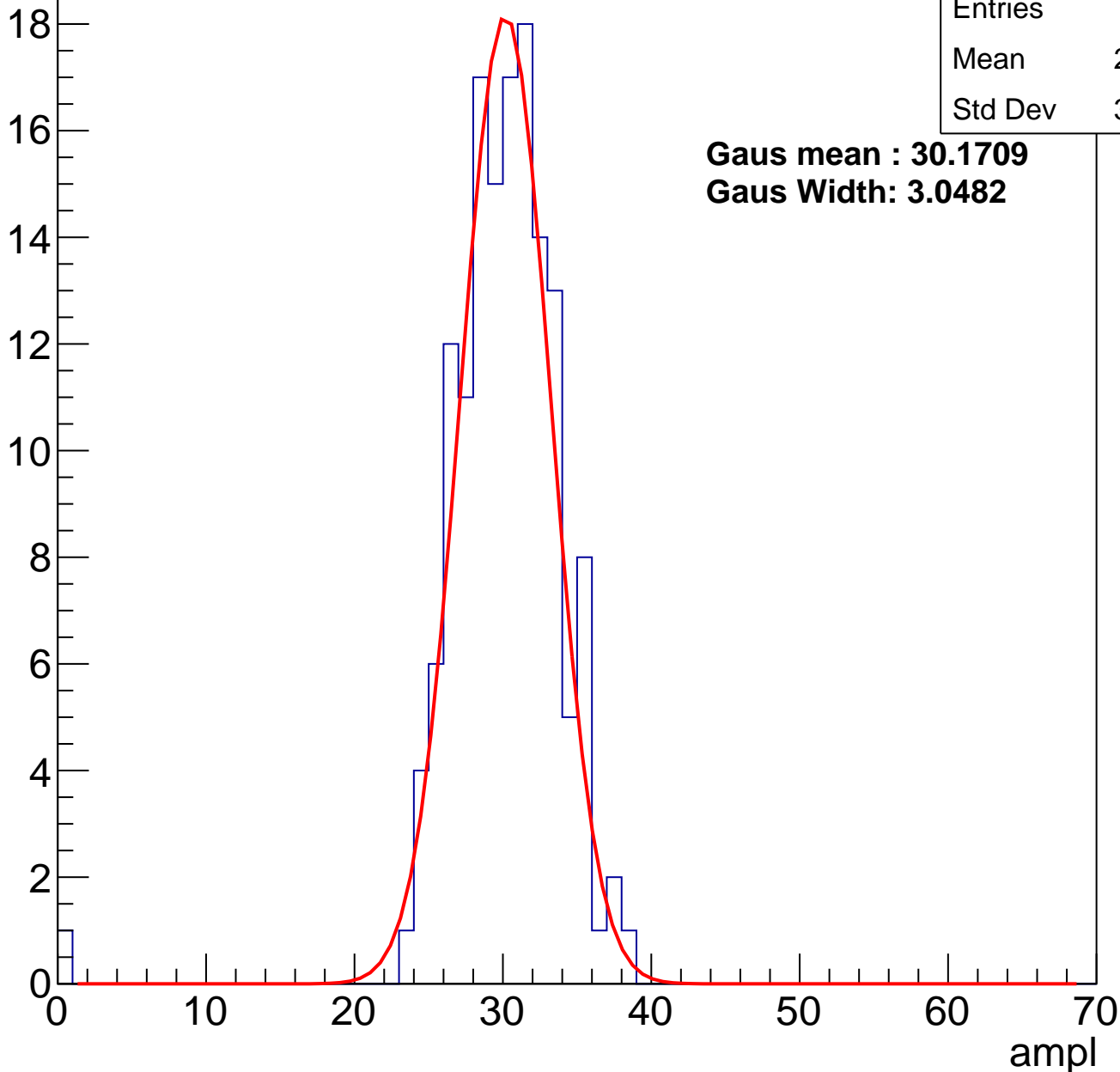
calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	29.67
Std Dev	3.943

Gaus mean : 30.1709

Gaus Width: 3.0482

Entry



B1L001S, U20-ch127, adc1

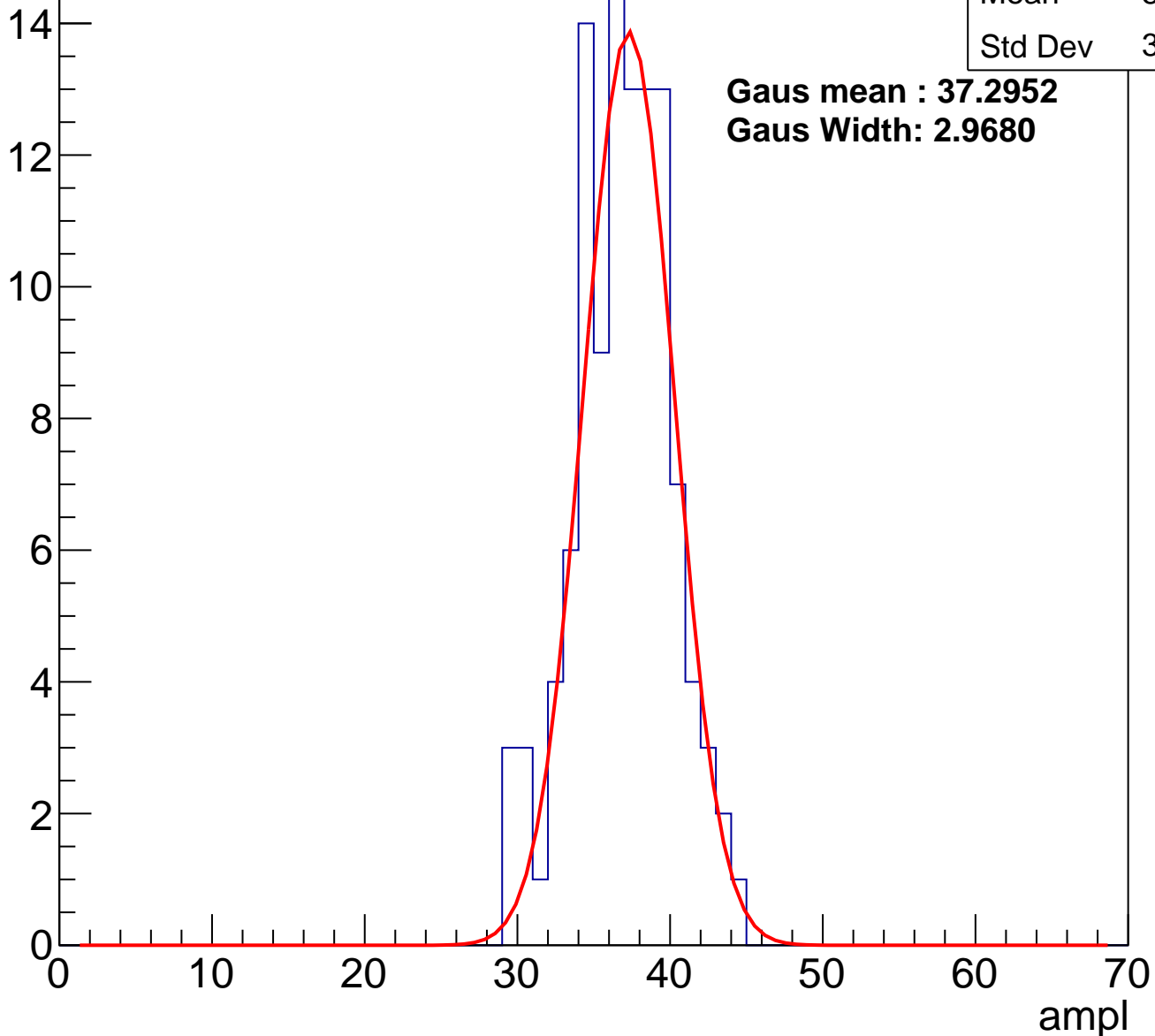
calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.46
Std Dev	3.153

Gaus mean : 37.2952

Gaus Width: 2.9680

Entry



B1L001S, U20-ch127, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

169

Mean

43.12

Std Dev

3.944

Gaus mean : 43.6377

Gaus Width: 3.9749

0

10

20

30

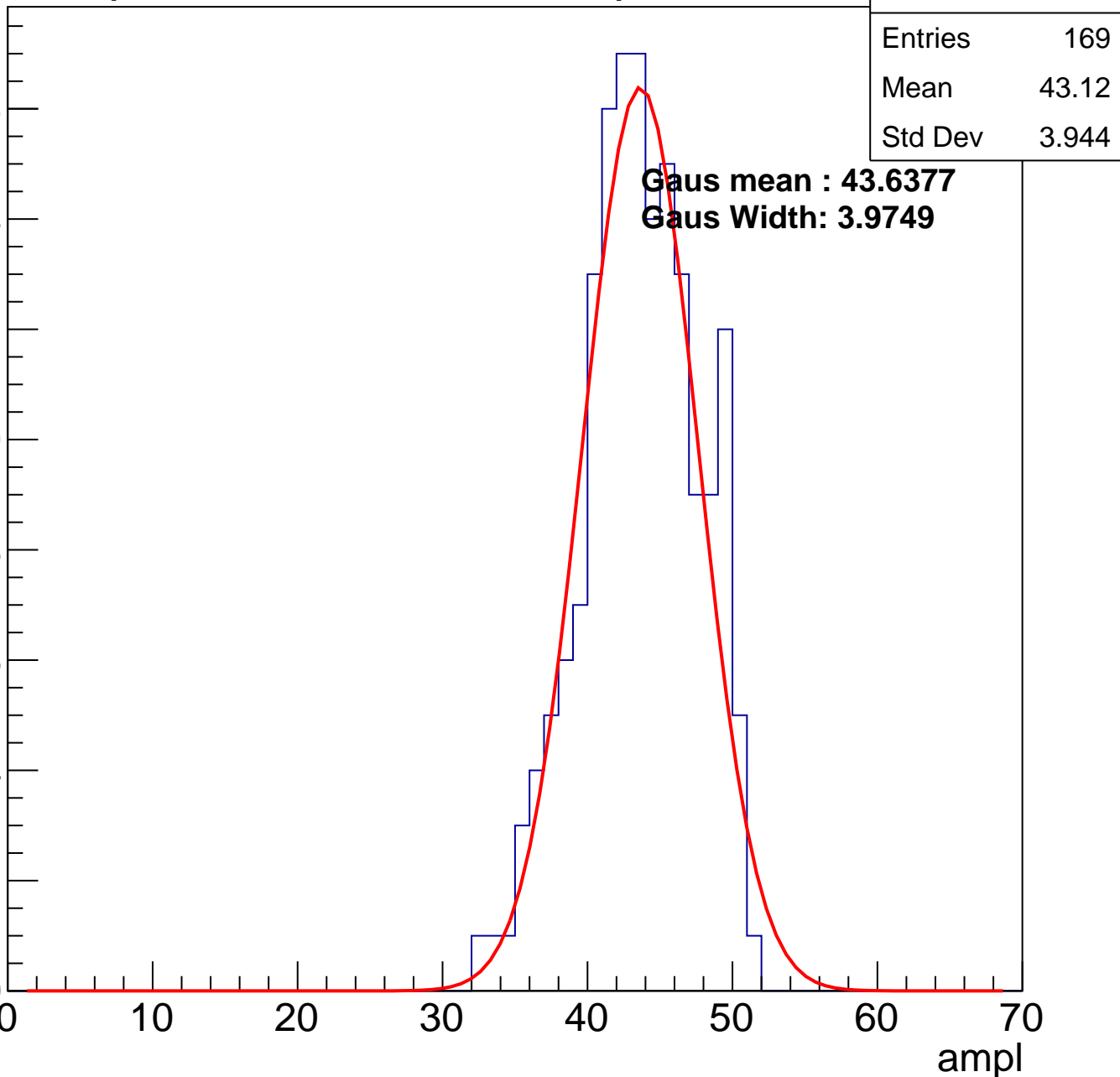
40

50

60

70

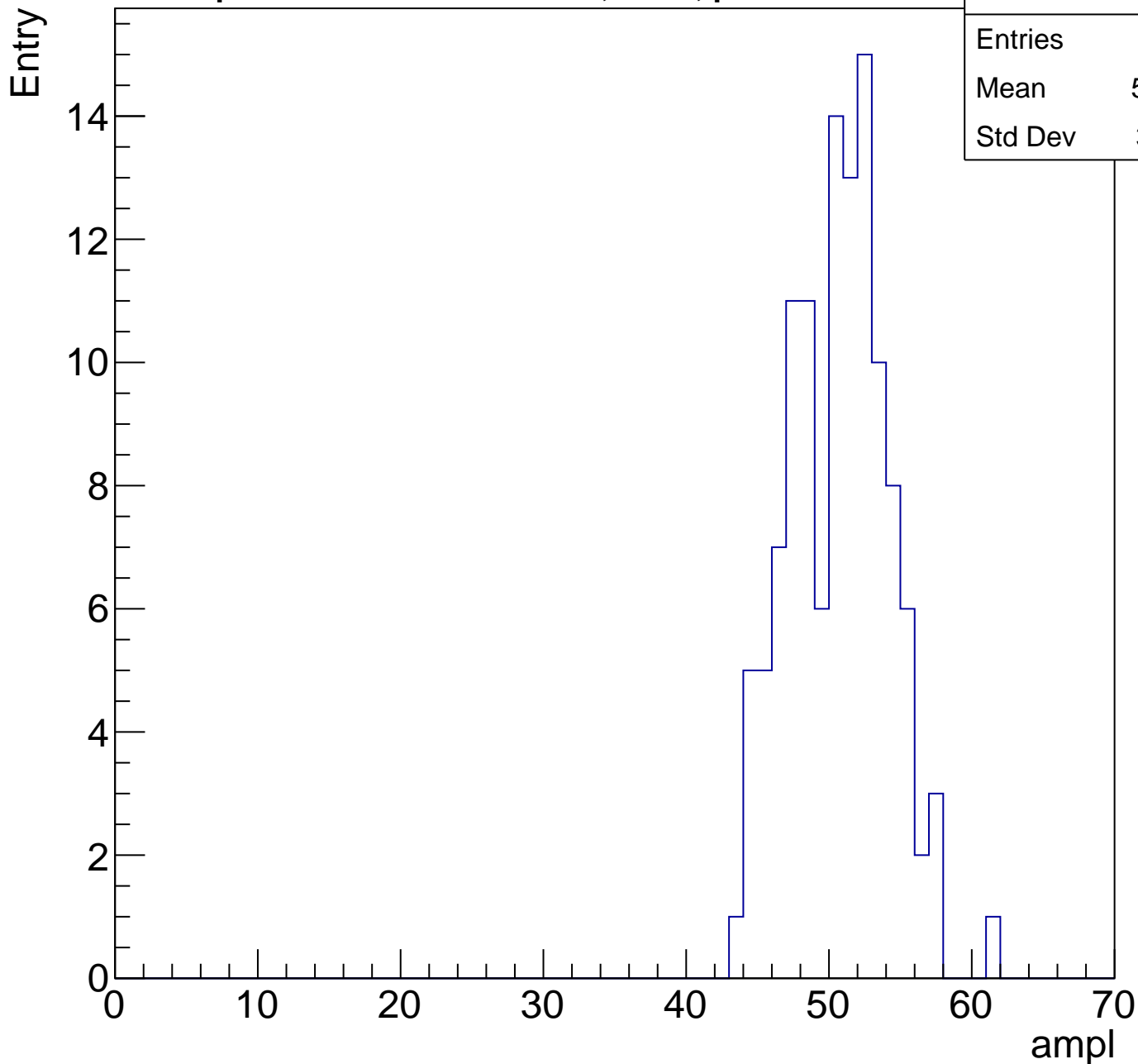
ampl



B1L001S, U20-ch127, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	50.24
Std Dev	3.431

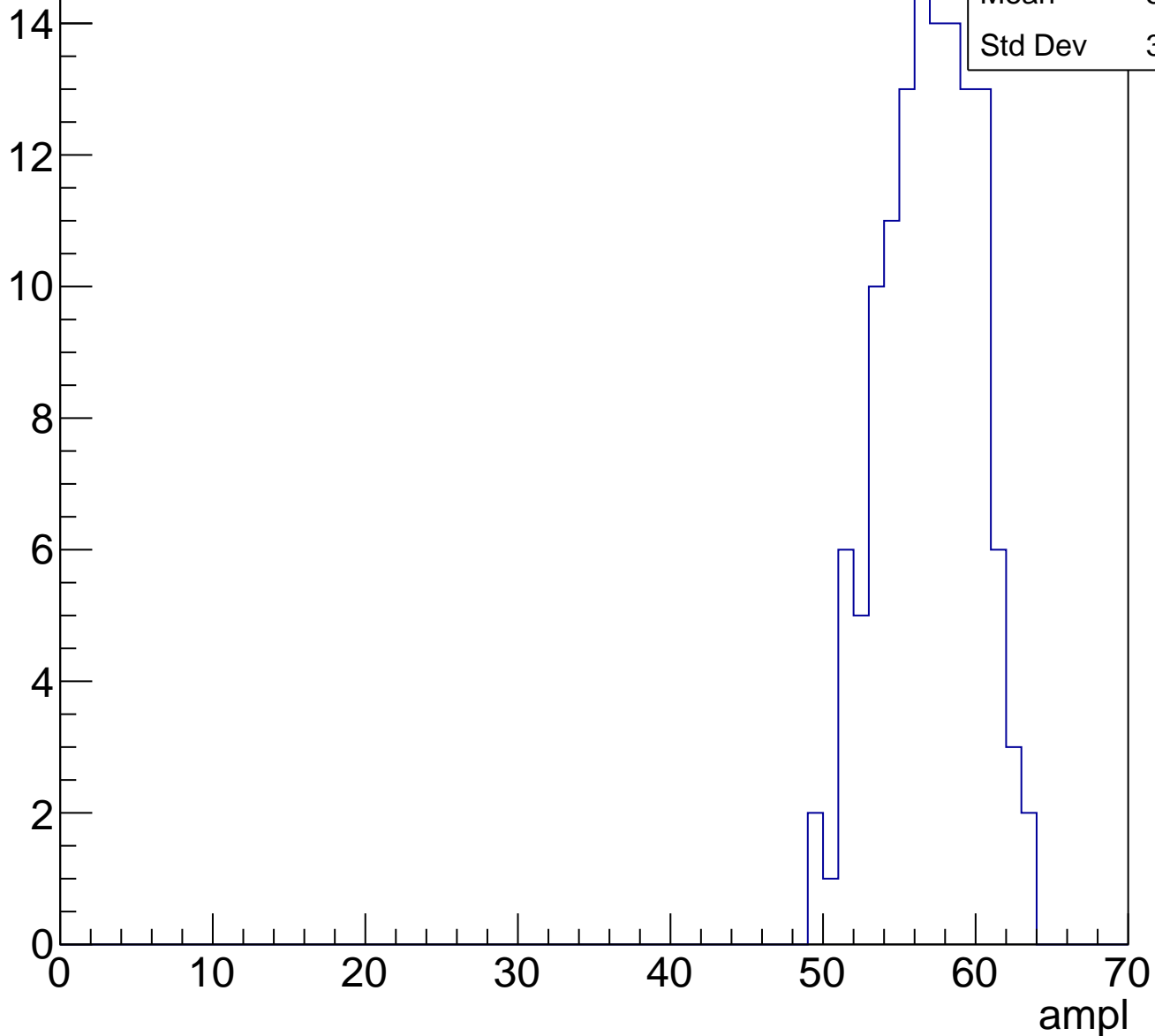


B1L001S, U20-ch127, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	56.47
Std Dev	3.105

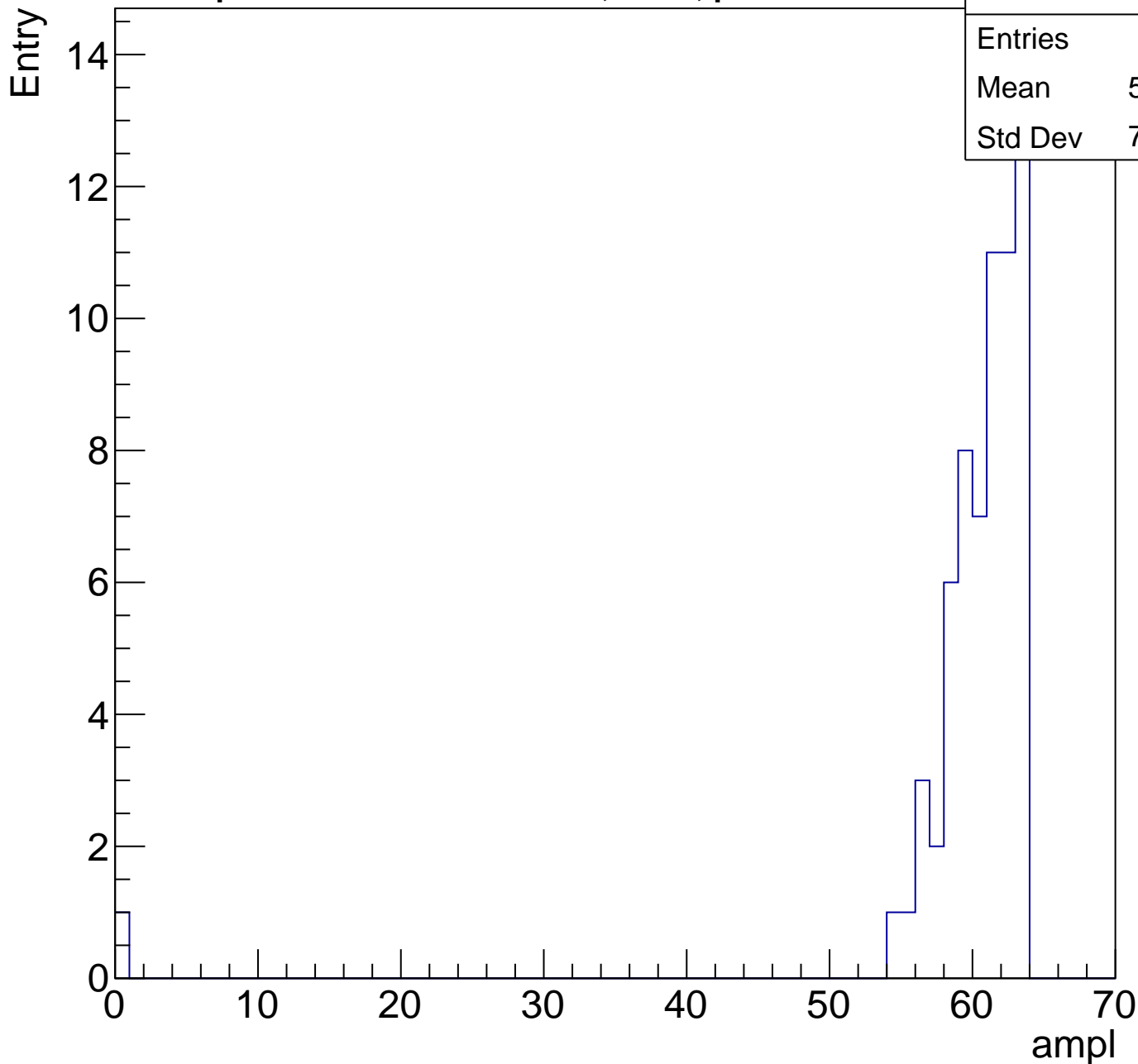
Entry



B1L001S, U20-ch127, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	65
Mean	59.48
Std Dev	7.768

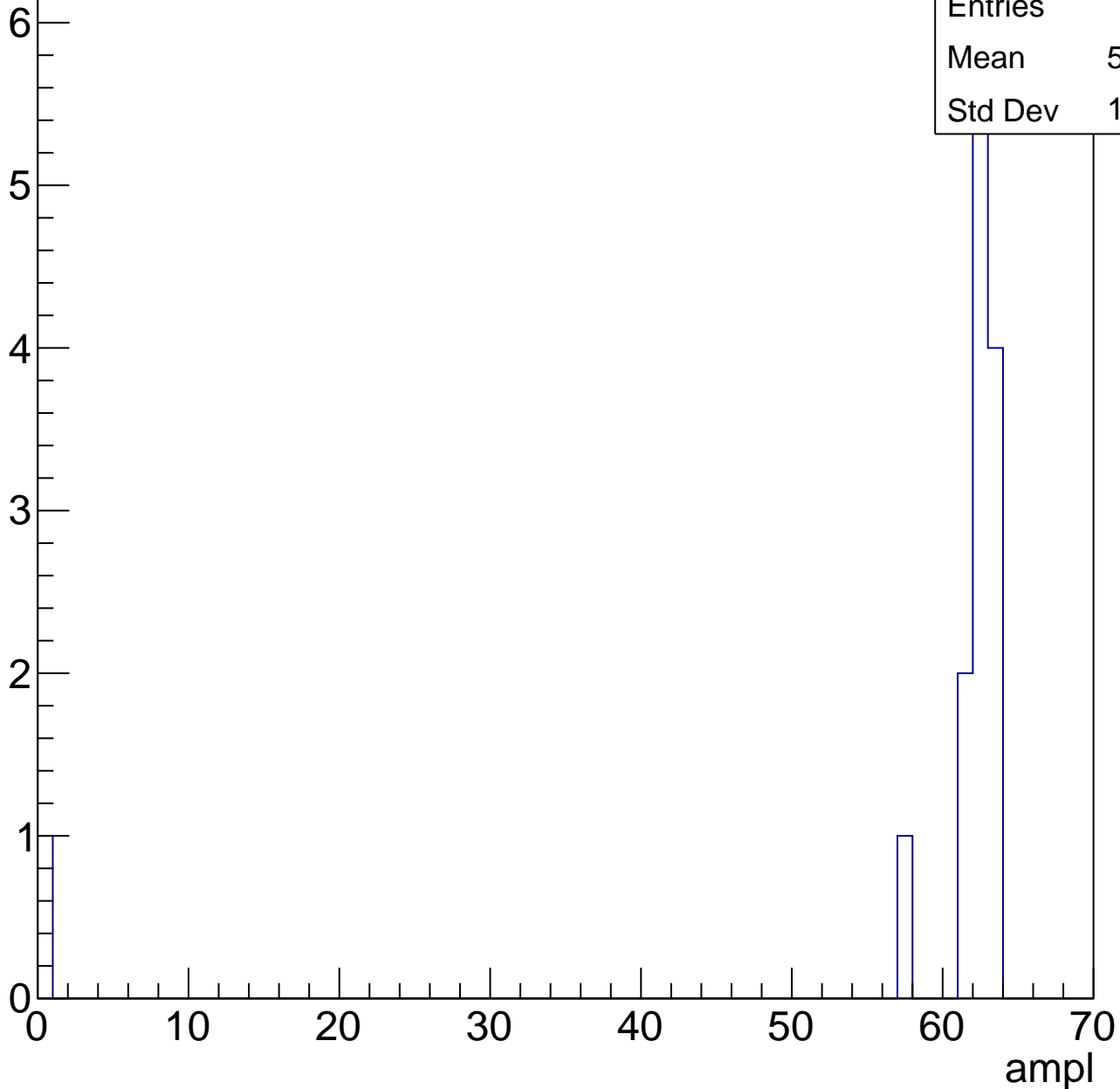


B1L001S, U20-ch127, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	57.36
Std Dev	15.98



B1L001S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U20-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

