

B1L000S, U11-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 28.1031

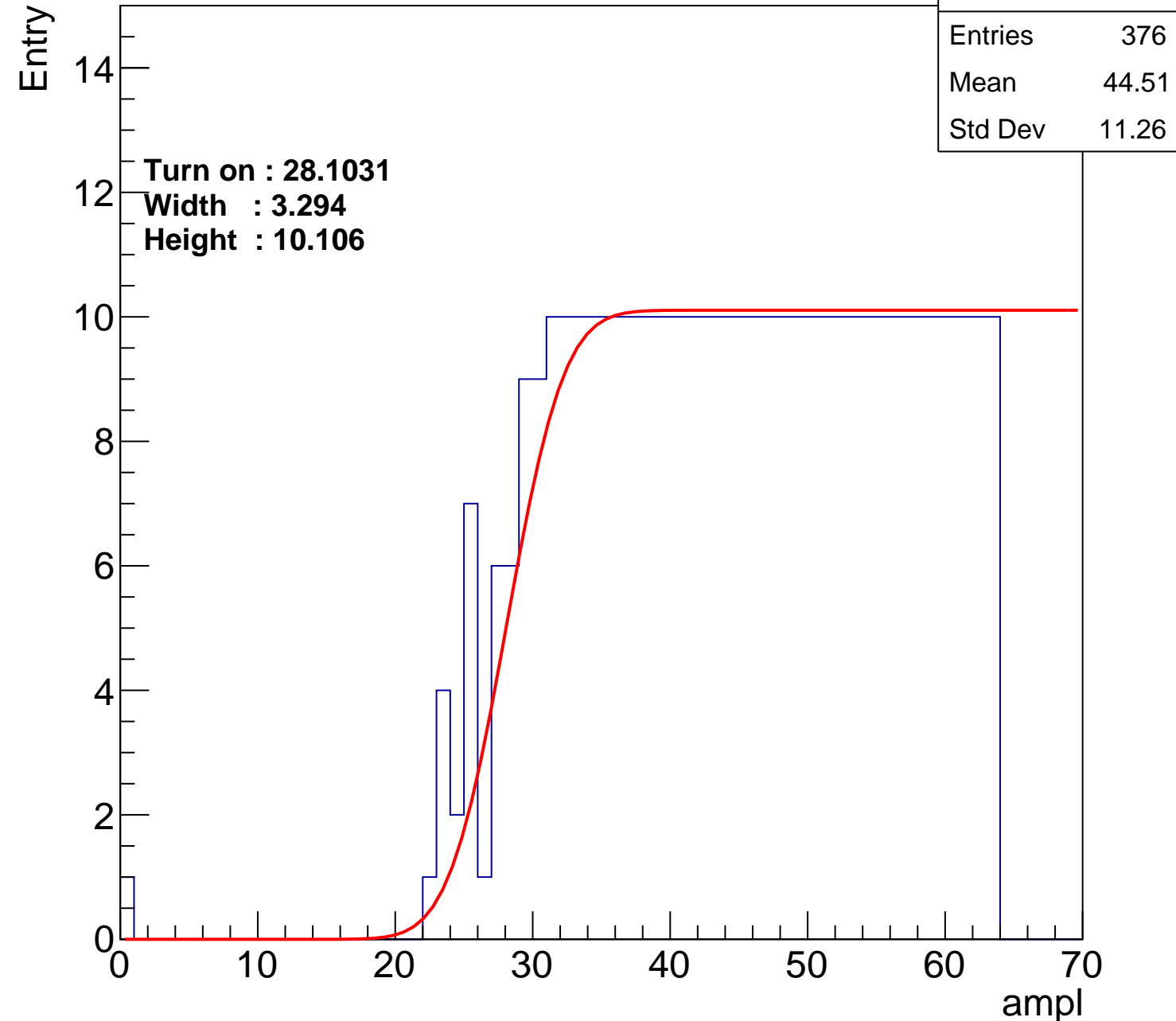
Width : 3.294

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.28
Std Dev	11.92

Turn on : 26.9842

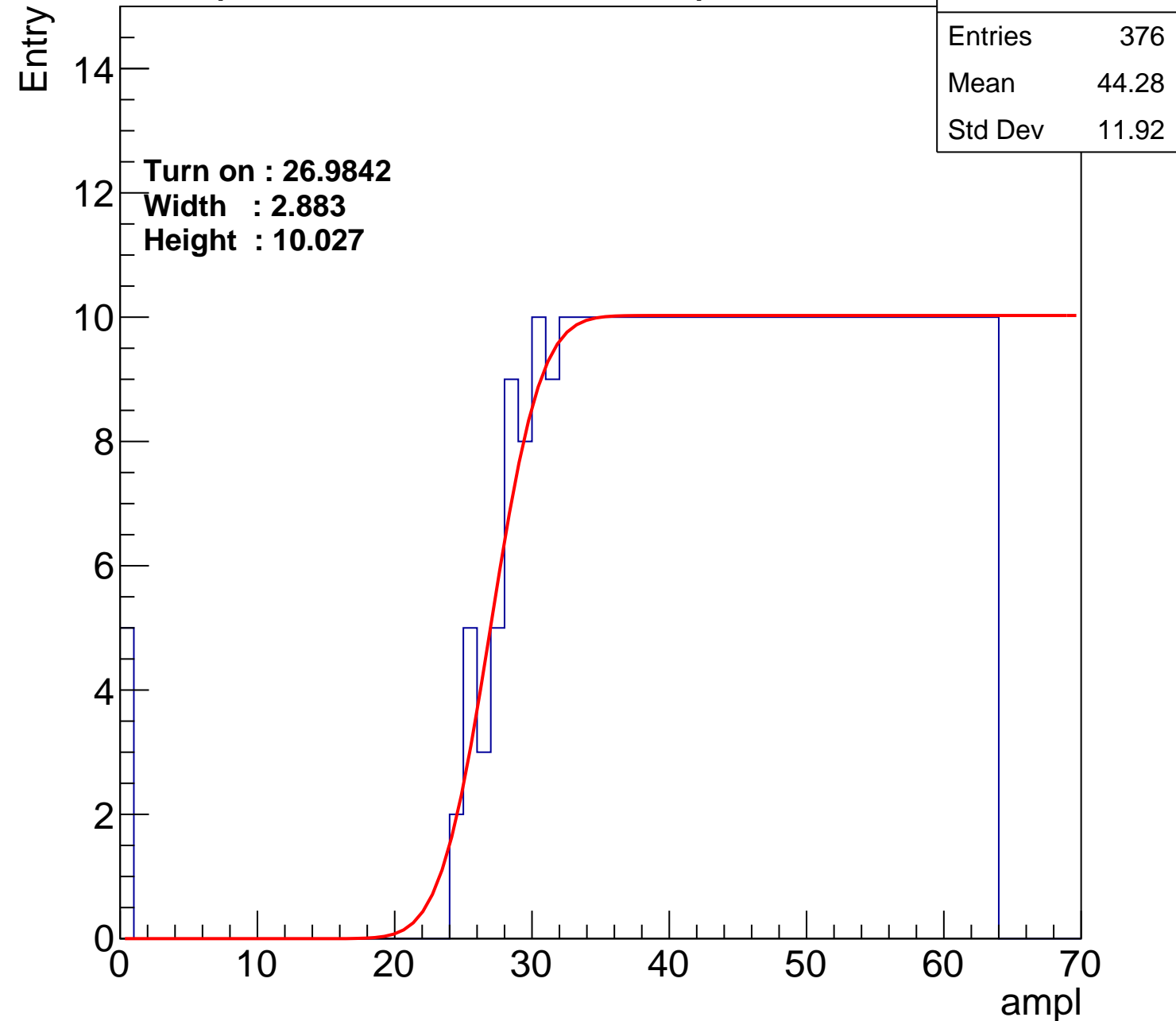
Width : 2.883

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch2

calib_packv5_042523_0143.root, FC#3, port B3

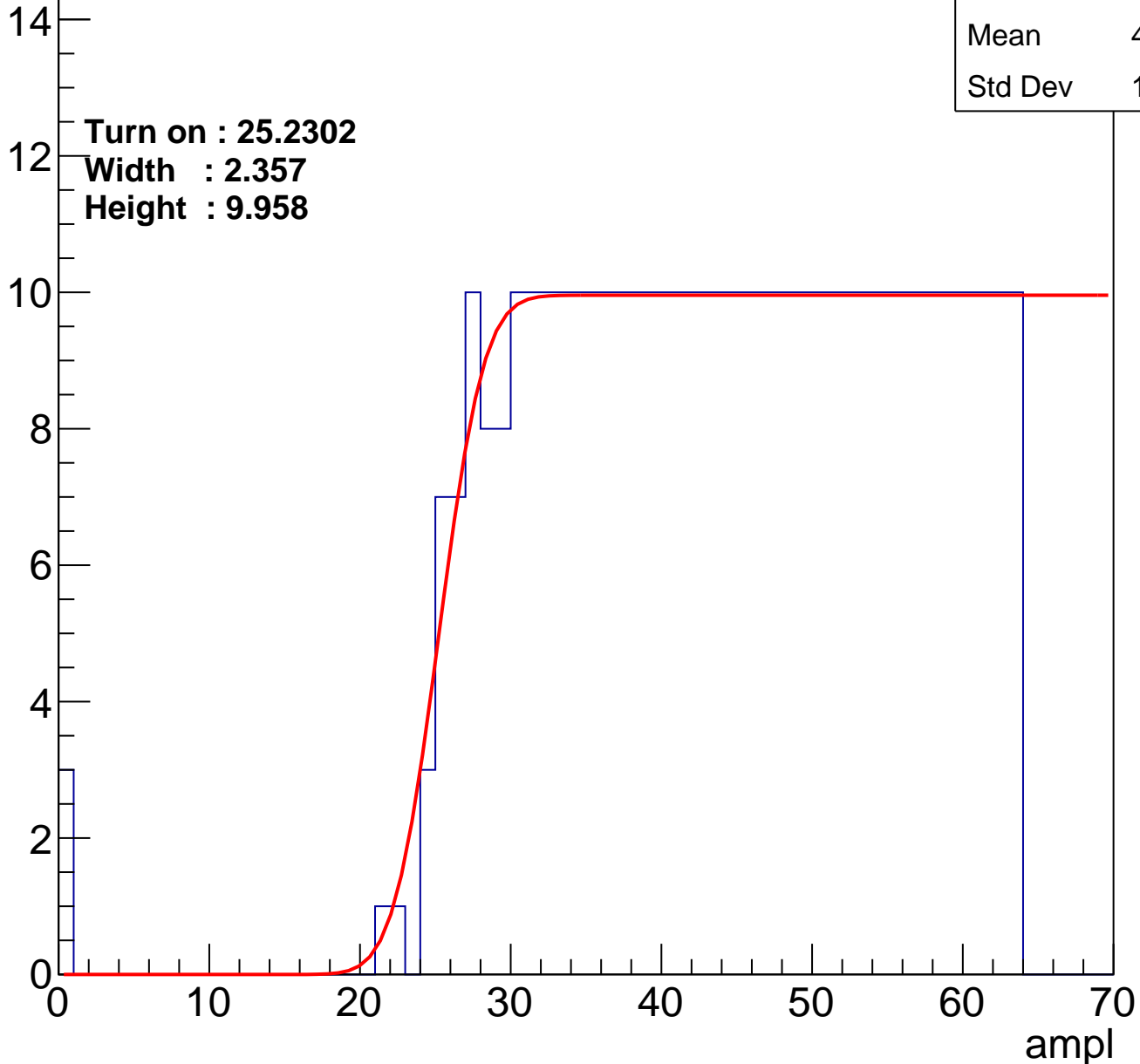
Entries	388
Mean	43.84
Std Dev	11.84

Turn on : 25.2302

Width : 2.357

Height : 9.958

Entry



B1L000S, U11-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.58
Std Dev	11.18

Turn on : 26.2887

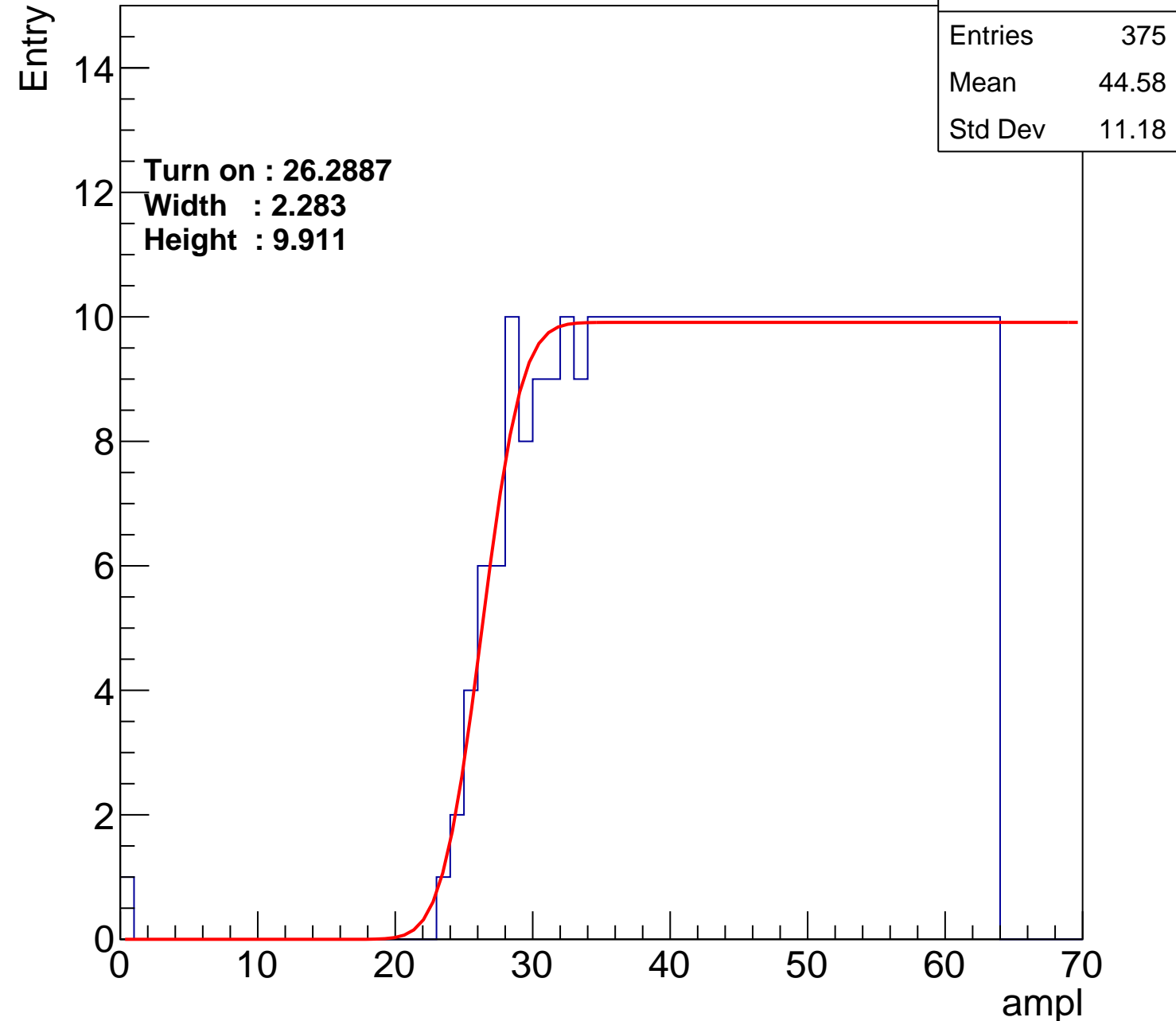
Width : 2.283

Height : 9.911

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch4

calib_packv5_042523_0143.root, FC#3, port B3

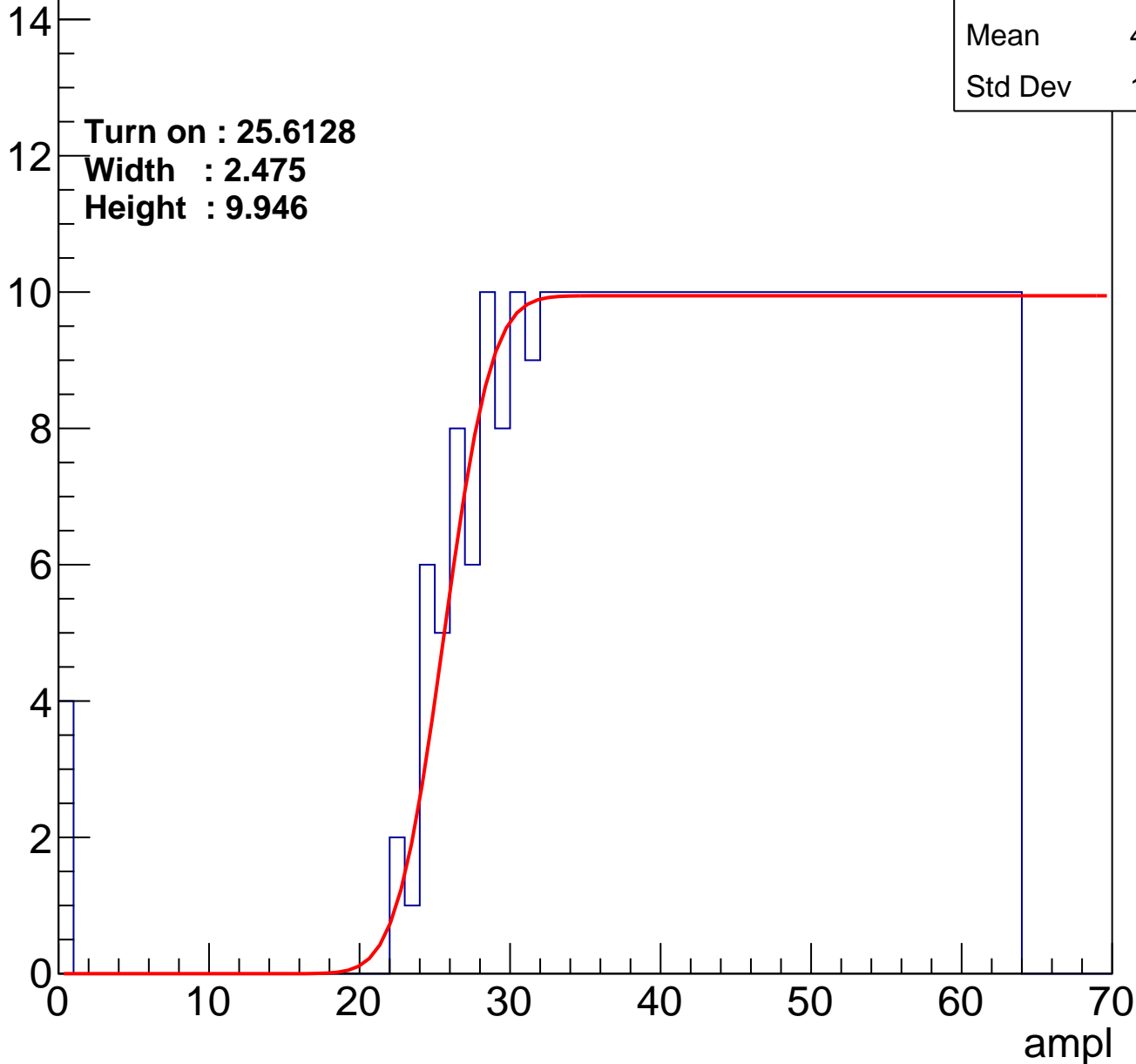
Entry

Entries	389
Mean	43.69
Std Dev	12.07

Turn on : 25.6128

Width : 2.475

Height : 9.946



B1L000S, U11-ch5

calib_packv5_042523_0143.root, FC#3, port B3

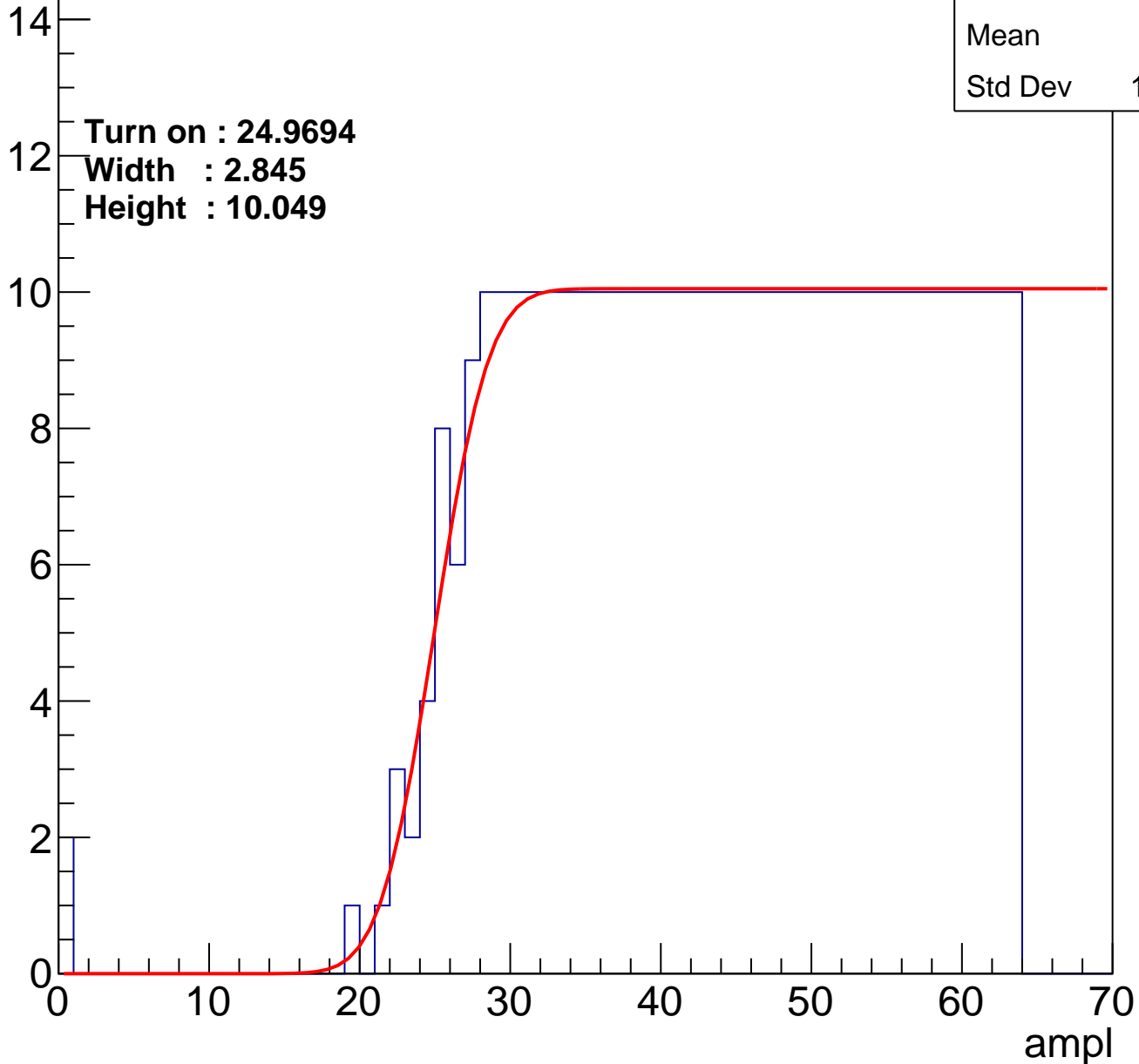
Entries	396
Mean	43.5
Std Dev	11.89

Turn on : 24.9694

Width : 2.845

Height : 10.049

Entry



B1L000S, U11-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.03
Std Dev	11.75

Turn on : 25.9642

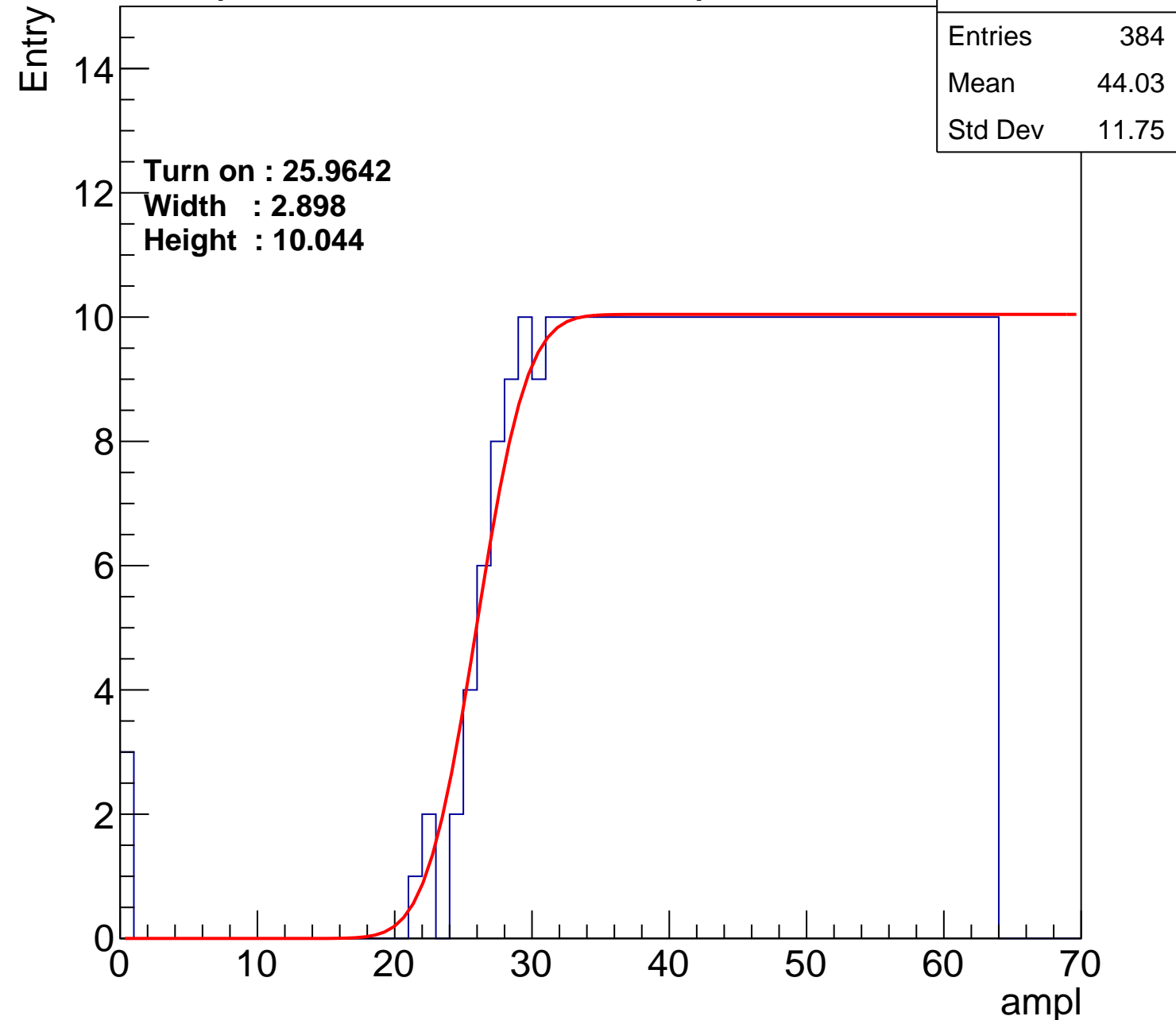
Width : 2.898

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.44
Std Dev	12.28

Turn on : 24.9606

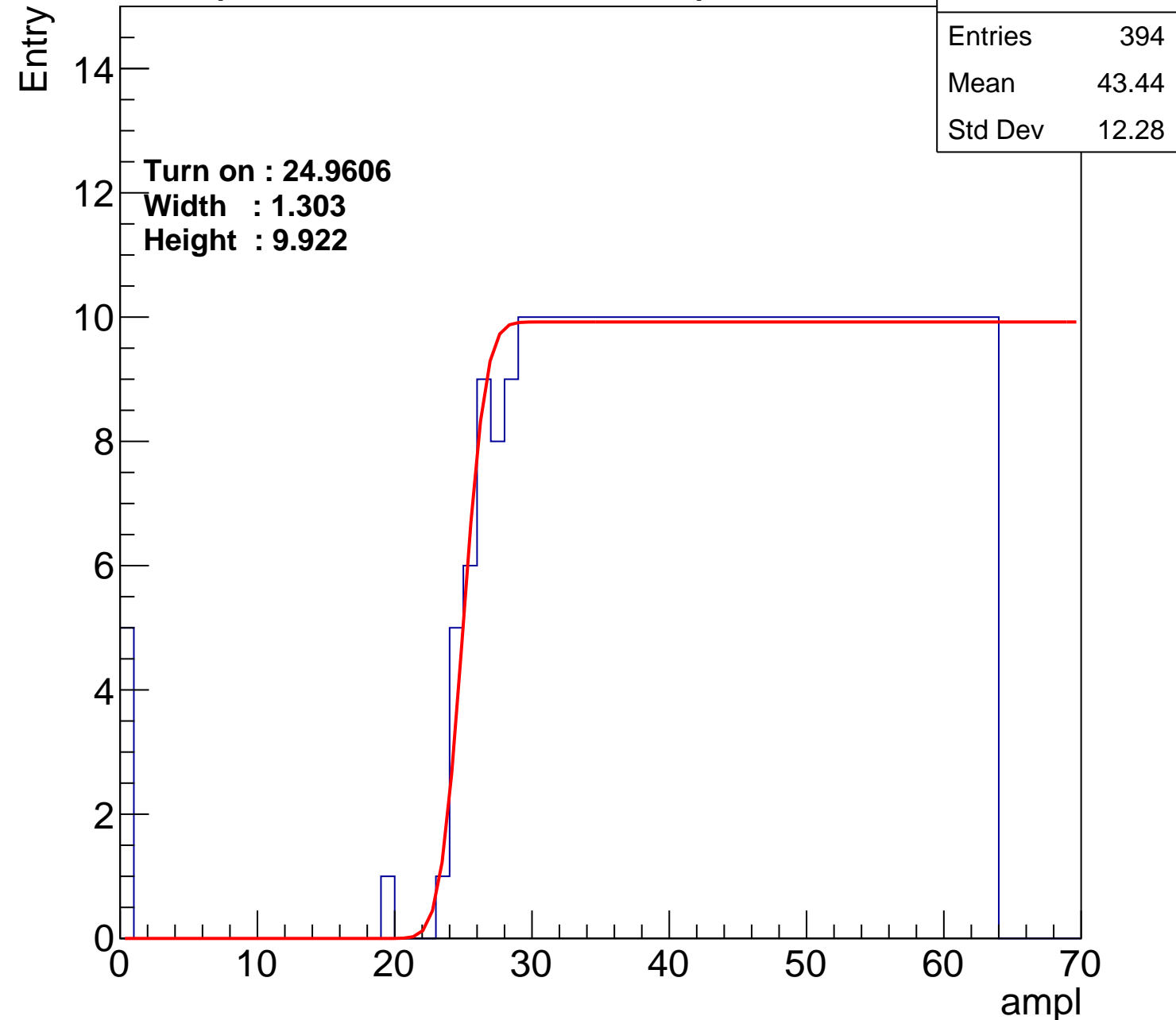
Width : 1.303

Height : 9.922

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch8

calib_packv5_042523_0143.root, FC#3, port B3

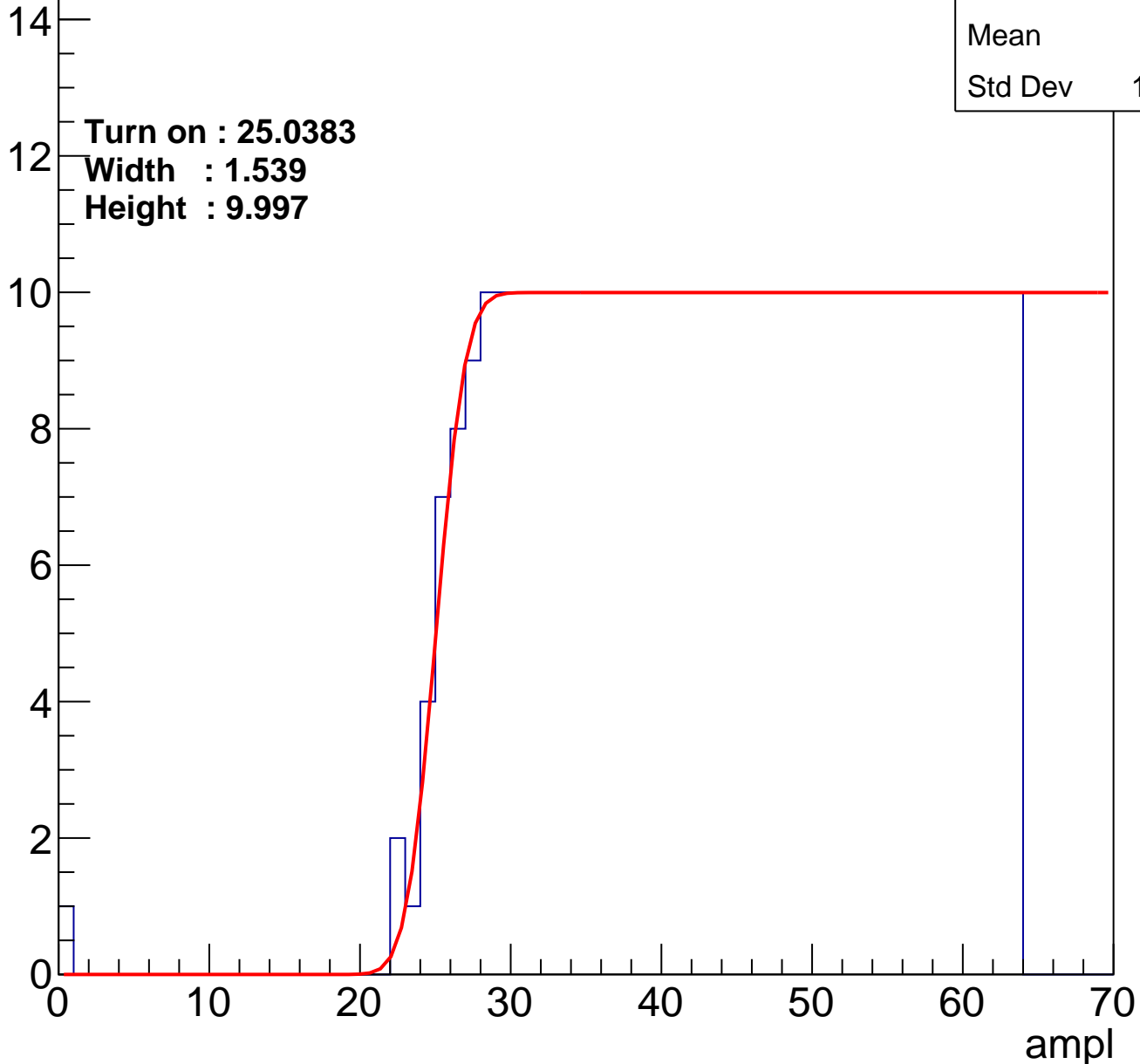
Entries	392
Mean	43.8
Std Dev	11.55

Turn on : 25.0383

Width : 1.539

Height : 9.997

Entry



B1L000S, U11-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 26.1278

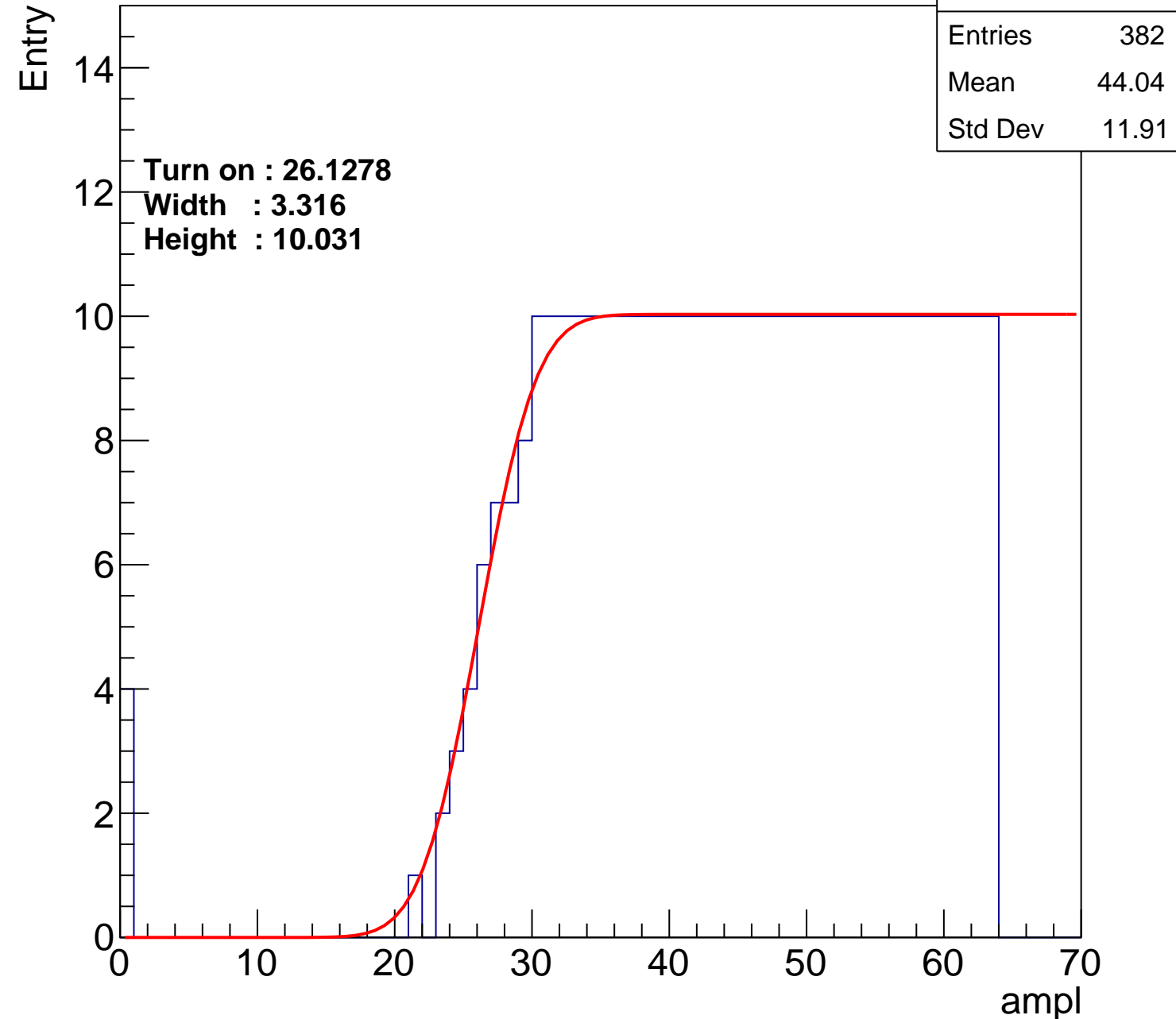
Width : 3.316

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.66
Std Dev	11.12

Turn on : 27.2774

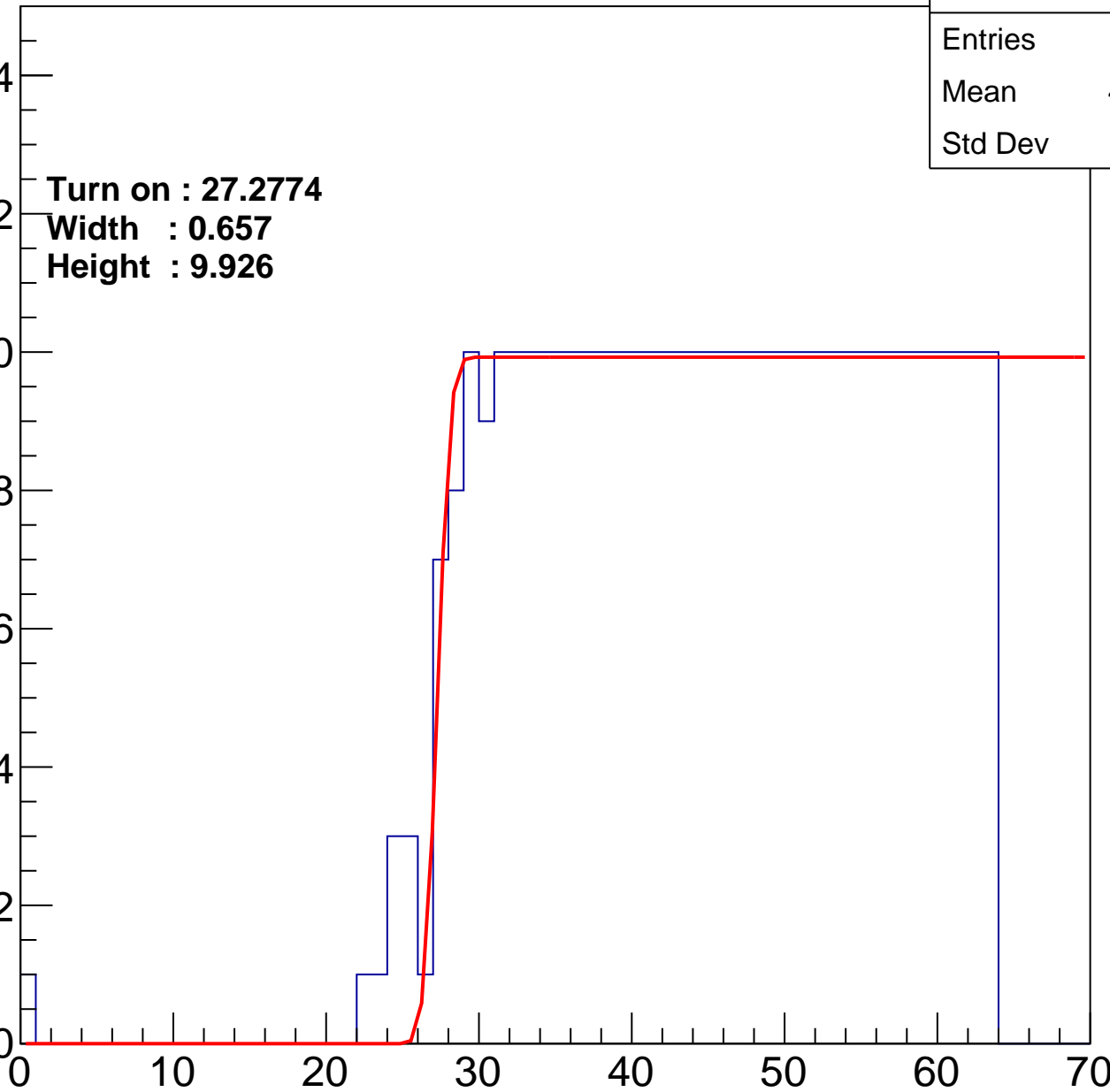
Width : 0.657

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch11

calib_packv5_042523_0143.root, FC#3, port B3

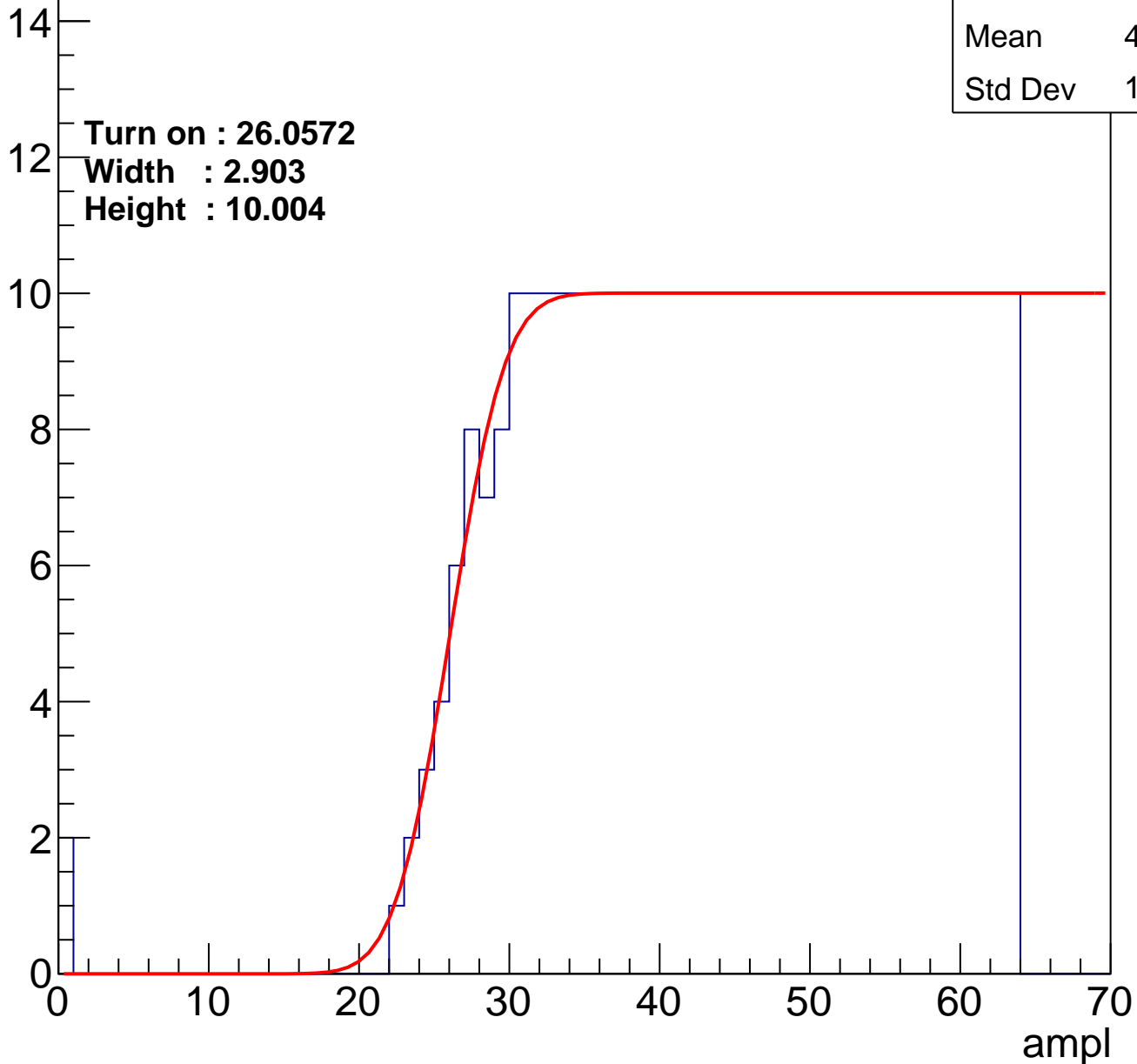
Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.0572

Width : 2.903

Height : 10.004

Entry



B1L000S, U11-ch12

calib_packv5_042523_0143.root, FC#3, port B3

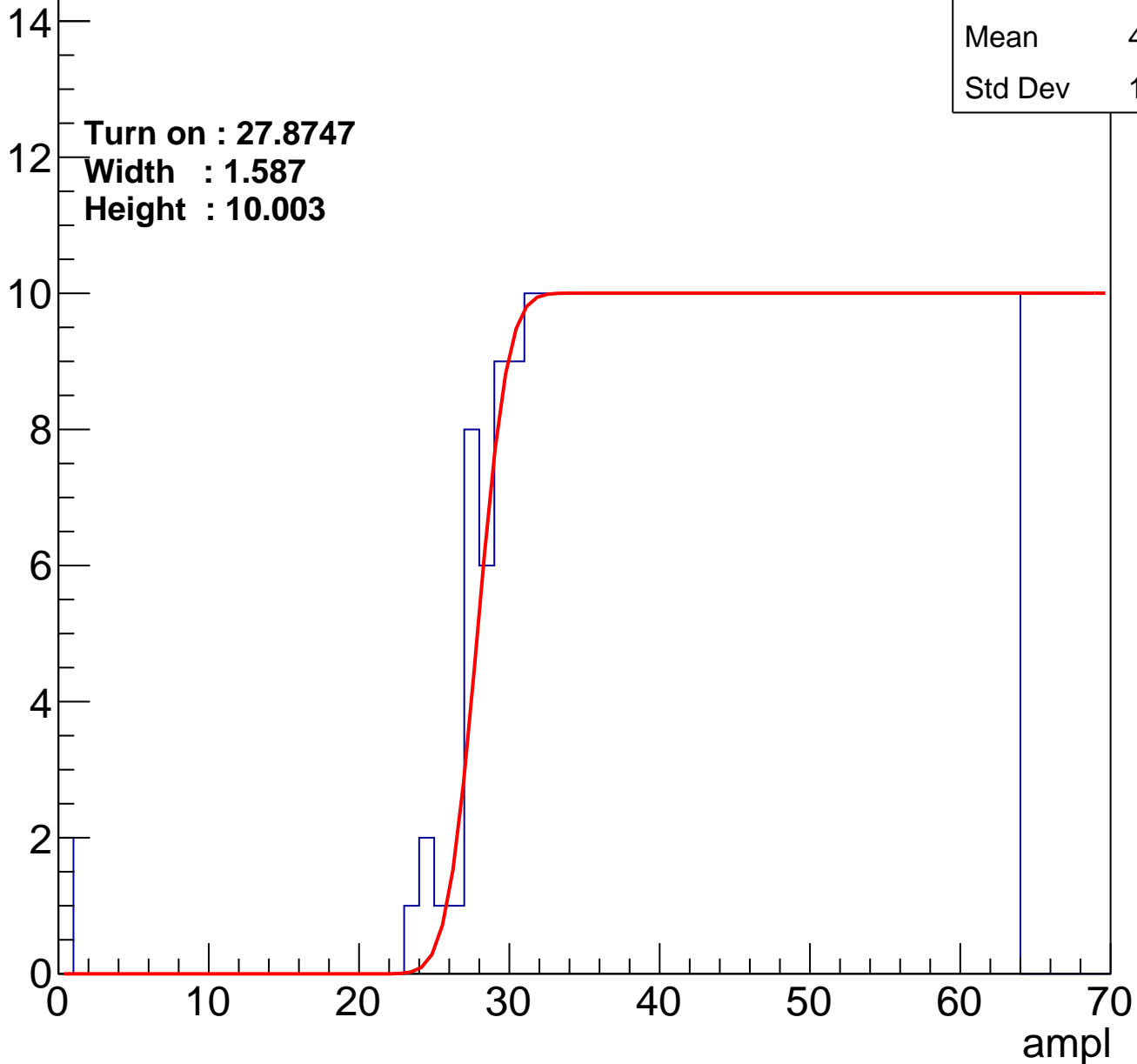
Entries	369
Mean	44.84
Std Dev	11.17

Turn on : 27.8747

Width : 1.587

Height : 10.003

Entry



B1L000S, U11-ch13

calib_packv5_042523_0143.root, FC#3, port B3

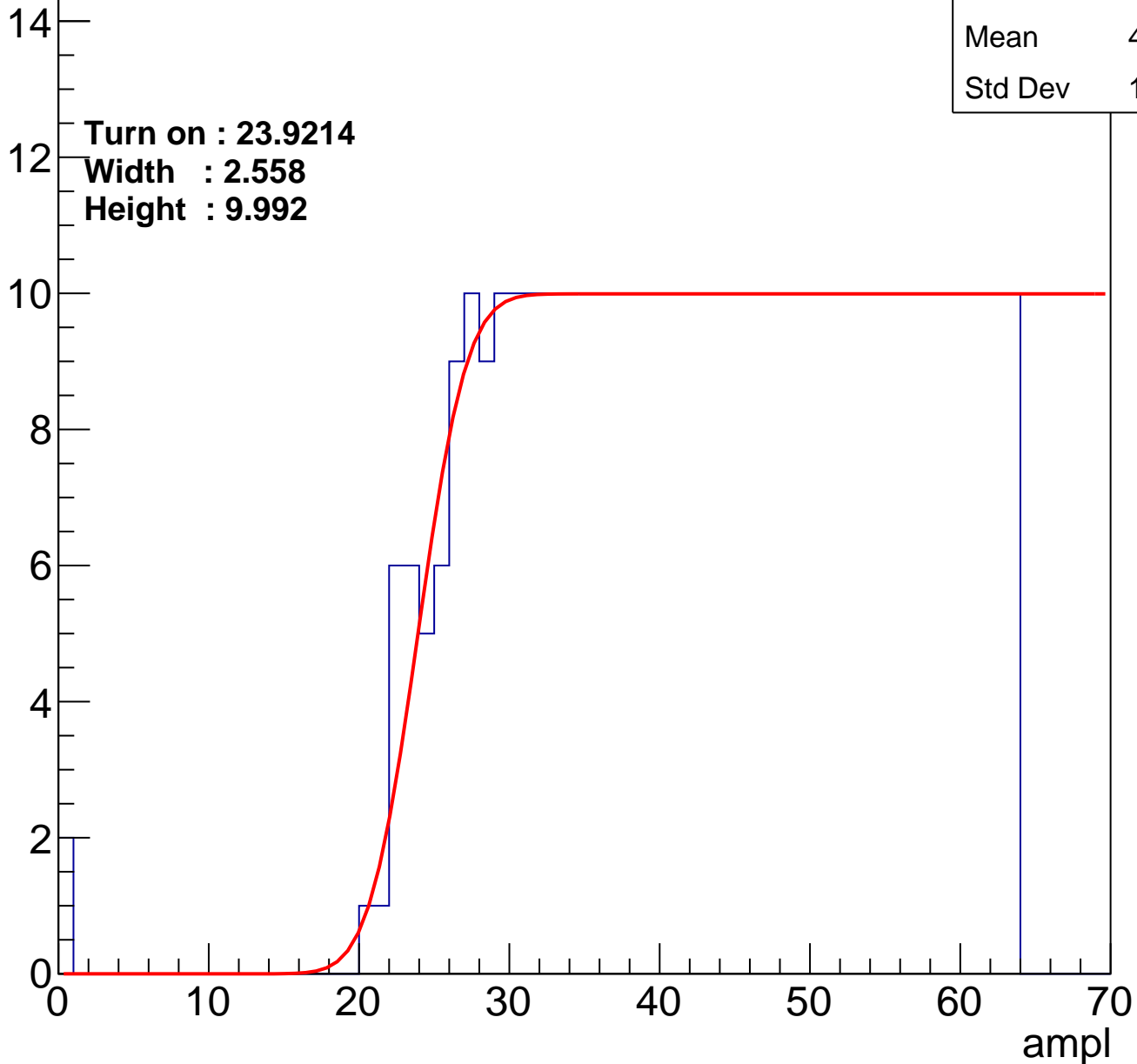
Entries	405
Mean	43.05
Std Dev	12.12

Turn on : 23.9214

Width : 2.558

Height : 9.992

Entry



B1L000S, U11-ch14

calib_packv5_042523_0143.root, FC#3, port B3

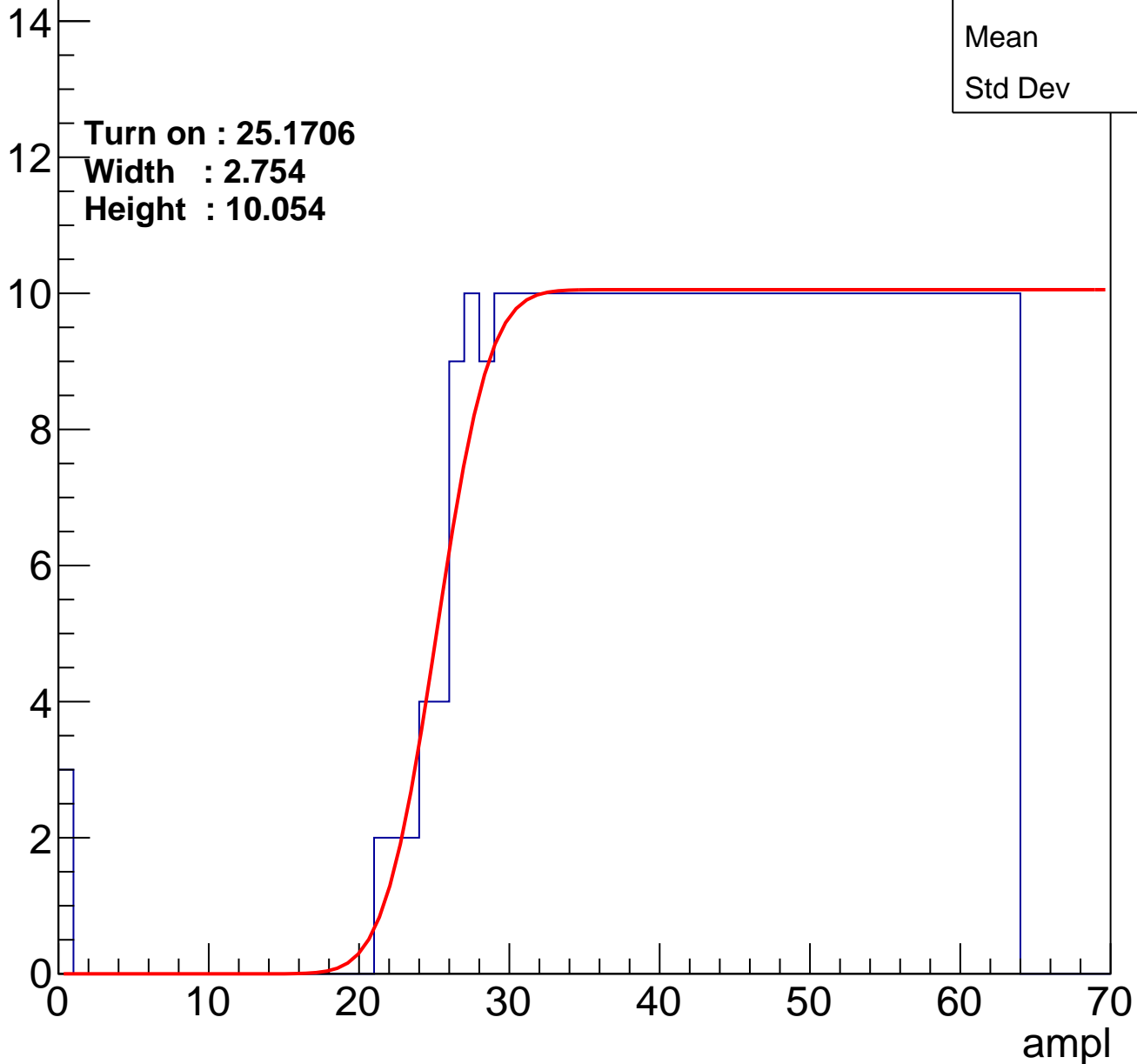
Entries	395
Mean	43.5
Std Dev	12

Turn on : 25.1706

Width : 2.754

Height : 10.054

Entry



B1L000S, U11-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 27.3622

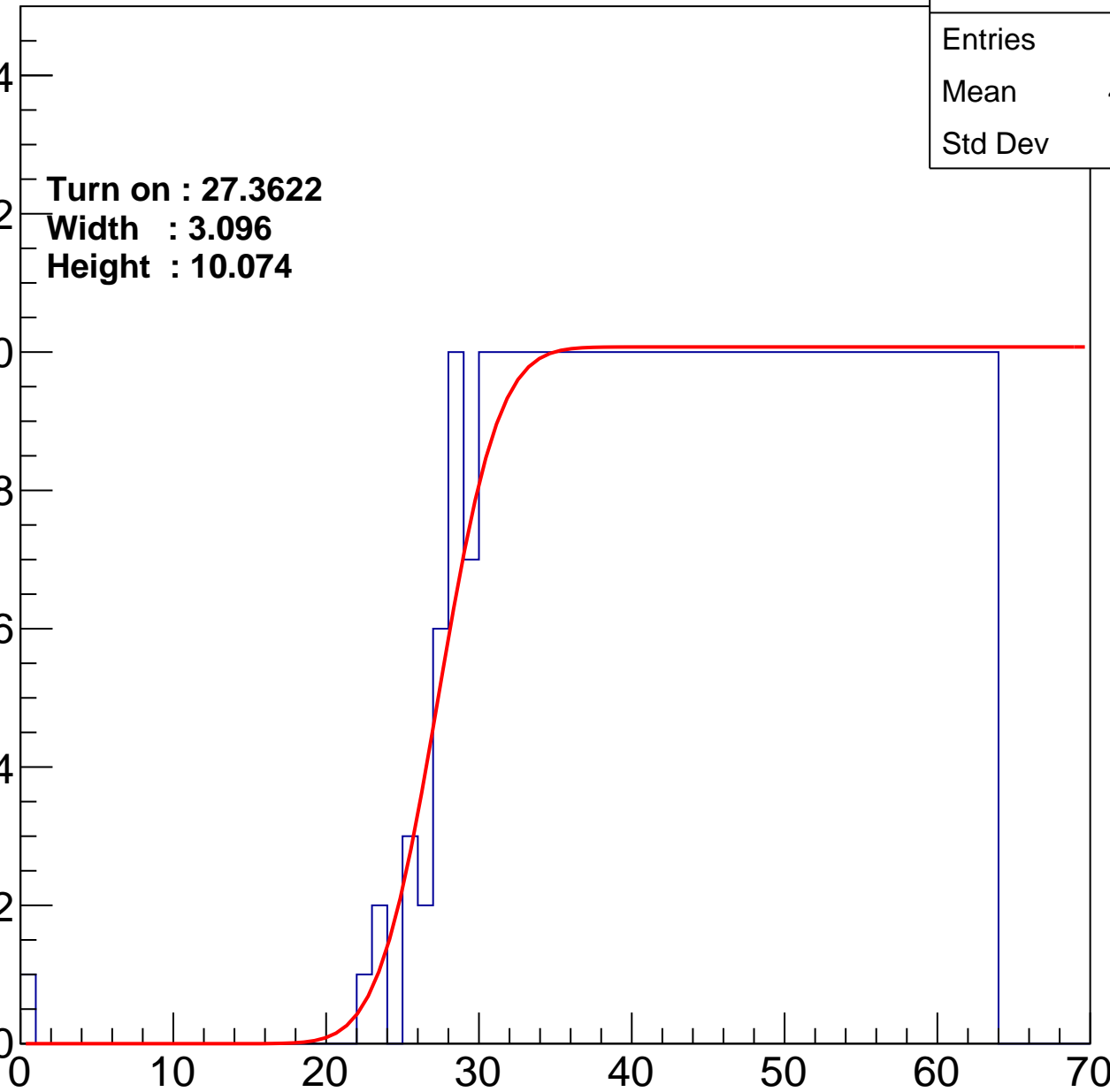
Width : 3.096

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch16

calib_packv5_042523_0143.root, FC#3, port B3

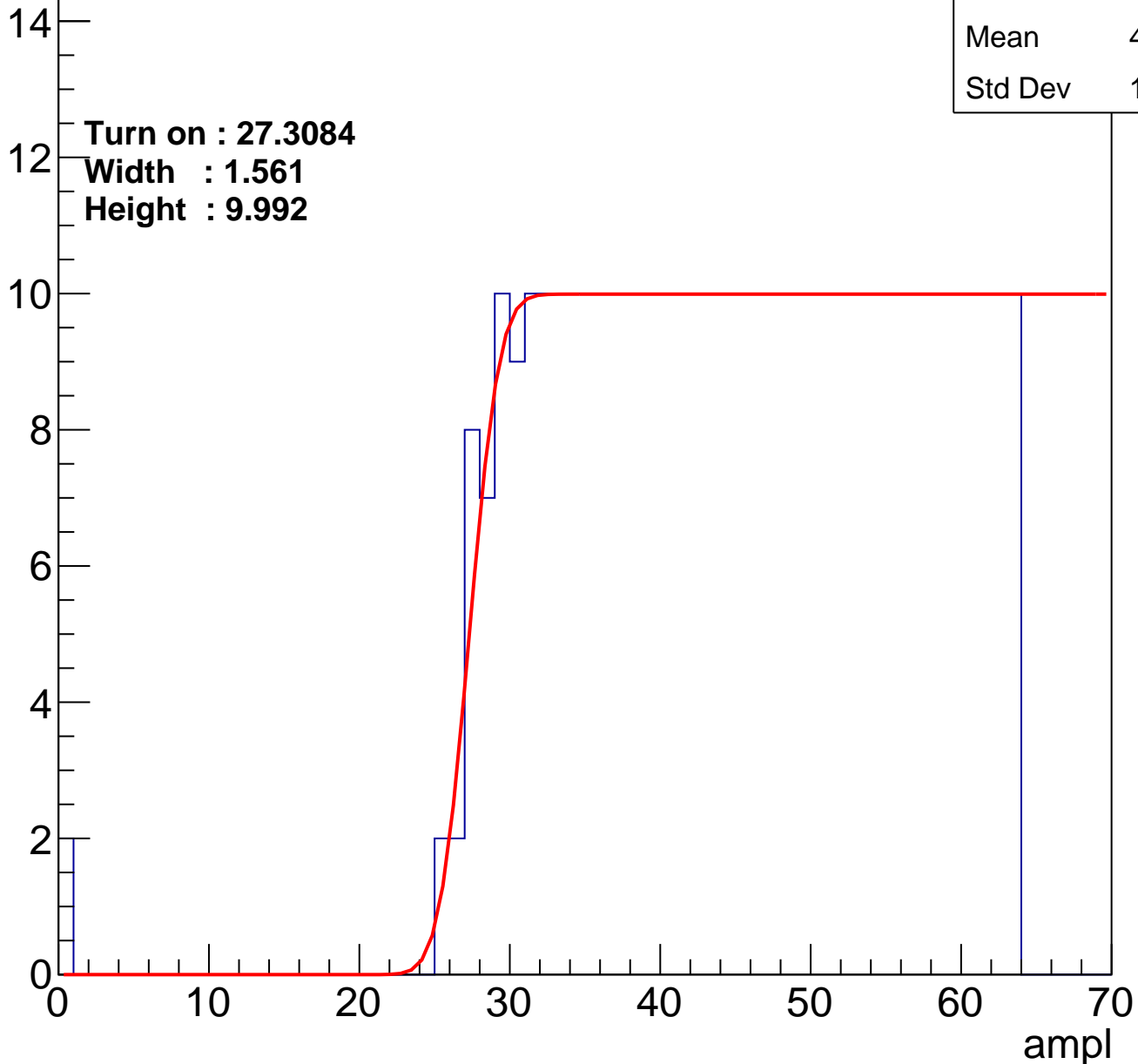
Entries	370
Mean	44.82
Std Dev	11.15

Turn on : 27.3084

Width : 1.561

Height : 9.992

Entry



B1L000S, U11-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.95
Std Dev	11.16

Turn on : 28.9915

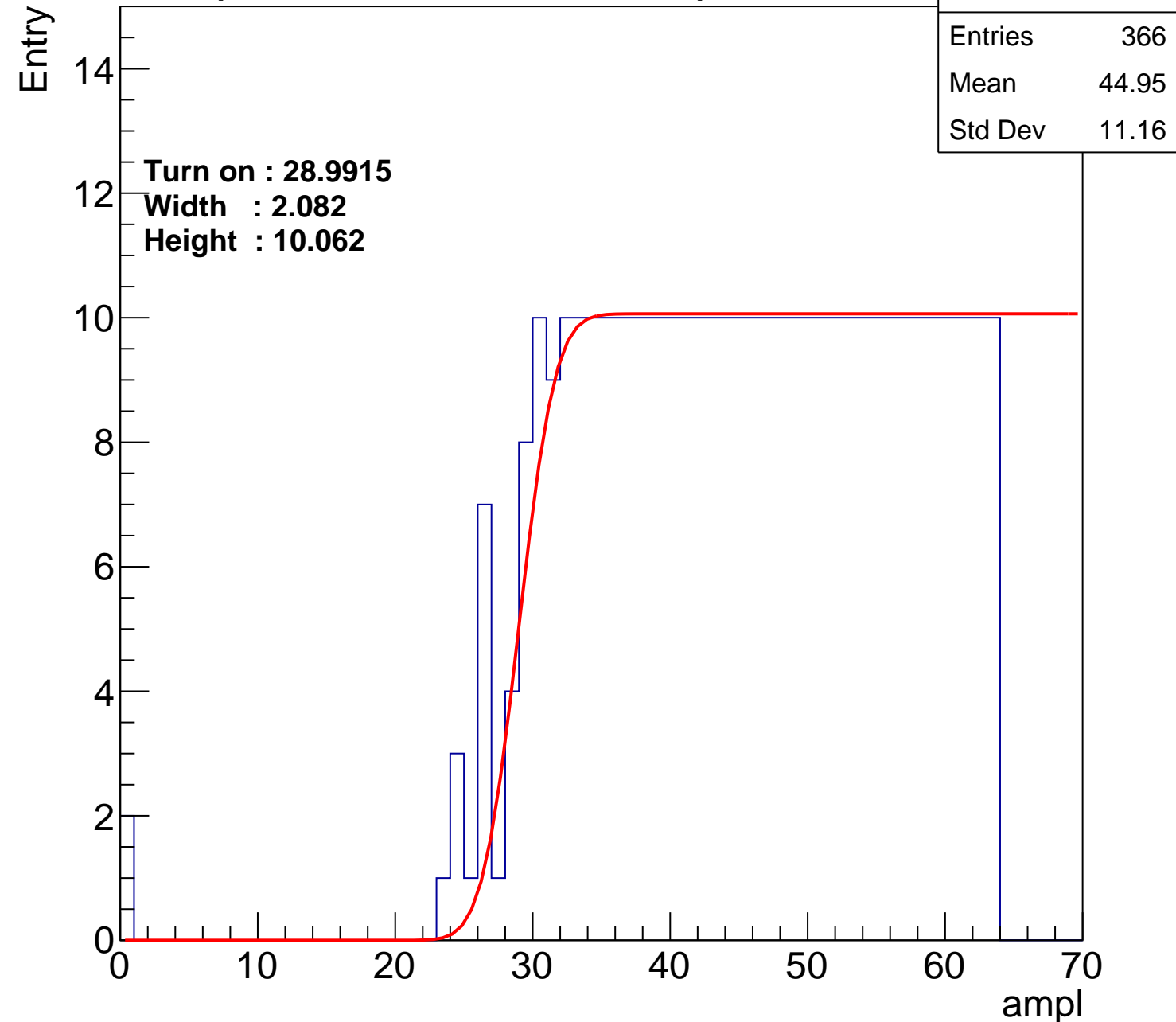
Width : 2.082

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch18

calib_packv5_042523_0143.root, FC#3, port B3

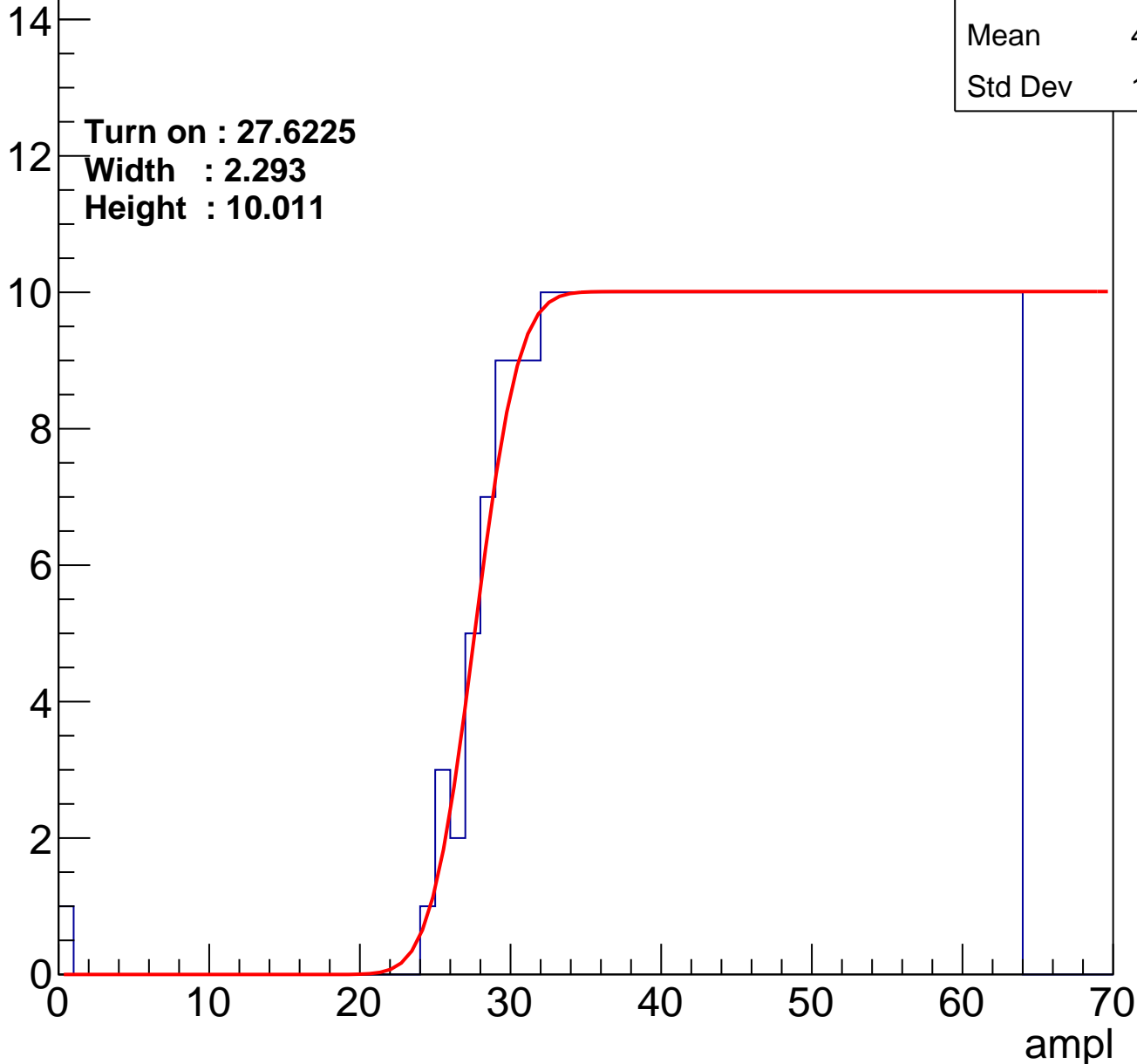
Entries	366
Mean	45.06
Std Dev	10.89

Turn on : 27.6225

Width : 2.293

Height : 10.011

Entry



B1L000S, U11-ch19

calib_packv5_042523_0143.root, FC#3, port B3

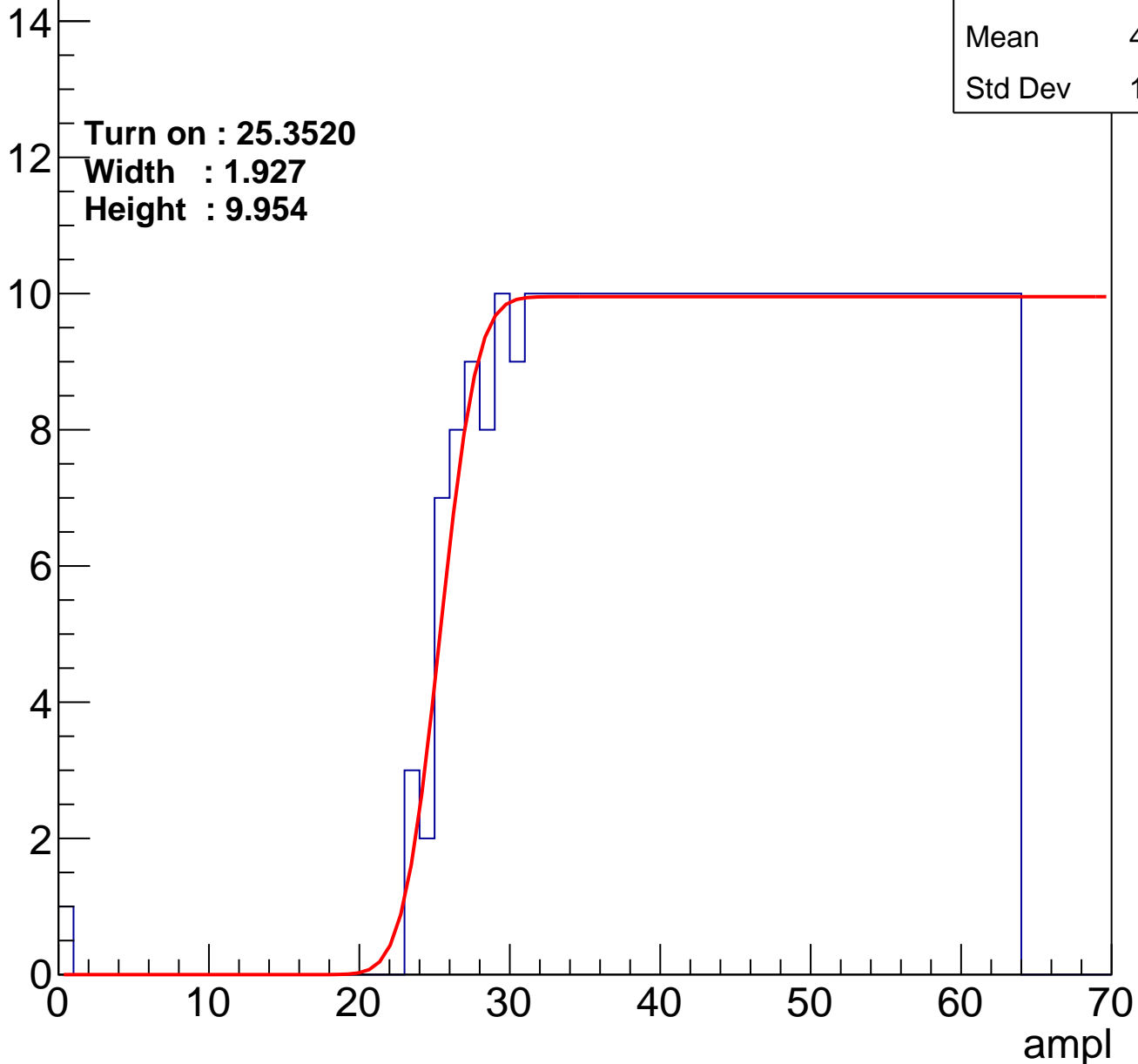
Entries	387
Mean	44.02
Std Dev	11.45

Turn on : 25.3520

Width : 1.927

Height : 9.954

Entry



B1L000S, U11-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.82
Std Dev	11.7

Turn on : 25.4508

Width : 2.844

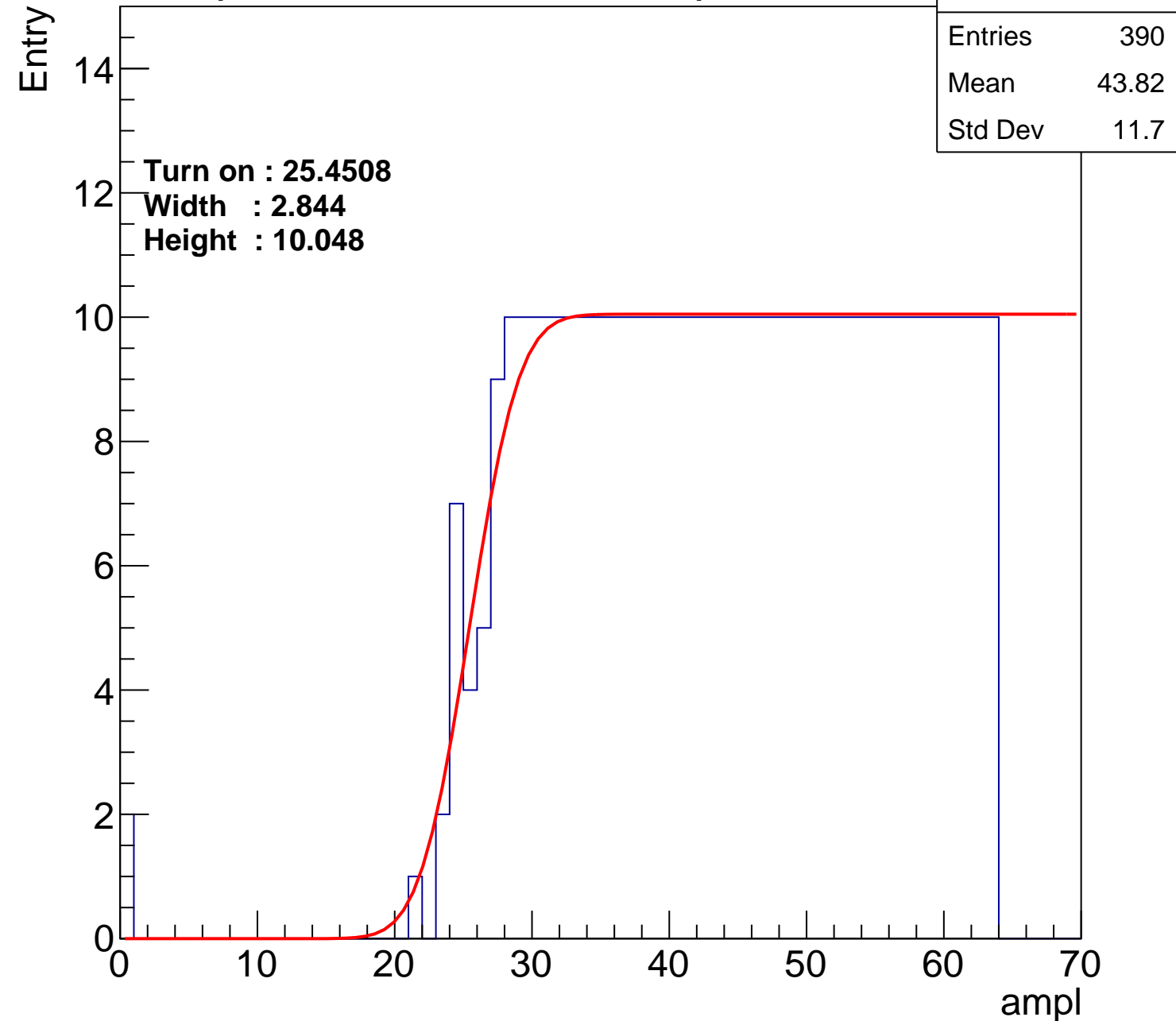
Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U11-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.74
Std Dev	12.18

Turn on : 25.7302

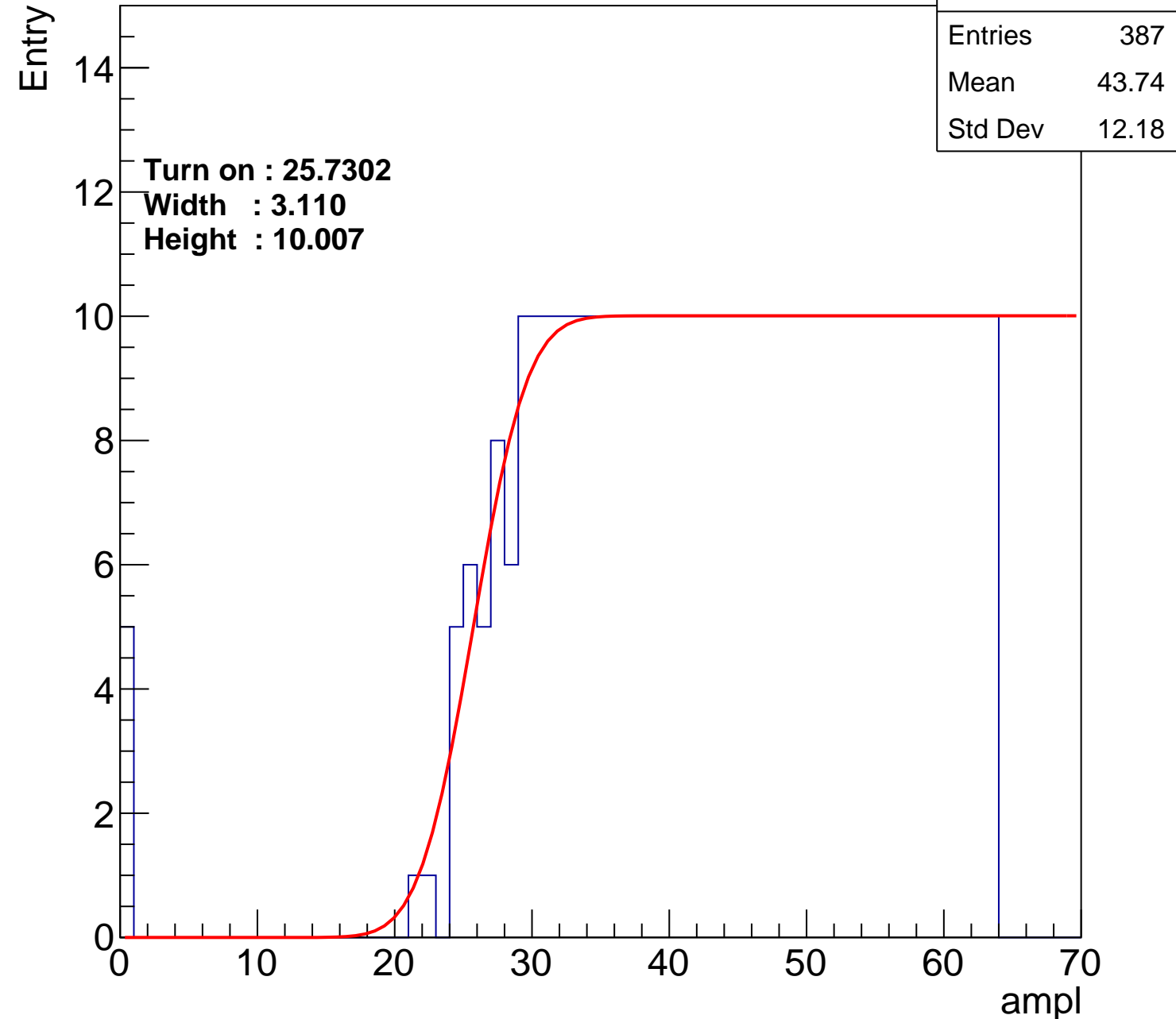
Width : 3.110

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch22

calib_packv5_042523_0143.root, FC#3, port B3

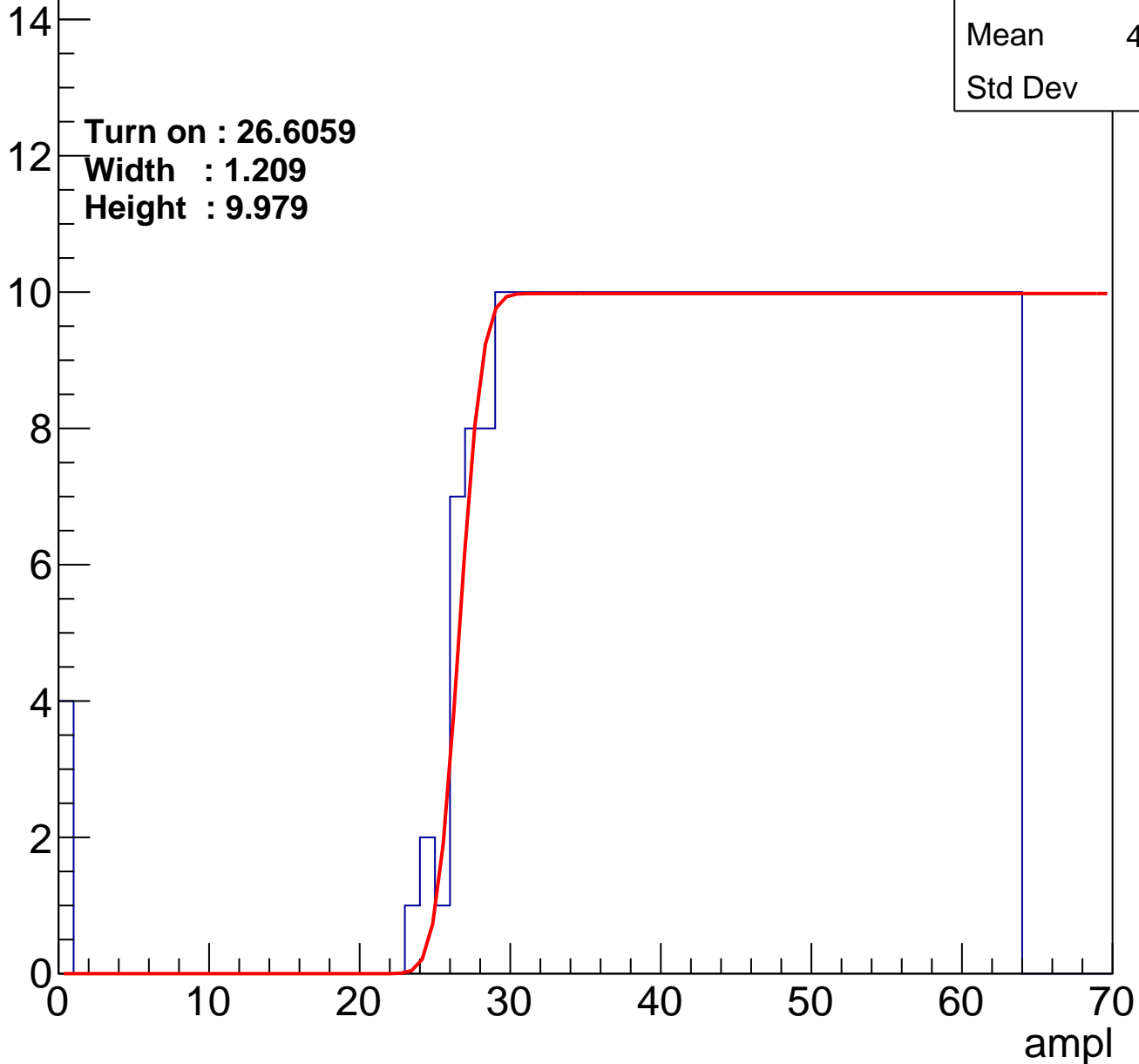
Entries	381
Mean	44.14
Std Dev	11.8

Turn on : 26.6059

Width : 1.209

Height : 9.979

Entry



B1L000S, U11-ch23

calib_packv5_042523_0143.root, FC#3, port B3

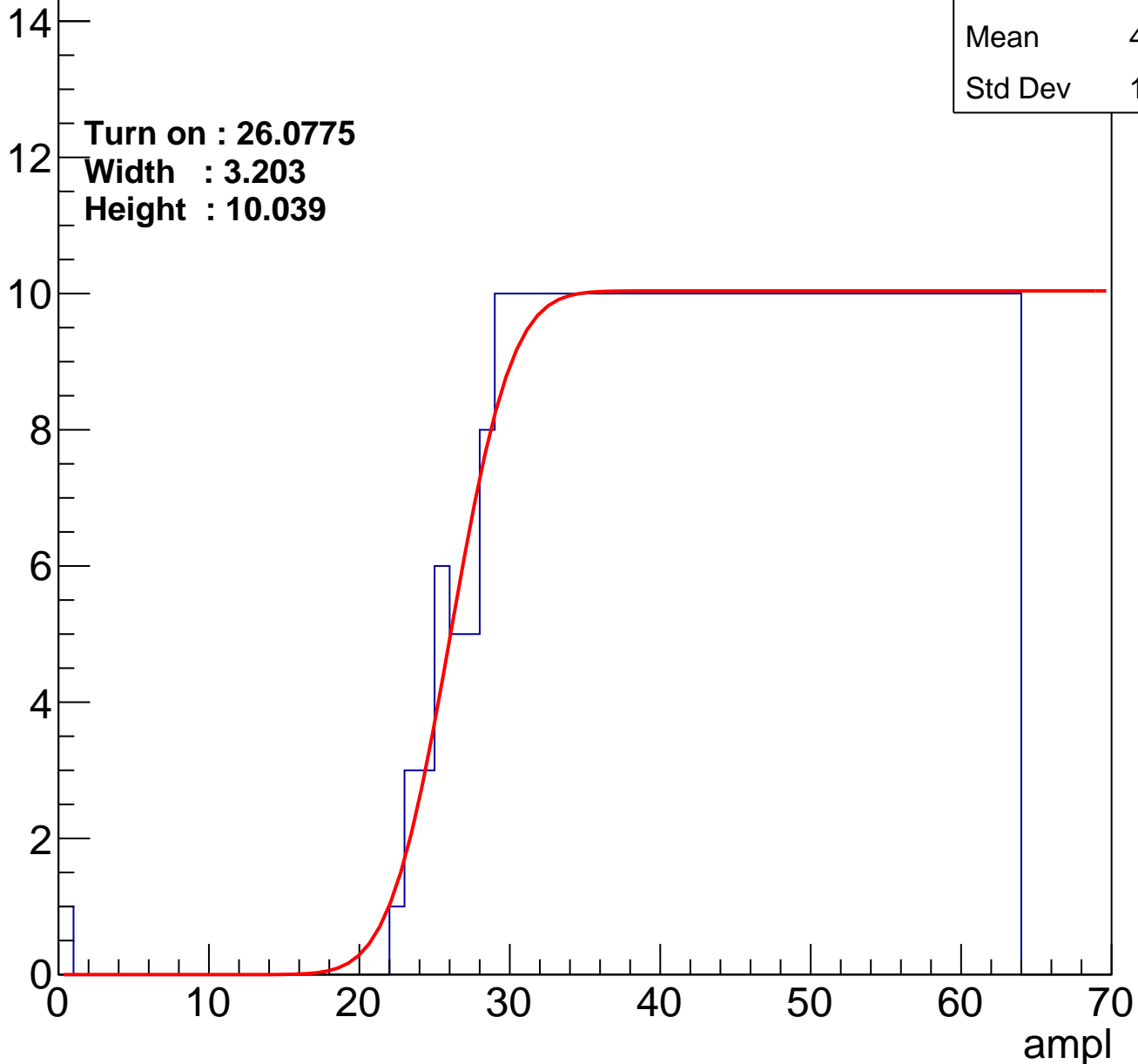
Entries	382
Mean	44.25
Std Dev	11.36

Turn on : 26.0775

Width : 3.203

Height : 10.039

Entry



B1L000S, U11-ch24

calib_packv5_042523_0143.root, FC#3, port B3

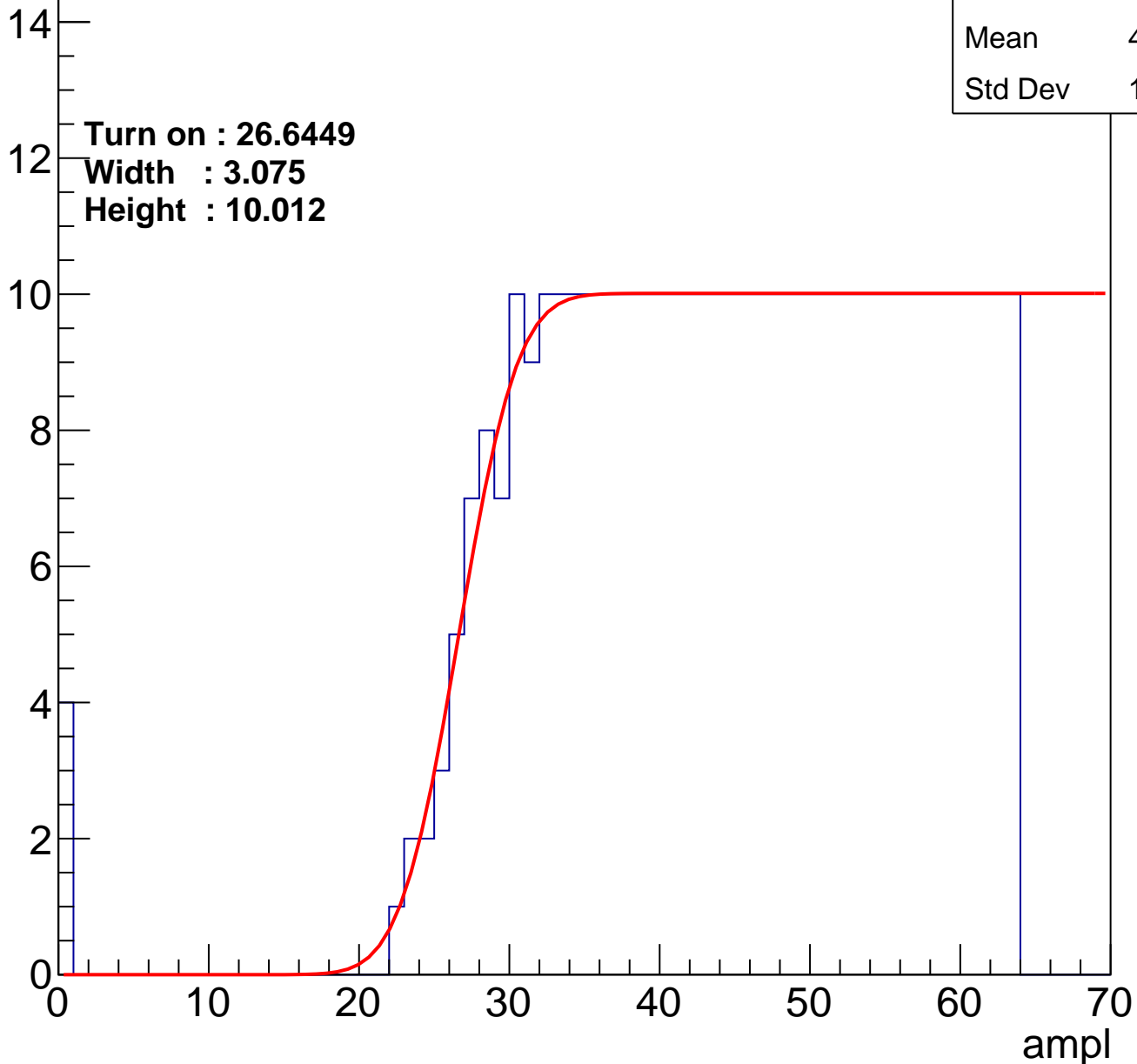
Entries	378
Mean	44.22
Std Dev	11.83

Turn on : 26.6449

Width : 3.075

Height : 10.012

Entry



B1L000S, U11-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.71
Std Dev	12.04

Turn on : 26.0425

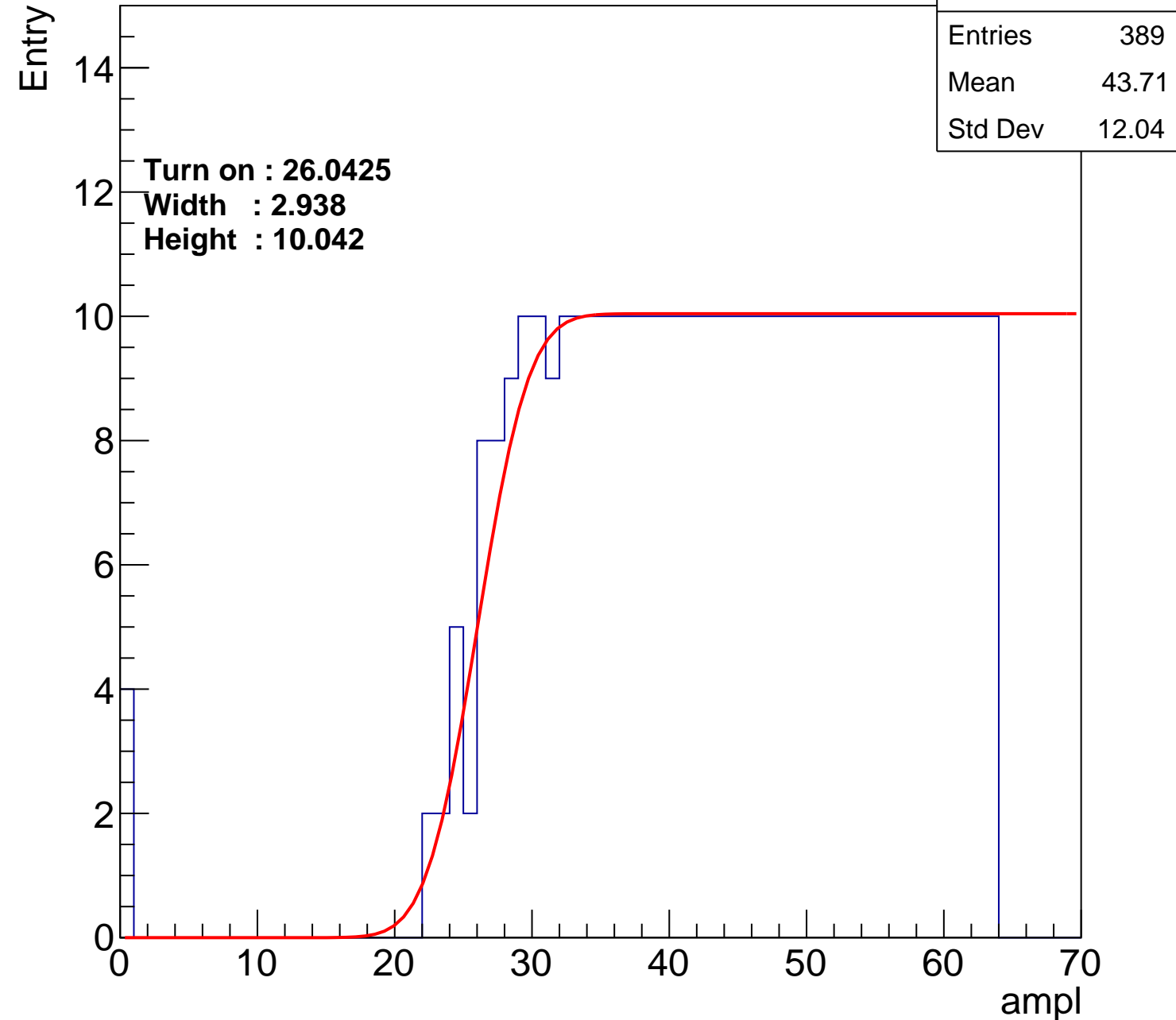
Width : 2.938

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.27
Std Dev	11.82

Turn on : 27.4563

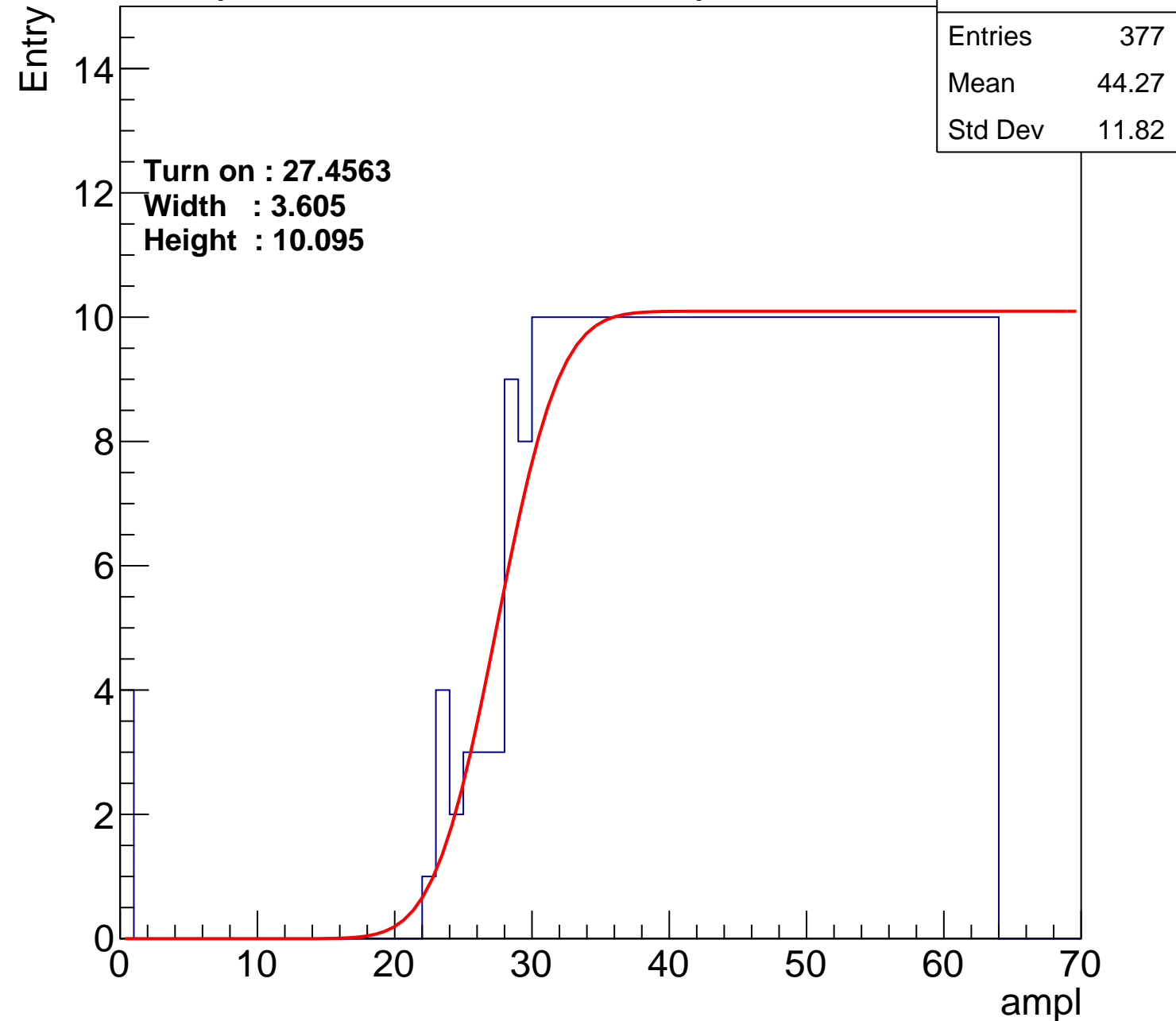
Width : 3.605

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.31
Std Dev	10.81

Turn on : 28.5884

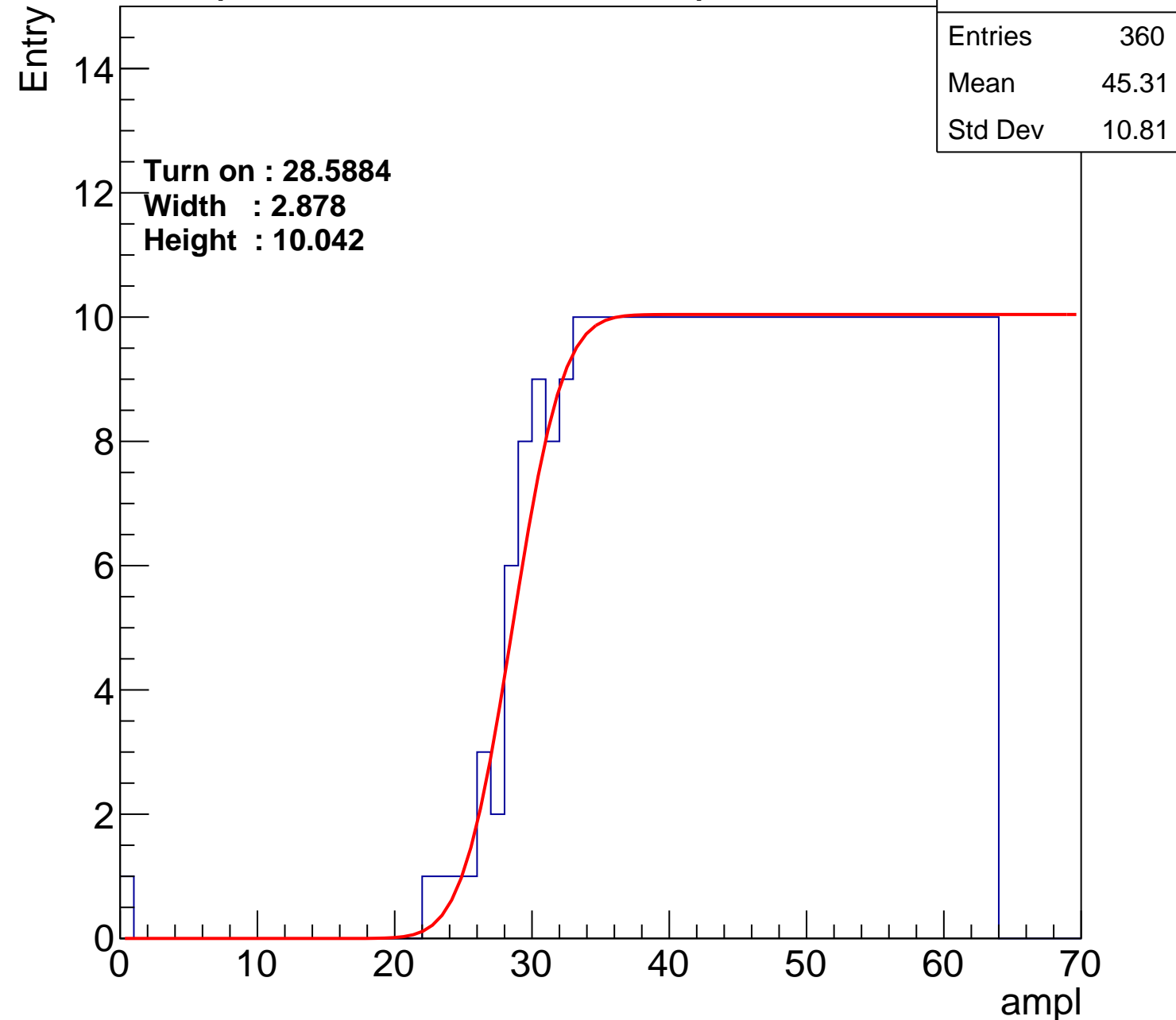
Width : 2.878

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.11
Std Dev	11.66

Turn on : 25.8739

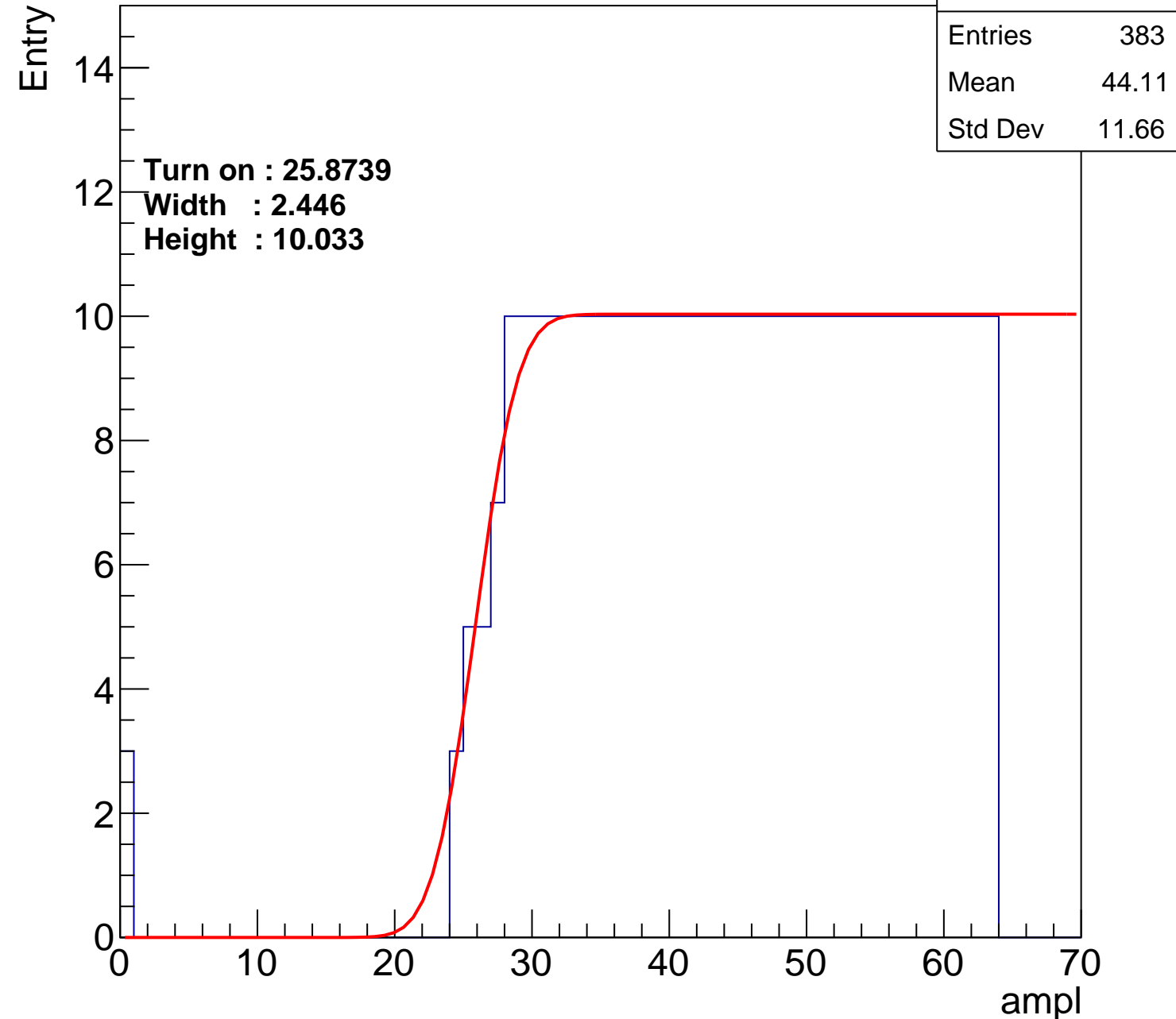
Width : 2.446

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch29

calib_packv5_042523_0143.root, FC#3, port B3

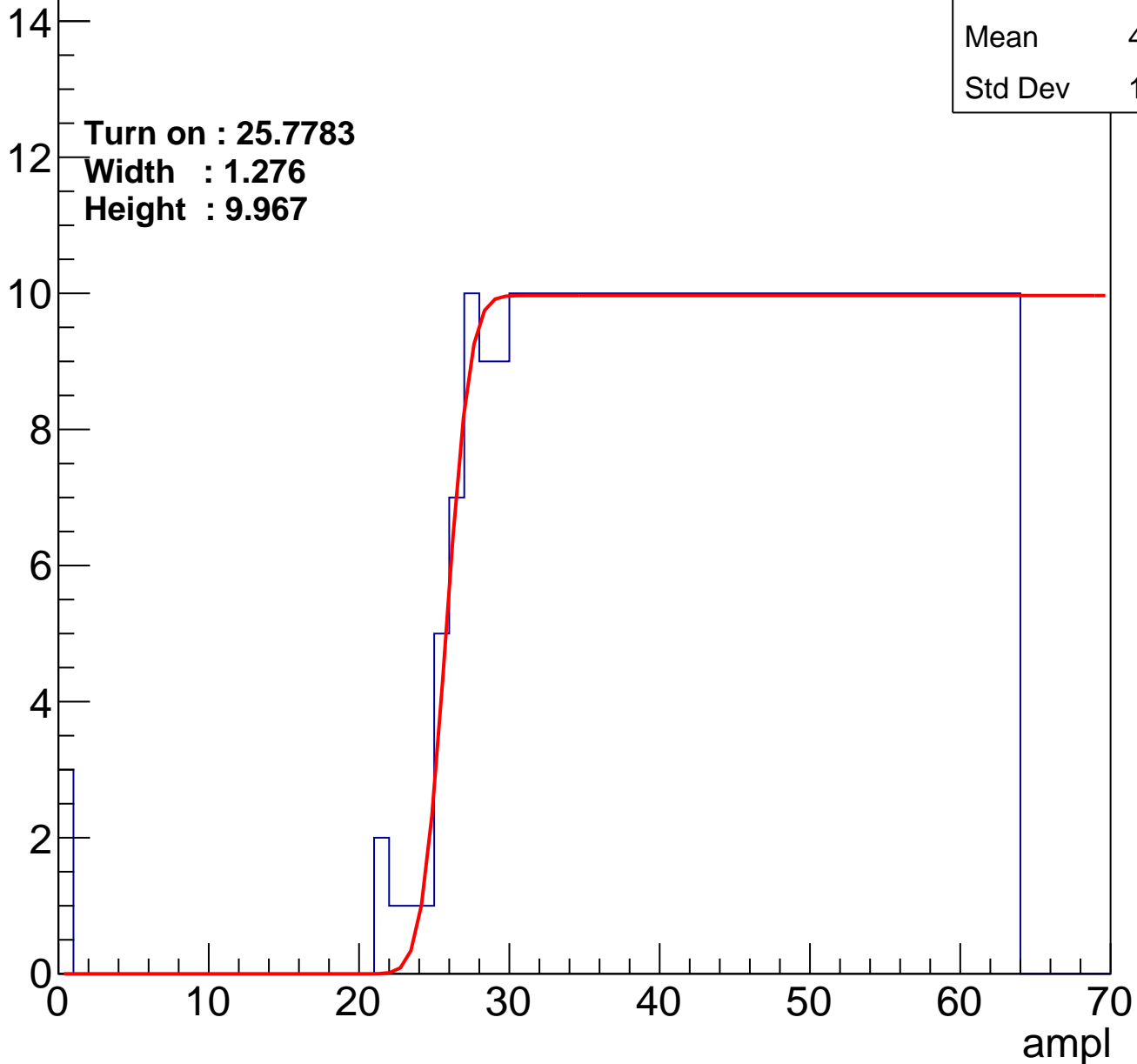
Entries	388
Mean	43.84
Std Dev	11.83

Turn on : 25.7783

Width : 1.276

Height : 9.967

Entry



B1L000S, U11-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.88
Std Dev	11.01

Turn on : 27.0582

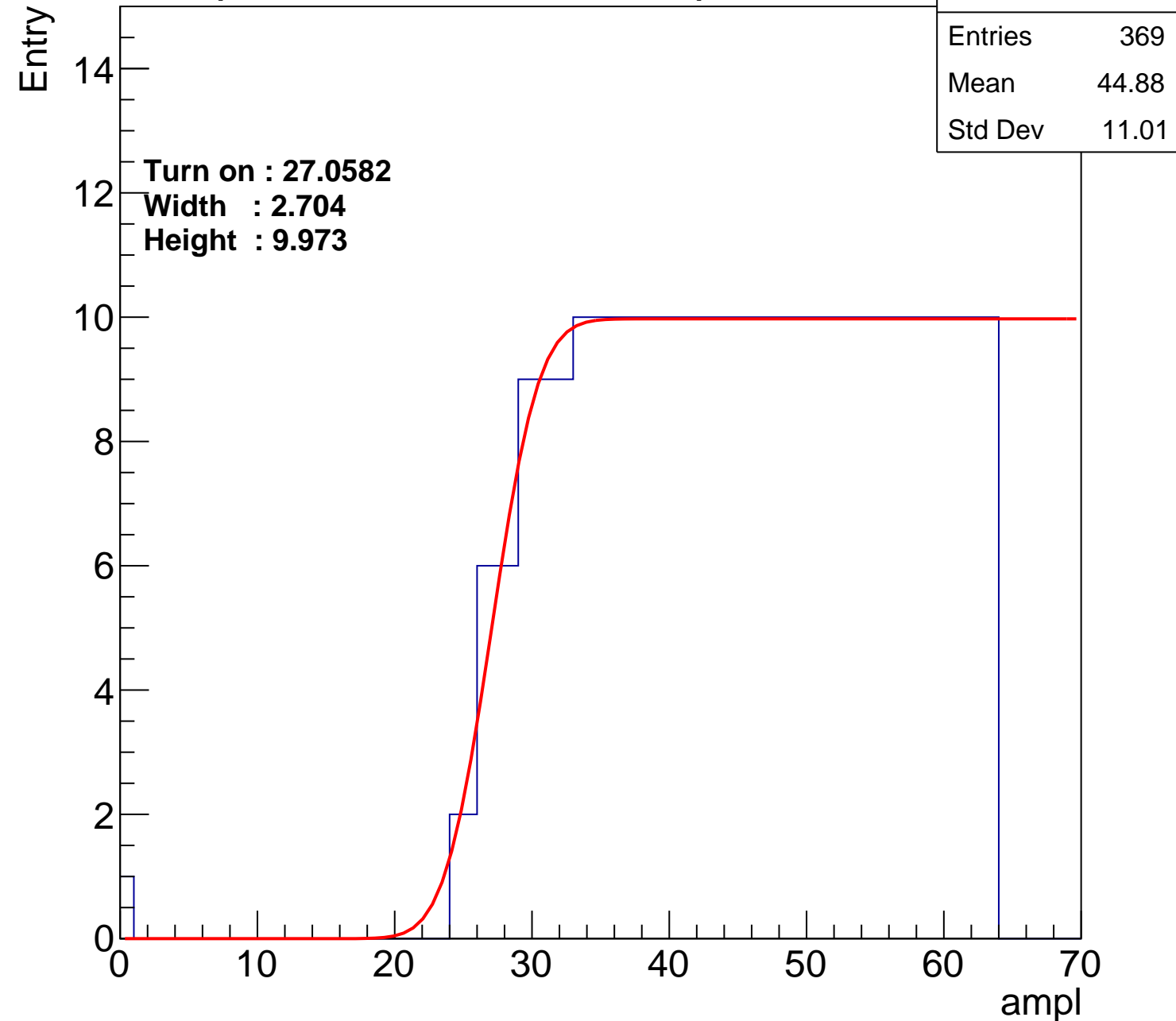
Width : 2.704

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.34
Std Dev	11.44

Turn on : 26.3394

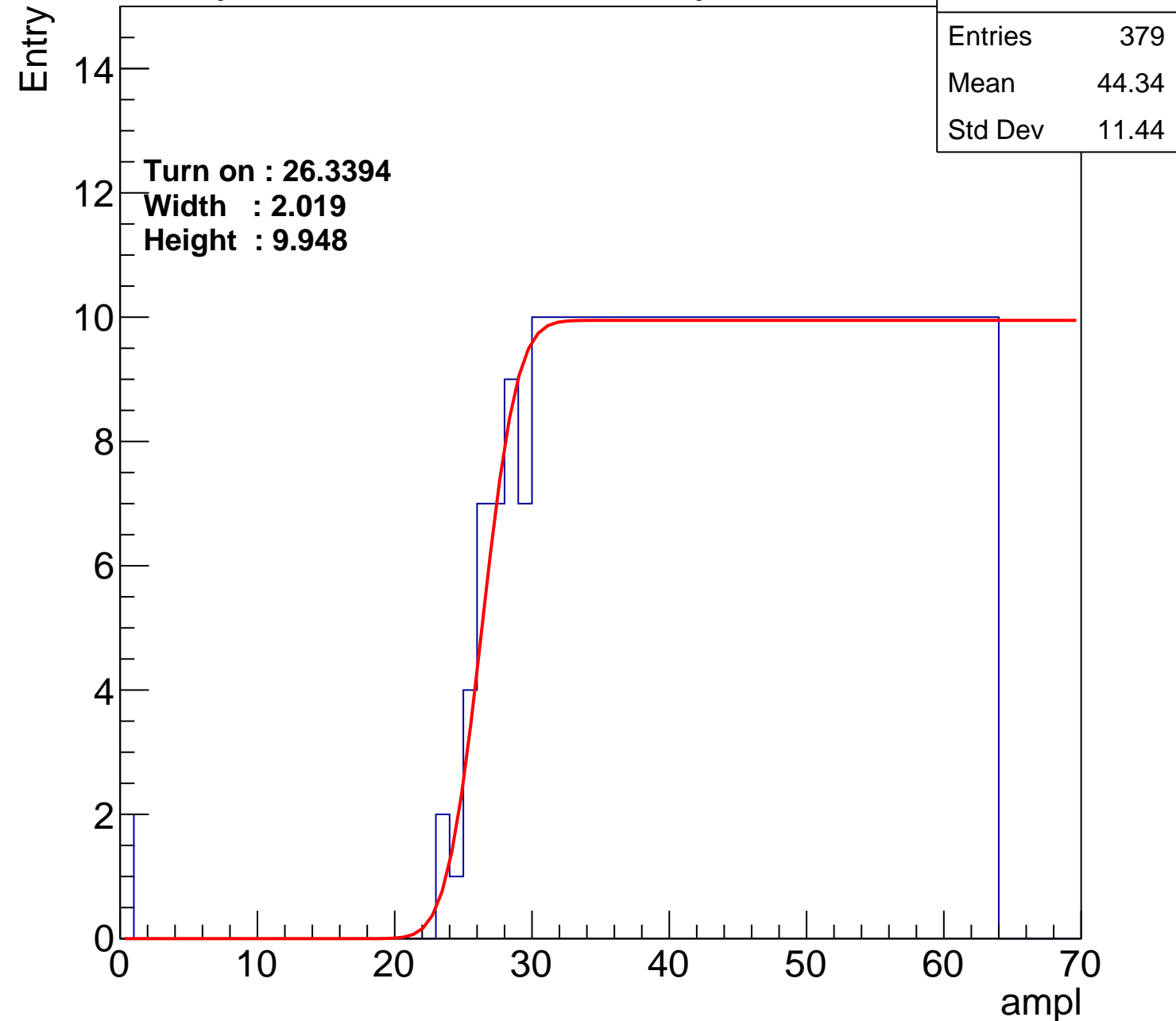
Width : 2.019

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch32

calib_packv5_042523_0143.root, FC#3, port B3

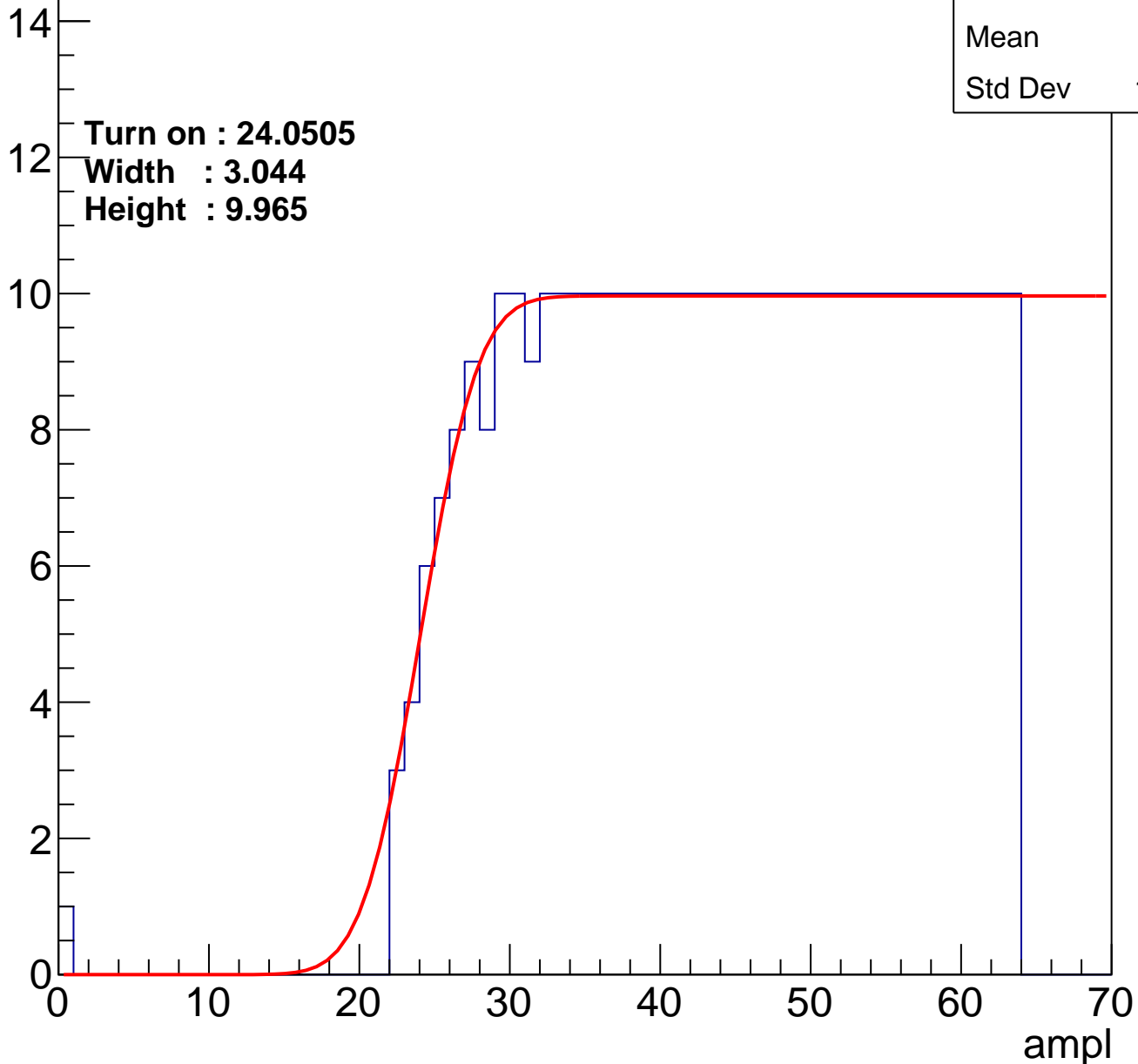
Entries	395
Mean	43.6
Std Dev	11.71

Turn on : 24.0505

Width : 3.044

Height : 9.965

Entry



B1L000S, U11-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.2
Std Dev	10.82

Turn on : 28.3384

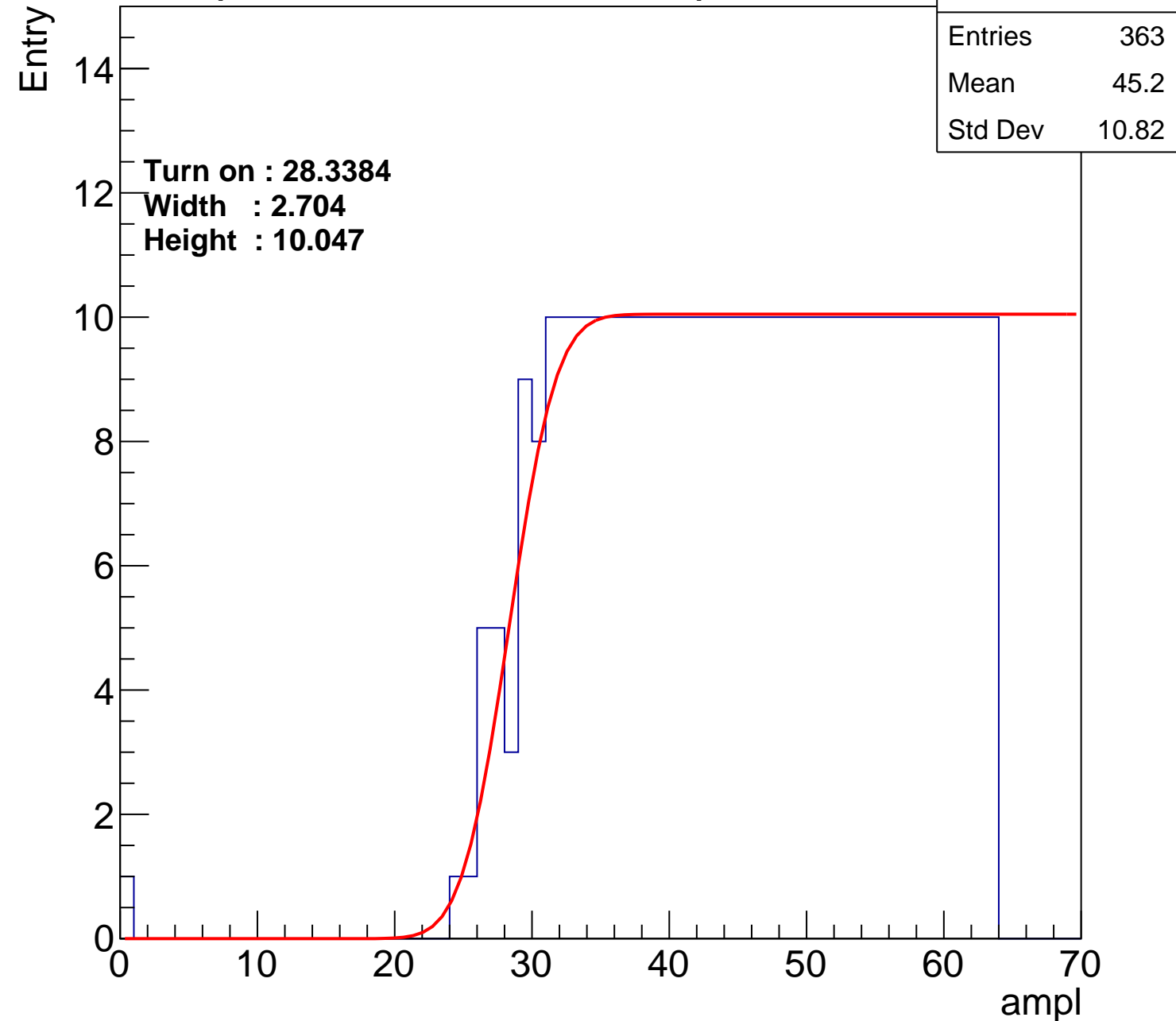
Width : 2.704

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.8307

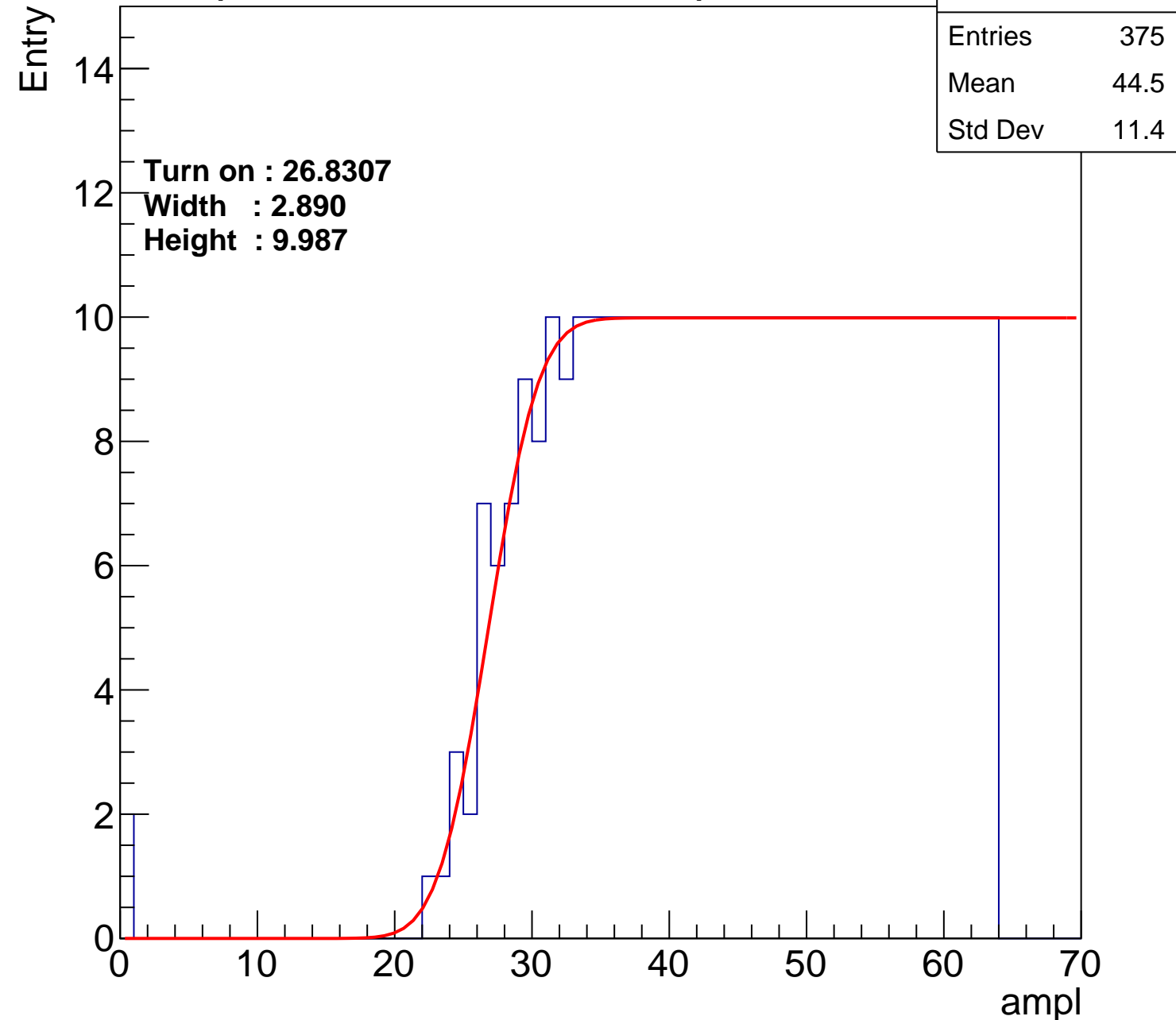
Width : 2.890

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 25.8507

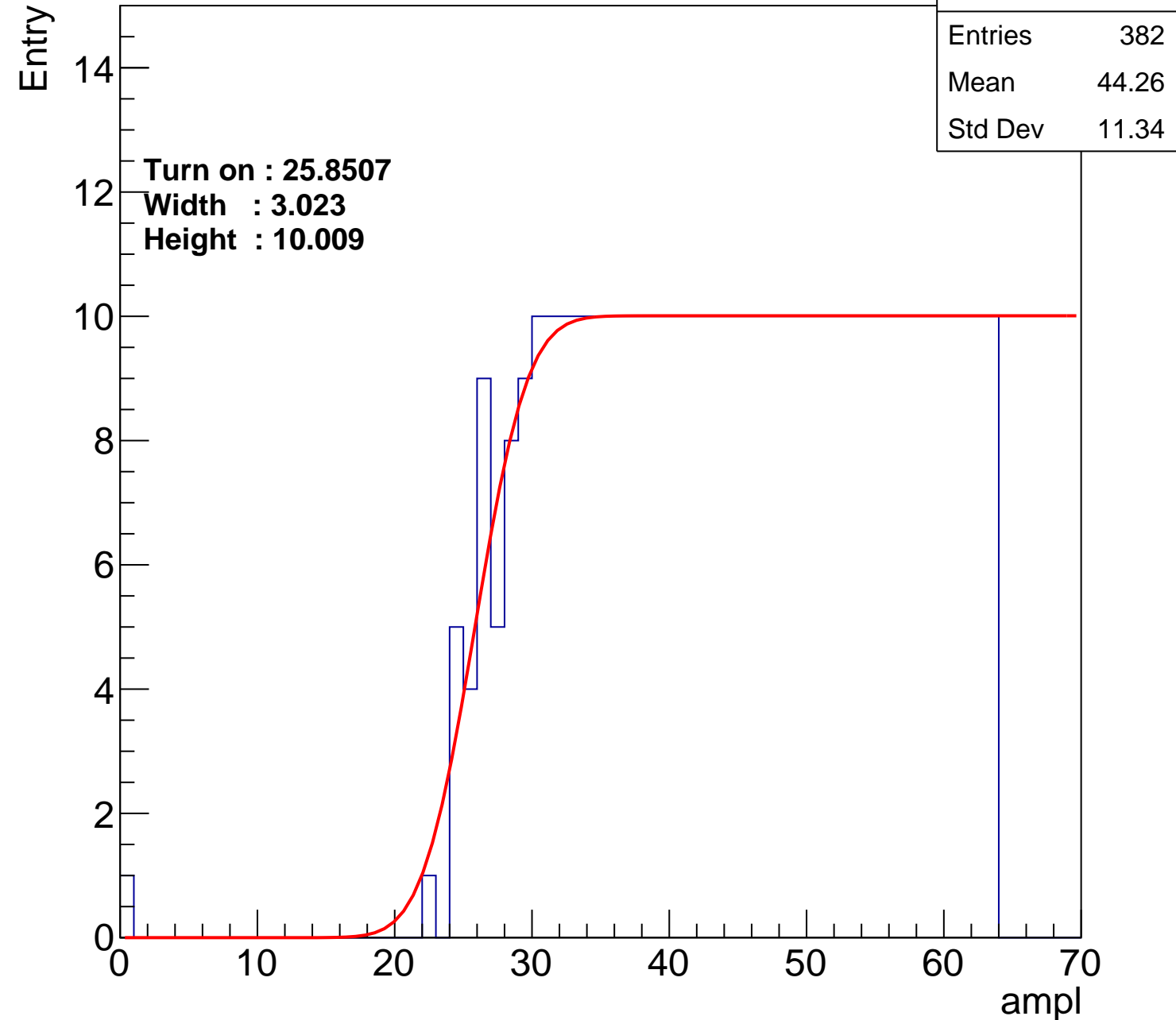
Width : 3.023

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch36

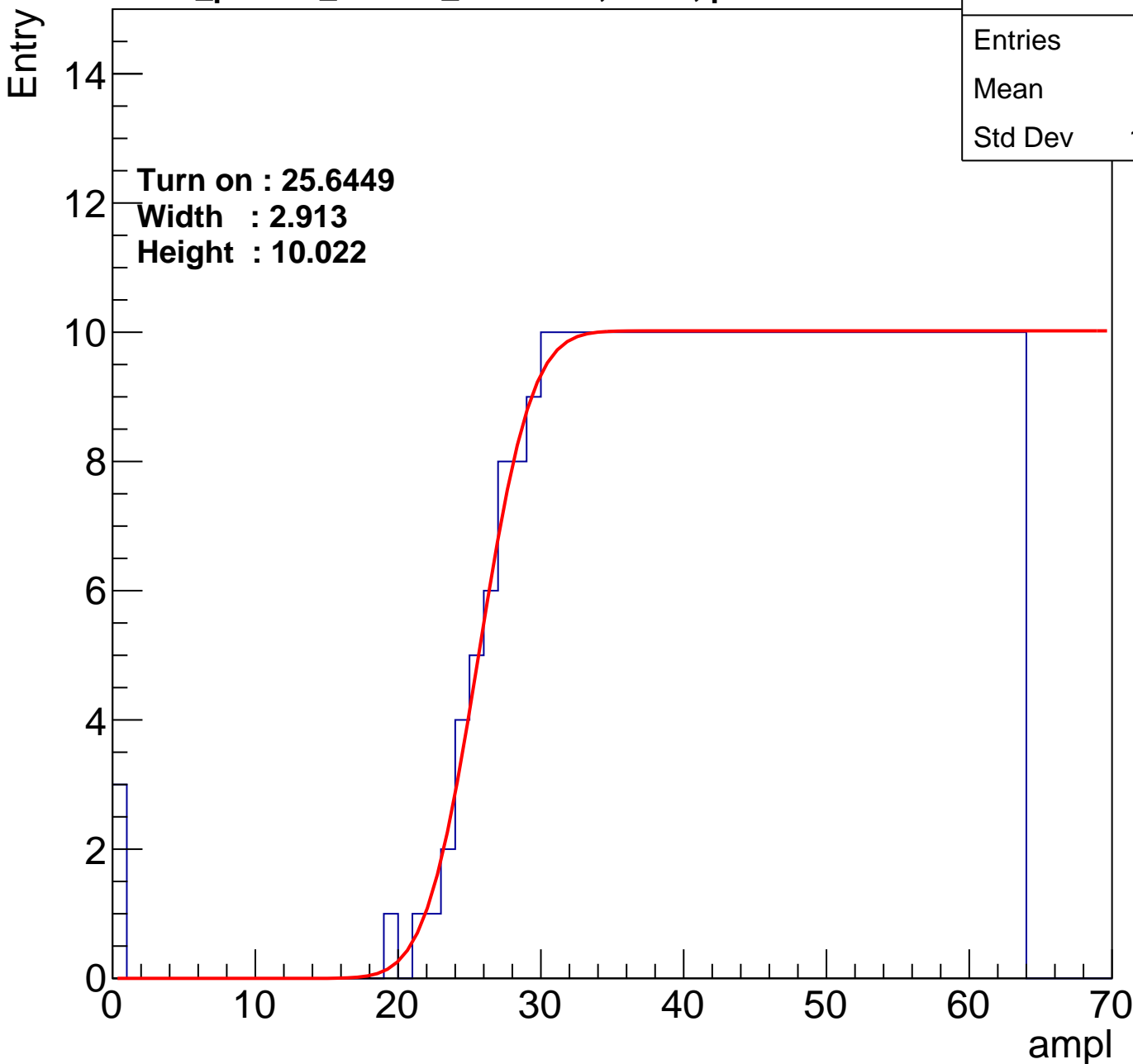
calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.8
Std Dev	11.89

Turn on : 25.6449

Width : 2.913

Height : 10.022



B1L000S, U11-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.05
Std Dev	11.72

Turn on : 26.0291

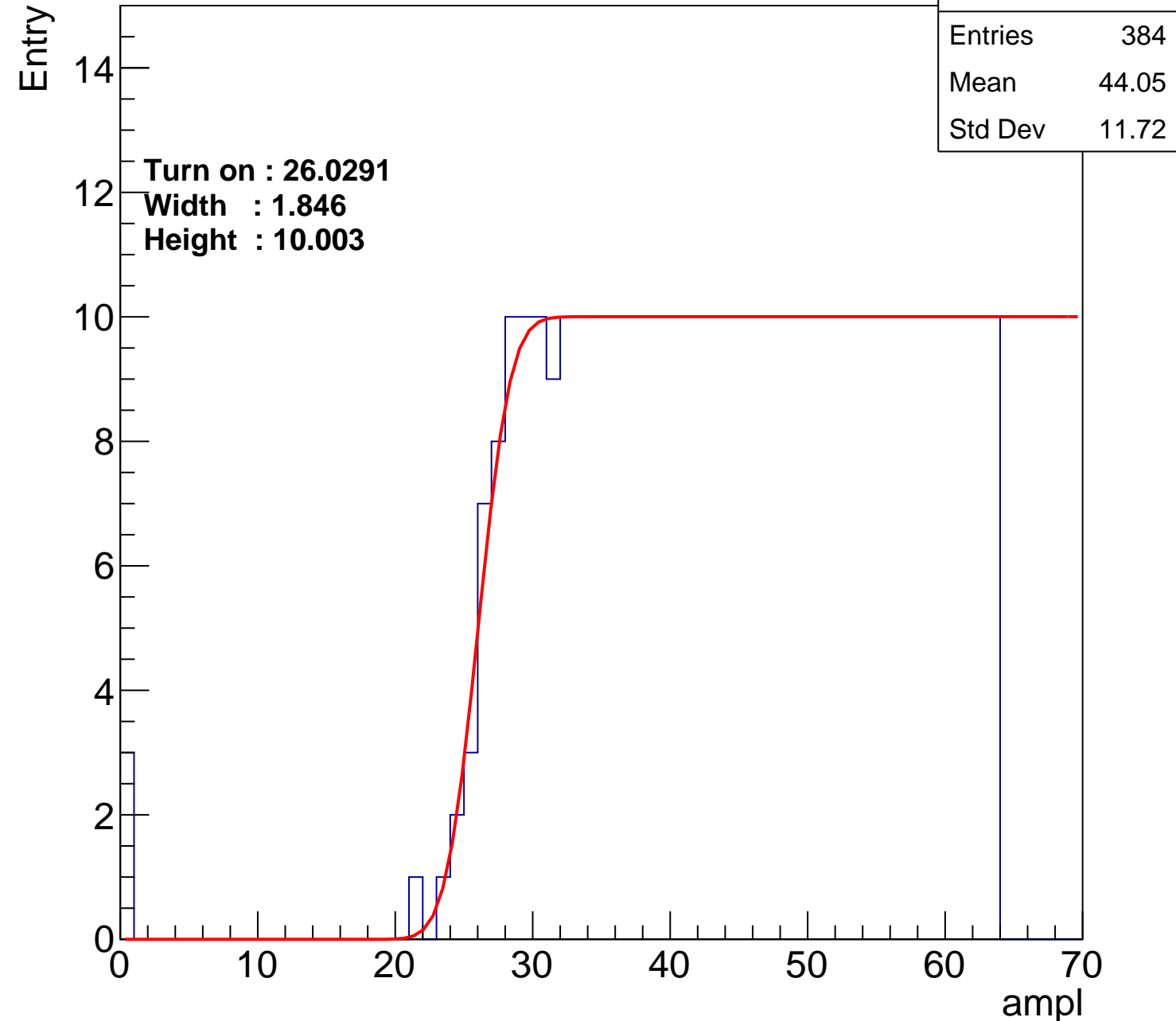
Width : 1.846

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.32
Std Dev	11.35

Turn on : 26.6230

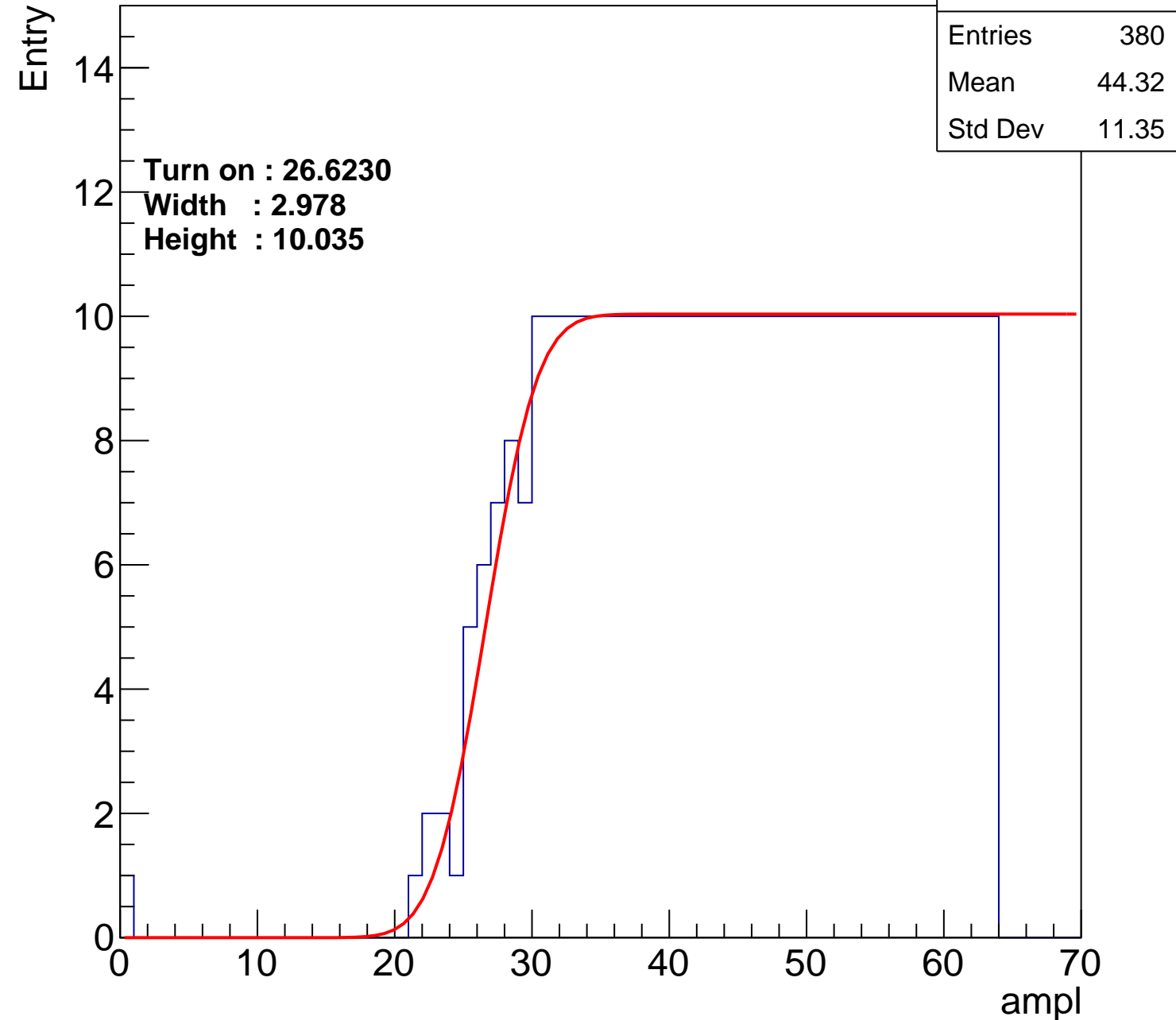
Width : 2.978

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.6409

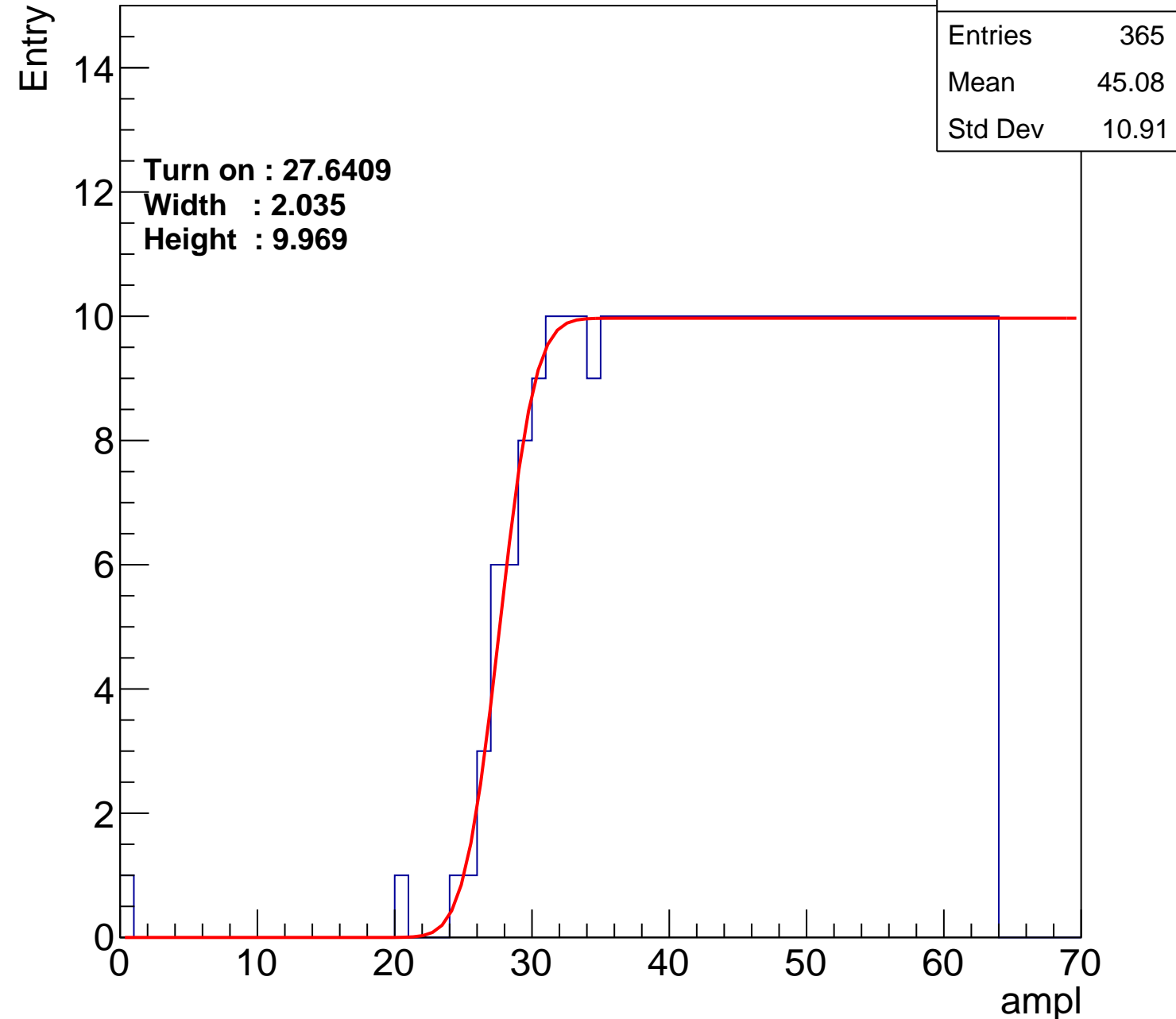
Width : 2.035

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch40

calib_packv5_042523_0143.root, FC#3, port B3

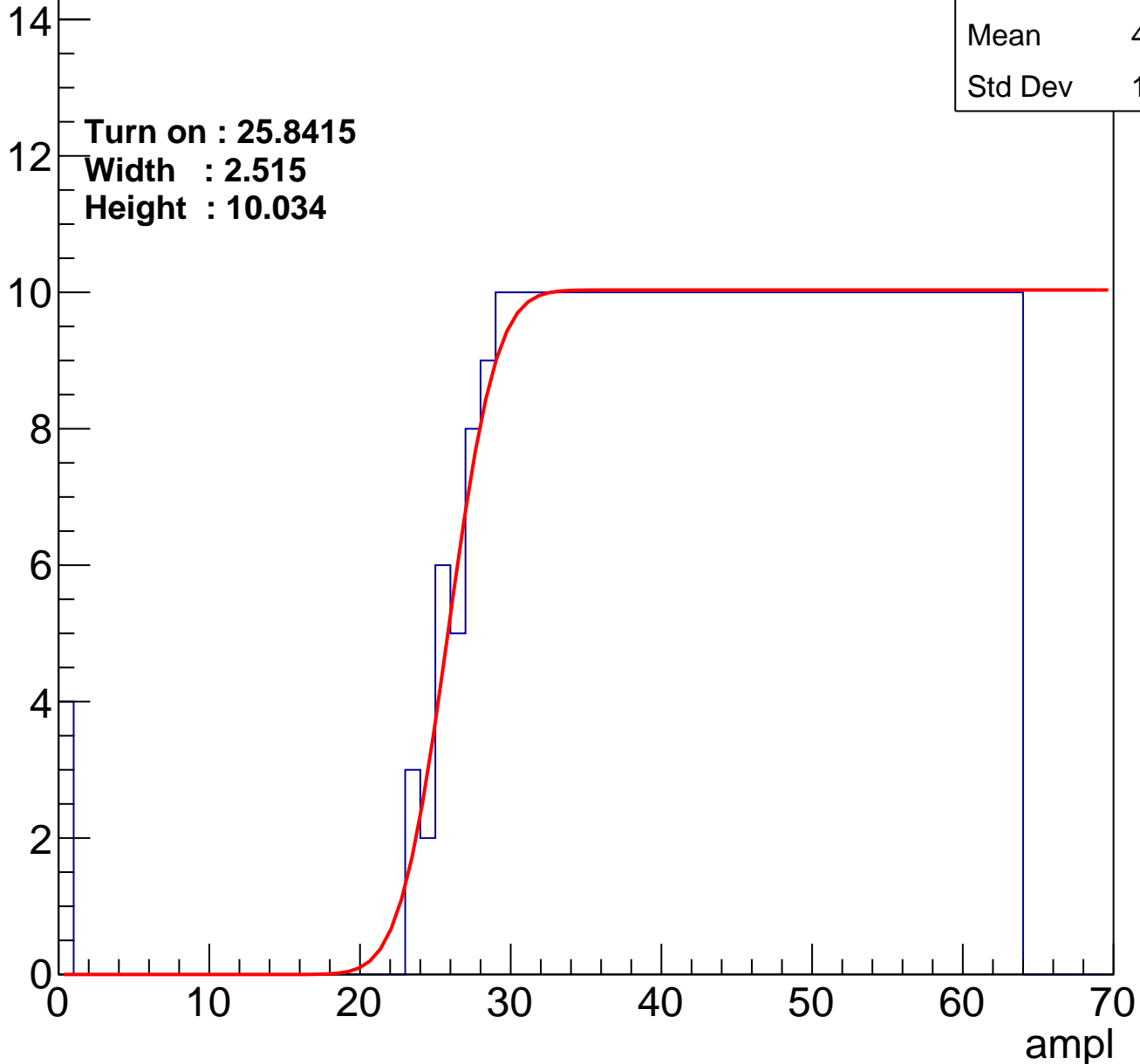
Entries	387
Mean	43.84
Std Dev	11.96

Turn on : 25.8415

Width : 2.515

Height : 10.034

Entry



B1L000S, U11-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	408
Mean	42.89
Std Dev	12.23

Turn on : 23.9044

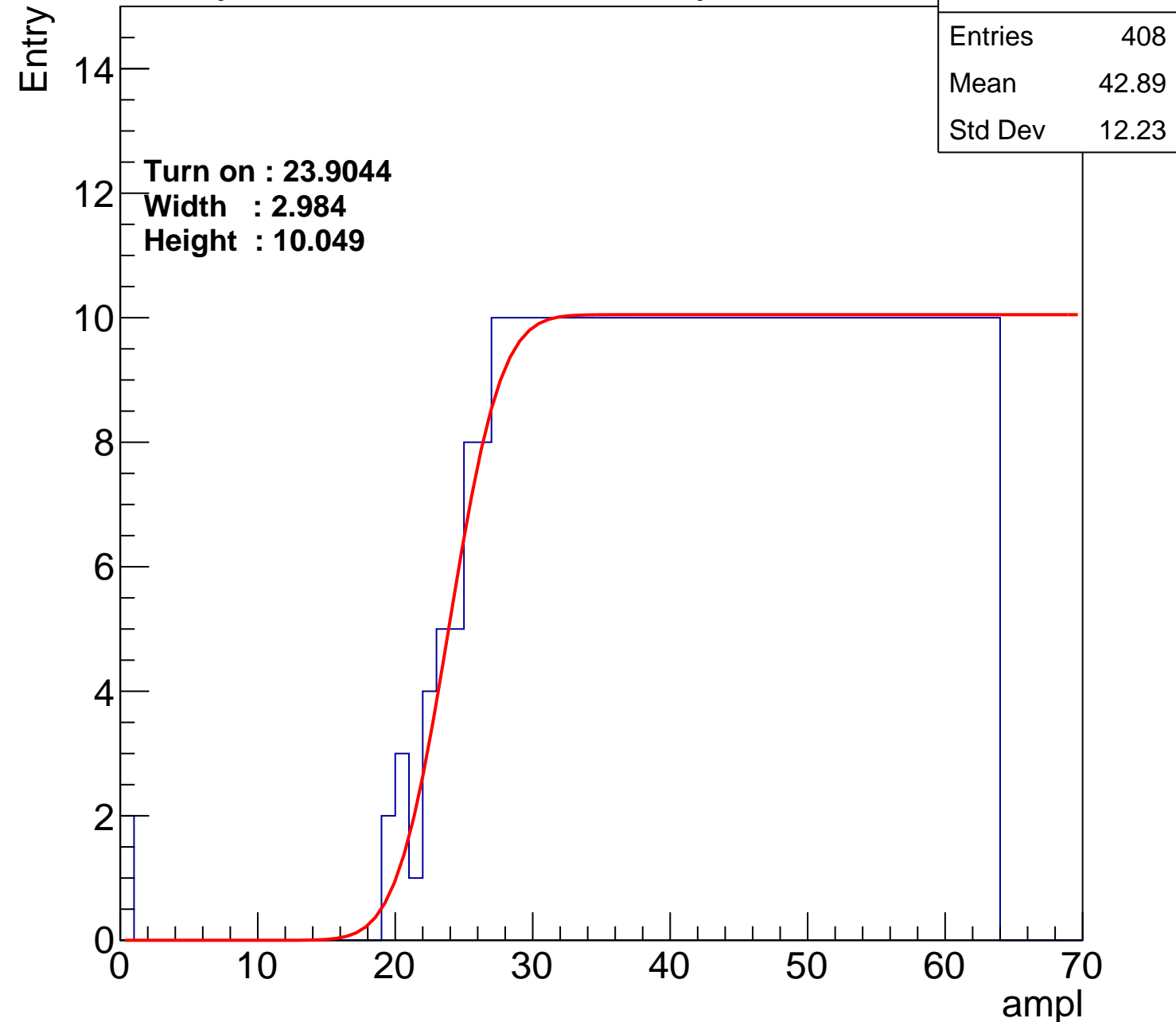
Width : 2.984

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.8
Std Dev	12.02

Turn on : 25.8920

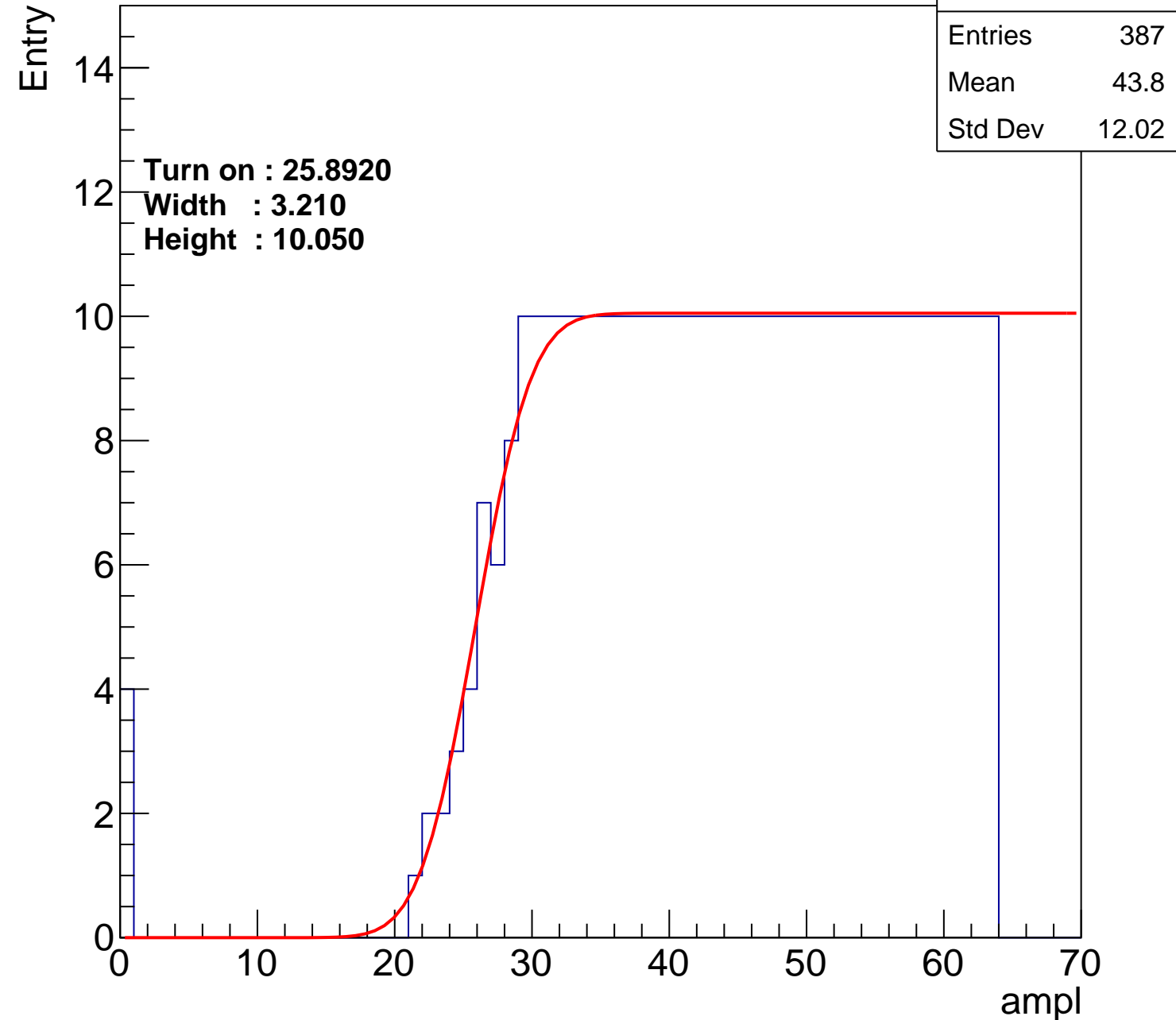
Width : 3.210

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.56
Std Dev	11.54

Turn on : 27.6594

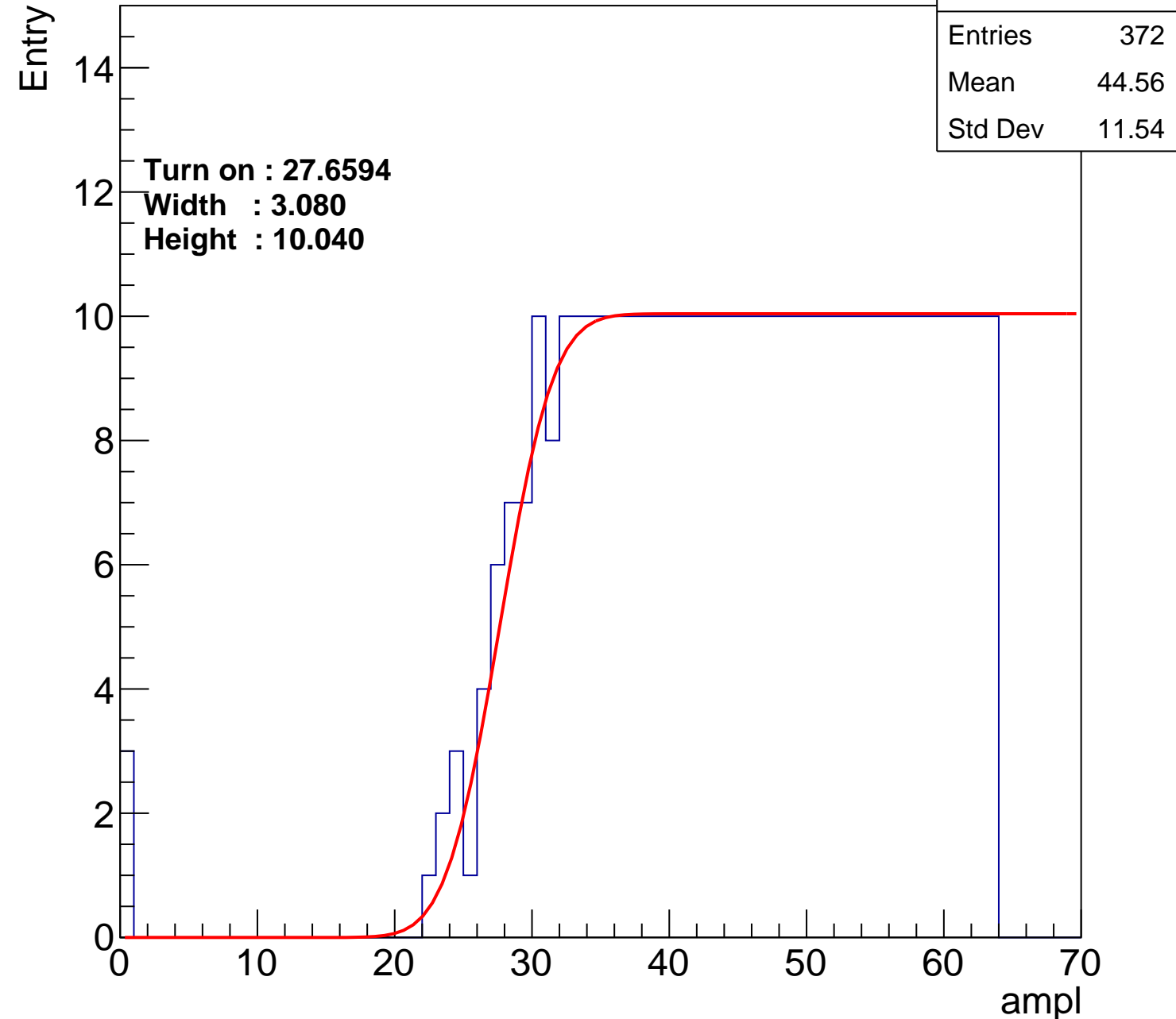
Width : 3.080

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch44

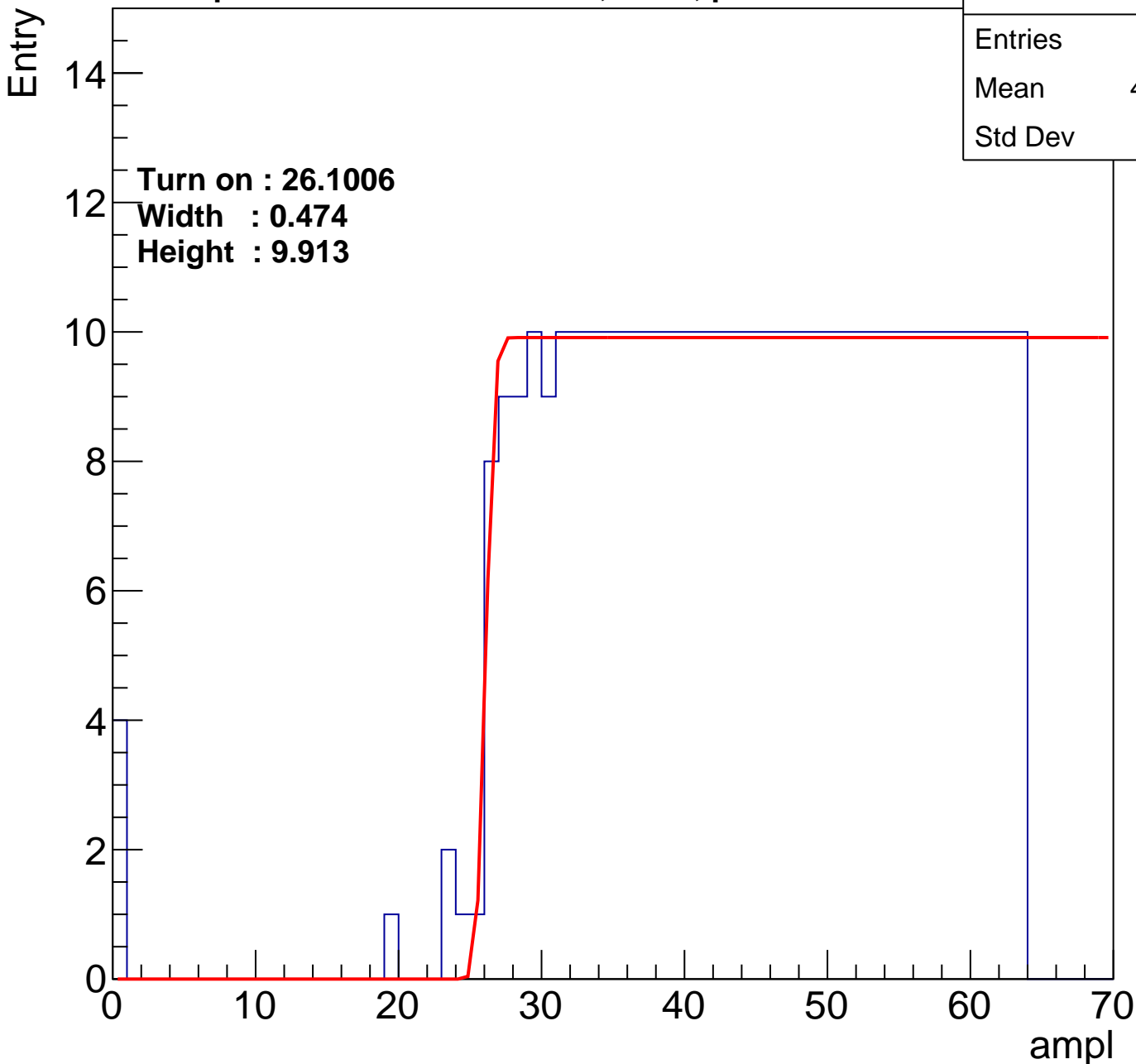
calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.98
Std Dev	11.9

Turn on : 26.1006

Width : 0.474

Height : 9.913



B1L000S, U11-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	350
Mean	45.63
Std Dev	11.04

Turn on : 30.0836

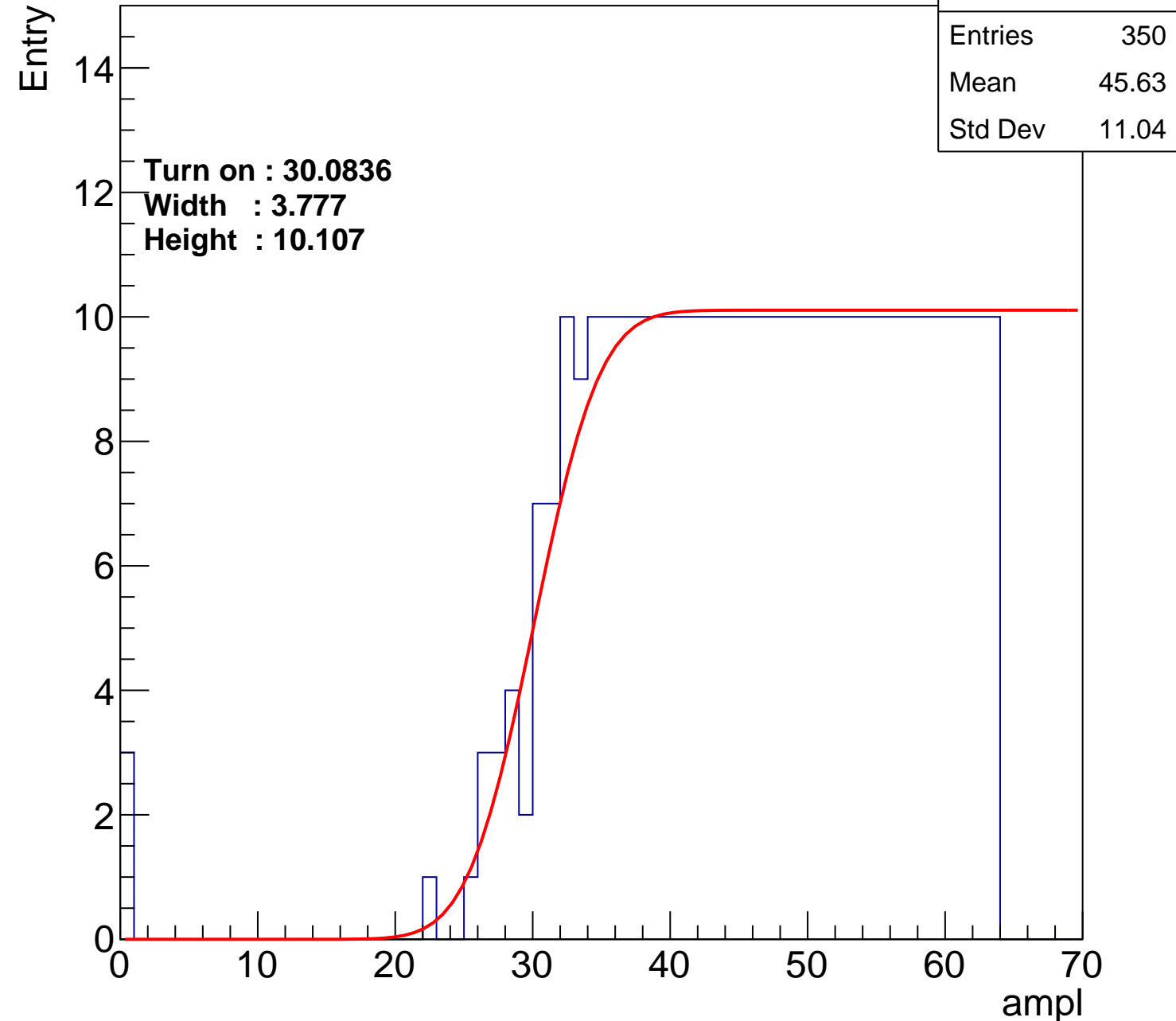
Width : 3.777

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch46

calib_packv5_042523_0143.root, FC#3, port B3

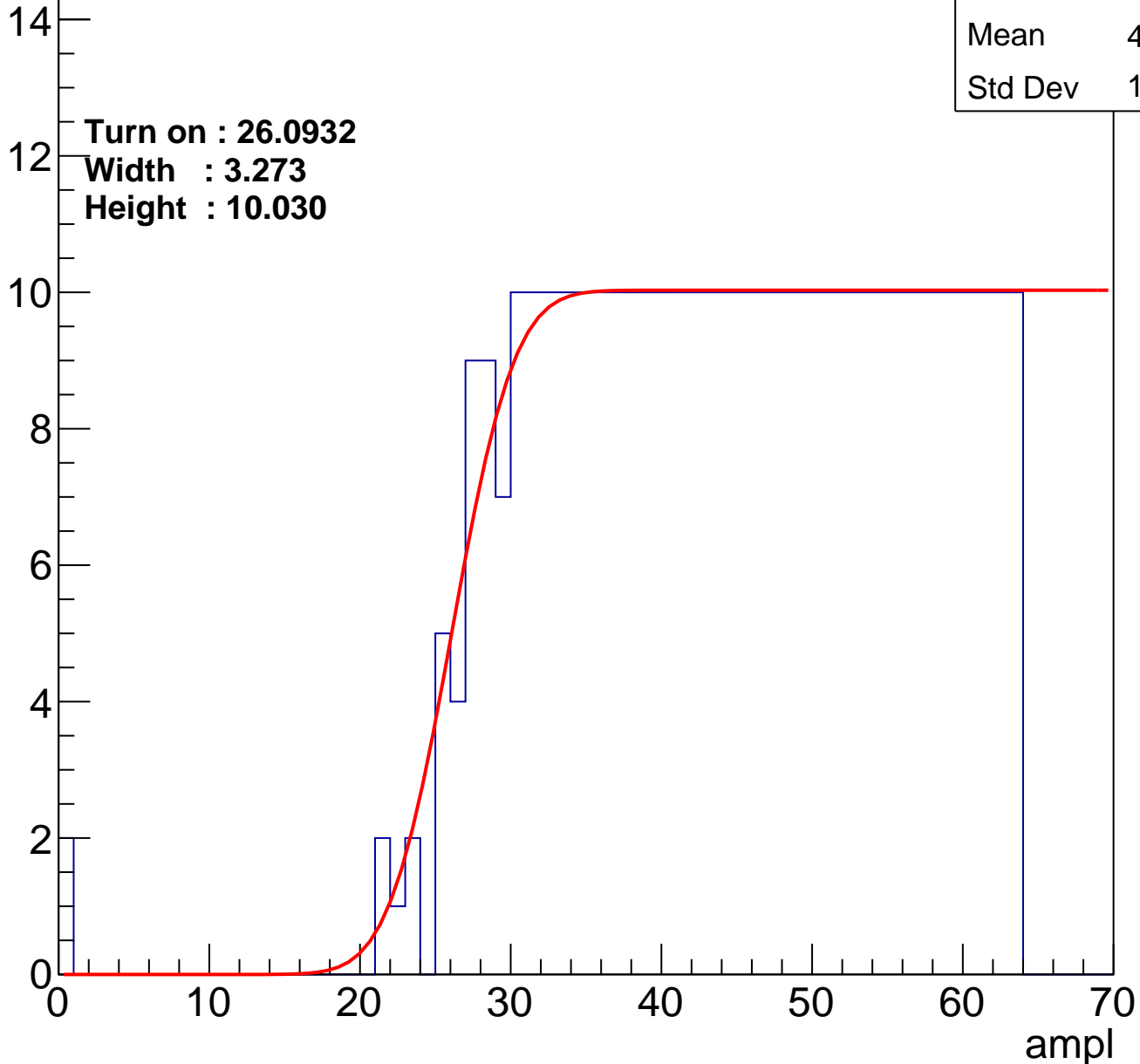
Entries	381
Mean	44.22
Std Dev	11.54

Turn on : 26.0932

Width : 3.273

Height : 10.030

Entry



B1L000S, U11-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.66
Std Dev	11.44

Turn on : 27.1415

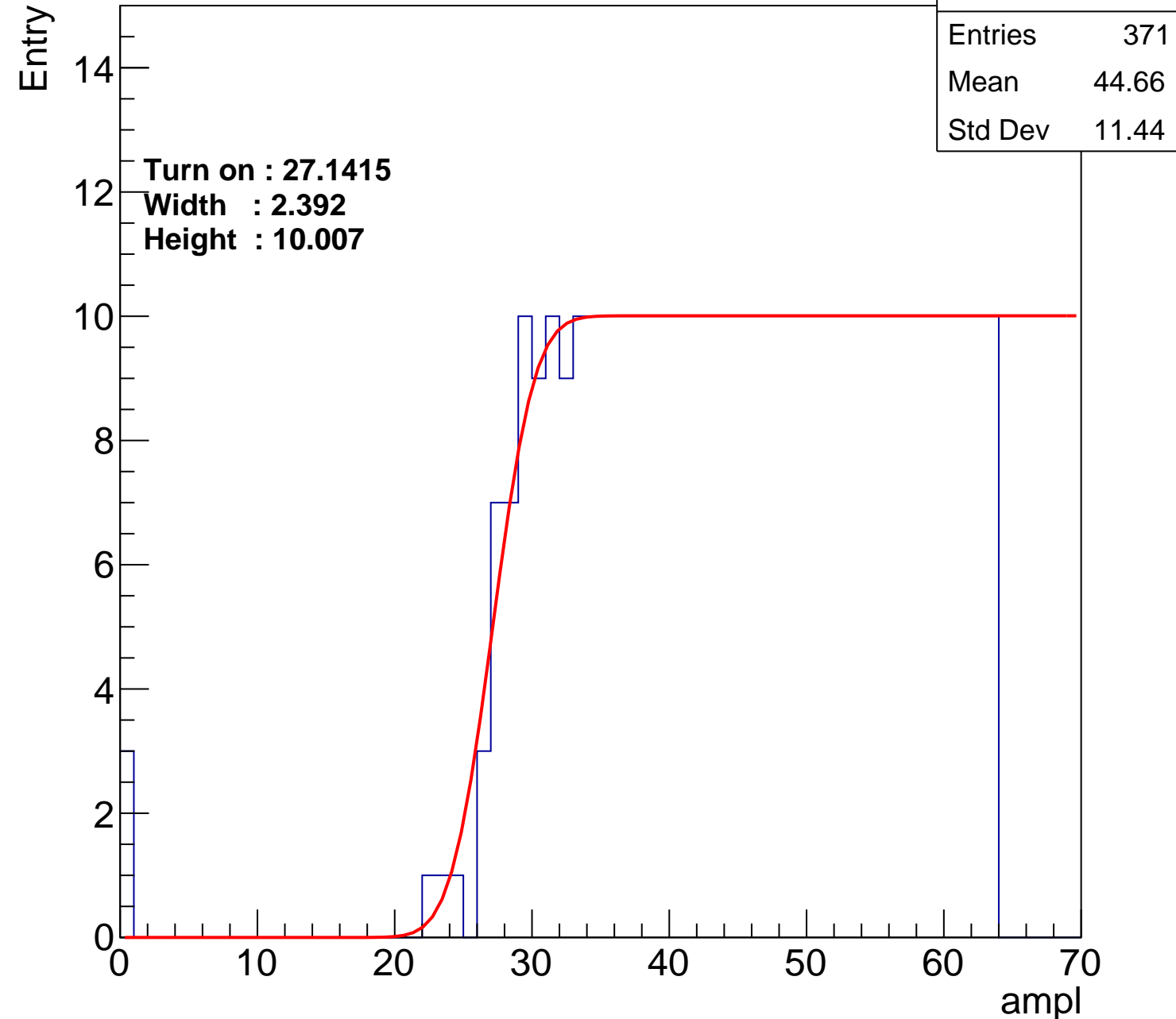
Width : 2.392

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.33
Std Dev	11.59

Turn on : 26.2995

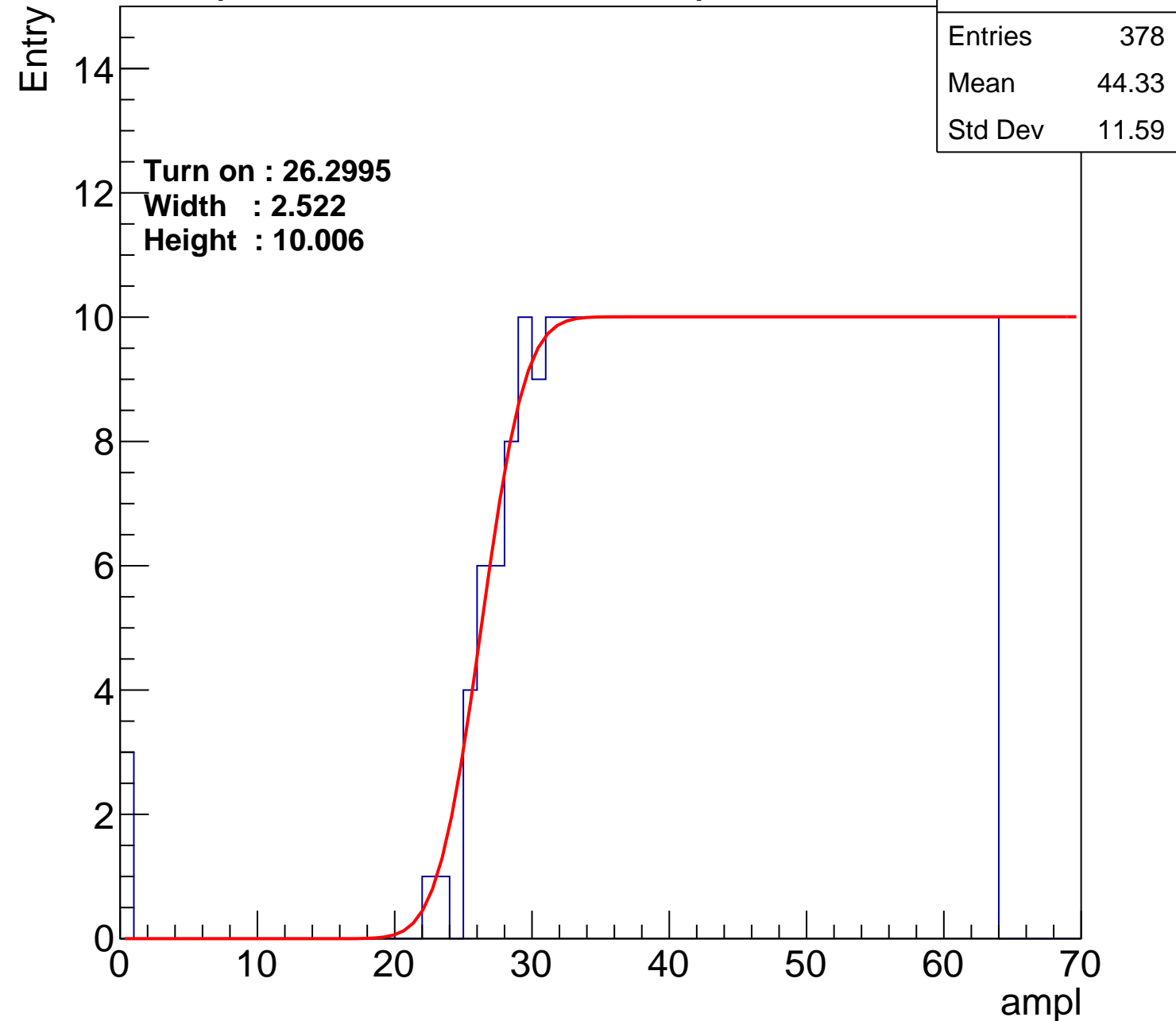
Width : 2.522

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch49

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.12
Std Dev	11.04

Turn on : 27.8306

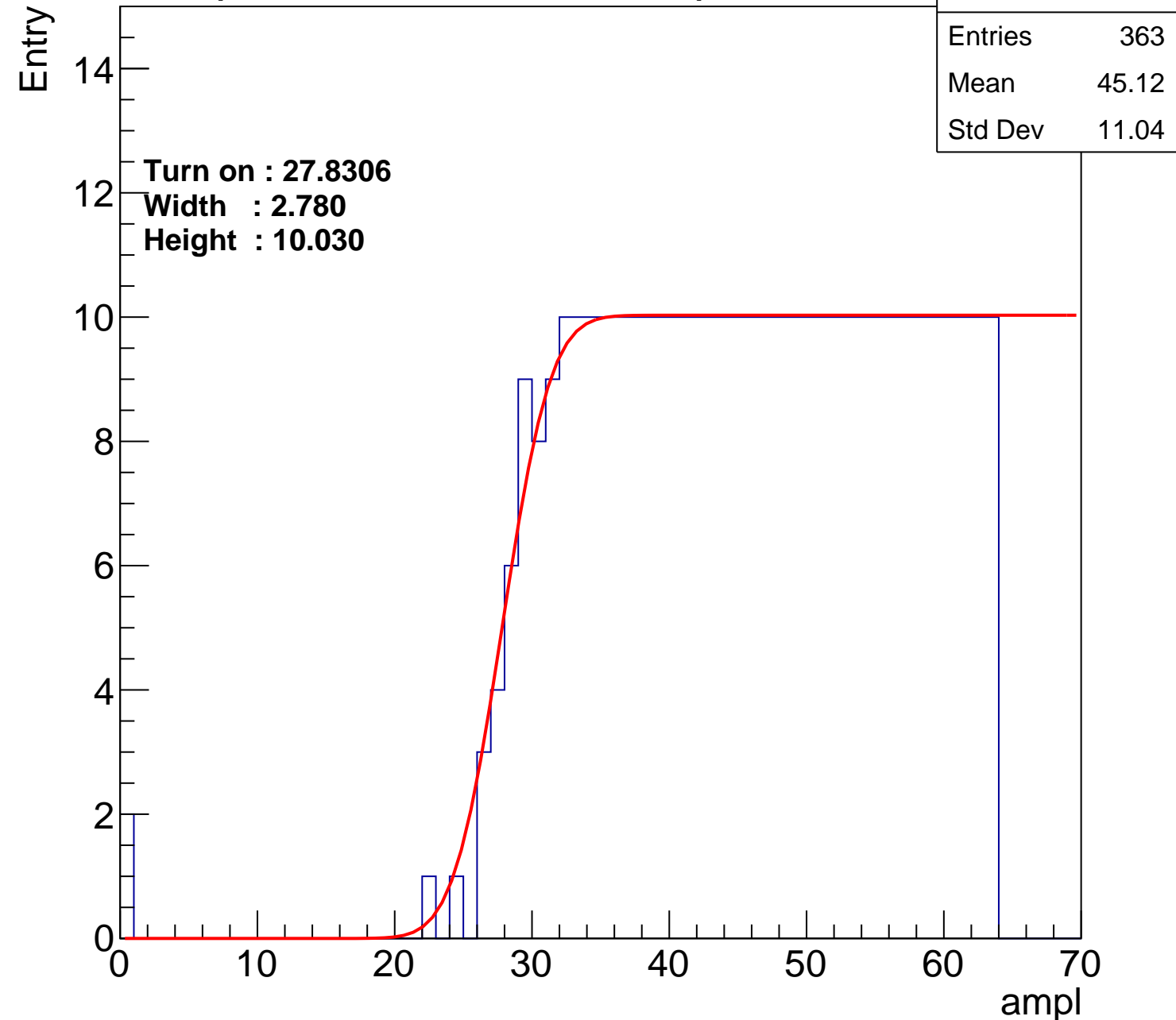
Width : 2.780

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch50

calib_packv5_042523_0143.root, FC#3, port B3

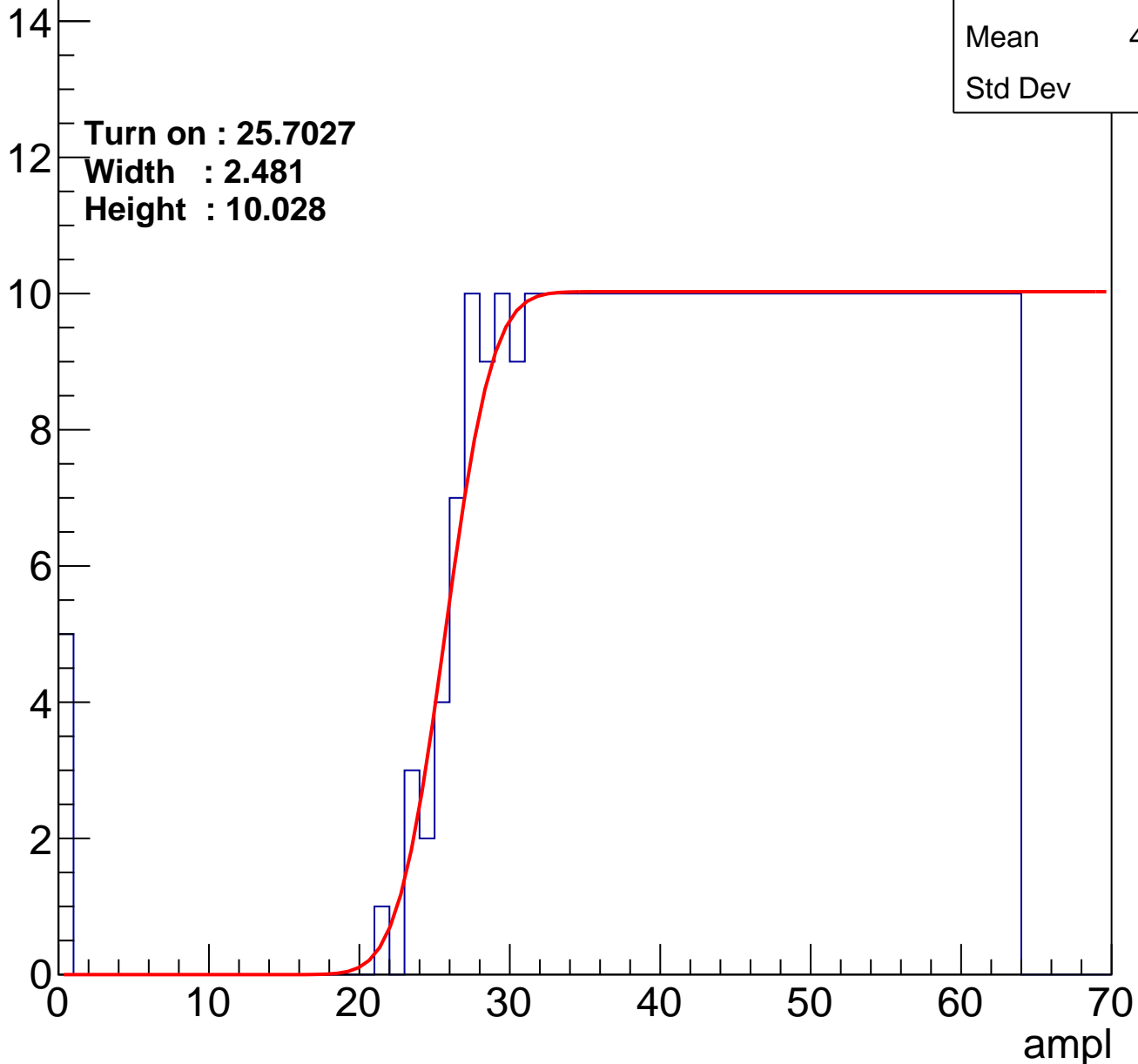
Entries	390
Mean	43.62
Std Dev	12.2

Turn on : 25.7027

Width : 2.481

Height : 10.028

Entry



B1L000S, U11-ch51

calib_packv5_042523_0143.root, FC#3, port B3

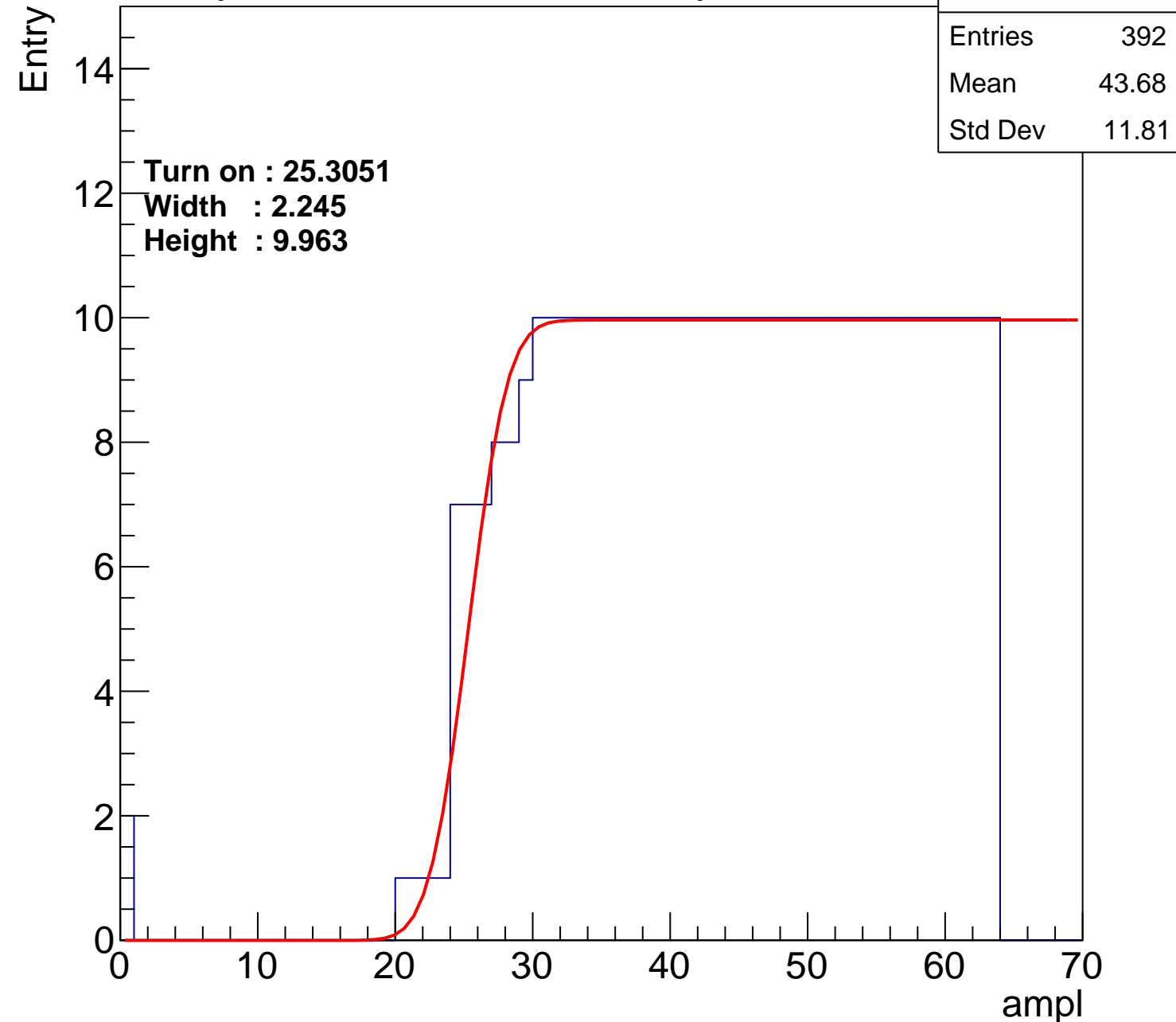
Entry

14
12
10
8
6
4
2
0

Turn on : 25.3051
Width : 2.245
Height : 9.963

Entries	392
Mean	43.68
Std Dev	11.81

ampl



B1L000S, U11-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.26
Std Dev	11.46

Turn on : 26.3309

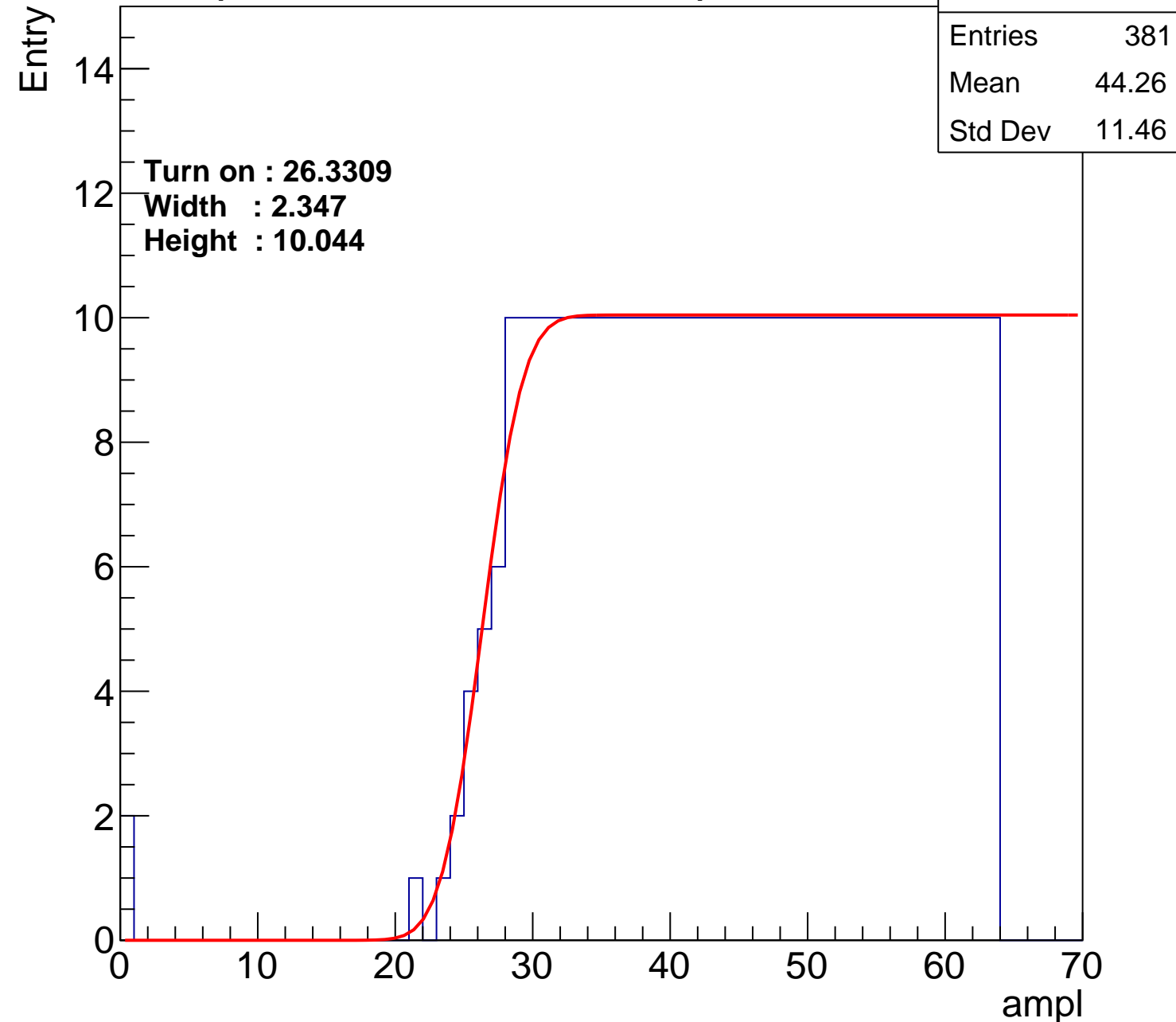
Width : 2.347

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.78
Std Dev	11.23

Turn on : 27.4666

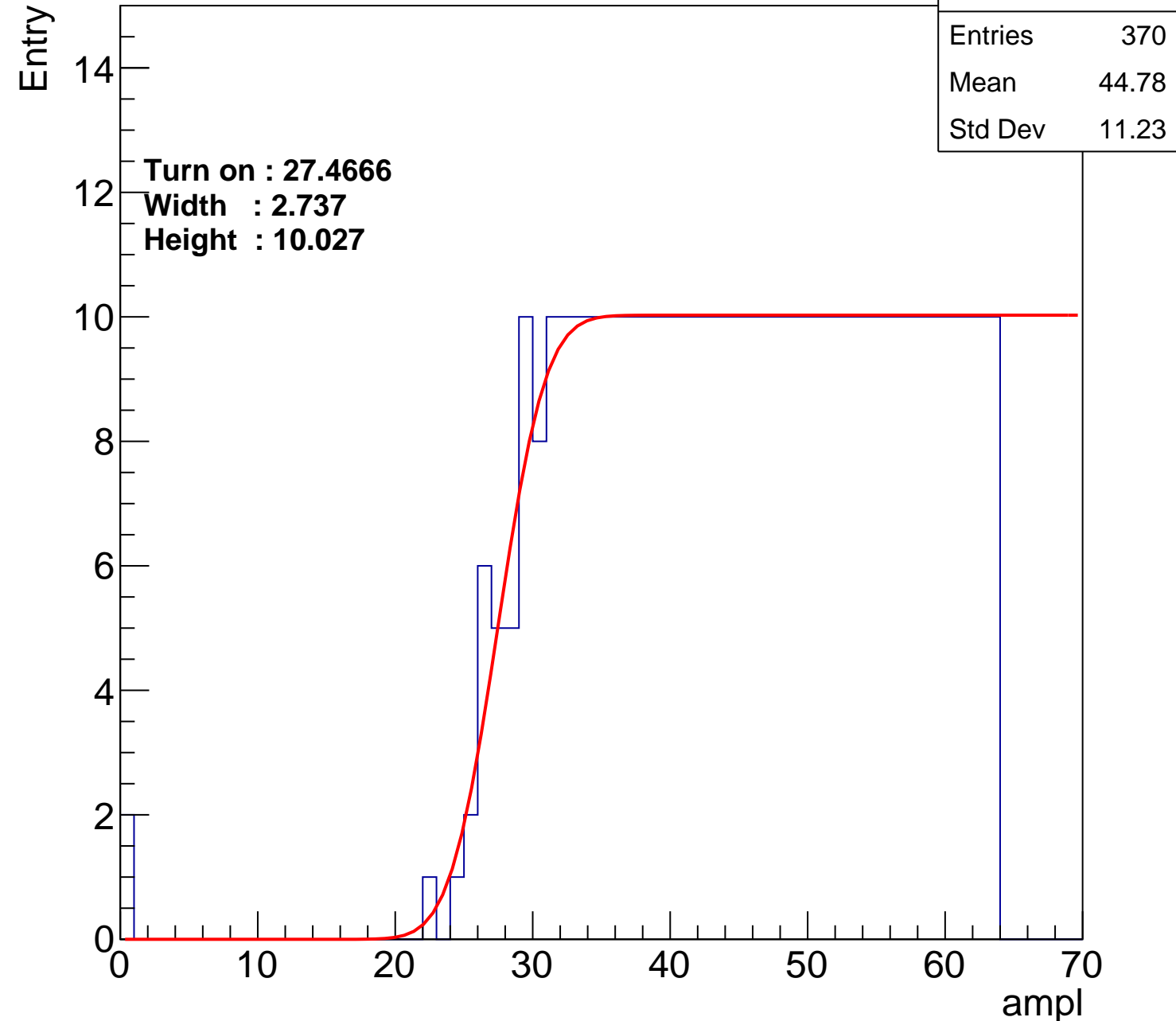
Width : 2.737

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.4026

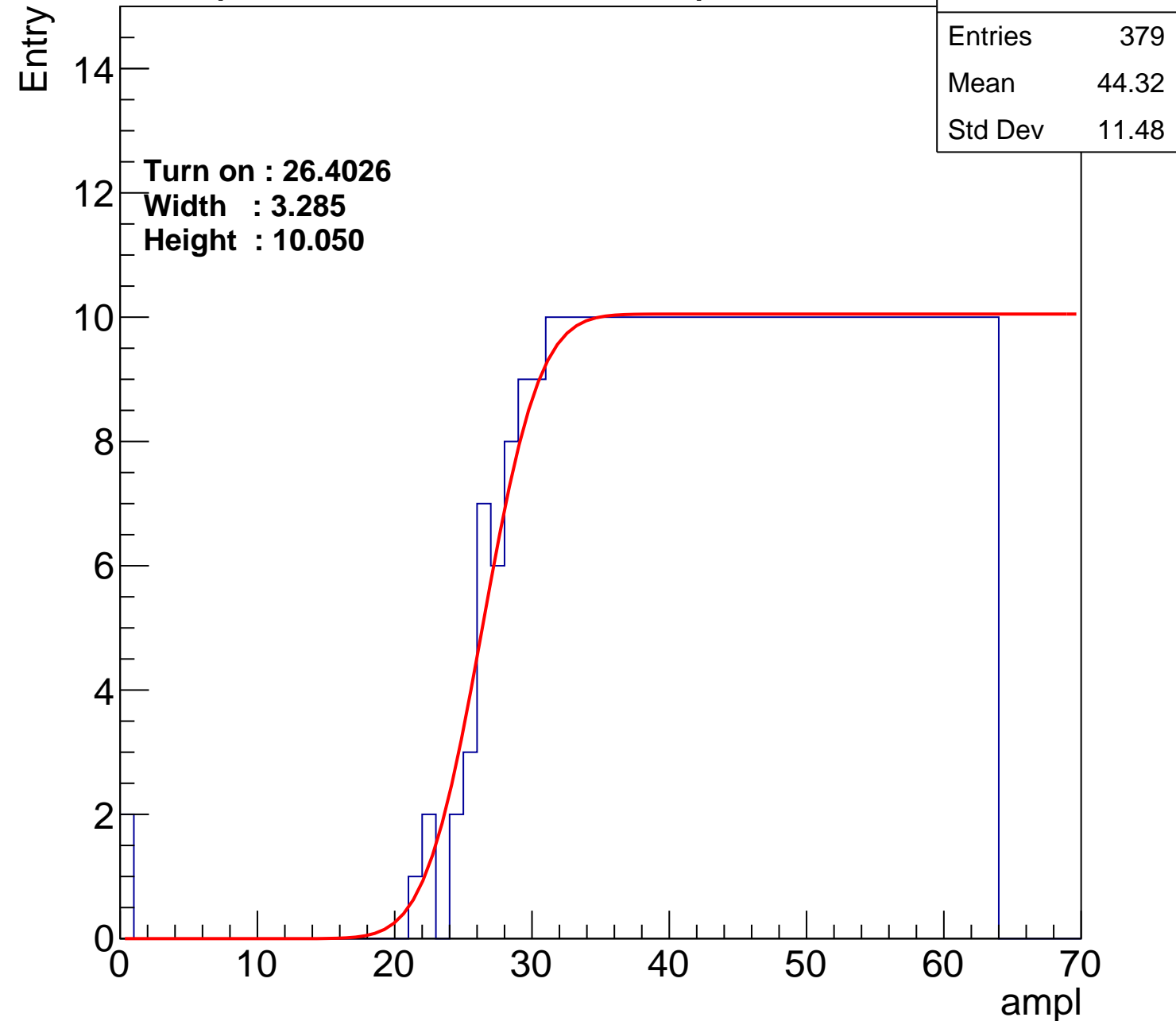
Width : 3.285

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch55

calib_packv5_042523_0143.root, FC#3, port B3

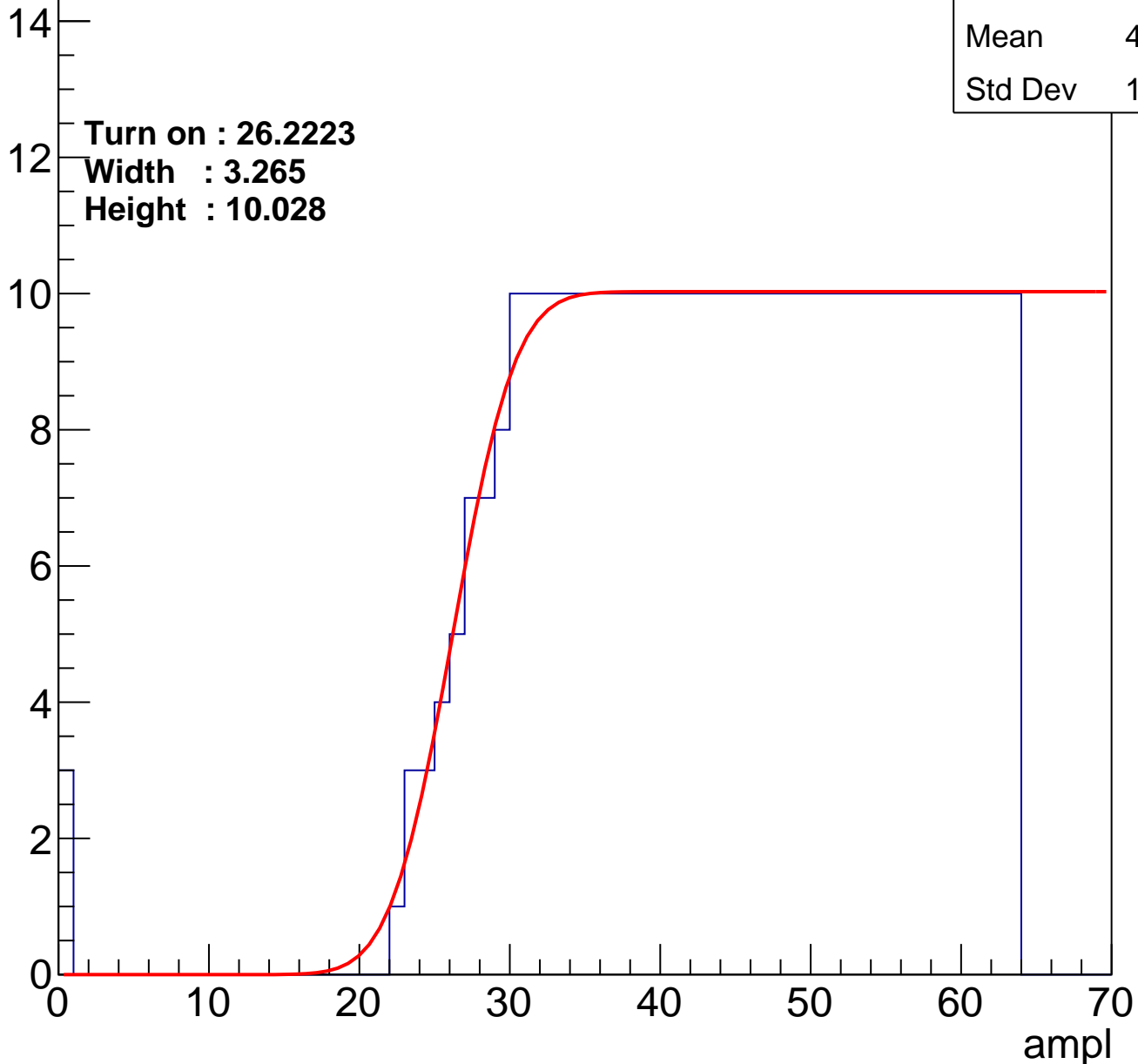
Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.2223

Width : 3.265

Height : 10.028

Entry



B1L000S, U11-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.56
Std Dev	11.85

Turn on : 25.2890

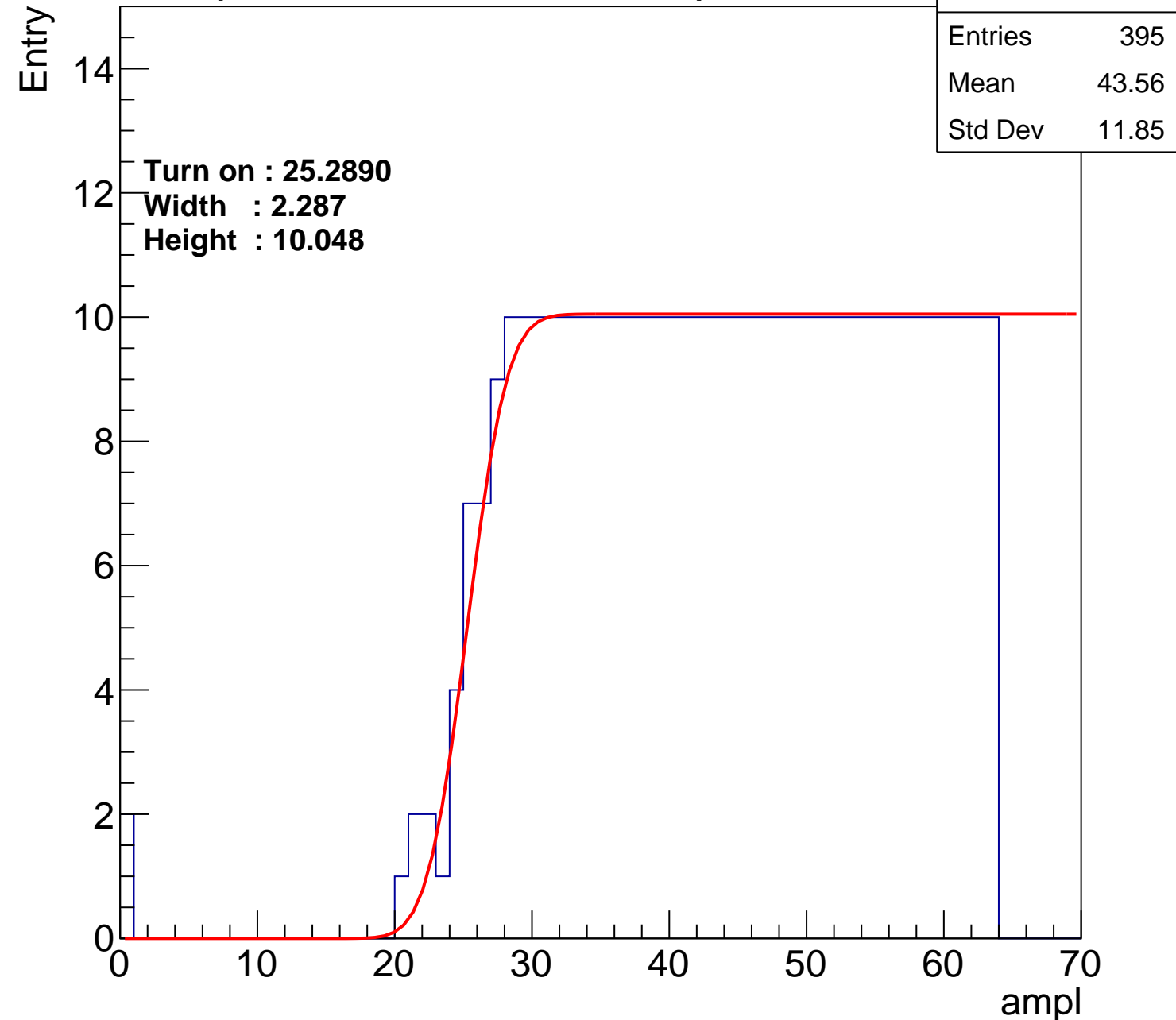
Width : 2.287

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch57

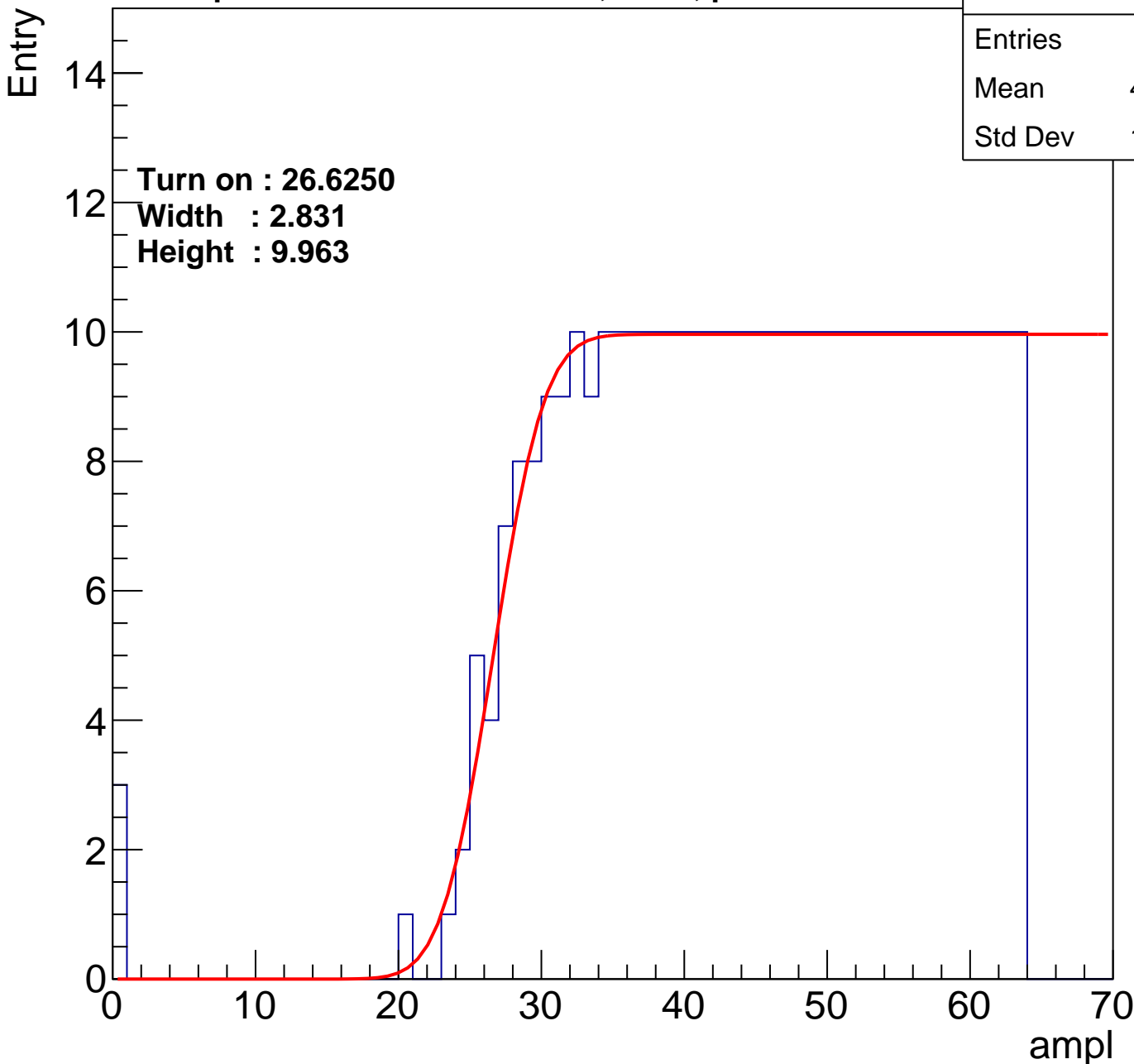
calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.36
Std Dev	11.64

Turn on : 26.6250

Width : 2.831

Height : 9.963



B1L000S, U11-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.5958

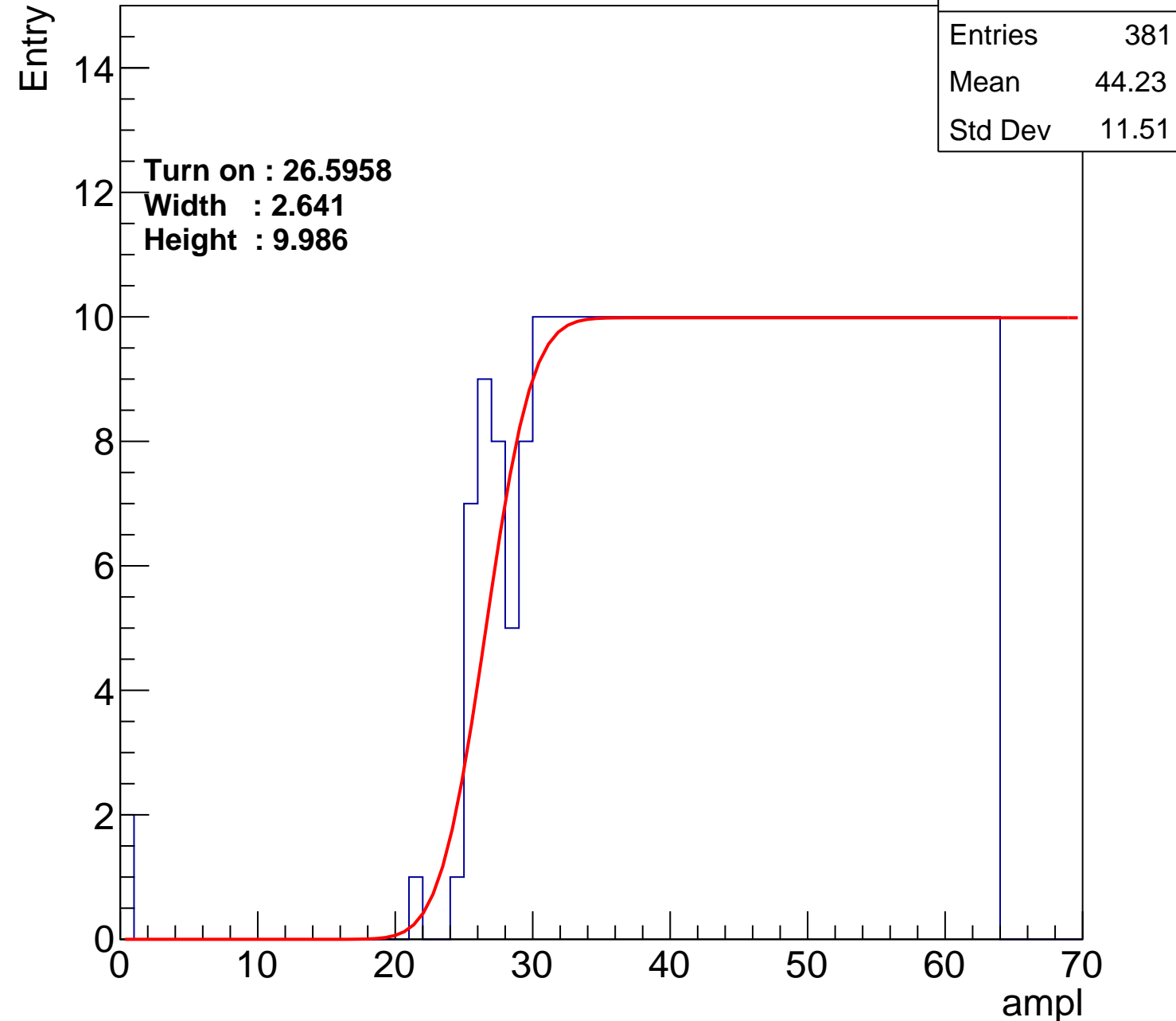
Width : 2.641

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.28
Std Dev	11.35

Turn on : 26.0658

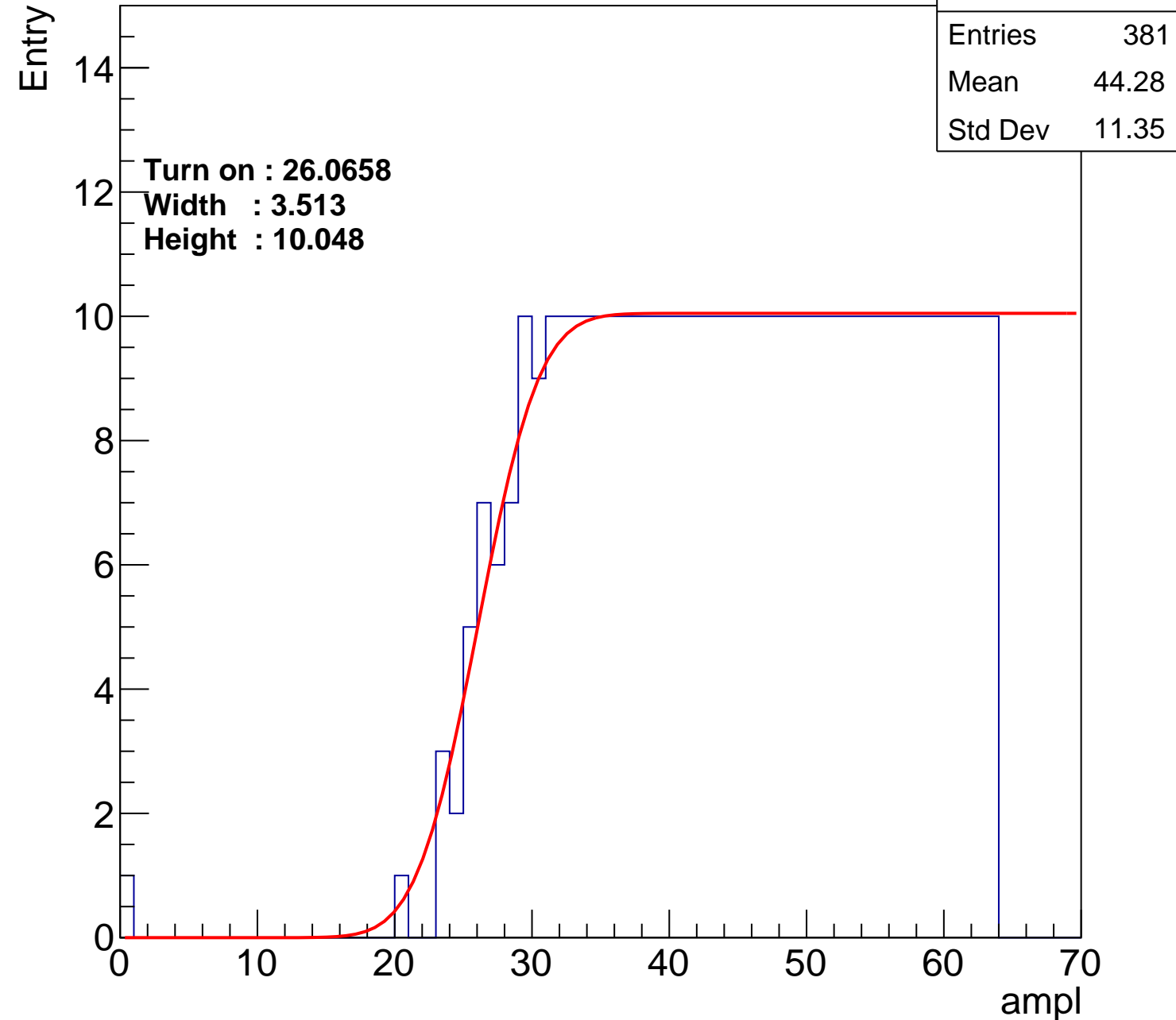
Width : 3.513

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch60

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.08
Std Dev	11.12

Turn on : 28.6184

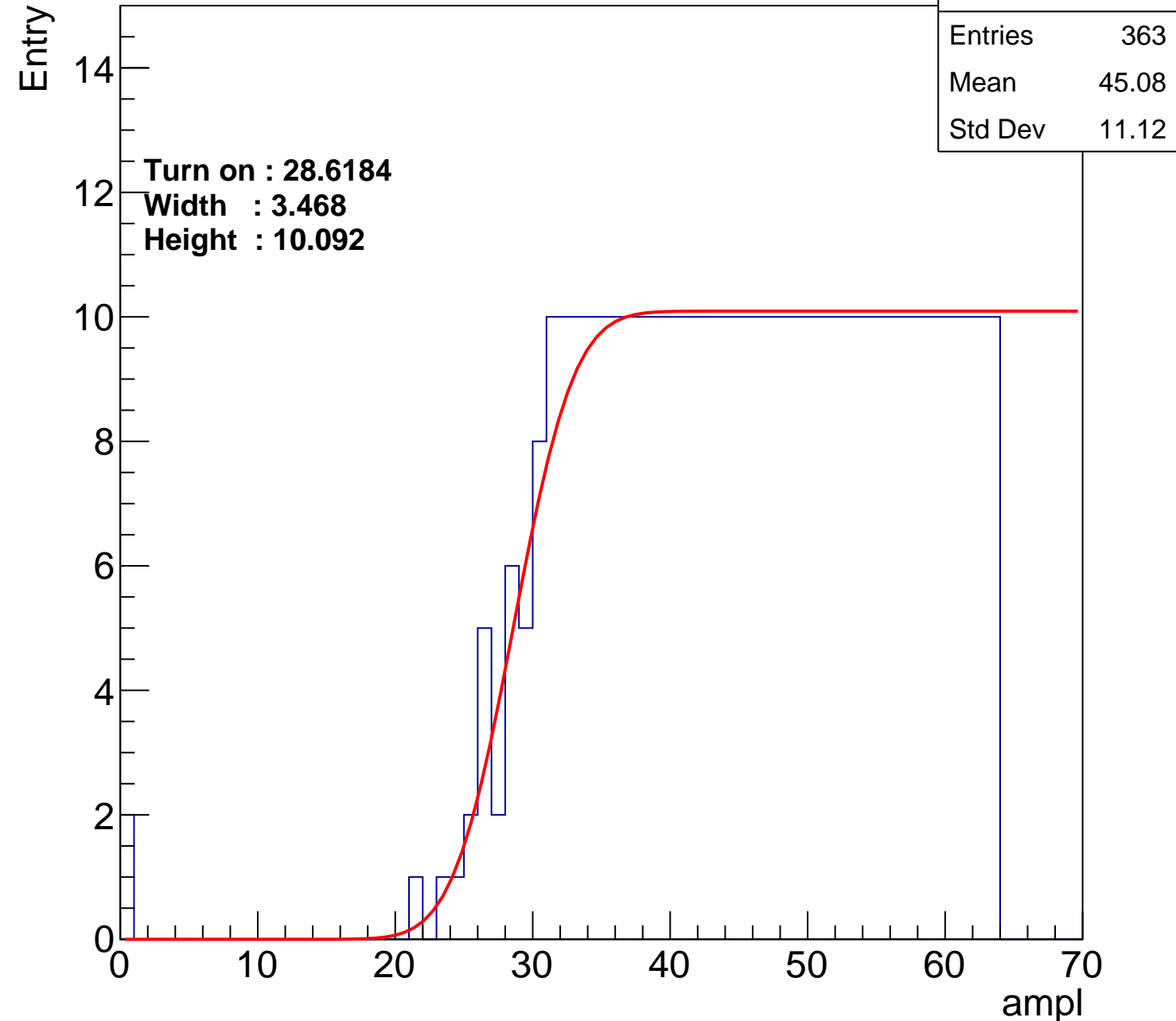
Width : 3.468

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 26.4570

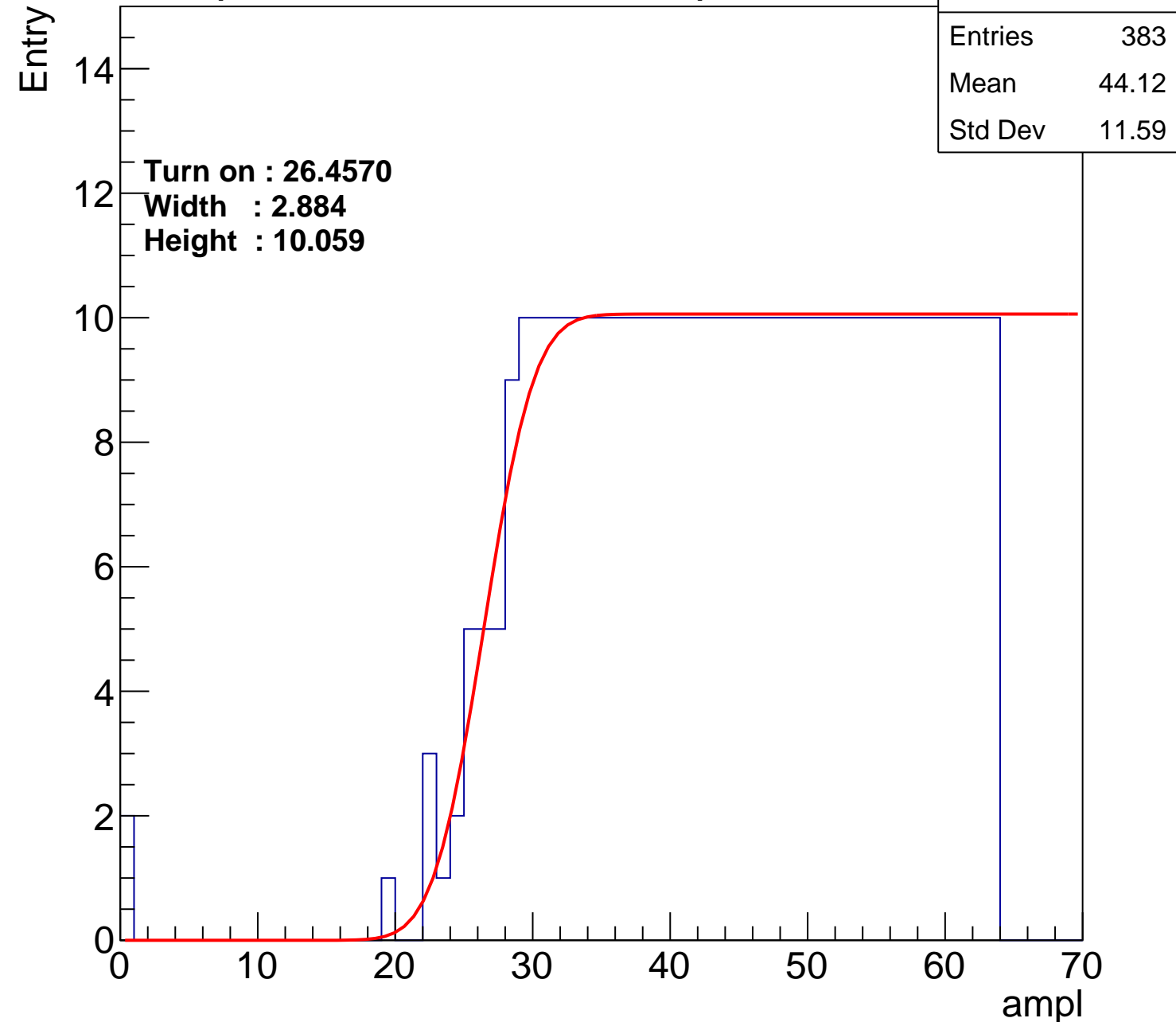
Width : 2.884

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.71
Std Dev	11.27

Turn on : 27.2107

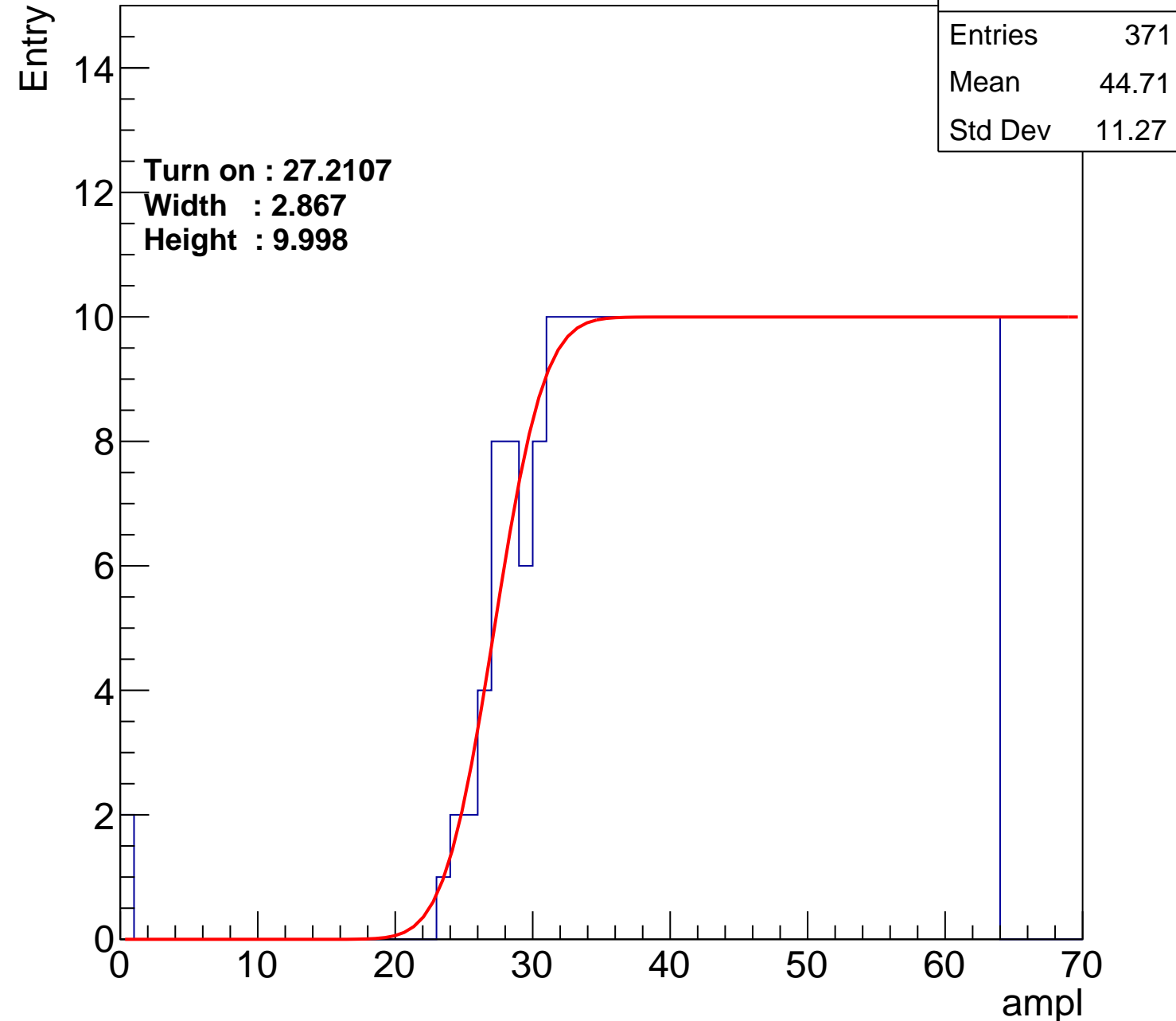
Width : 2.867

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch63

calib_packv5_042523_0143.root, FC#3, port B3

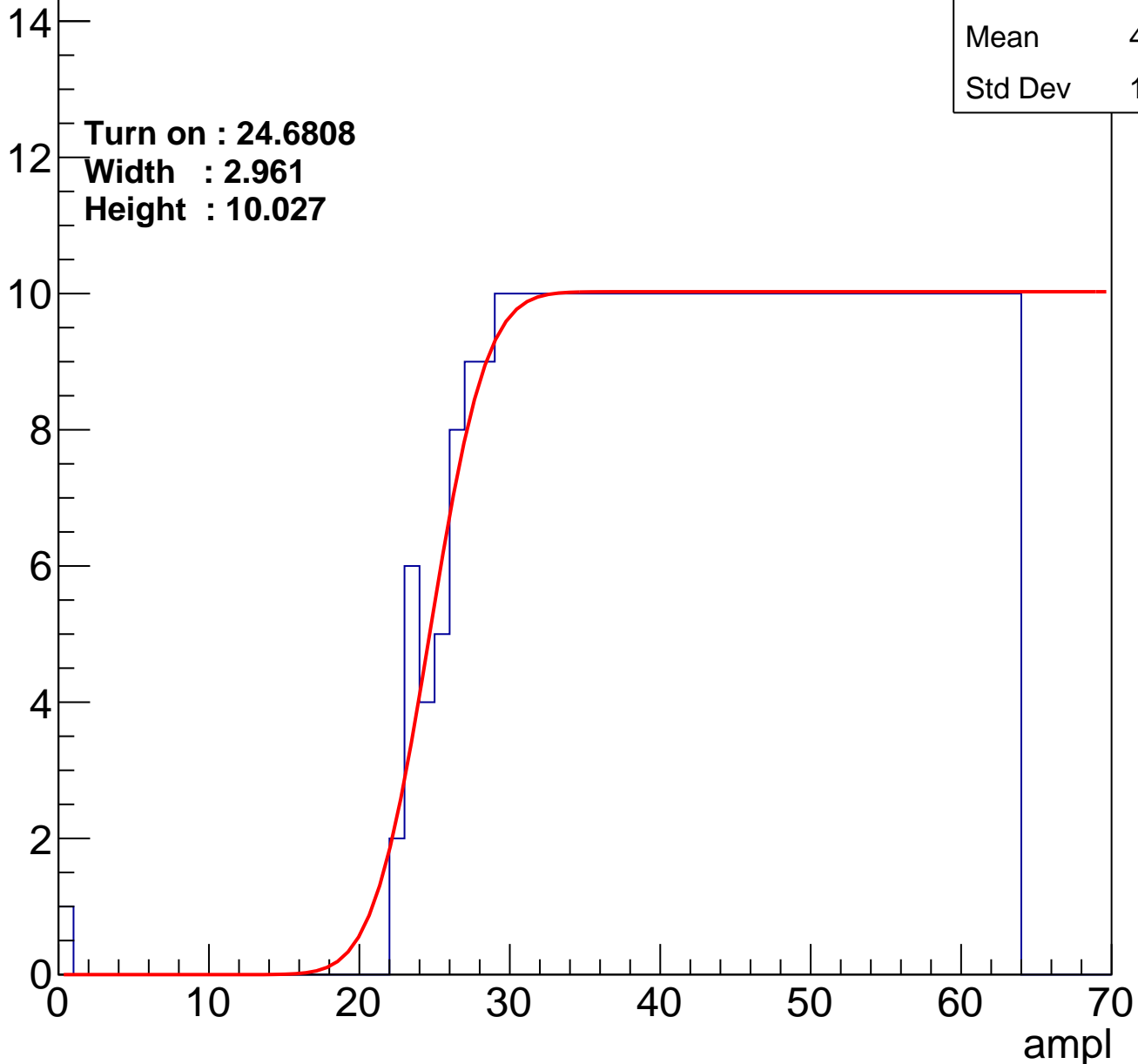
Entries	394
Mean	43.67
Std Dev	11.65

Turn on : 24.6808

Width : 2.961

Height : 10.027

Entry



B1L000S, U11-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.66
Std Dev	11.16

Turn on : 26.8308

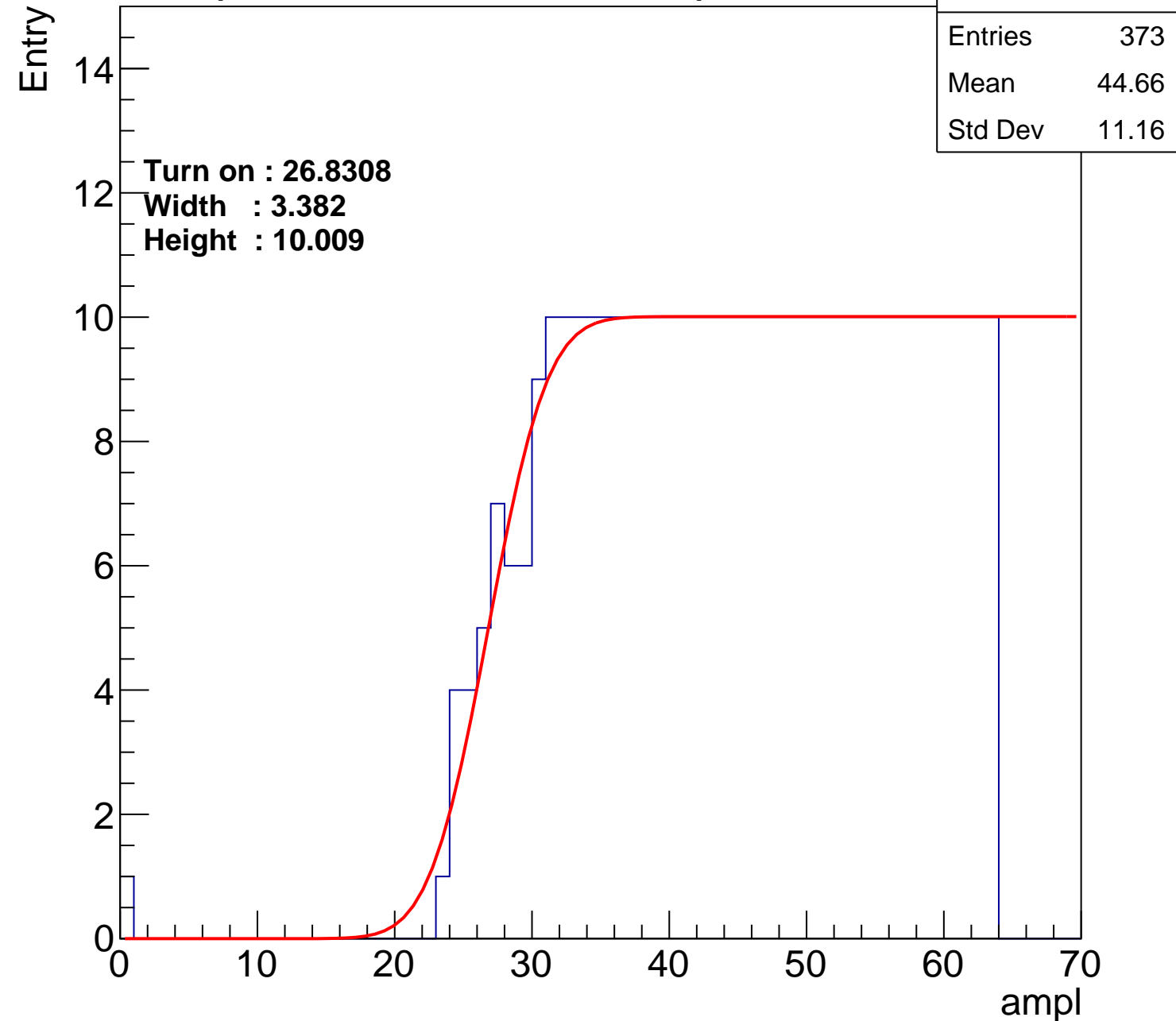
Width : 3.382

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch65

calib_packv5_042523_0143.root, FC#3, port B3

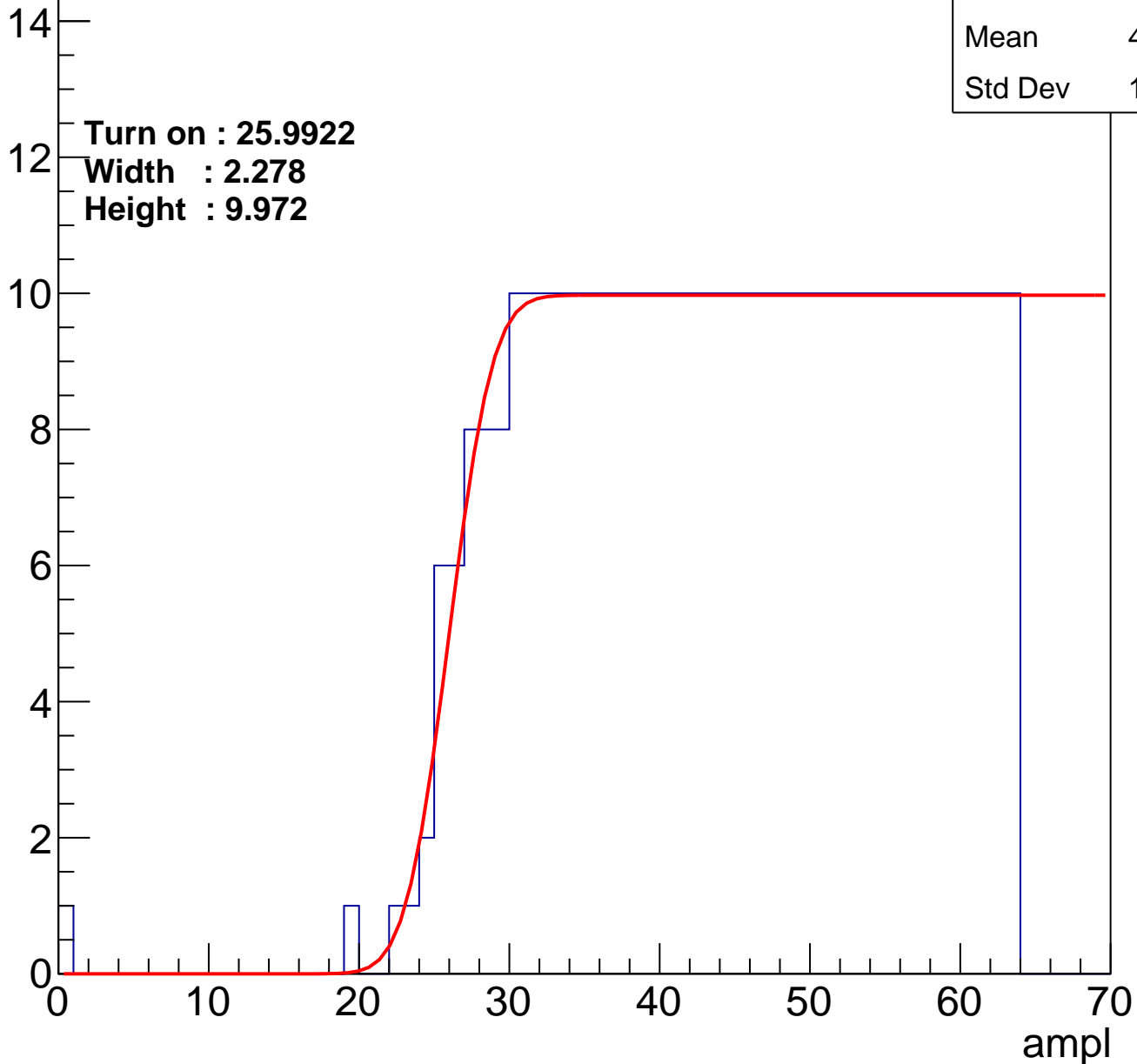
Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 25.9922

Width : 2.278

Height : 9.972

Entry



B1L000S, U11-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.25
Std Dev	10.85

Turn on : 27.1882

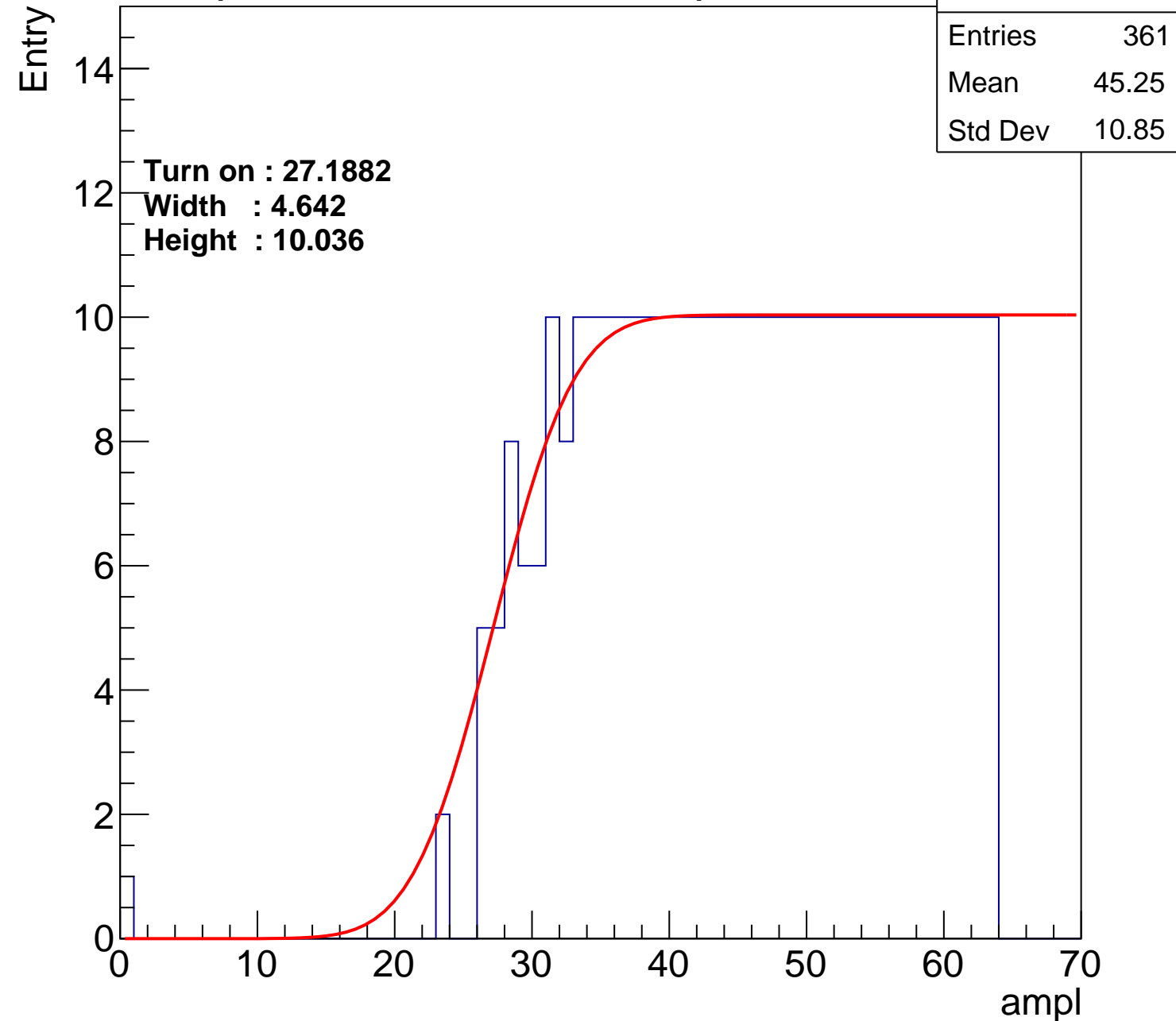
Width : 4.642

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	408
Mean	42.86
Std Dev	12.34

Turn on : 23.7714

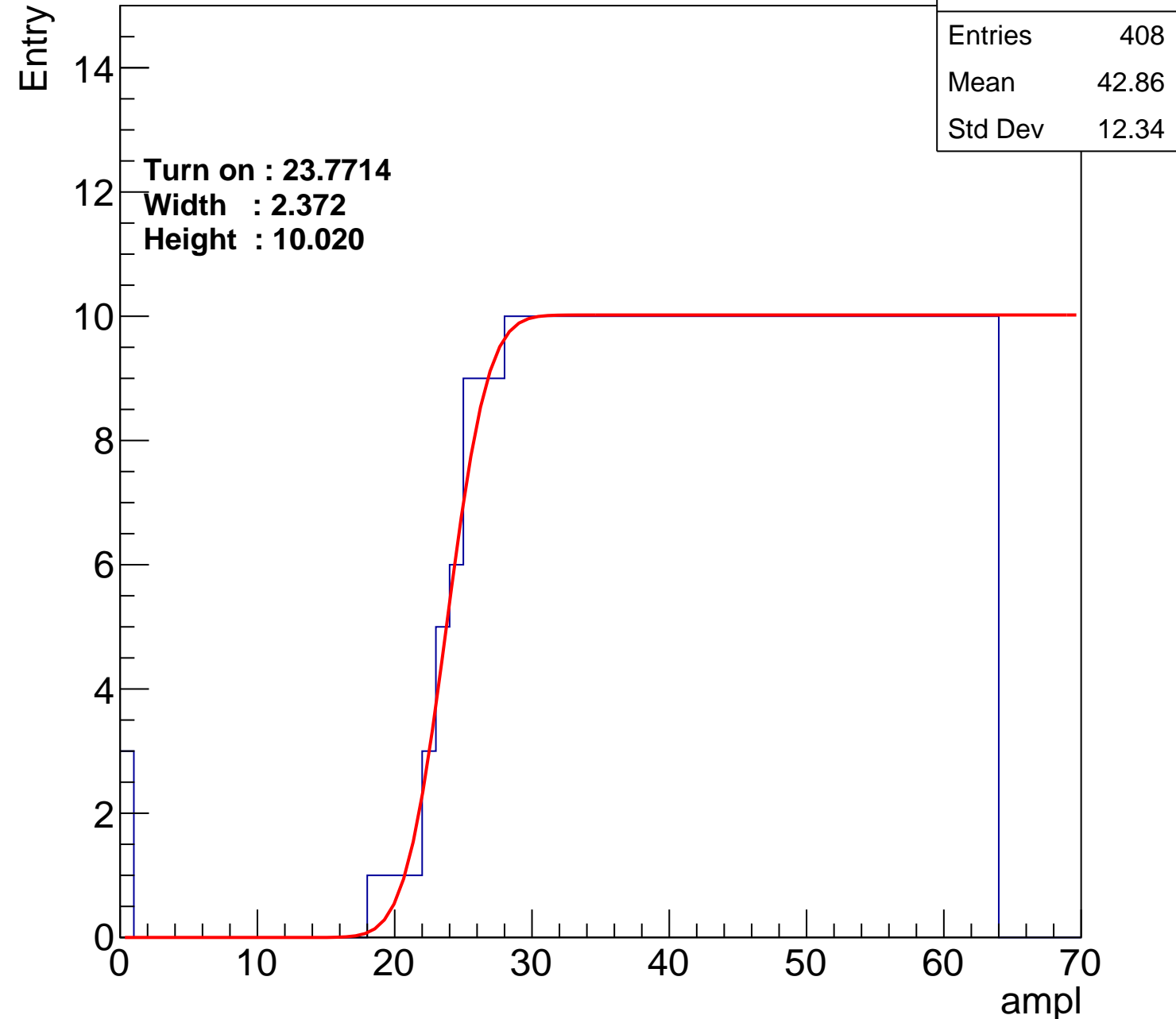
Width : 2.372

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch68

calib_packv5_042523_0143.root, FC#3, port B3

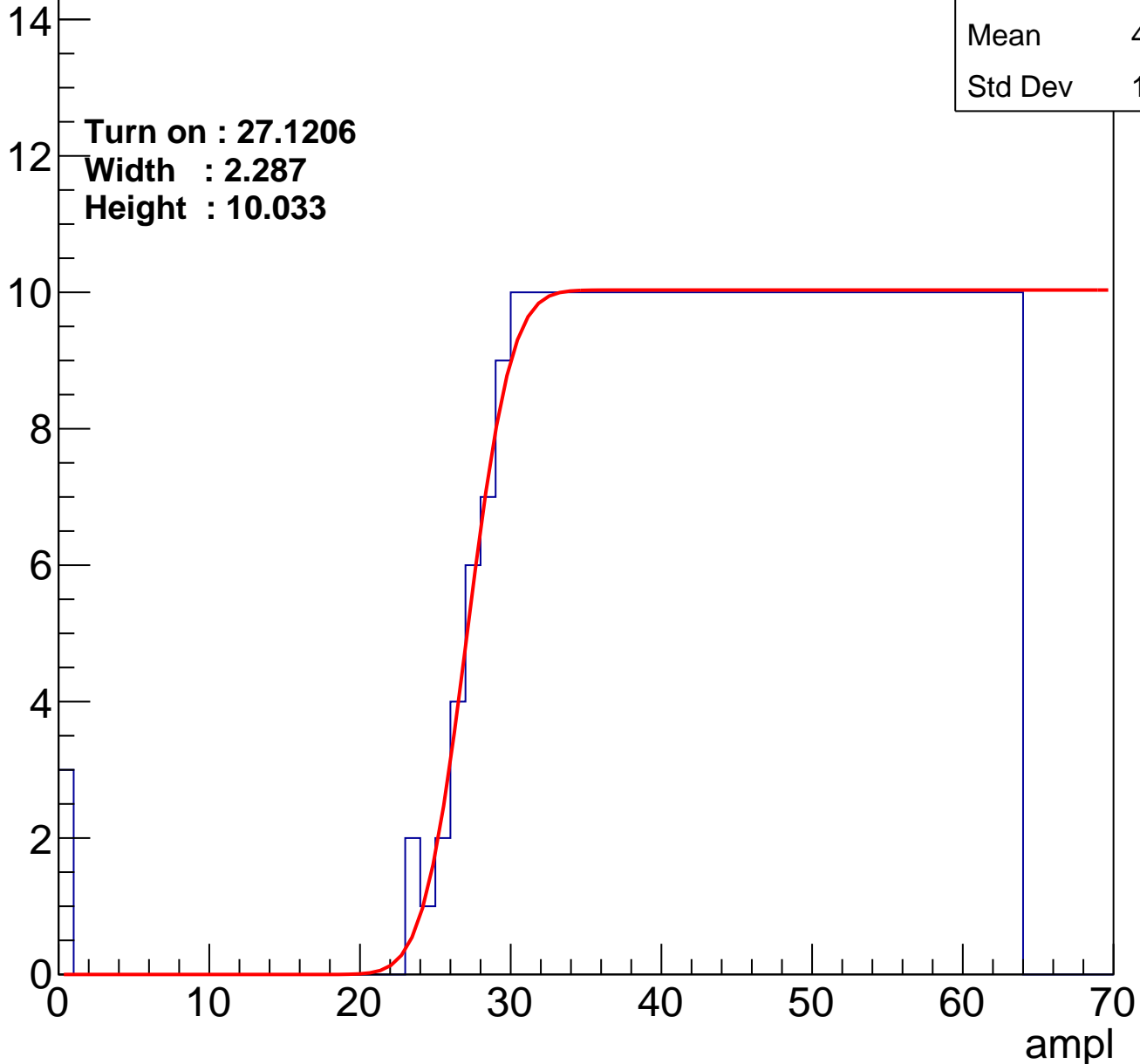
Entries	374
Mean	44.53
Std Dev	11.49

Turn on : 27.1206

Width : 2.287

Height : 10.033

Entry



B1L000S, U11-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.23
Std Dev	11.65

Turn on : 26.4001

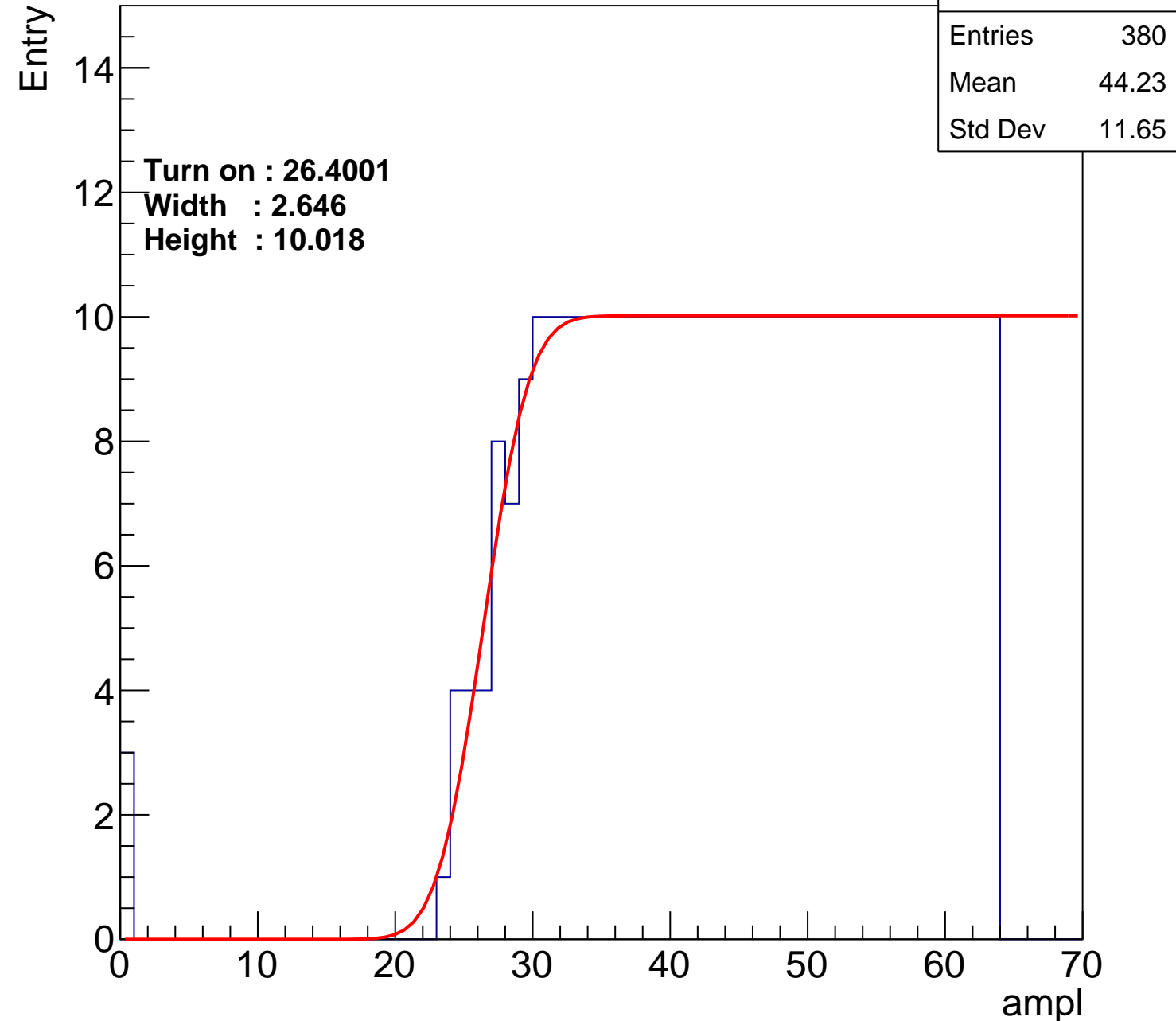
Width : 2.646

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.31
Std Dev	12.18

Turn on : 27.6814

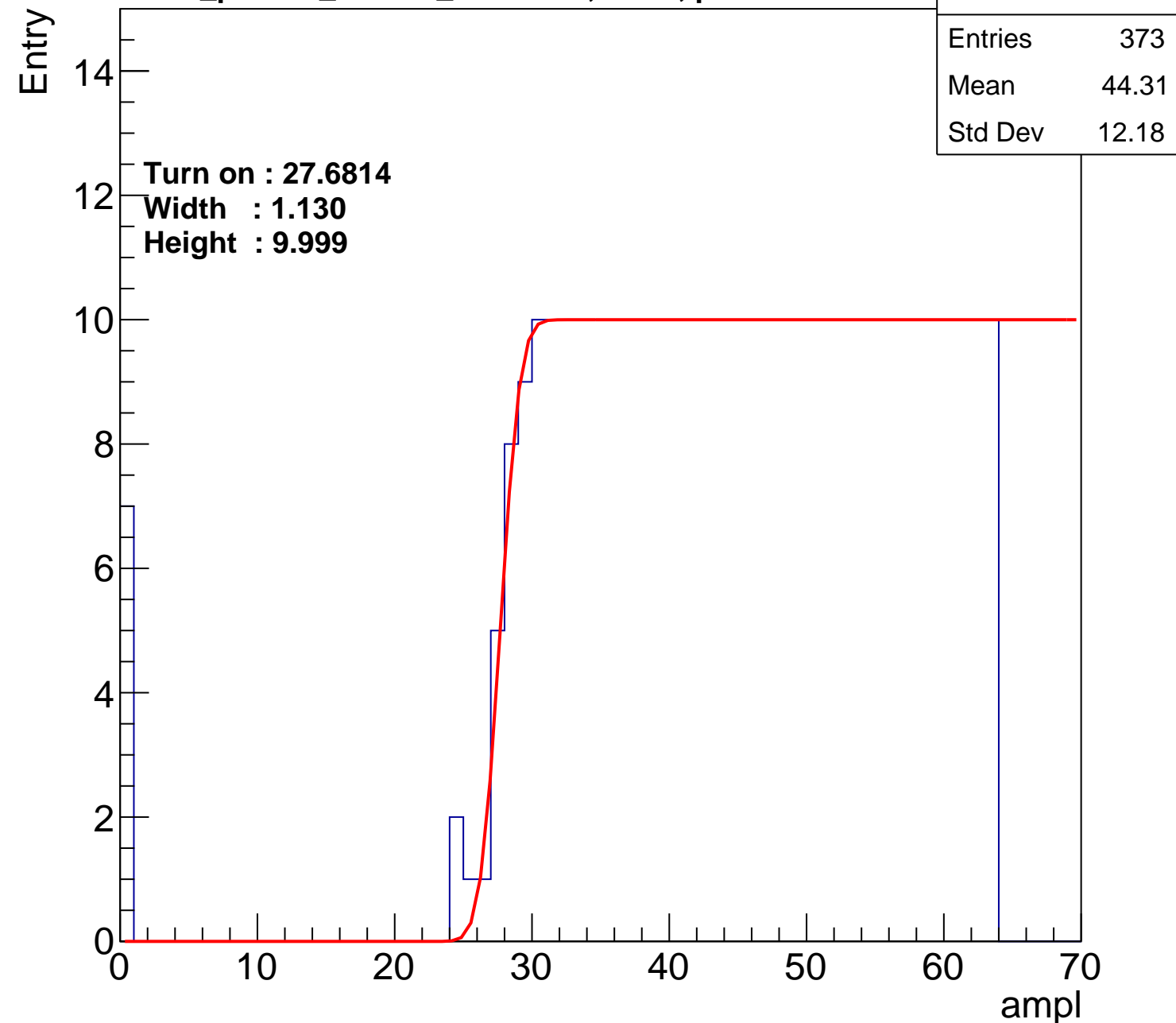
Width : 1.130

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch71

calib_packv5_042523_0143.root, FC#3, port B3

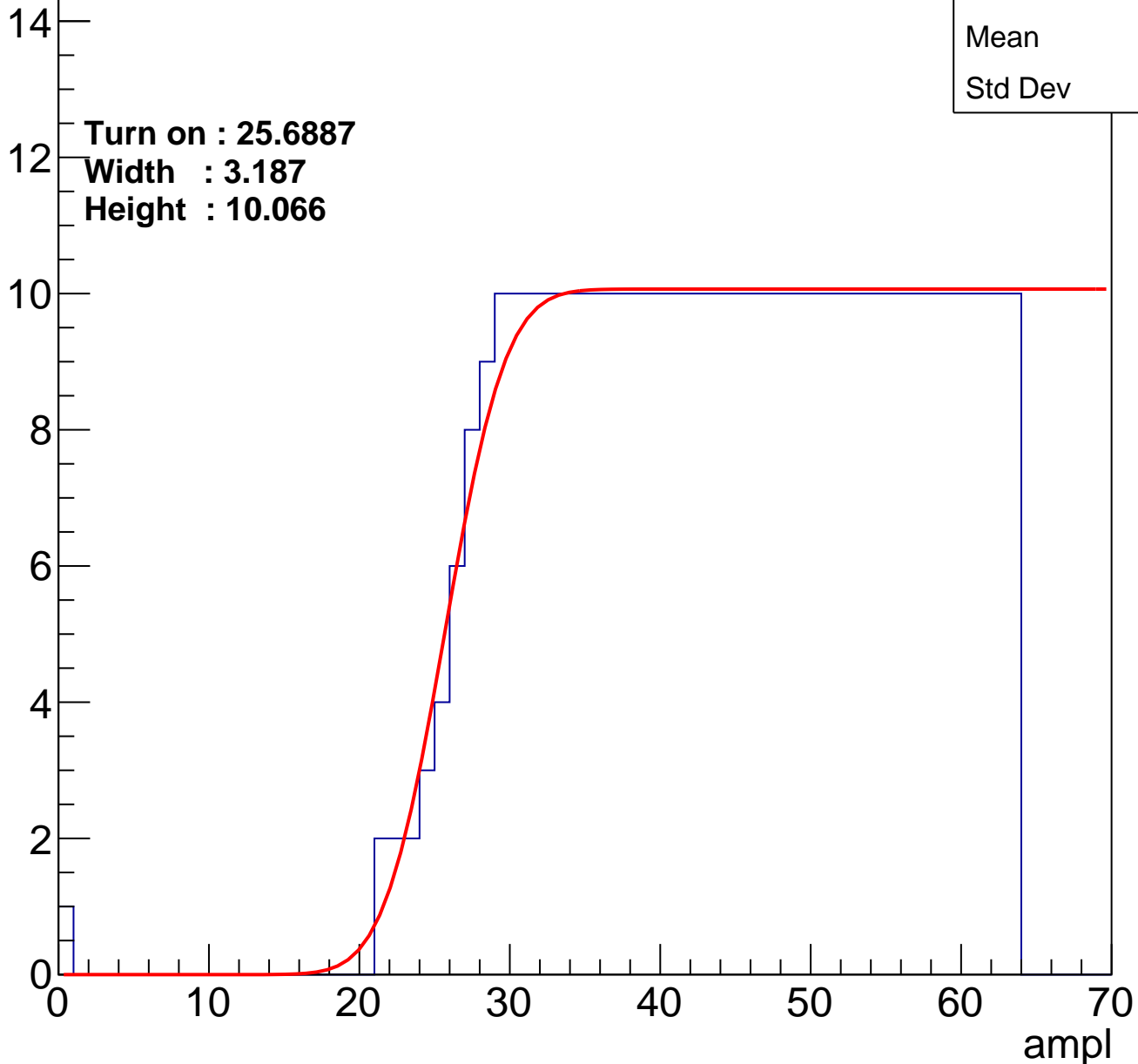
Entries	387
Mean	44
Std Dev	11.5

Turn on : 25.6887

Width : 3.187

Height : 10.066

Entry



B1L000S, U11-ch72

calib_packv5_042523_0143.root, FC#3, port B3

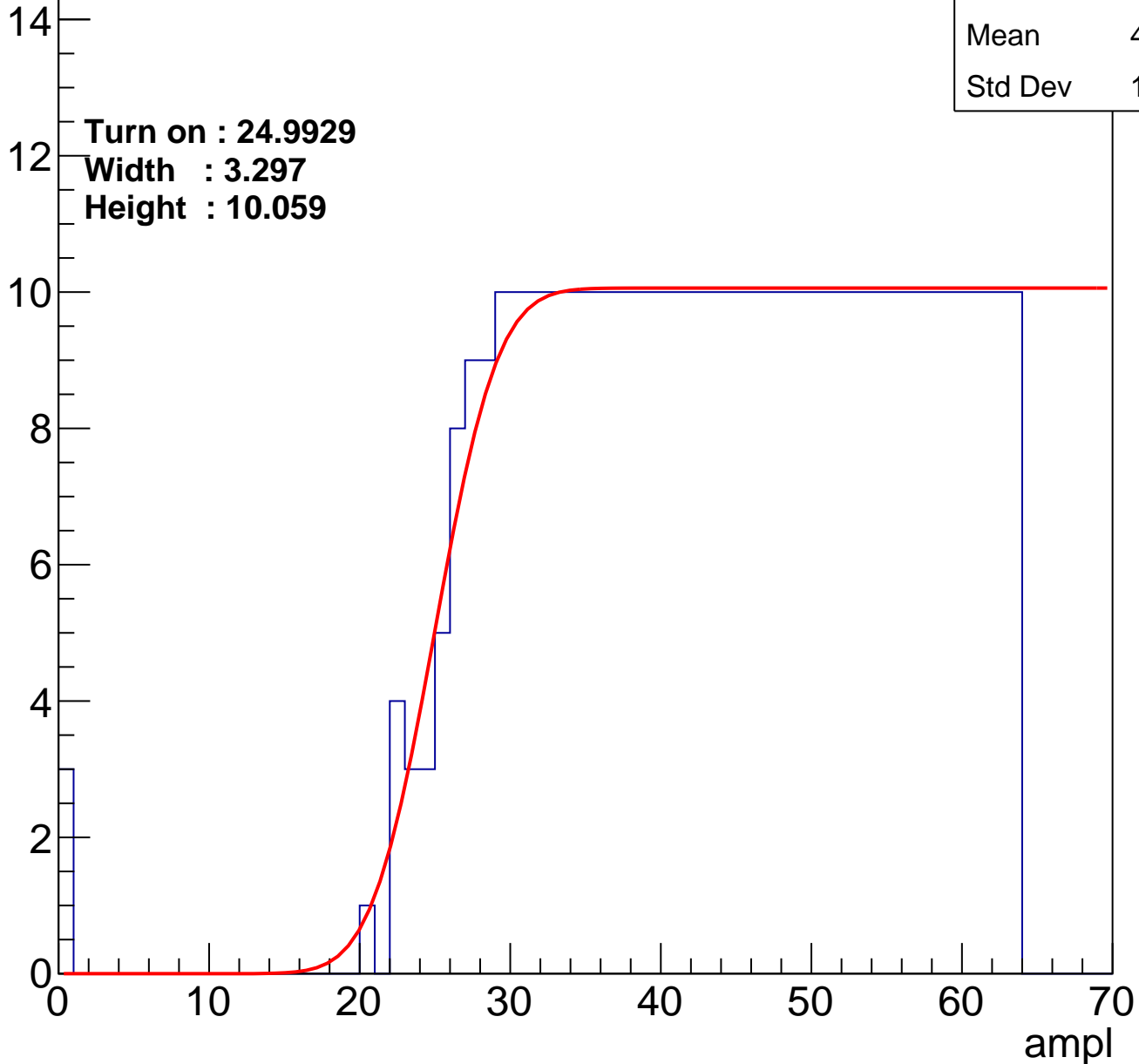
Entries	395
Mean	43.49
Std Dev	12.03

Turn on : 24.9929

Width : 3.297

Height : 10.059

Entry



B1L000S, U11-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.99
Std Dev	11.94

Turn on : 26.1812

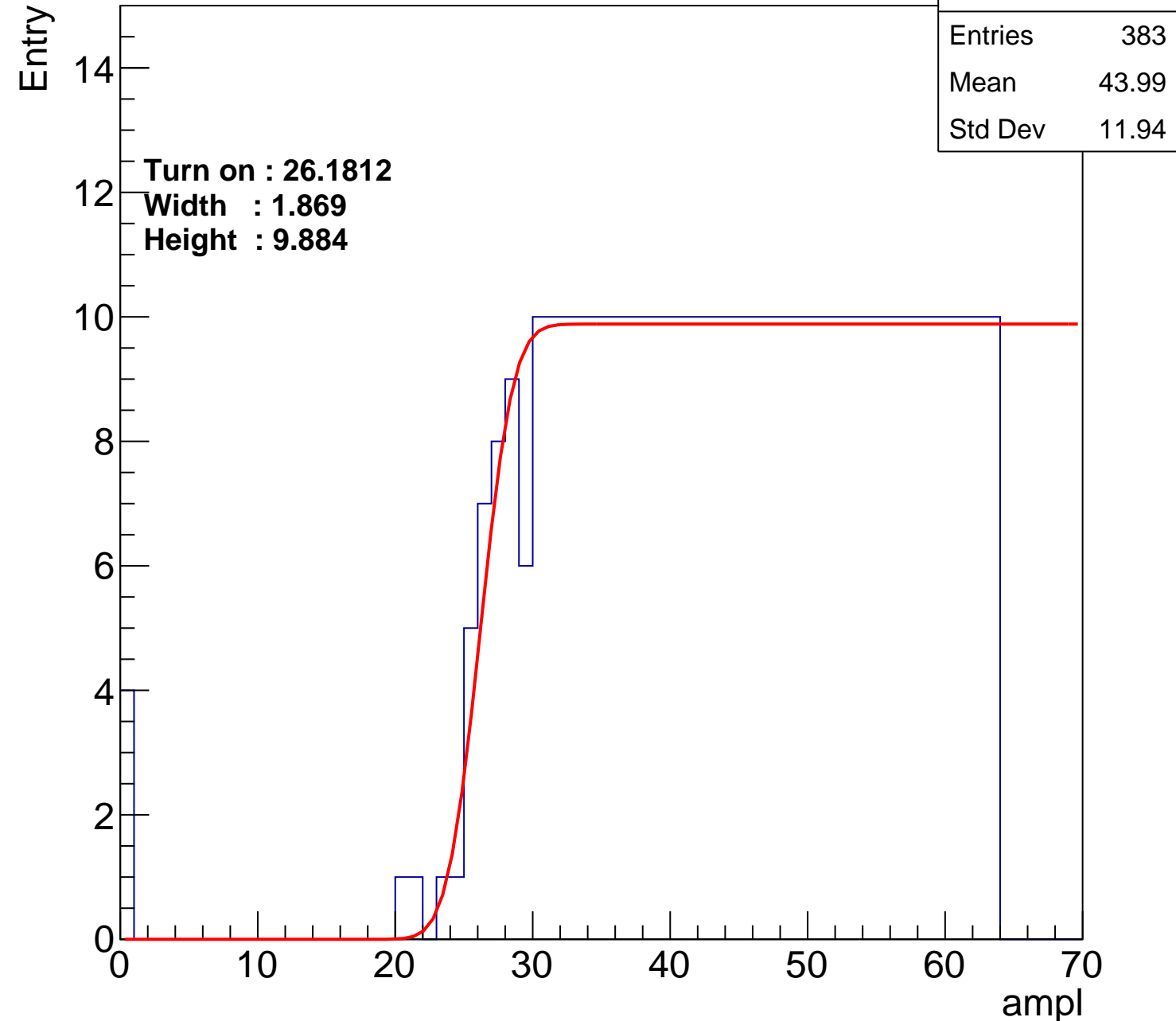
Width : 1.869

Height : 9.884

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.49
Std Dev	11.9

Turn on : 24.6562

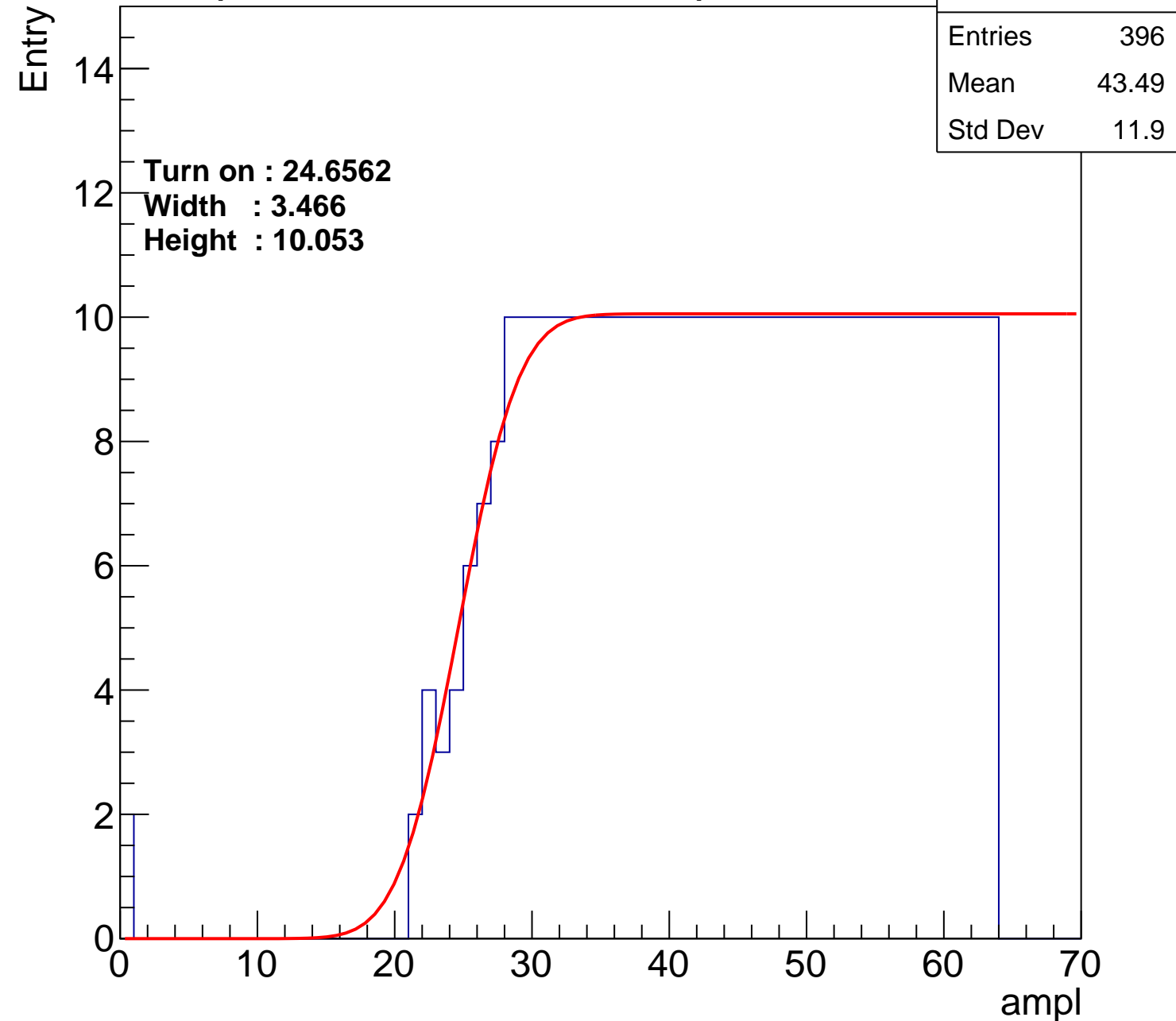
Width : 3.466

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.96
Std Dev	10.95

Turn on : 27.5147

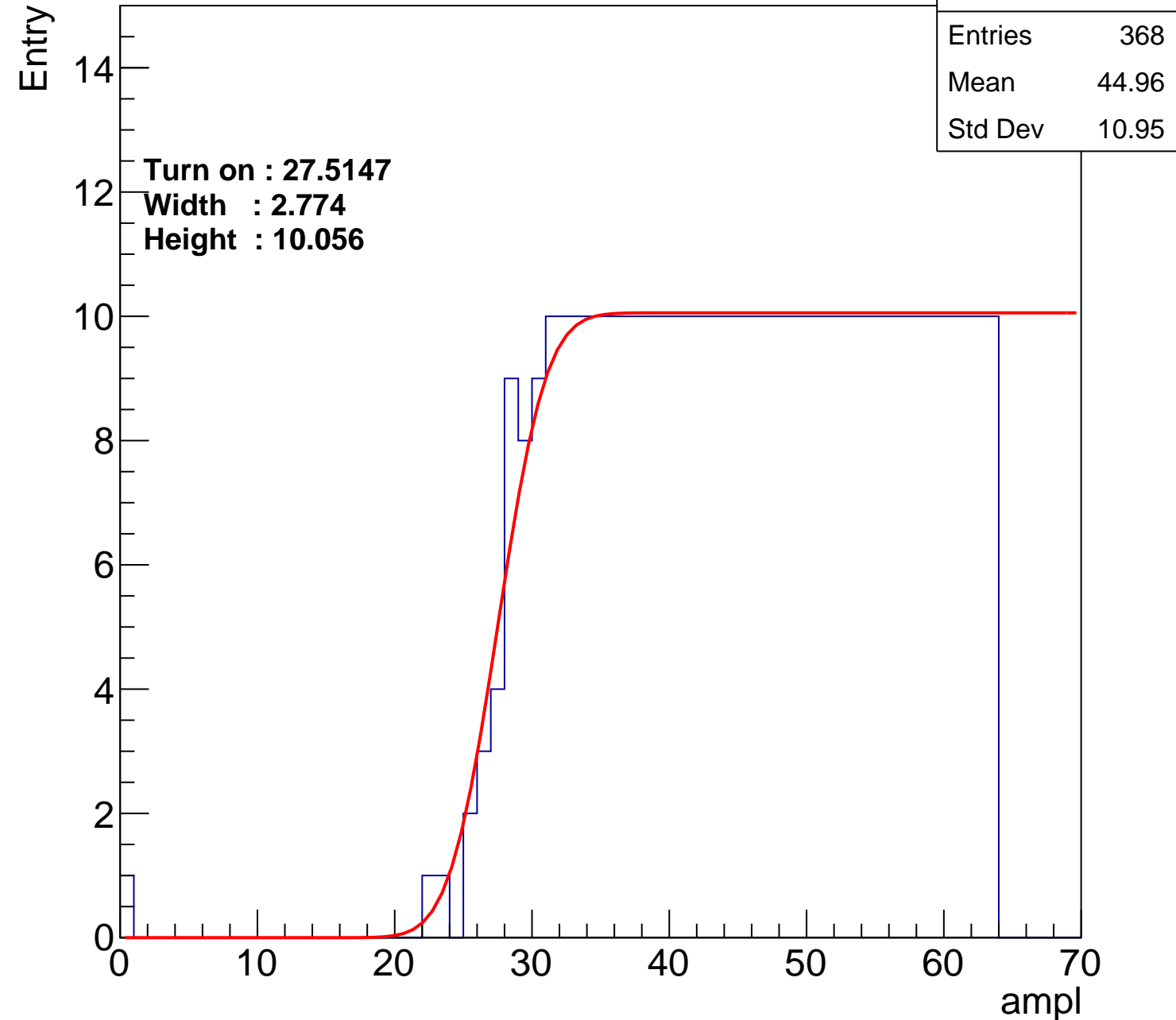
Width : 2.774

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch76

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.38
Std Dev	11.76

Turn on : 27.6319

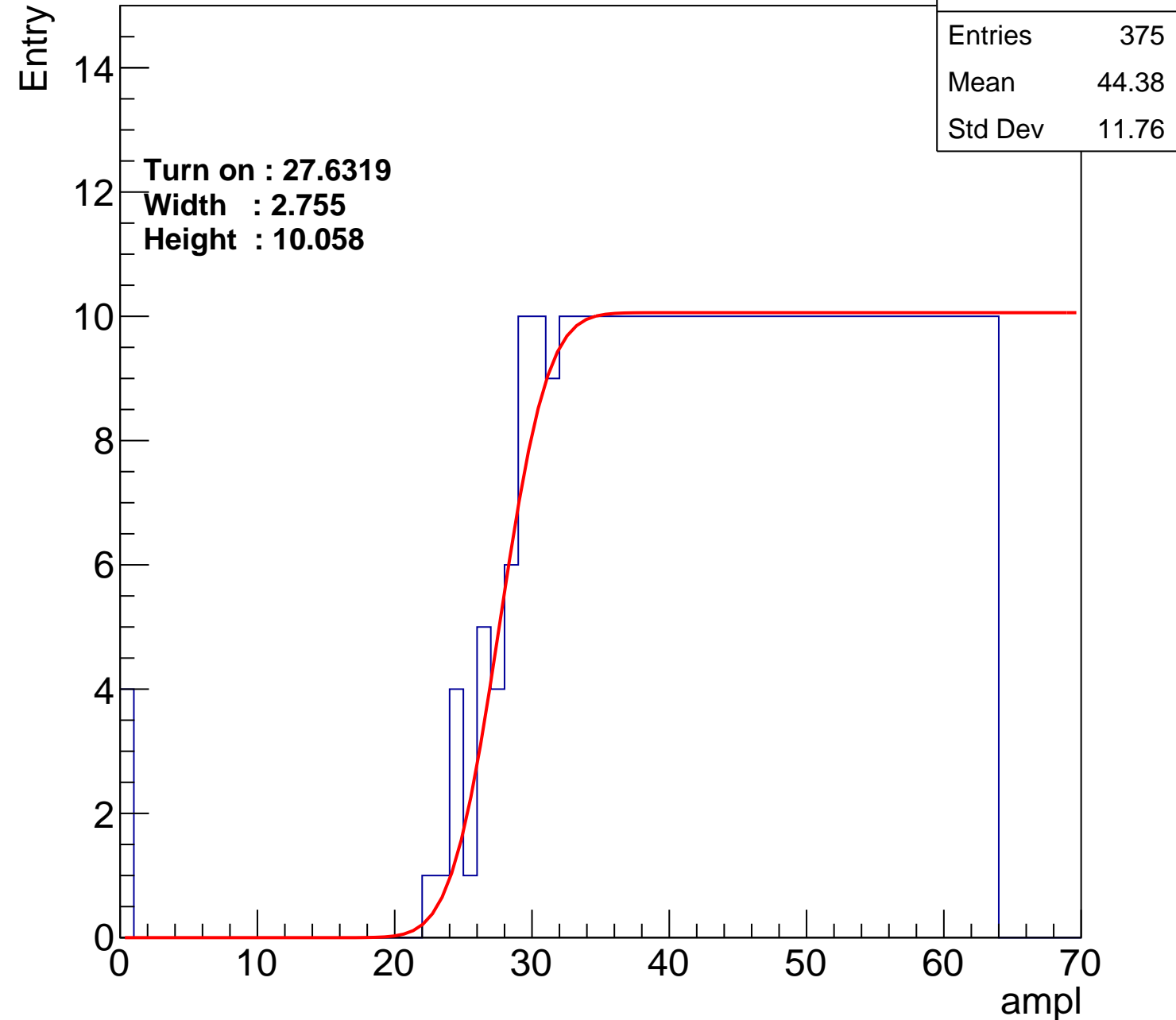
Width : 2.755

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.57
Std Dev	11.48

Turn on : 27.3825

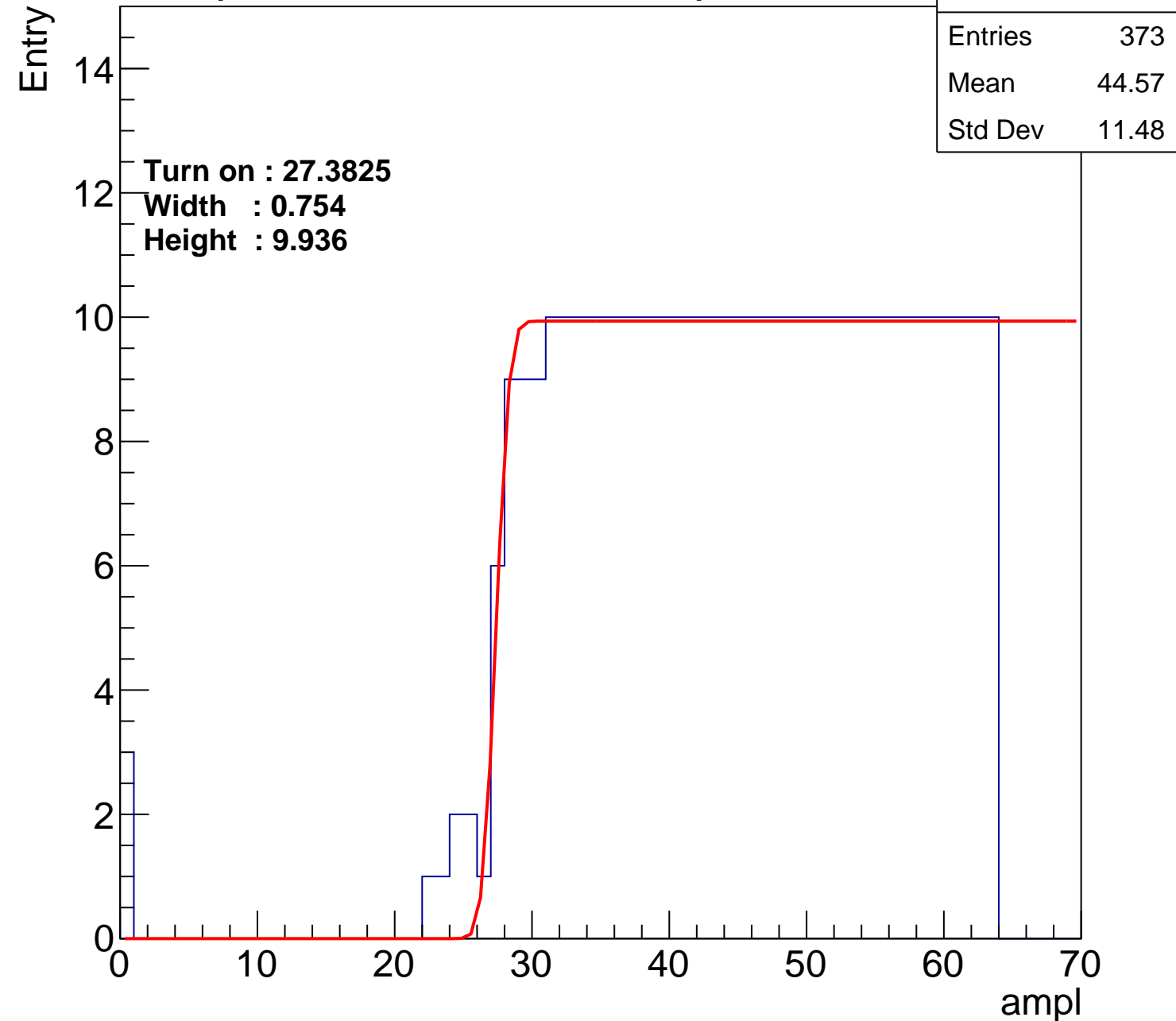
Width : 0.754

Height : 9.936

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch78

calib_packv5_042523_0143.root, FC#3, port B3

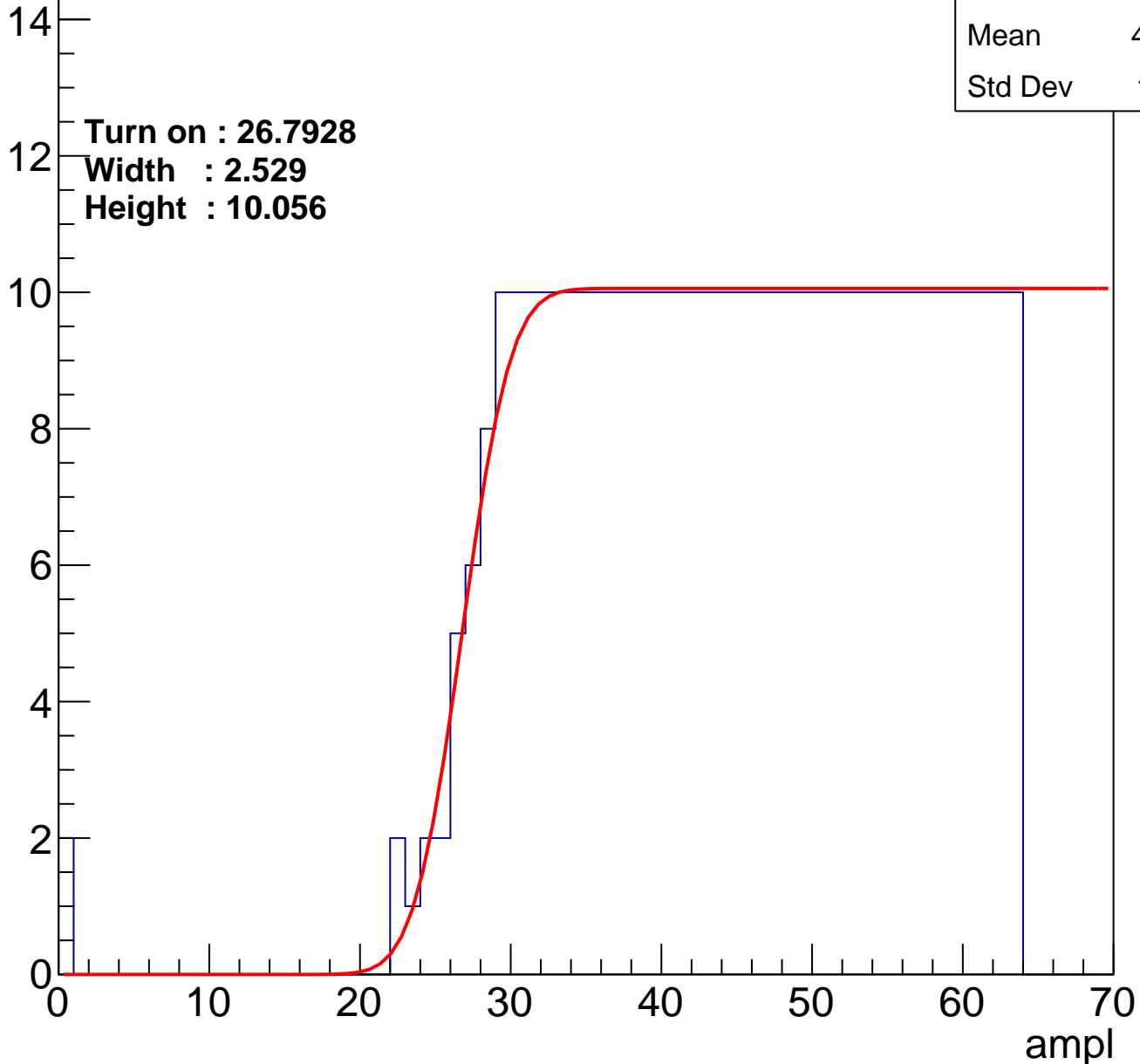
Entries	378
Mean	44.39
Std Dev	11.41

Turn on : 26.7928

Width : 2.529

Height : 10.056

Entry



B1L000S, U11-ch79

calib_packv5_042523_0143.root, FC#3, port B3

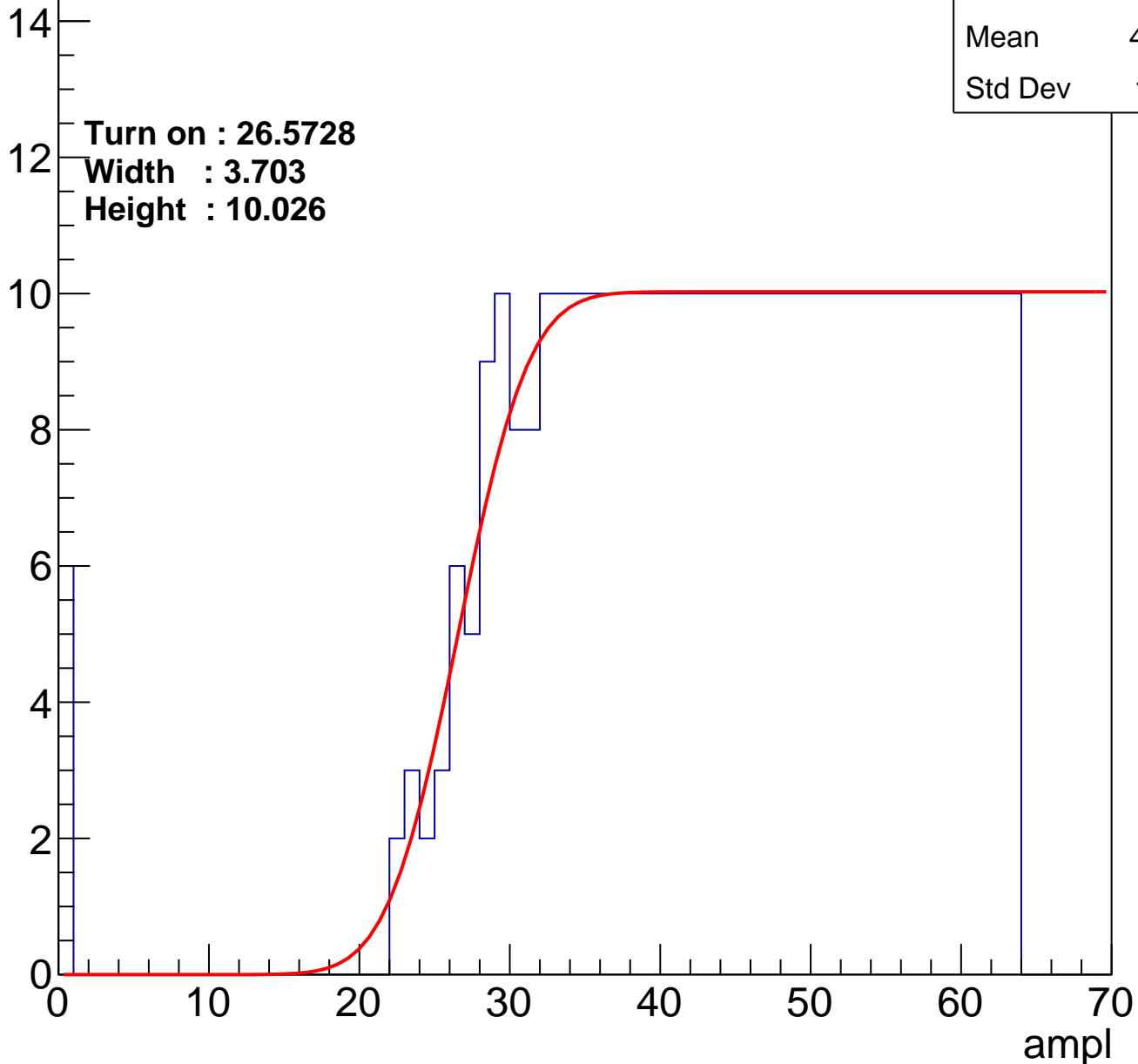
Entries	382
Mean	43.87
Std Dev	12.31

Turn on : 26.5728

Width : 3.703

Height : 10.026

Entry



B1L000S, U11-ch80

calib_packv5_042523_0143.root, FC#3, port B3

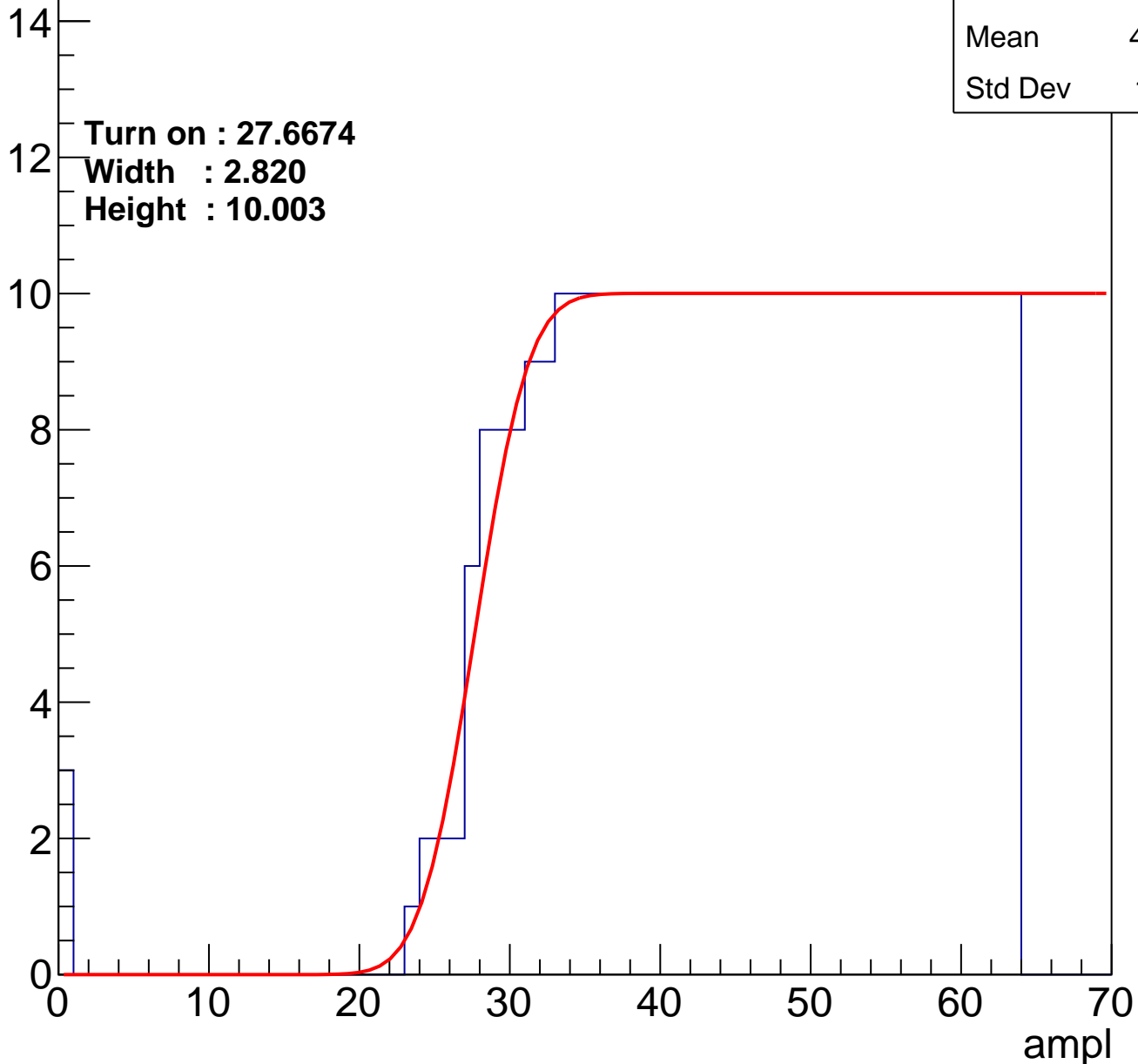
Entries	368
Mean	44.78
Std Dev	11.41

Turn on : 27.6674

Width : 2.820

Height : 10.003

Entry



B1L000S, U11-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.88
Std Dev	10.97

Turn on : 27.1136

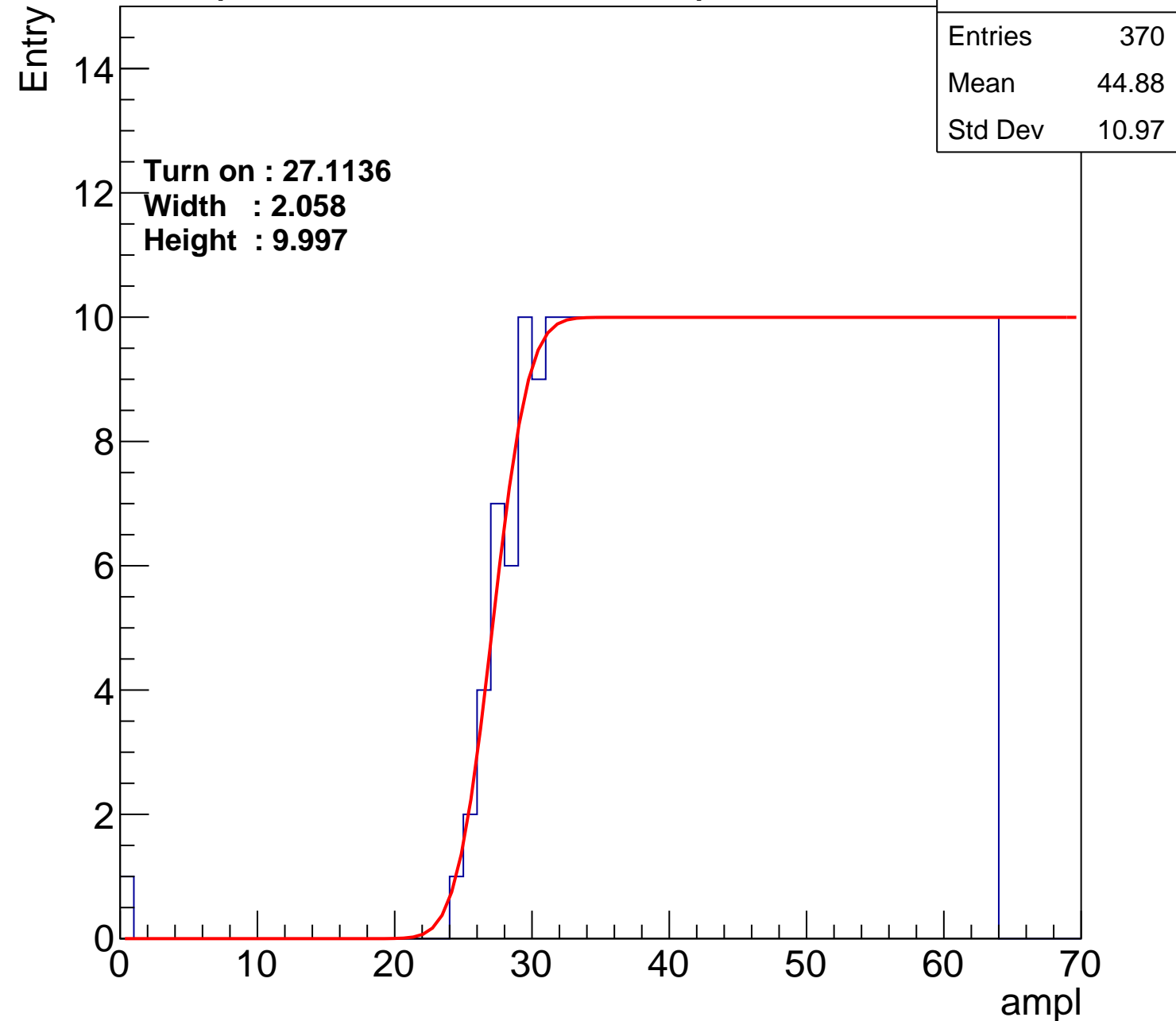
Width : 2.058

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.41
Std Dev	11.57

Turn on : 27.1750

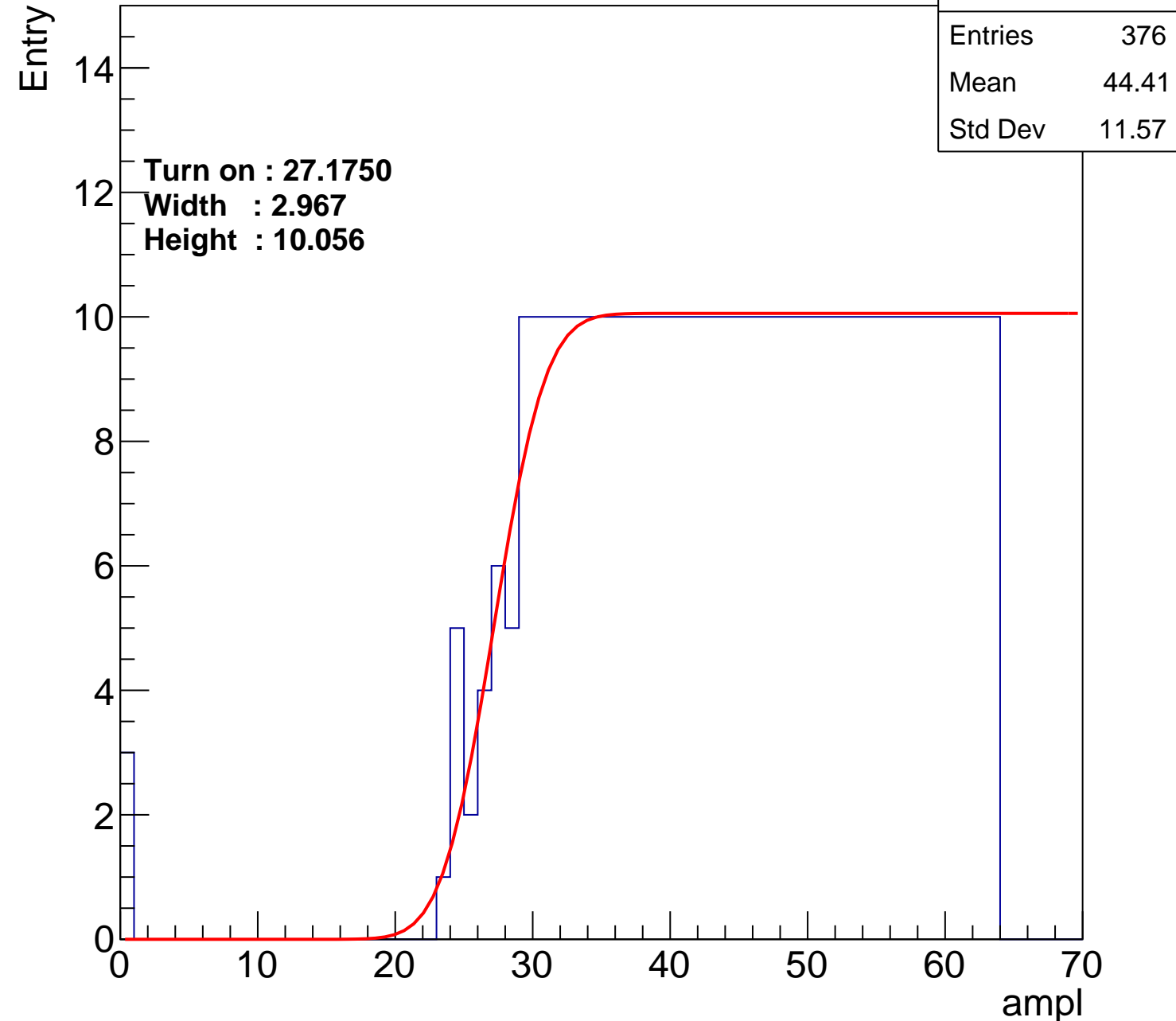
Width : 2.967

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 26.7458

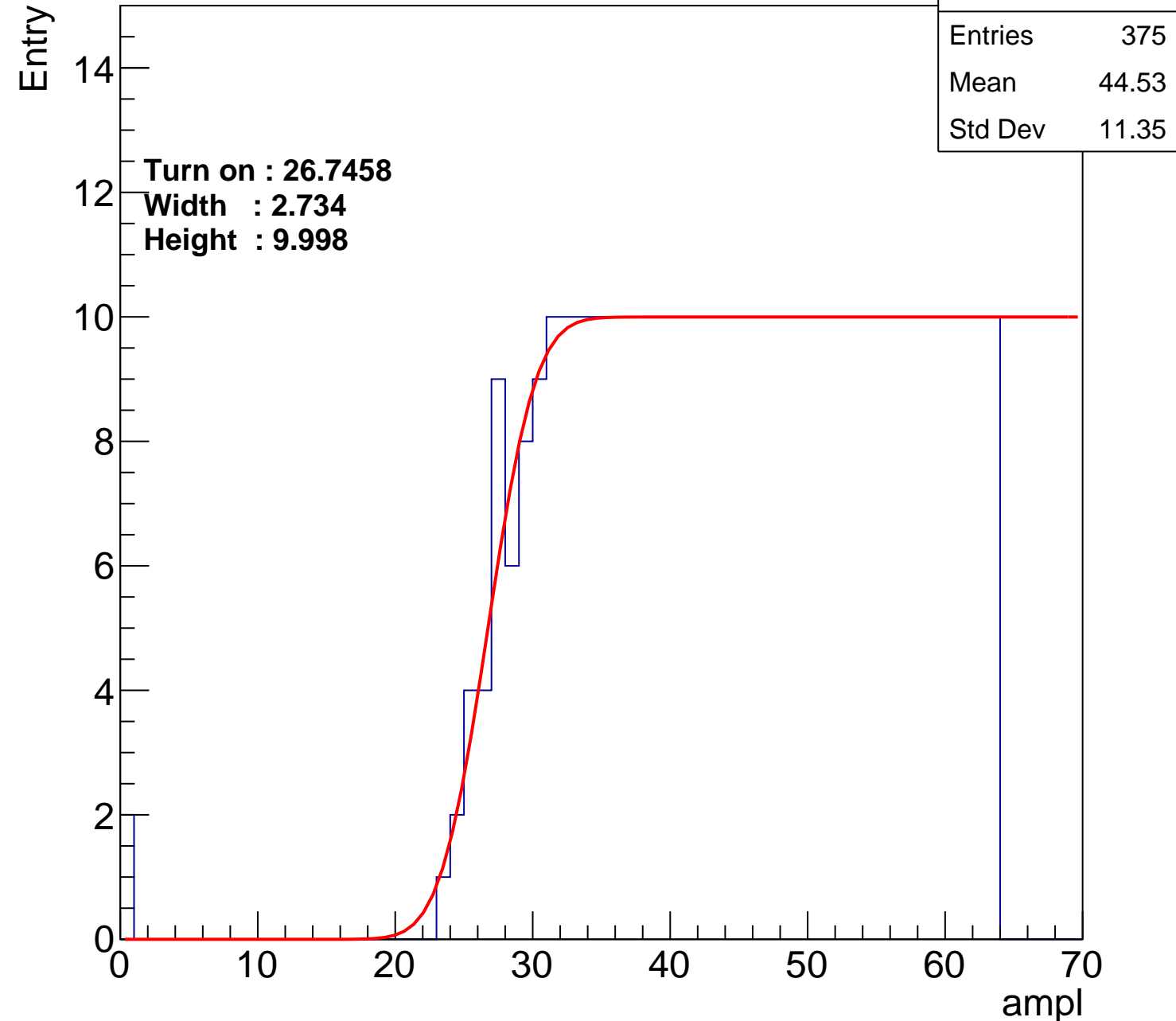
Width : 2.734

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.94
Std Dev	11.27

Turn on : 27.6631

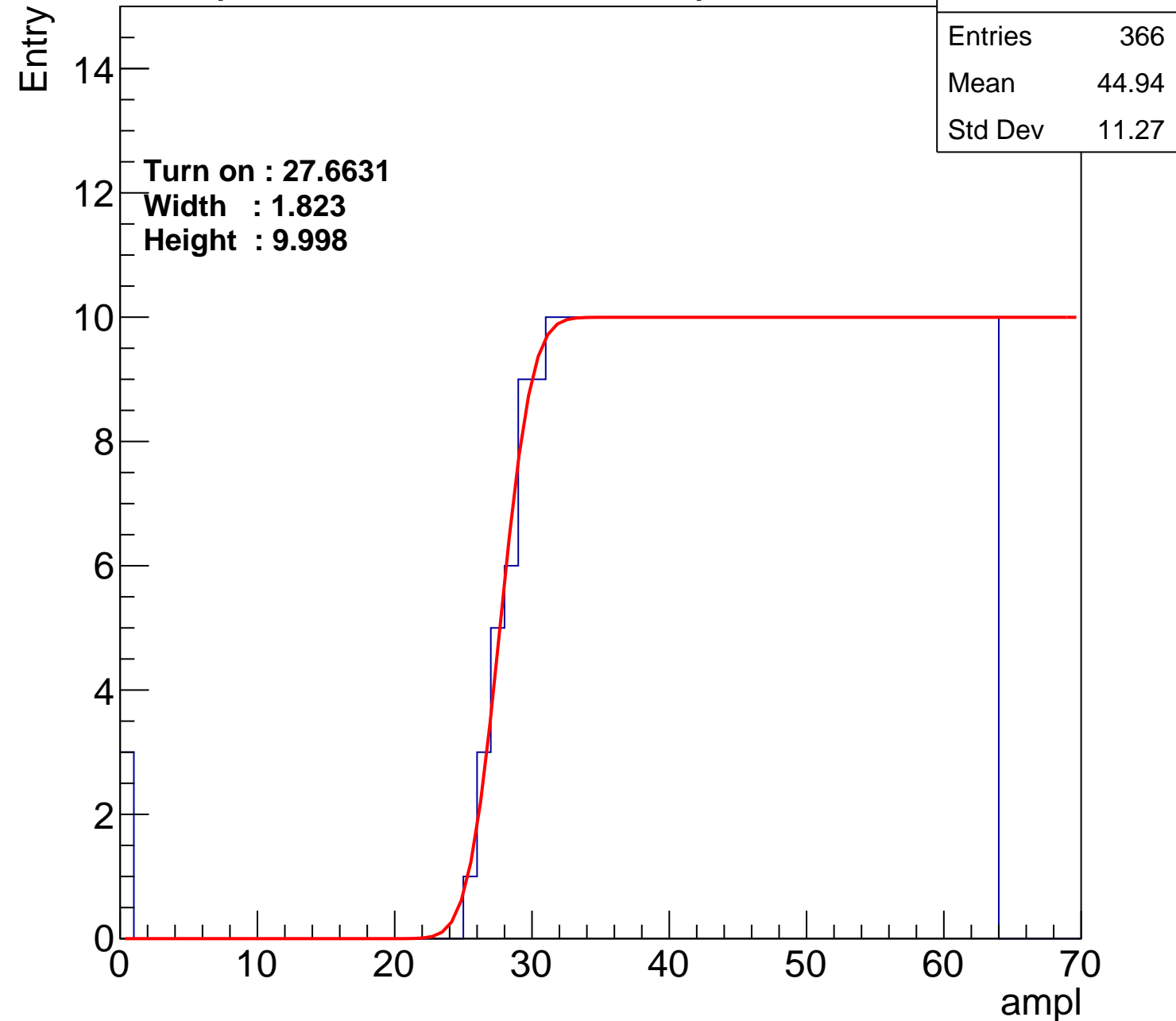
Width : 1.823

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch85

calib_packv5_042523_0143.root, FC#3, port B3

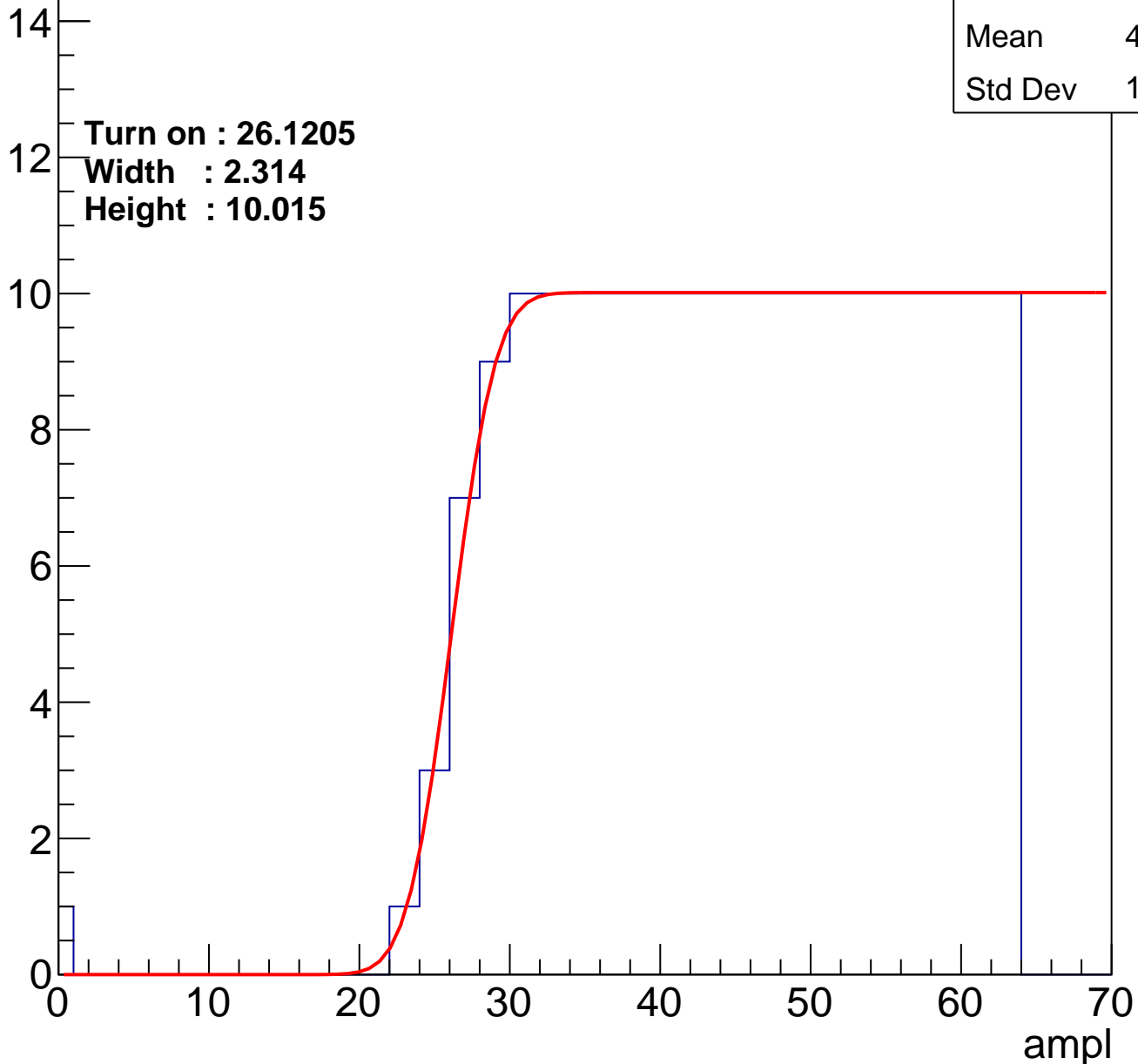
Entries	381
Mean	44.32
Std Dev	11.29

Turn on : 26.1205

Width : 2.314

Height : 10.015

Entry



B1L000S, U11-ch86

calib_packv5_042523_0143.root, FC#3, port B3

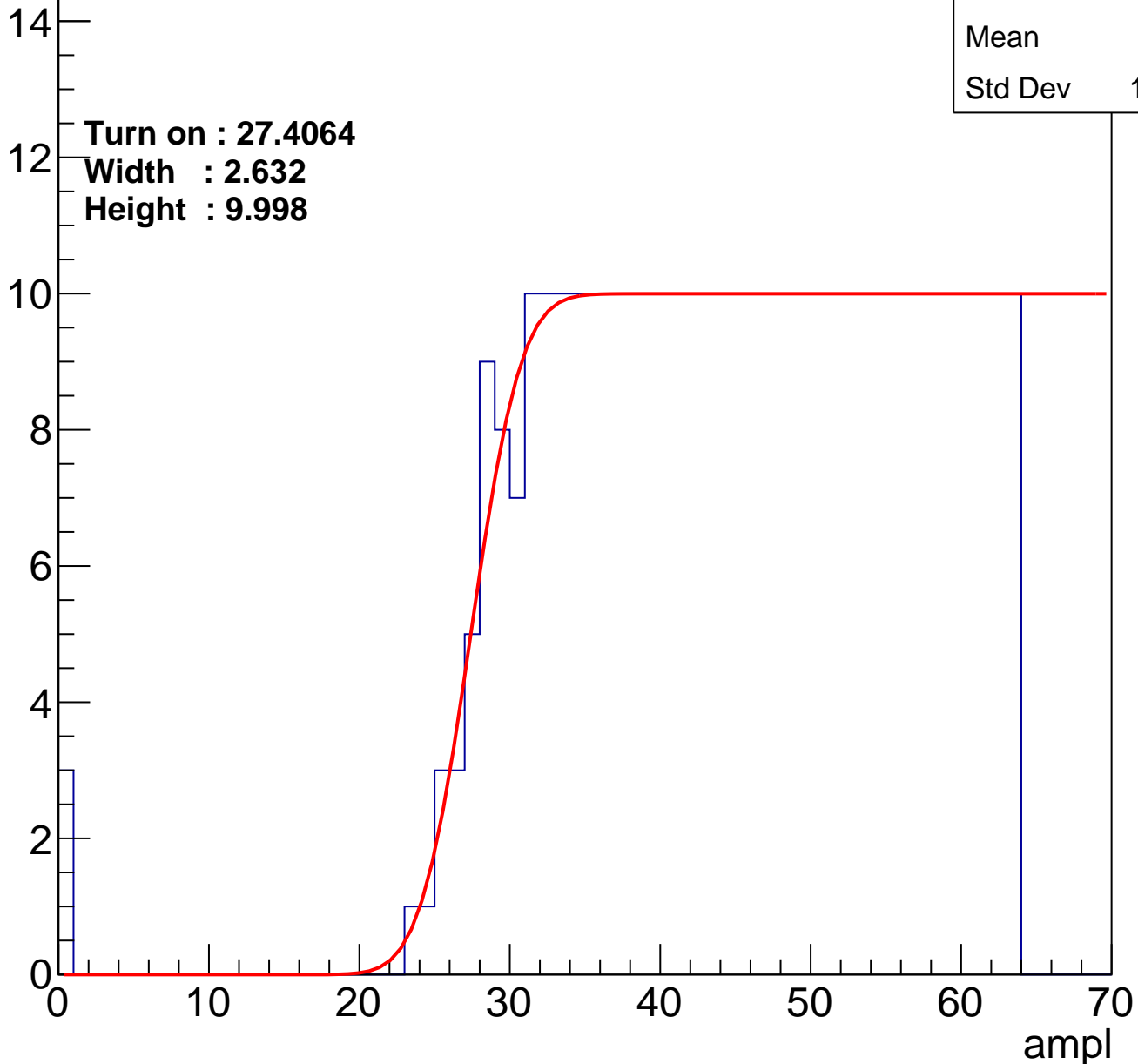
Entries	370
Mean	44.7
Std Dev	11.43

Turn on : 27.4064

Width : 2.632

Height : 9.998

Entry



B1L000S, U11-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.13
Std Dev	12.49

Turn on : 24.8607

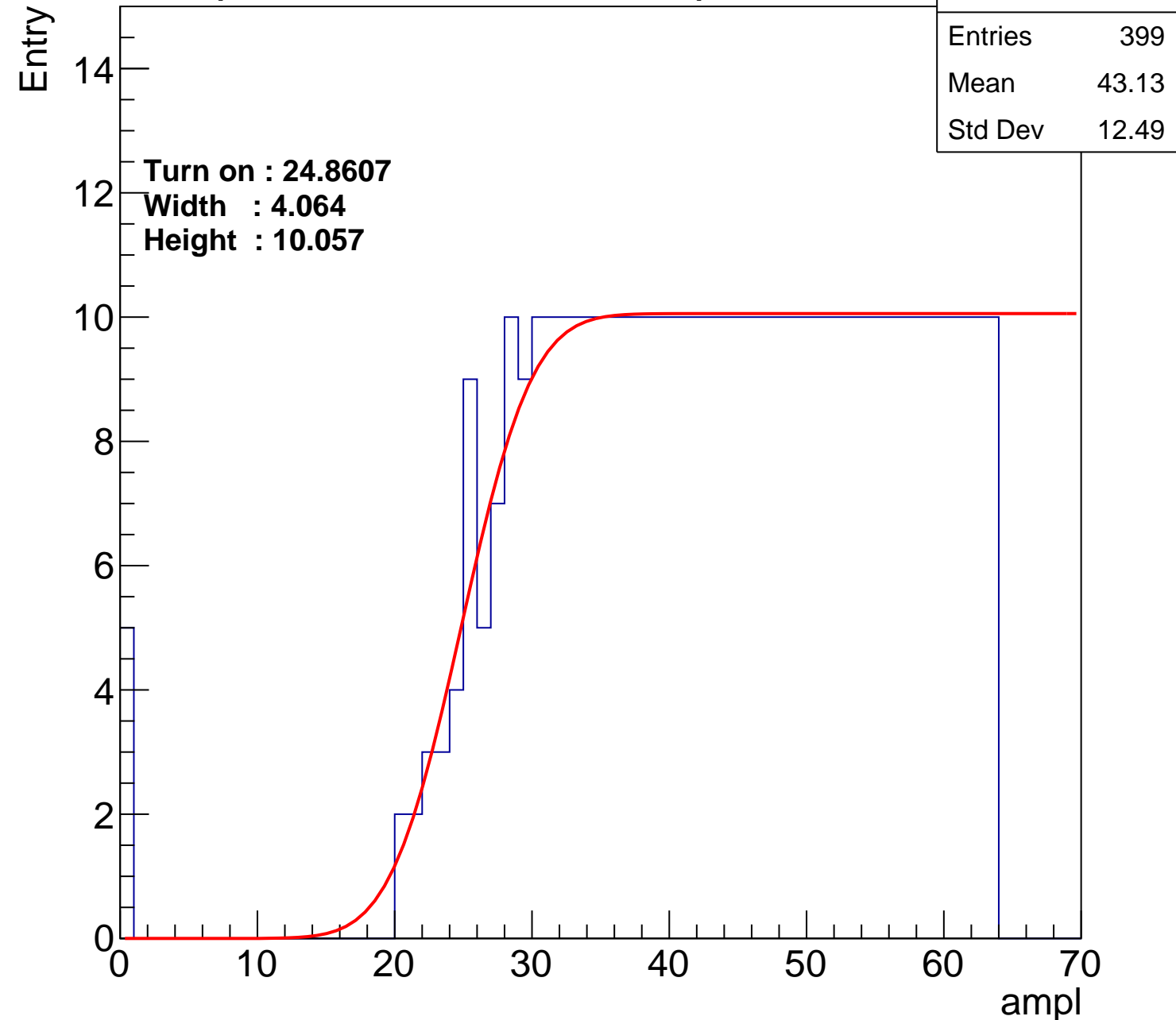
Width : 4.064

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.03
Std Dev	11.79

Turn on : 26.1791

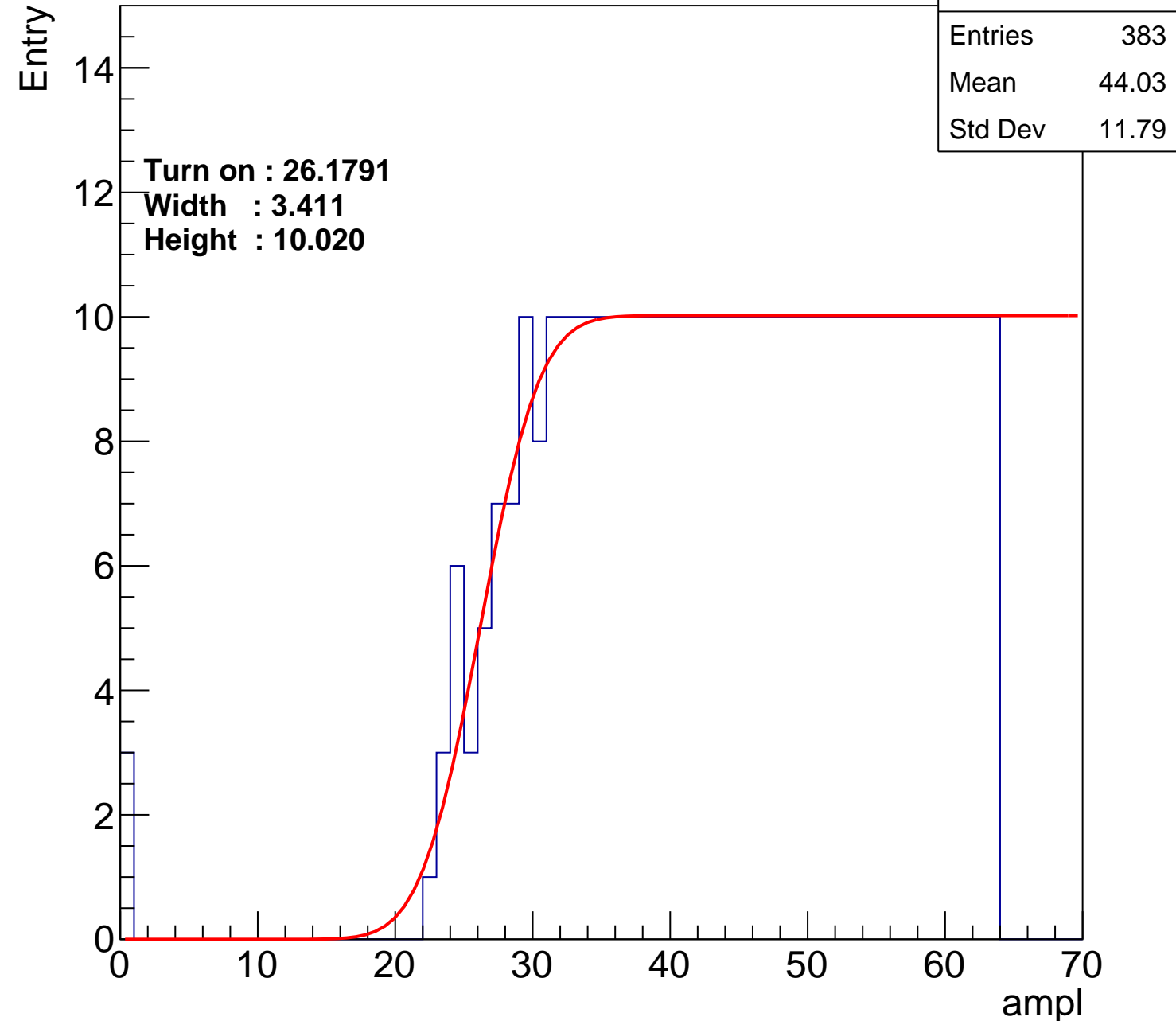
Width : 3.411

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.89
Std Dev	11.15

Turn on : 27.5753

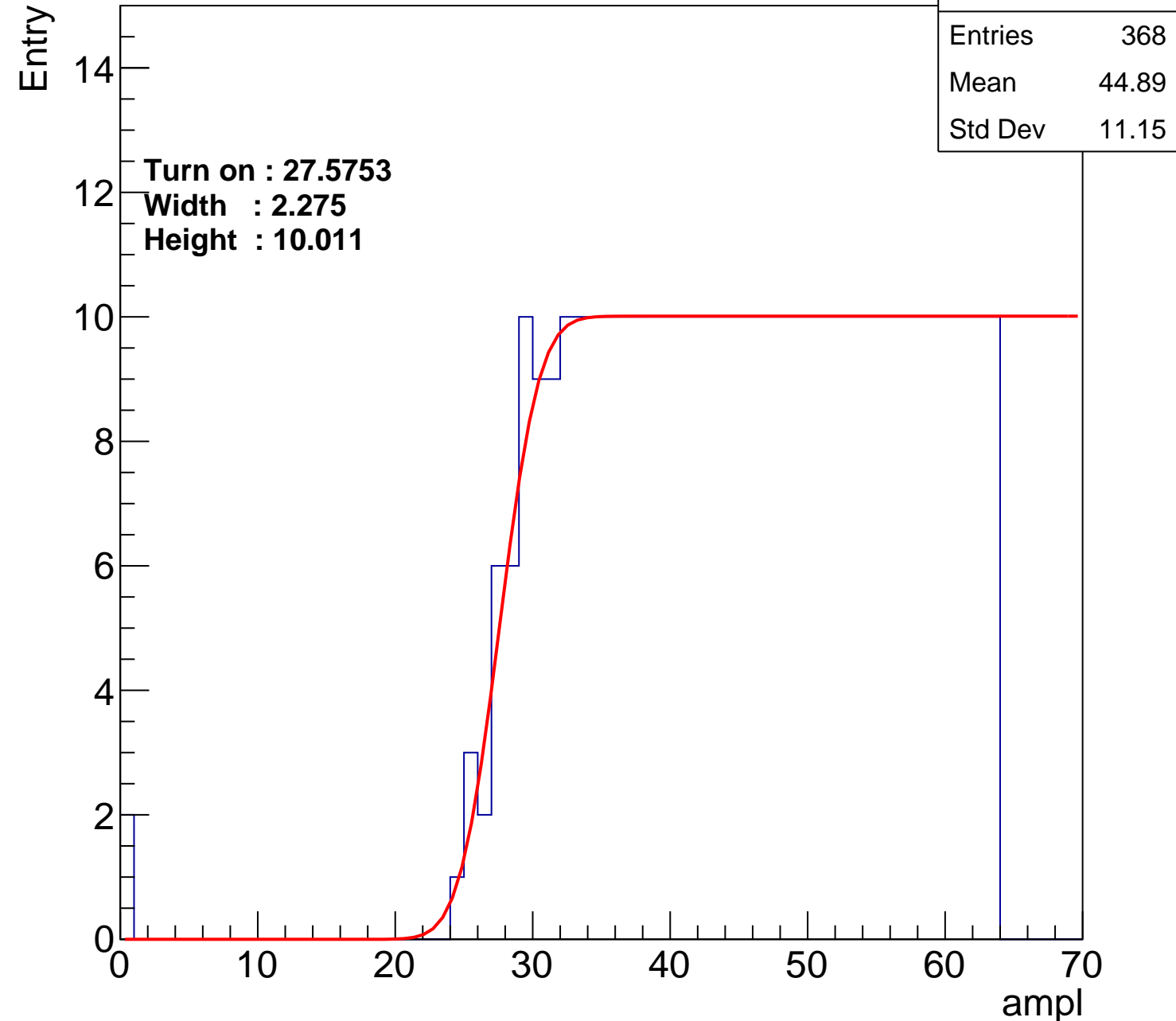
Width : 2.275

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.4434

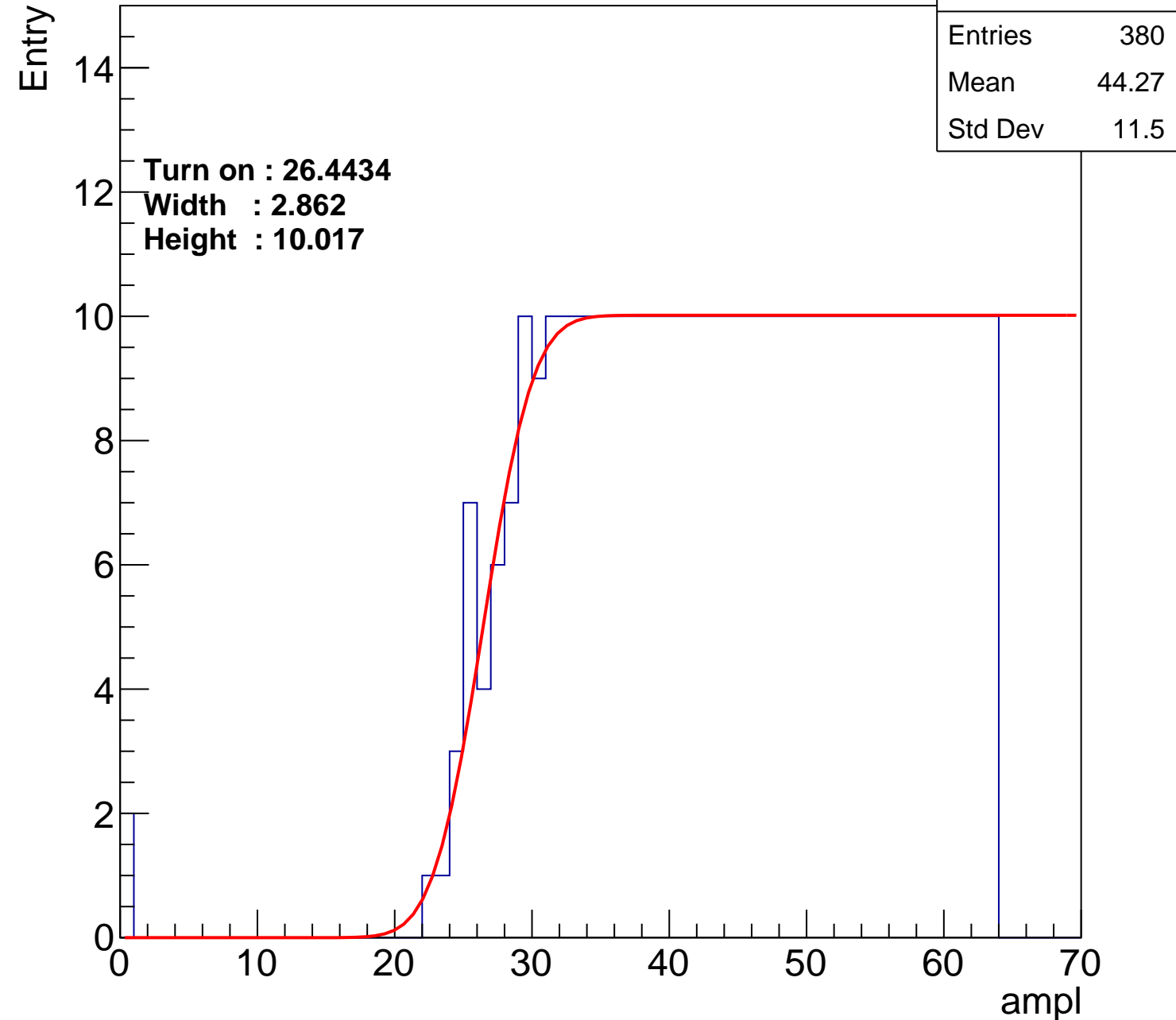
Width : 2.862

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.66
Std Dev	11.83

Turn on : 24.6927

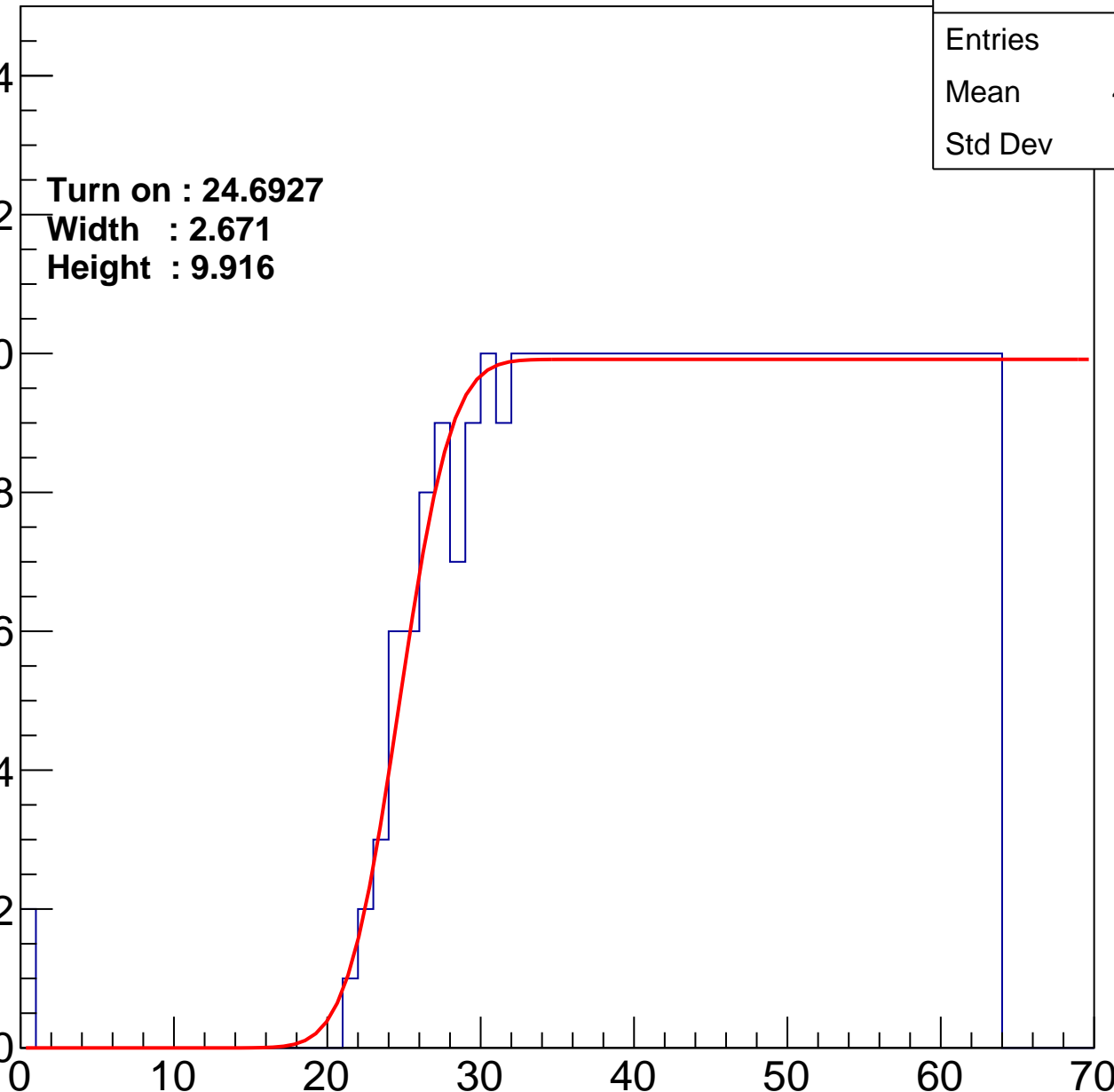
Width : 2.671

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.55
Std Dev	11.86

Turn on : 25.2037

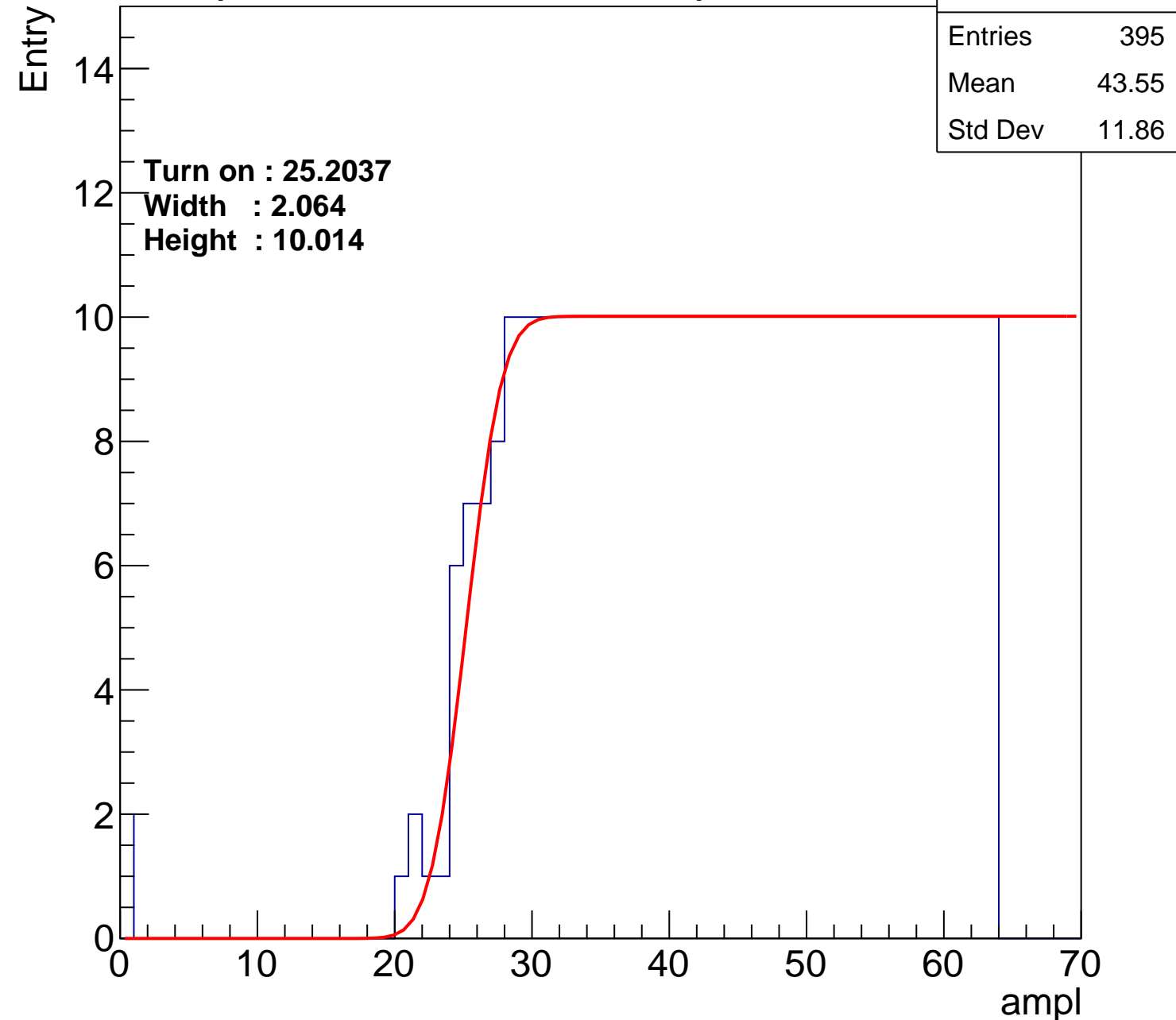
Width : 2.064

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch93

calib_packv5_042523_0143.root, FC#3, port B3

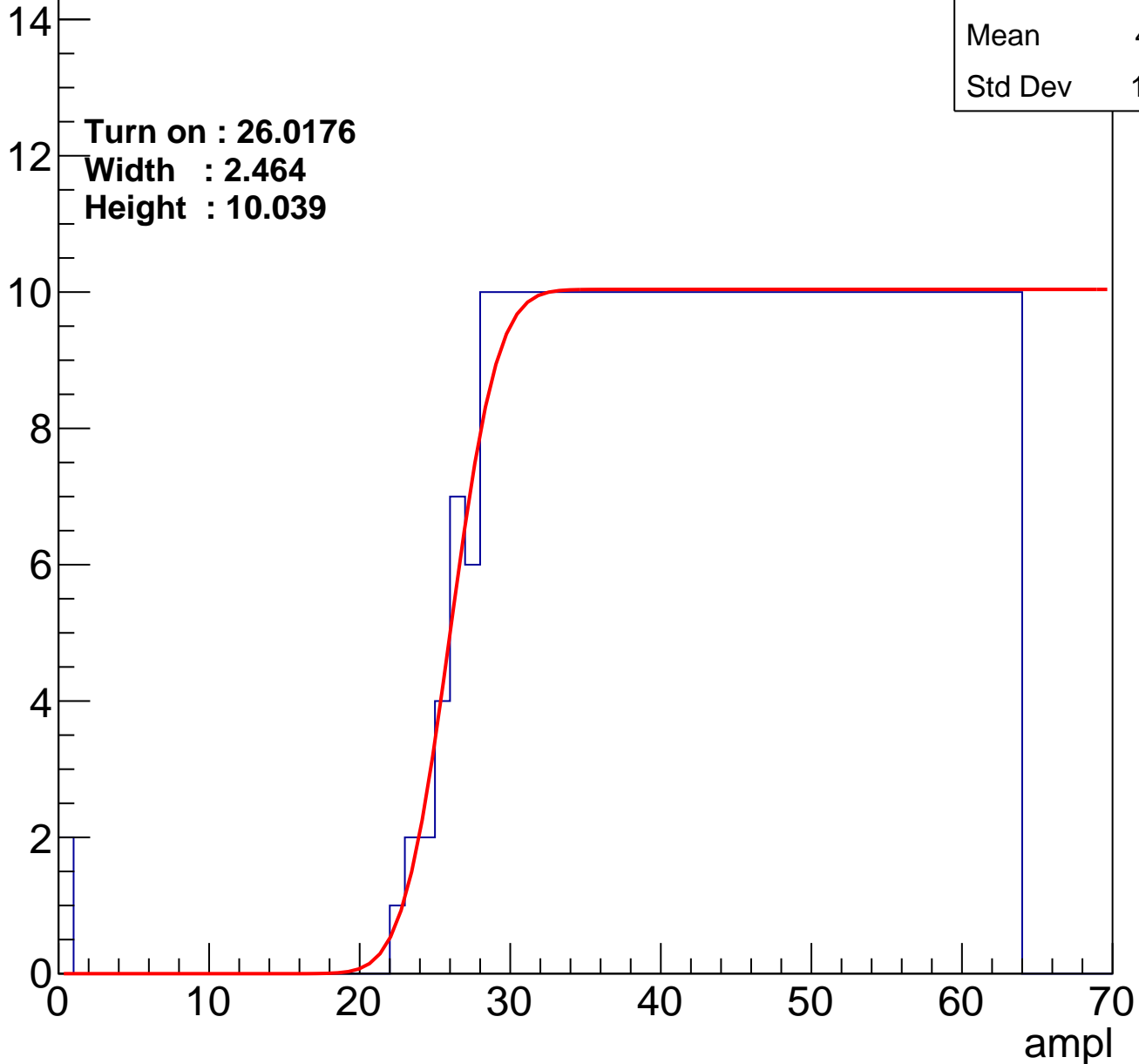
Entries	384
Mean	44.11
Std Dev	11.54

Turn on : 26.0176

Width : 2.464

Height : 10.039

Entry



B1L000S, U11-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.13
Std Dev	11.51

Turn on : 25.7383

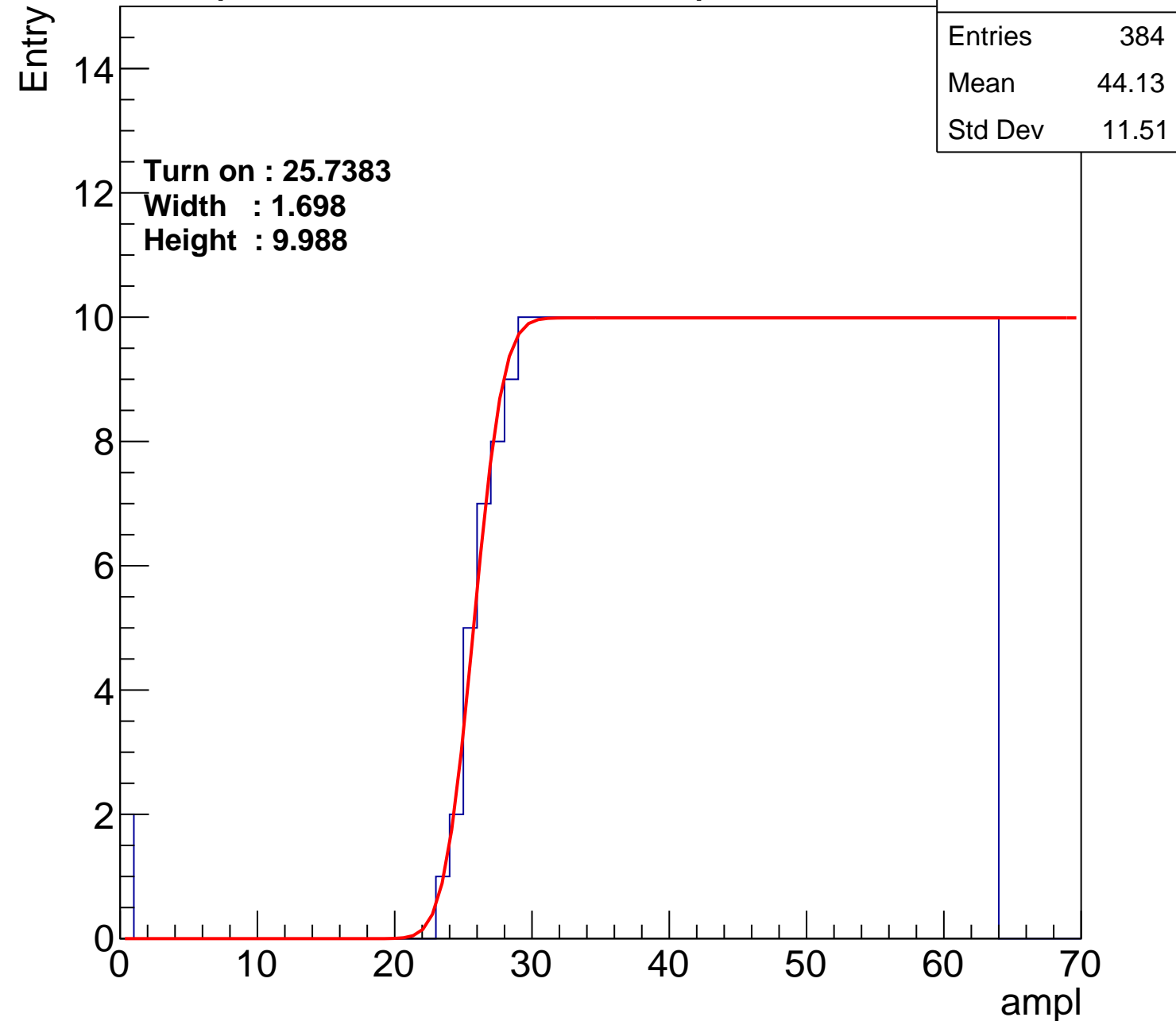
Width : 1.698

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.21
Std Dev	11.49

Turn on : 26.2205

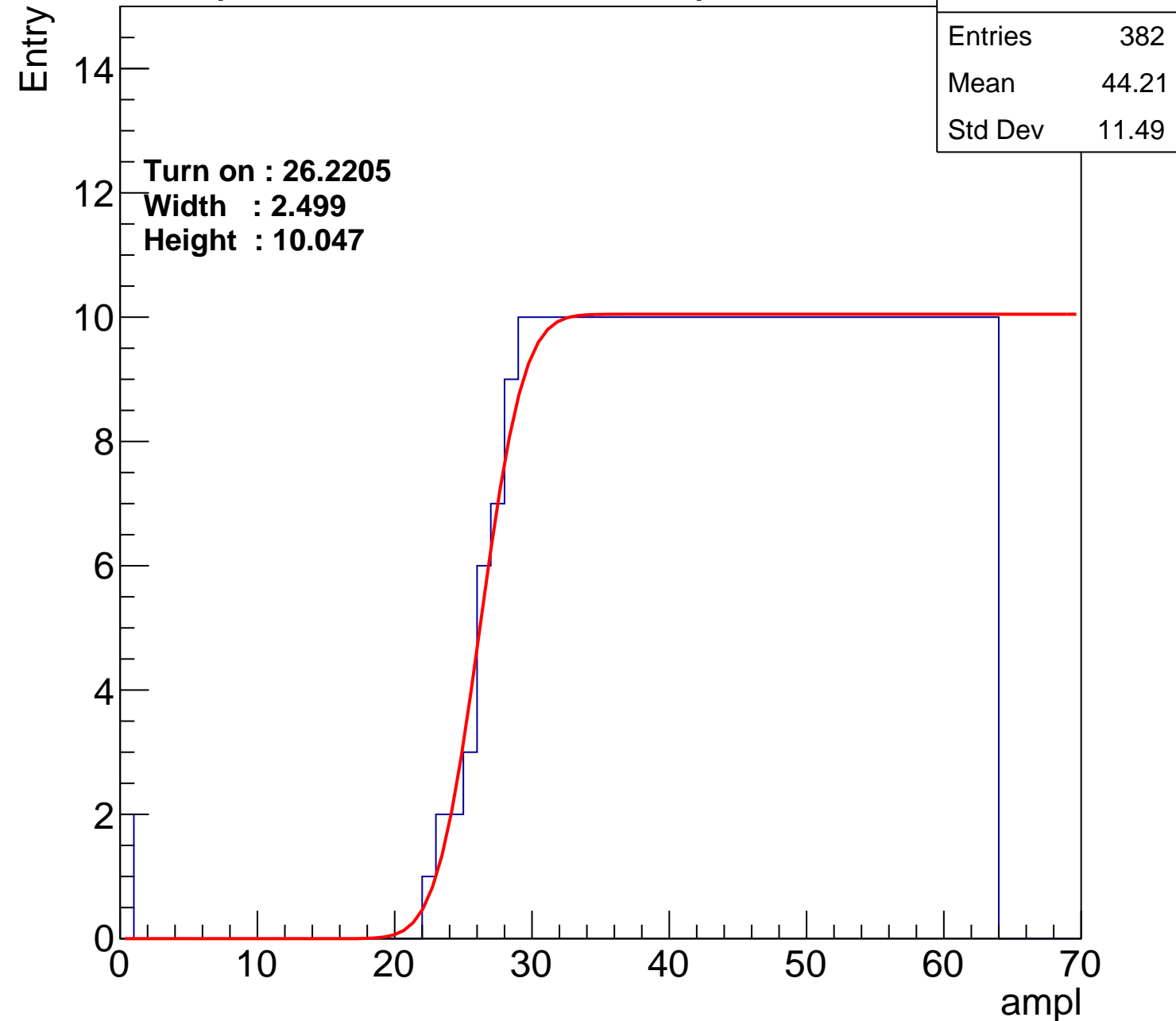
Width : 2.499

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 25.2278
Width : 2.968
Height : 10.039



B1L000S, U11-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.73
Std Dev	11.78

Turn on : 25.0277

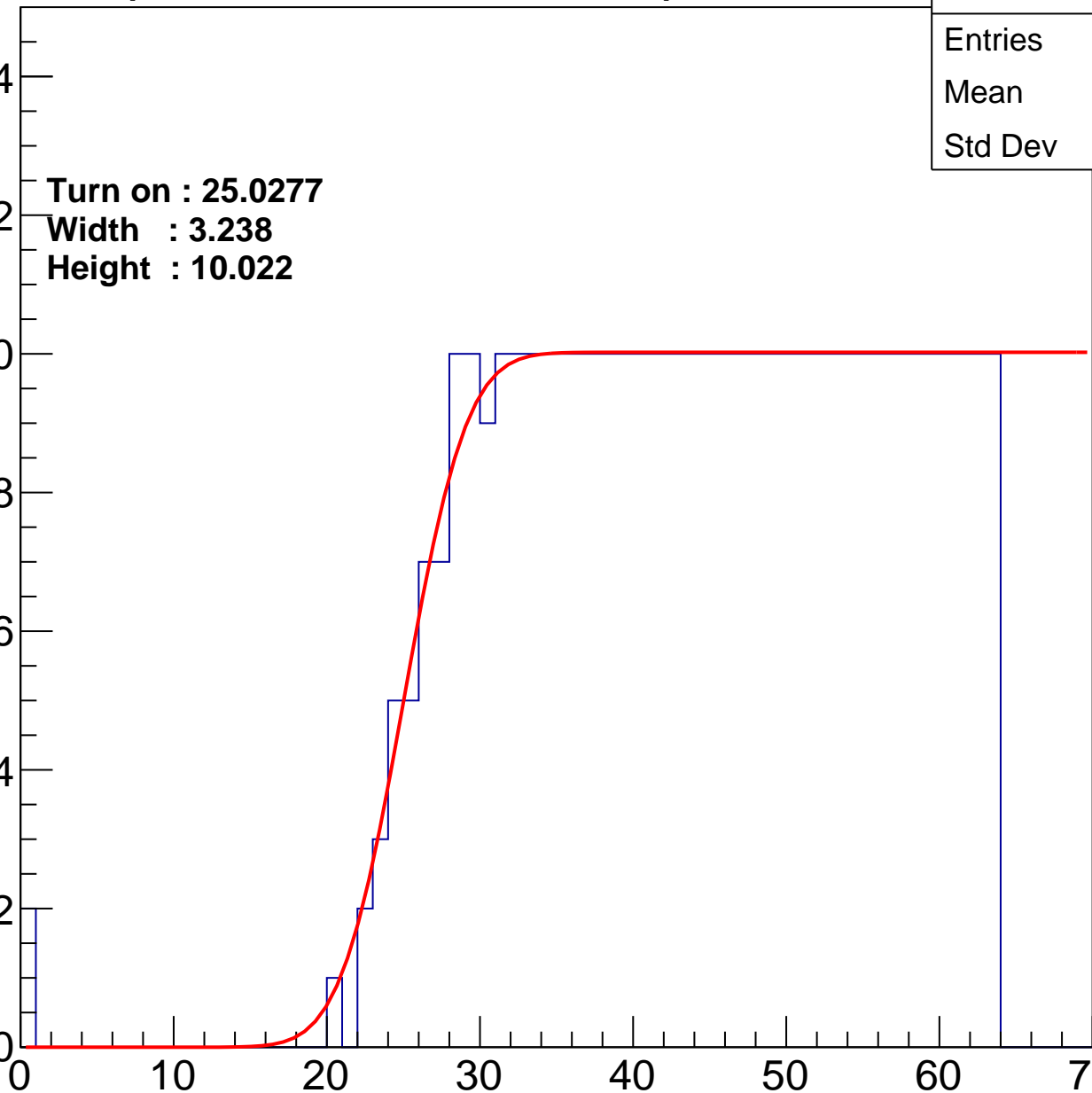
Width : 3.238

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.38
Std Dev	11.42

Turn on : 26.2692

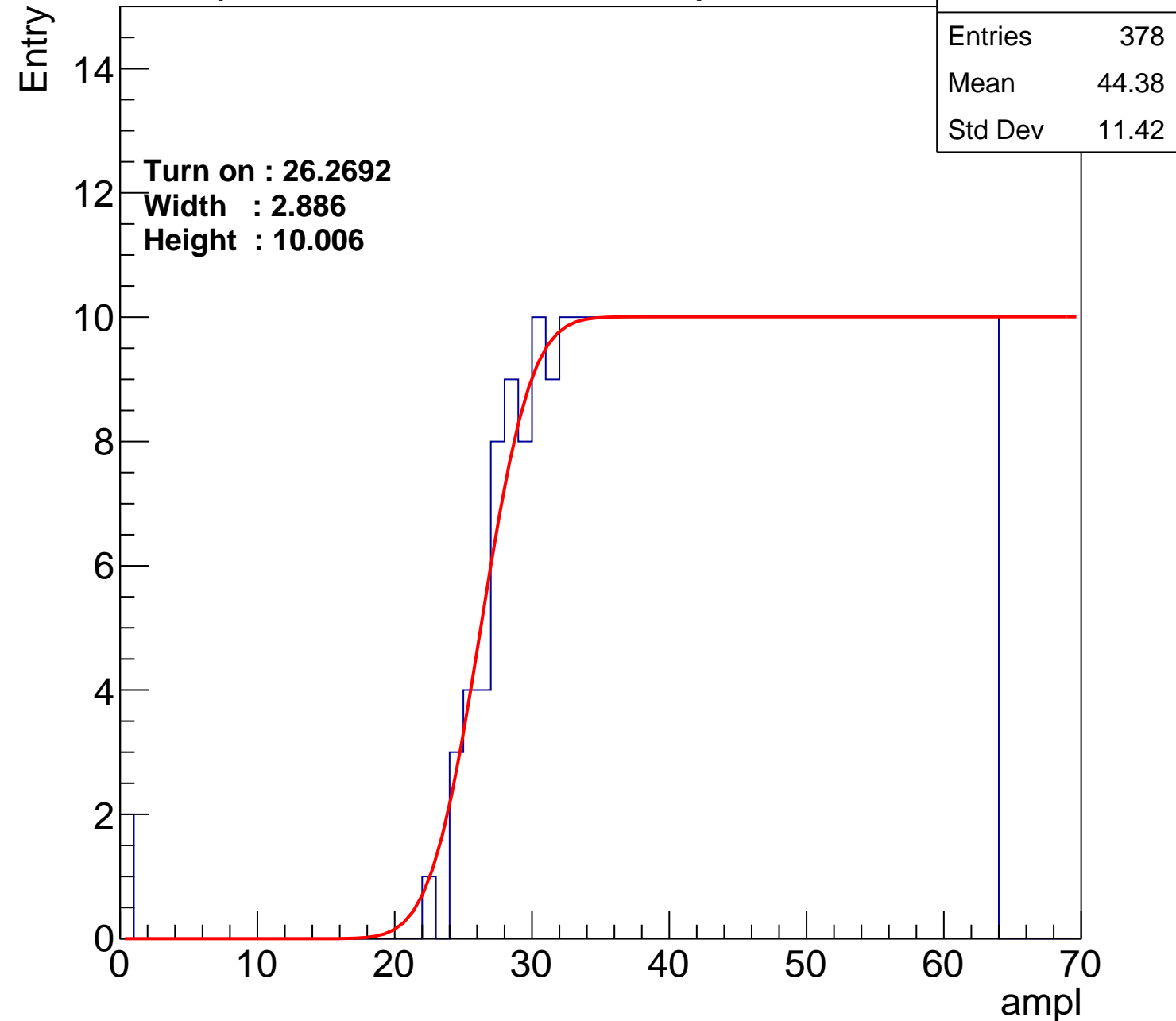
Width : 2.886

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.2905

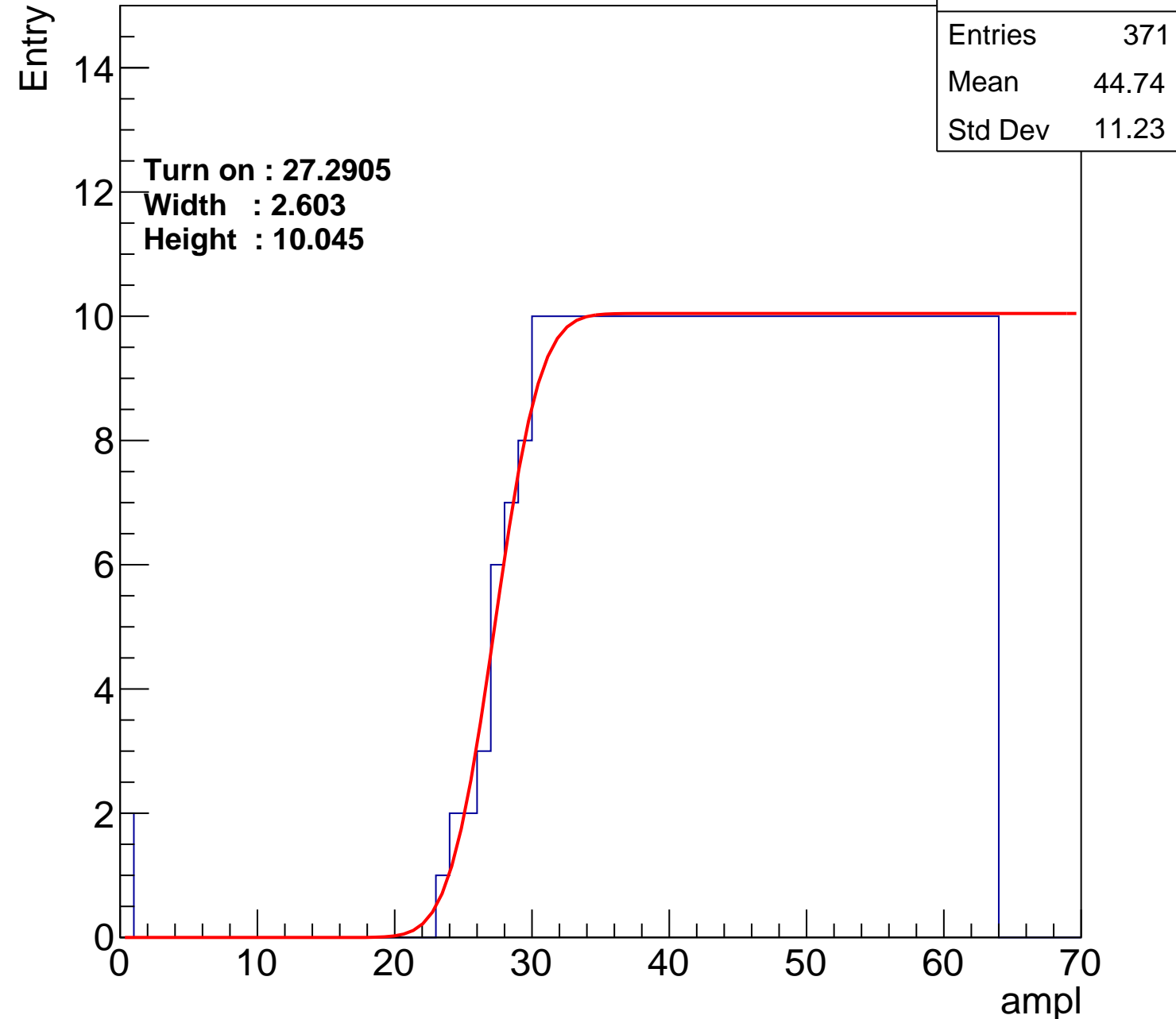
Width : 2.603

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.05
Std Dev	12.02

Turn on : 26.7416

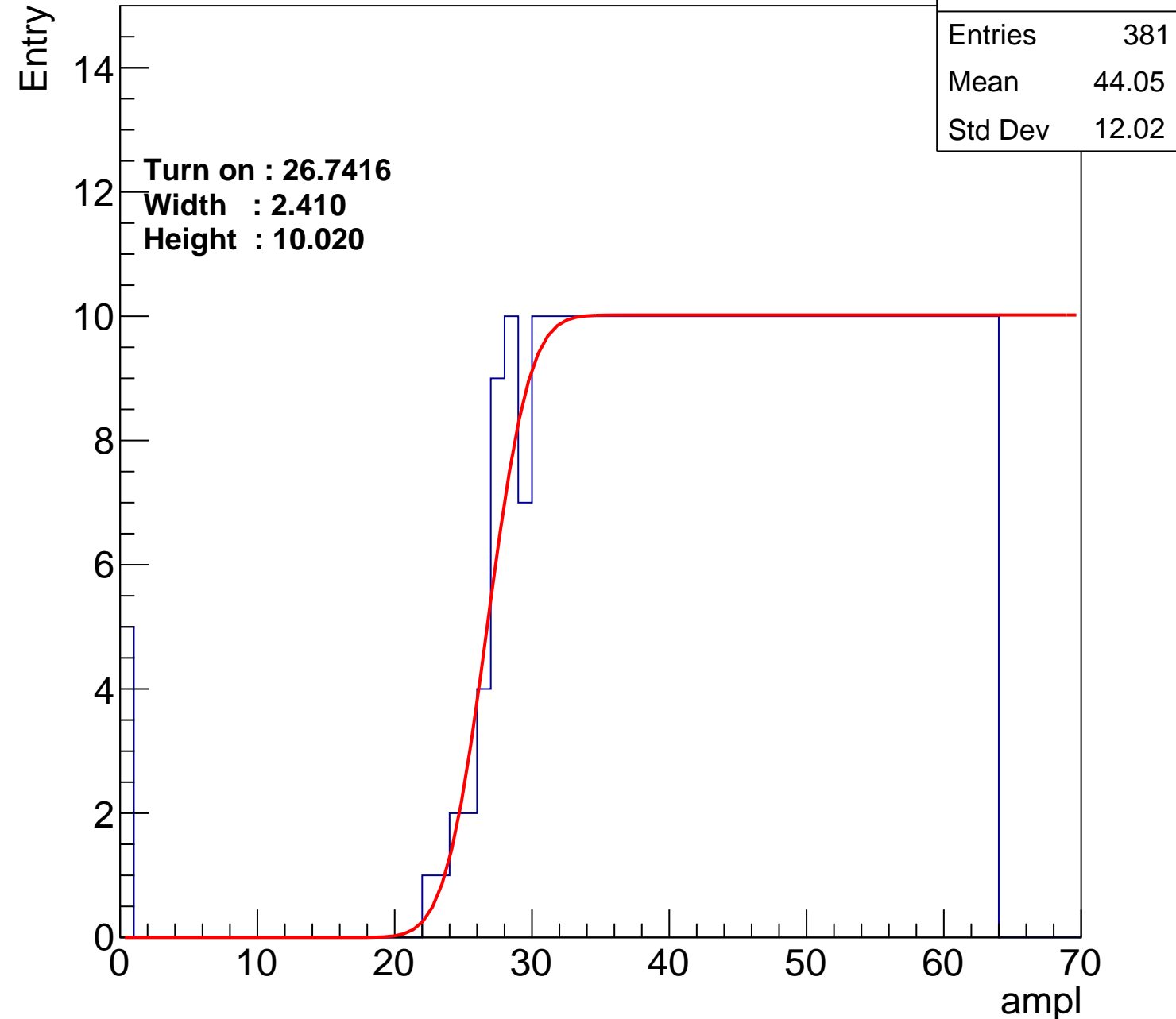
Width : 2.410

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.93
Std Dev	11.34

Turn on : 28.0824

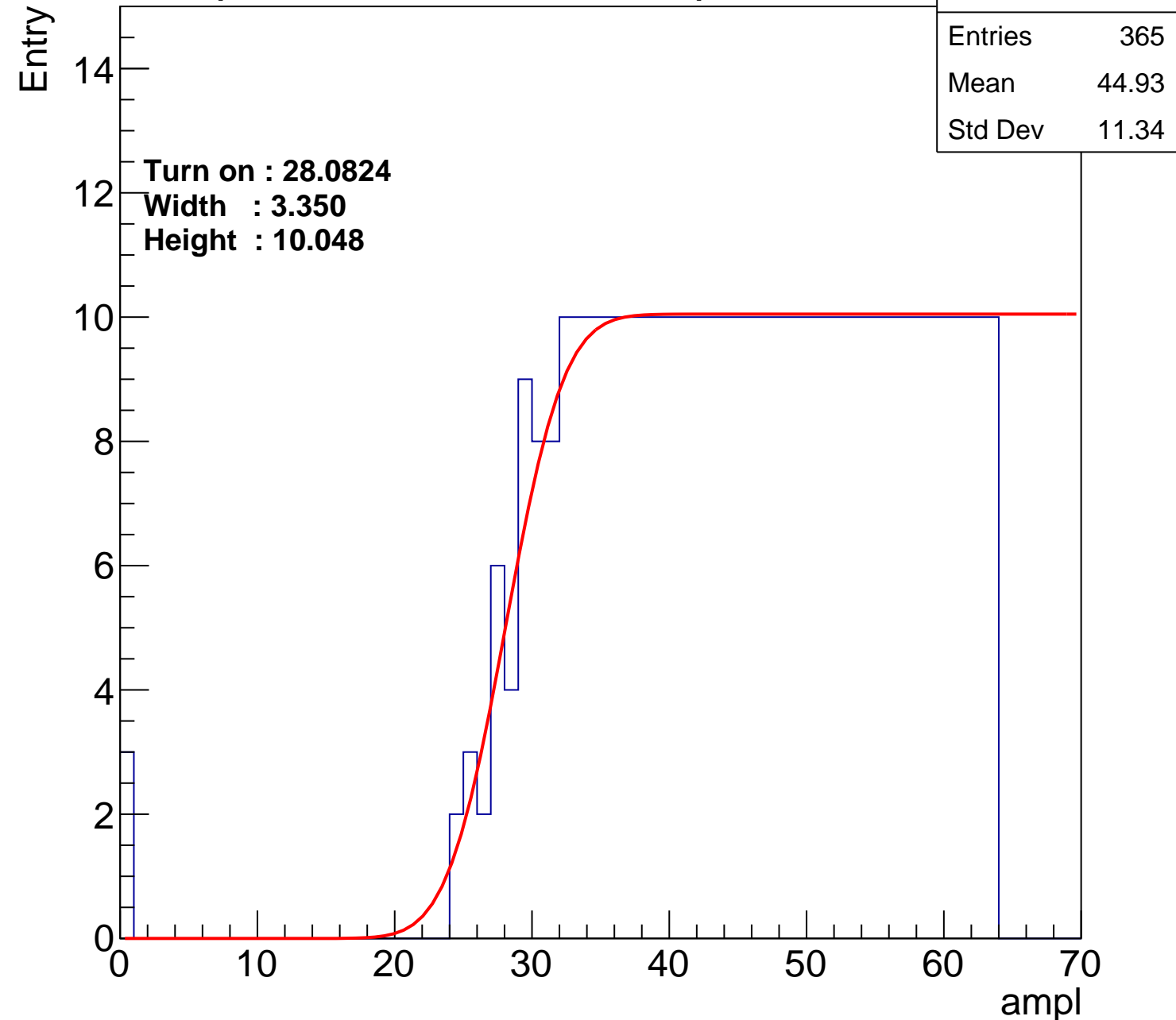
Width : 3.350

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.64
Std Dev	11.96

Turn on : 27.6678

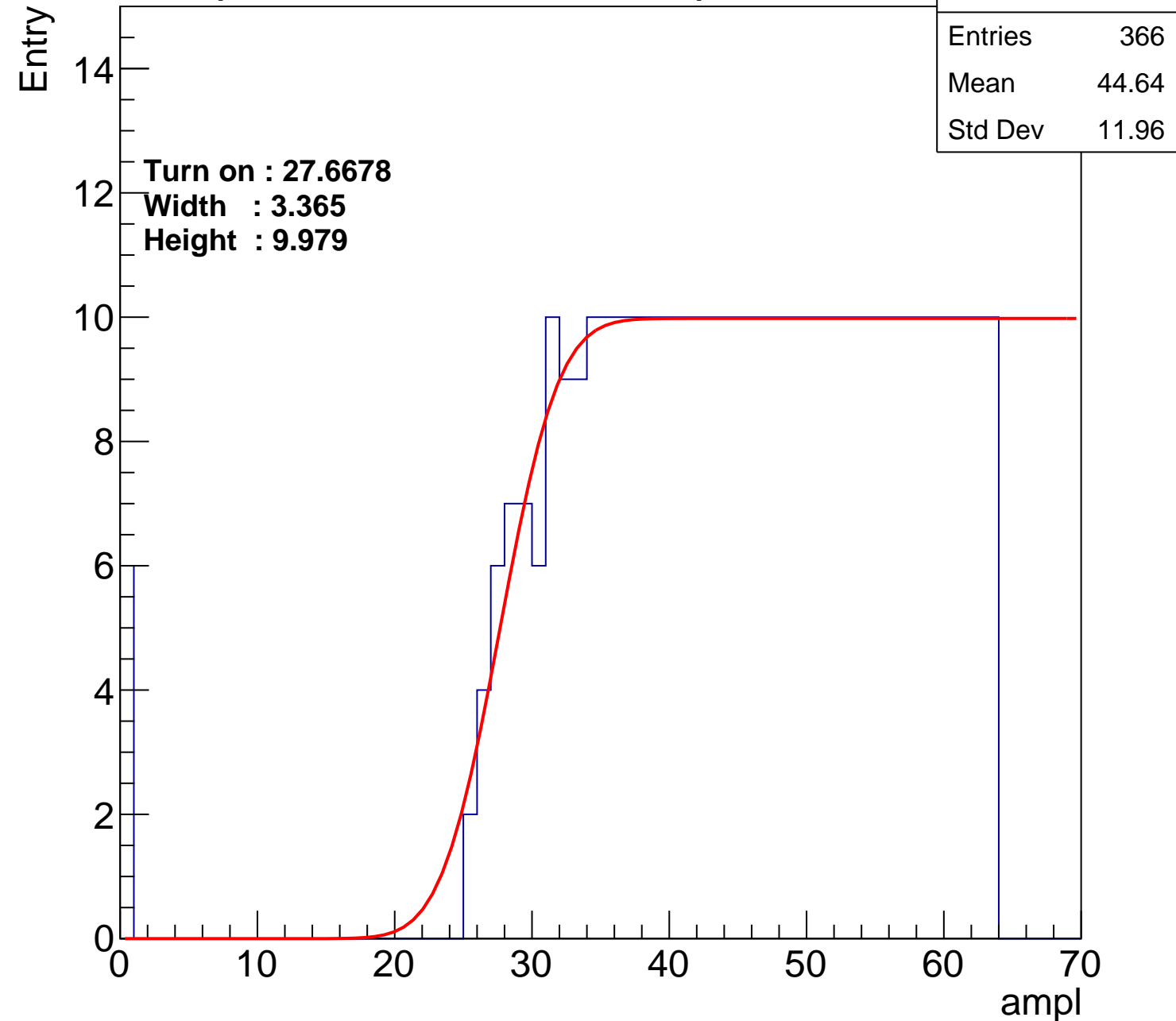
Width : 3.365

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.97
Std Dev	11.18

Turn on : 27.2762

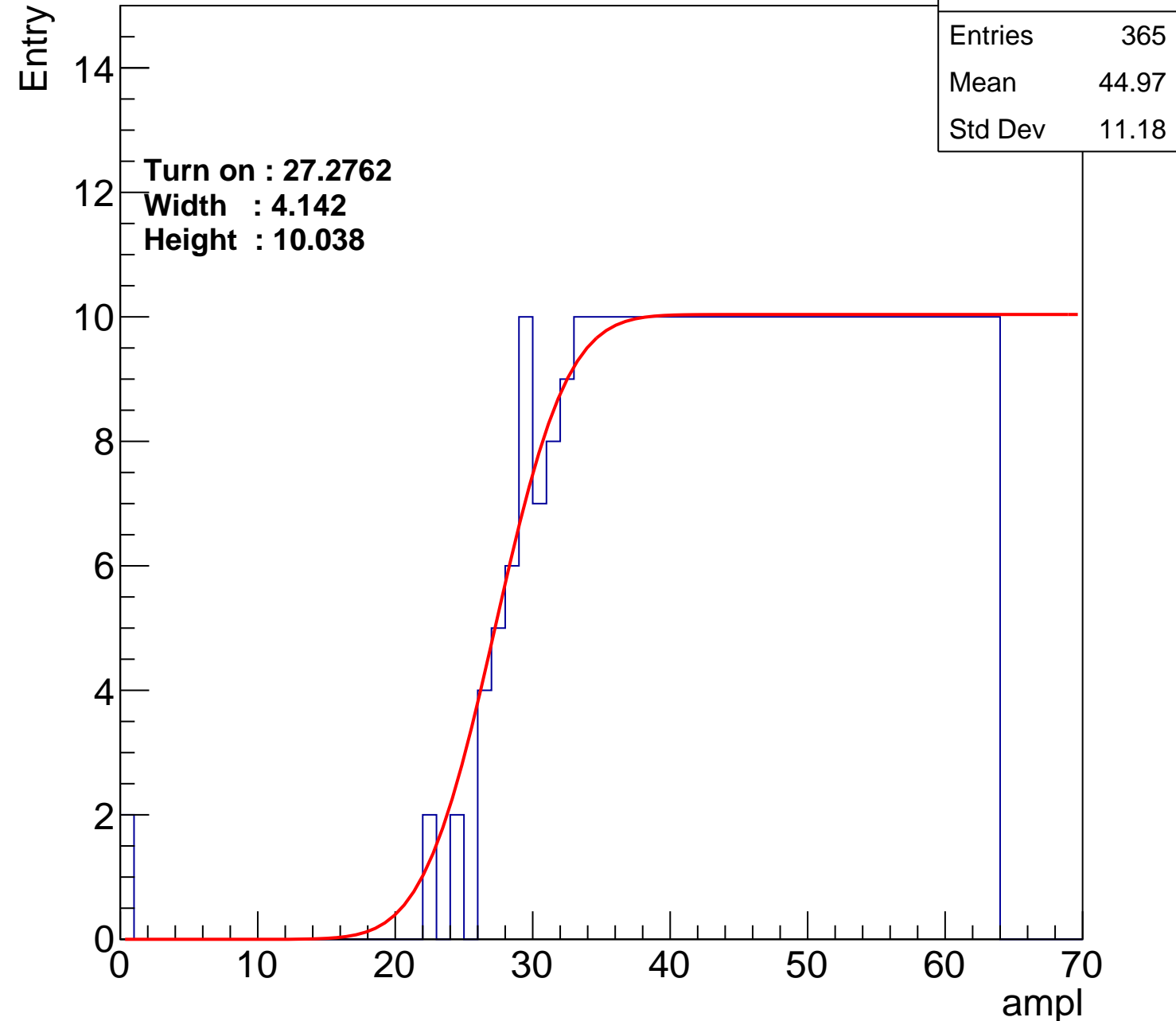
Width : 4.142

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.64
Std Dev	11.46

Turn on : 27.0687

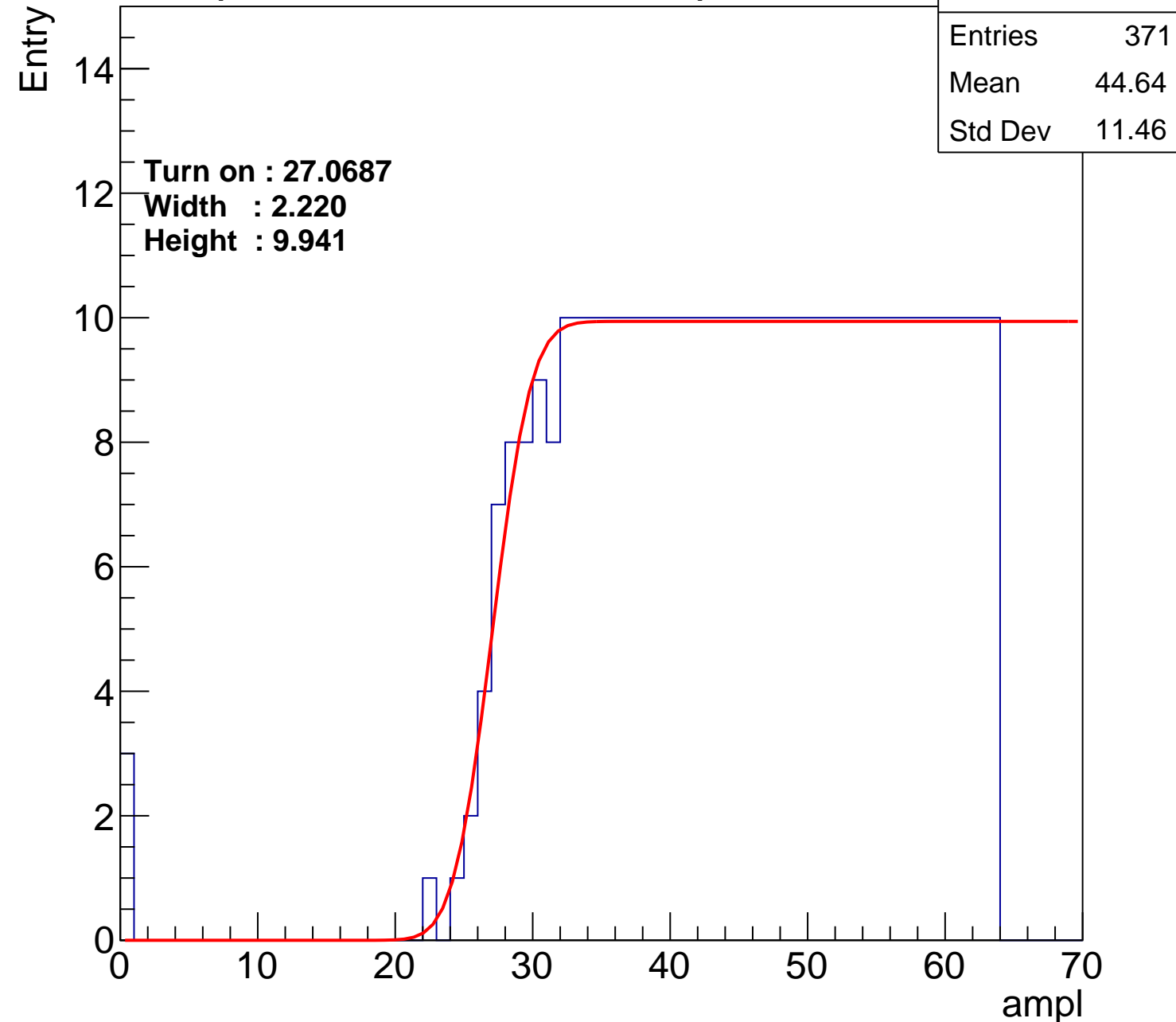
Width : 2.220

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.12
Std Dev	11.39

Turn on : 25.7881

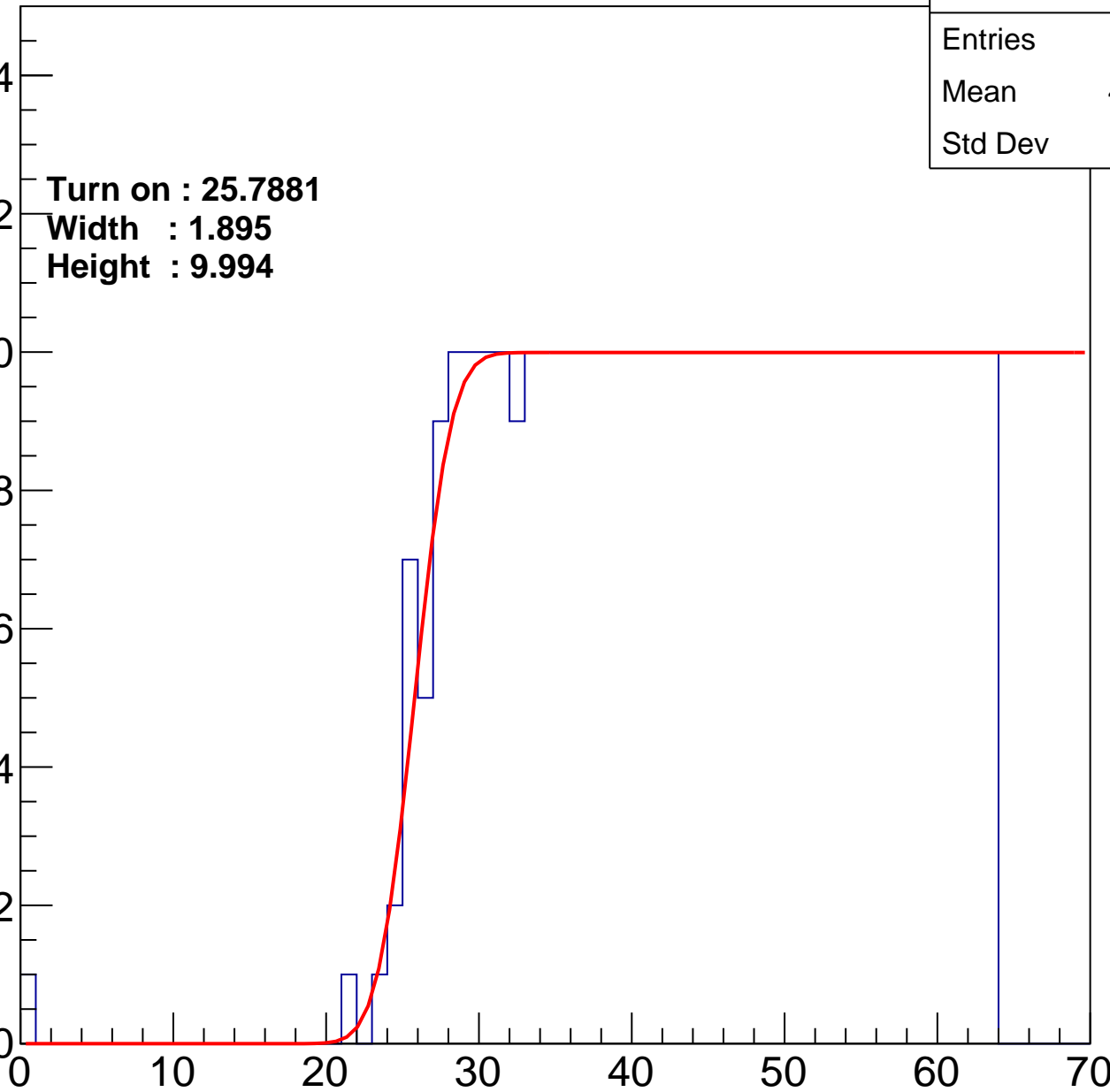
Width : 1.895

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.61
Std Dev	11.15

Turn on : 26.6514

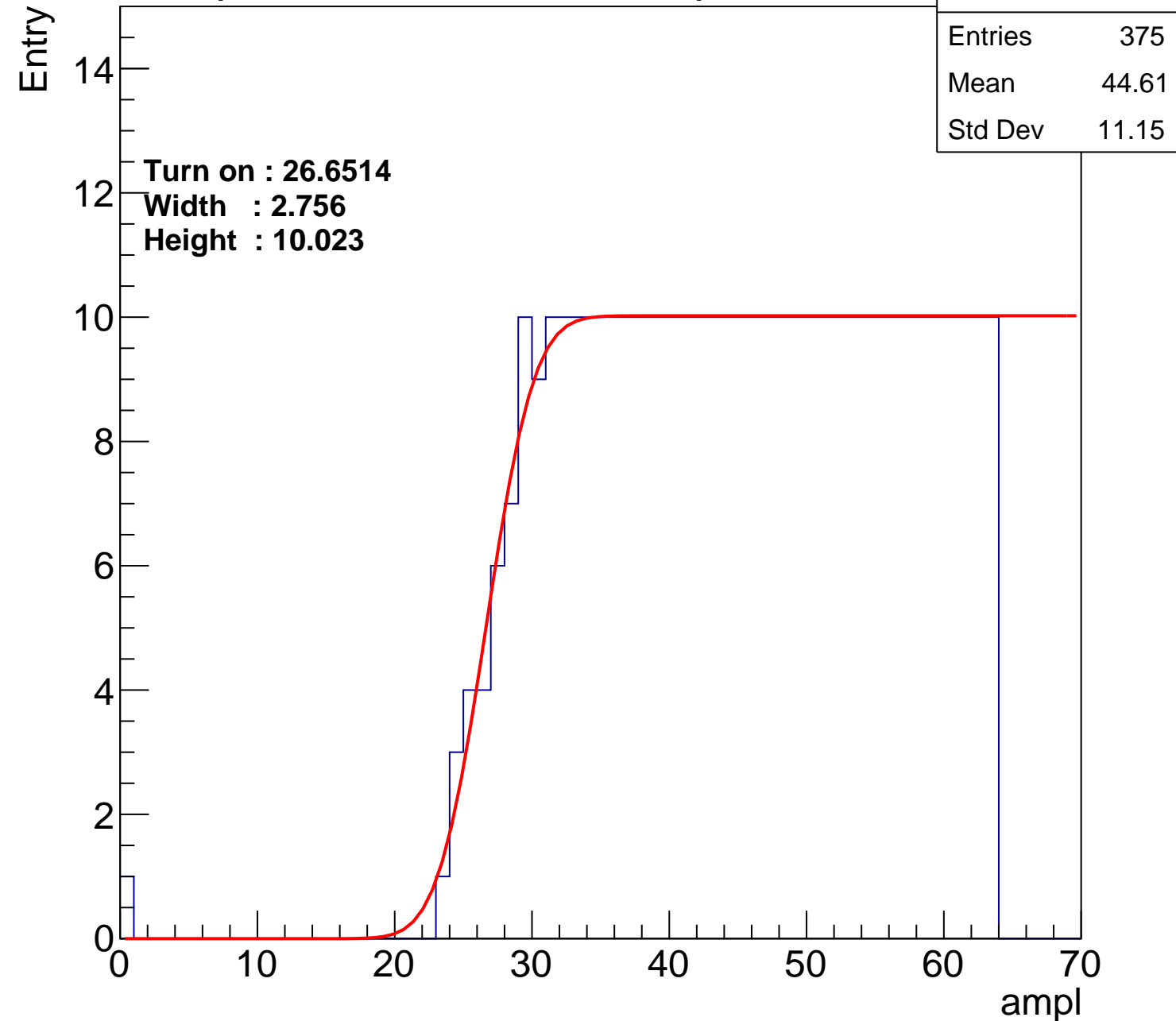
Width : 2.756

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.26
Std Dev	11.66

Turn on : 26.8747

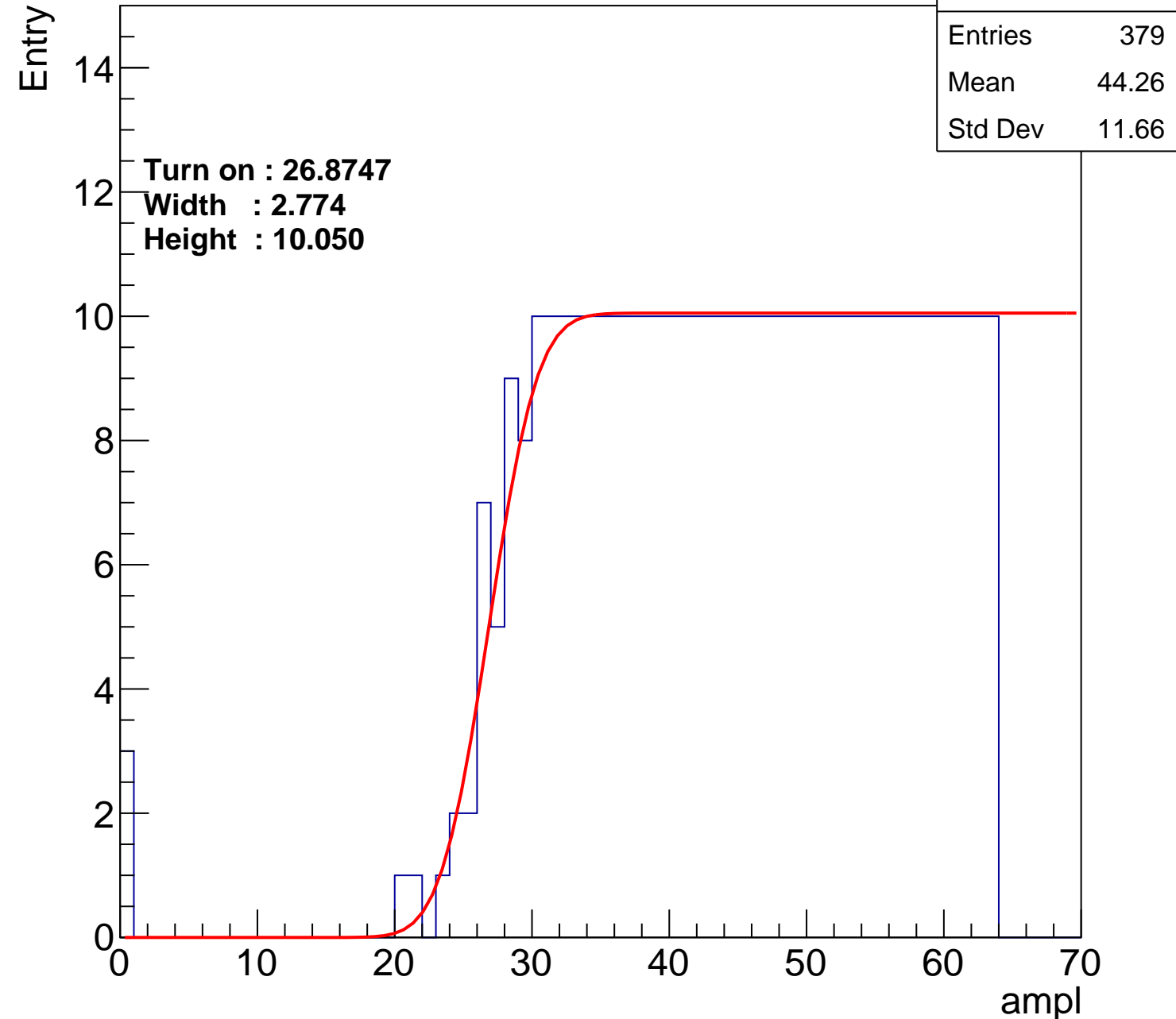
Width : 2.774

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch108

calib_packv5_042523_0143.root, FC#3, port B3

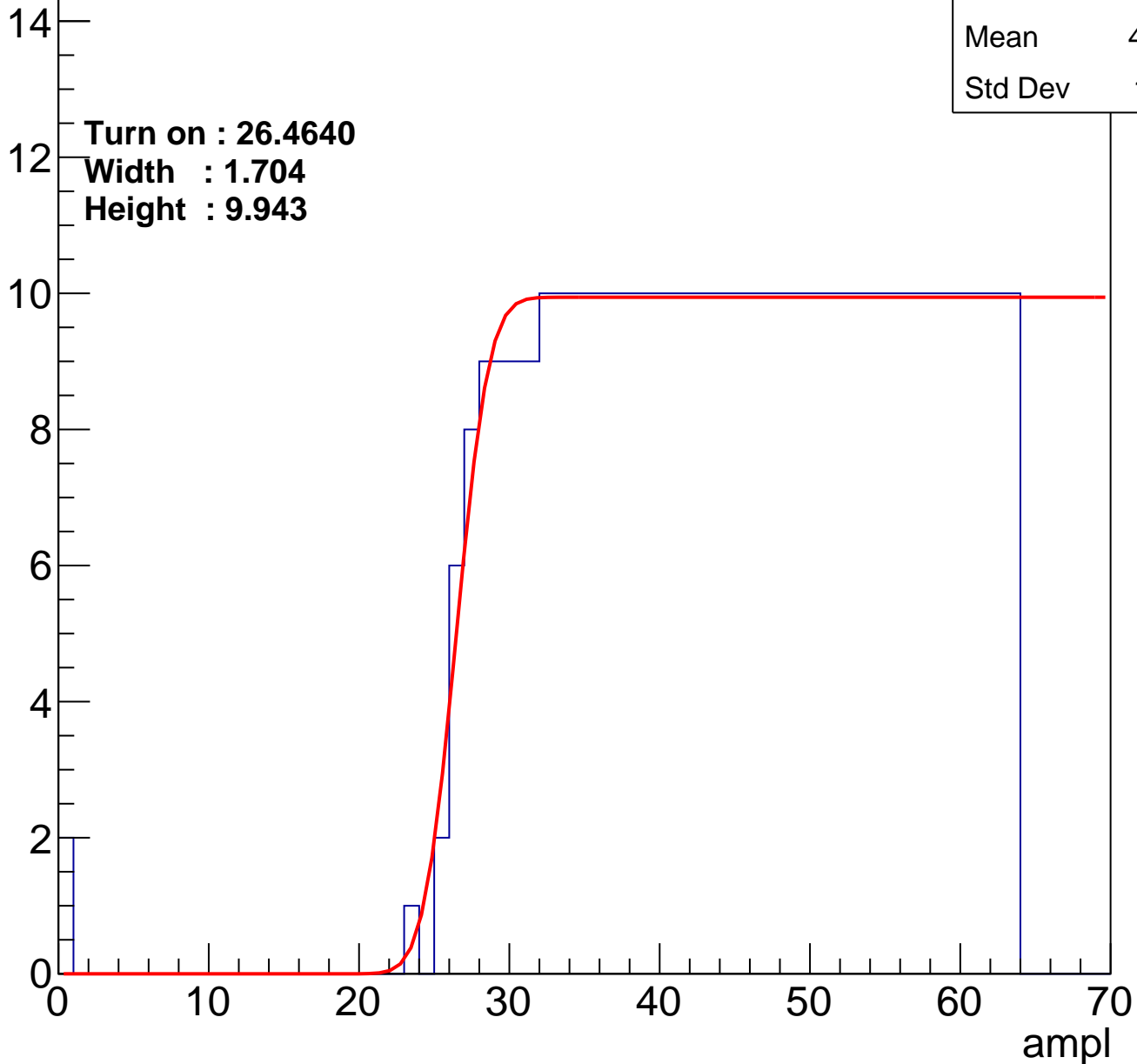
Entries	375
Mean	44.55
Std Dev	11.31

Turn on : 26.4640

Width : 1.704

Height : 9.943

Entry



B1L000S, U11-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.44
Std Dev	11.71

Turn on : 26.9971

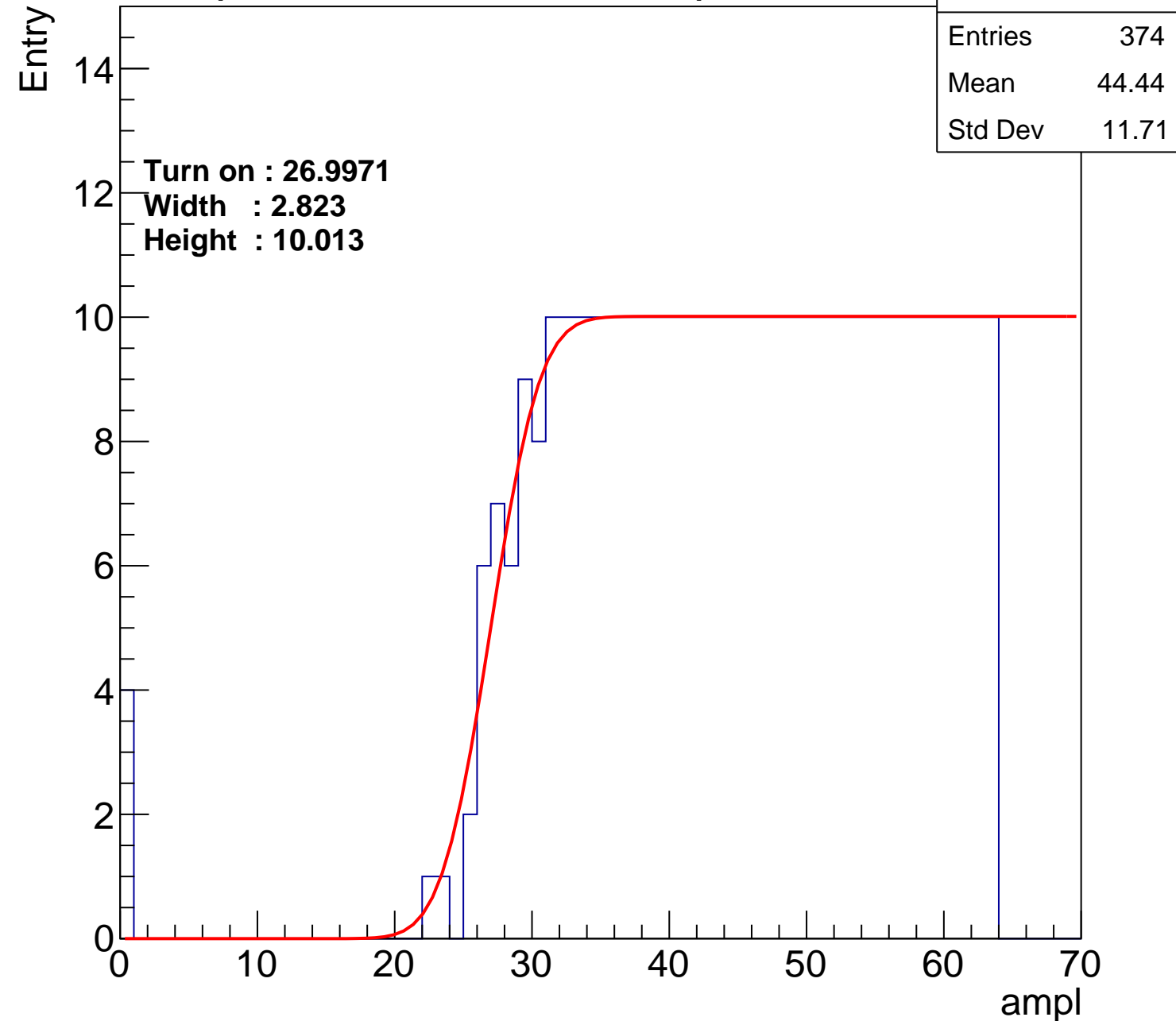
Width : 2.823

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 25.2042

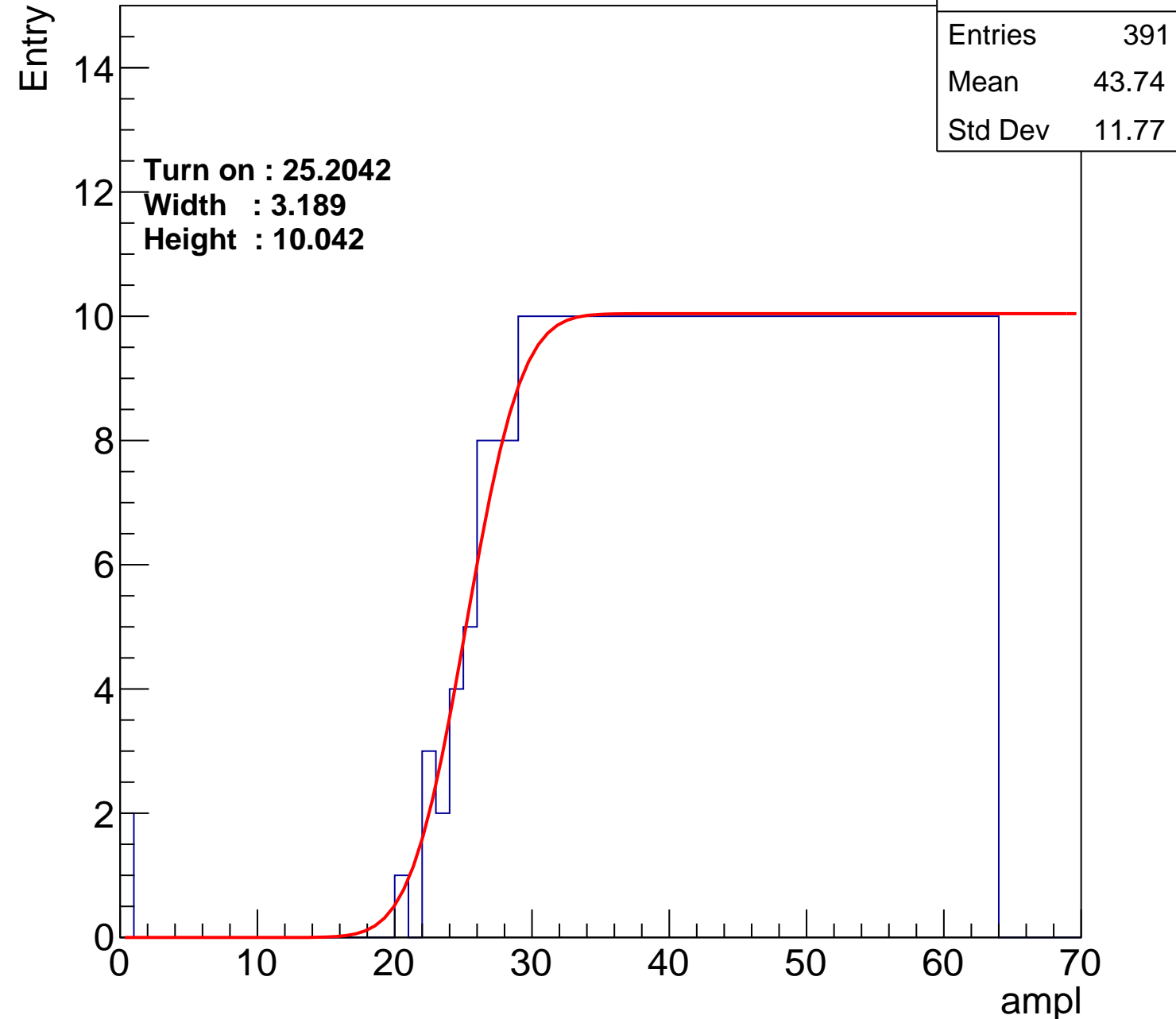
Width : 3.189

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch111

calib_packv5_042523_0143.root, FC#3, port B3

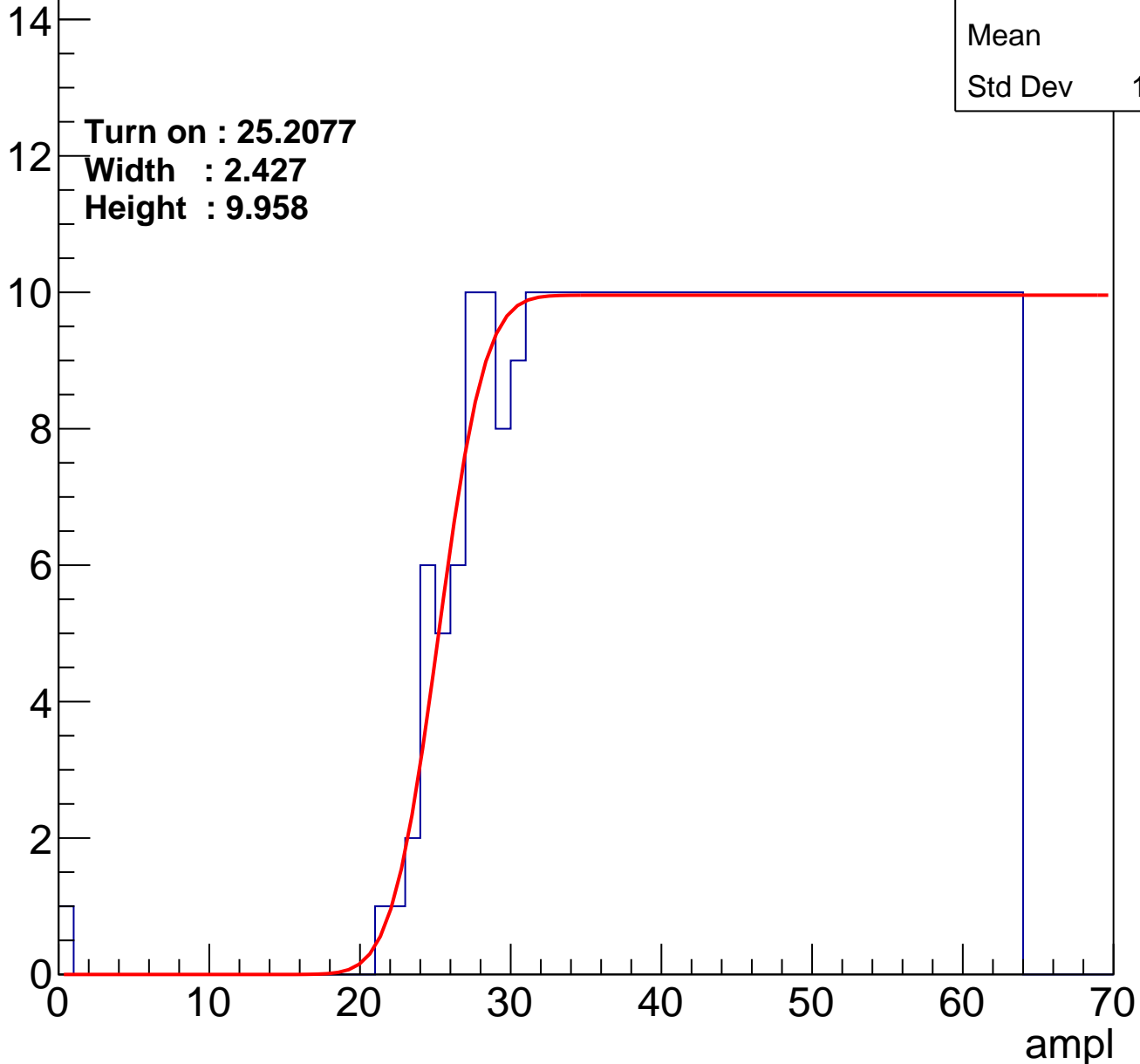
Entries	389
Mean	43.9
Std Dev	11.55

Turn on : 25.2077

Width : 2.427

Height : 9.958

Entry



B1L000S, U11-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 25.7156

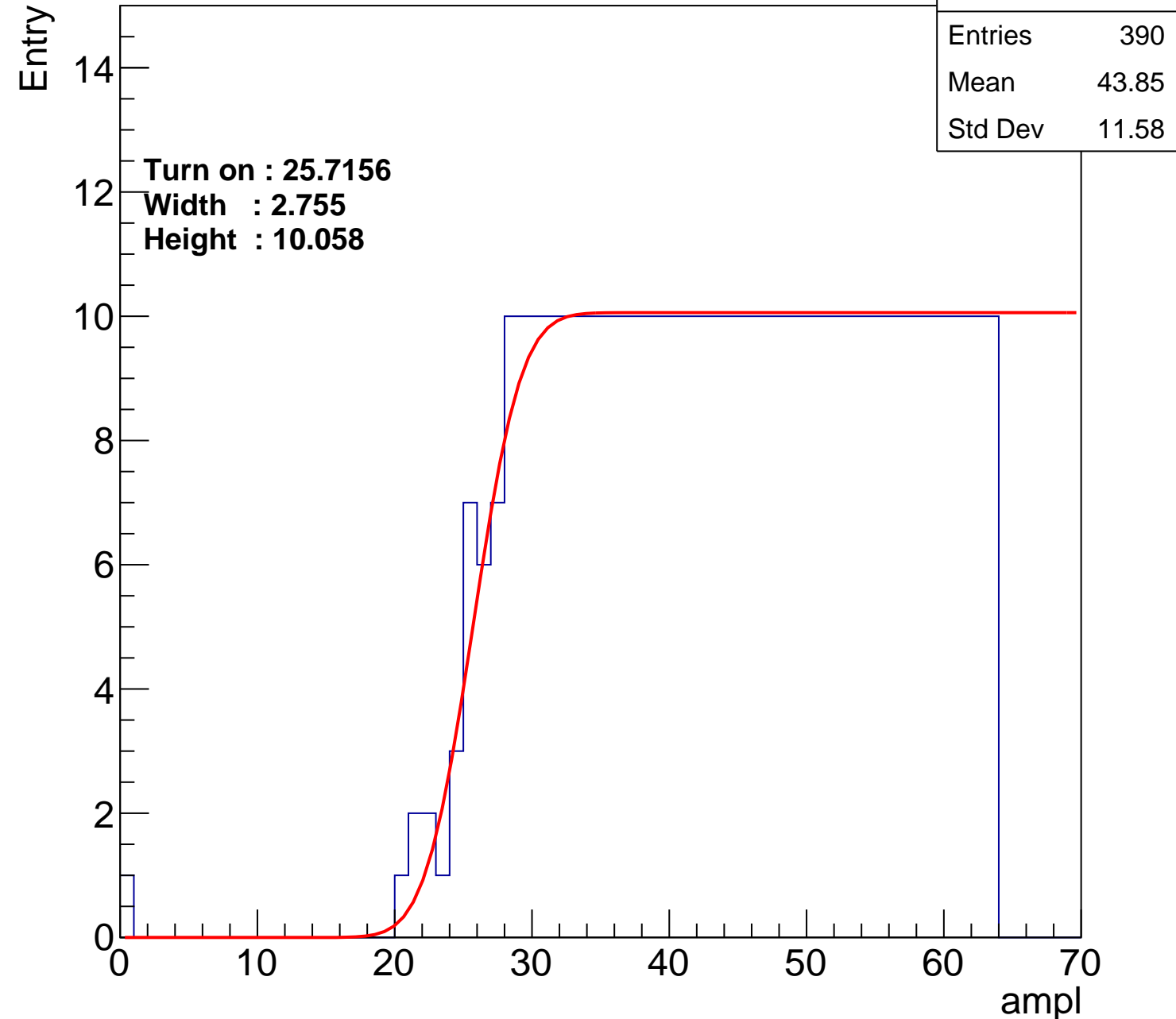
Width : 2.755

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.4357

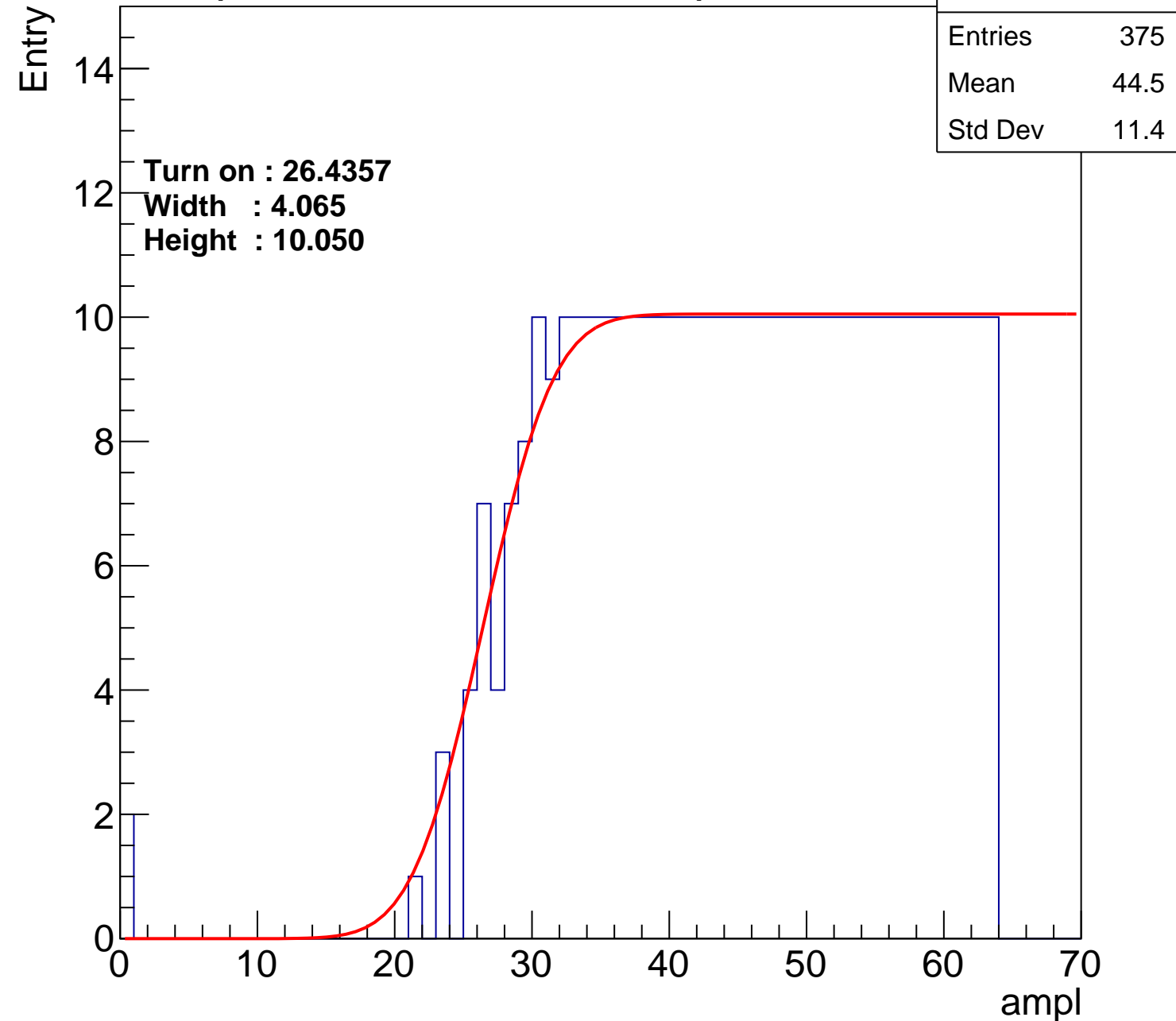
Width : 4.065

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.86
Std Dev	11.68

Turn on : 25.6491

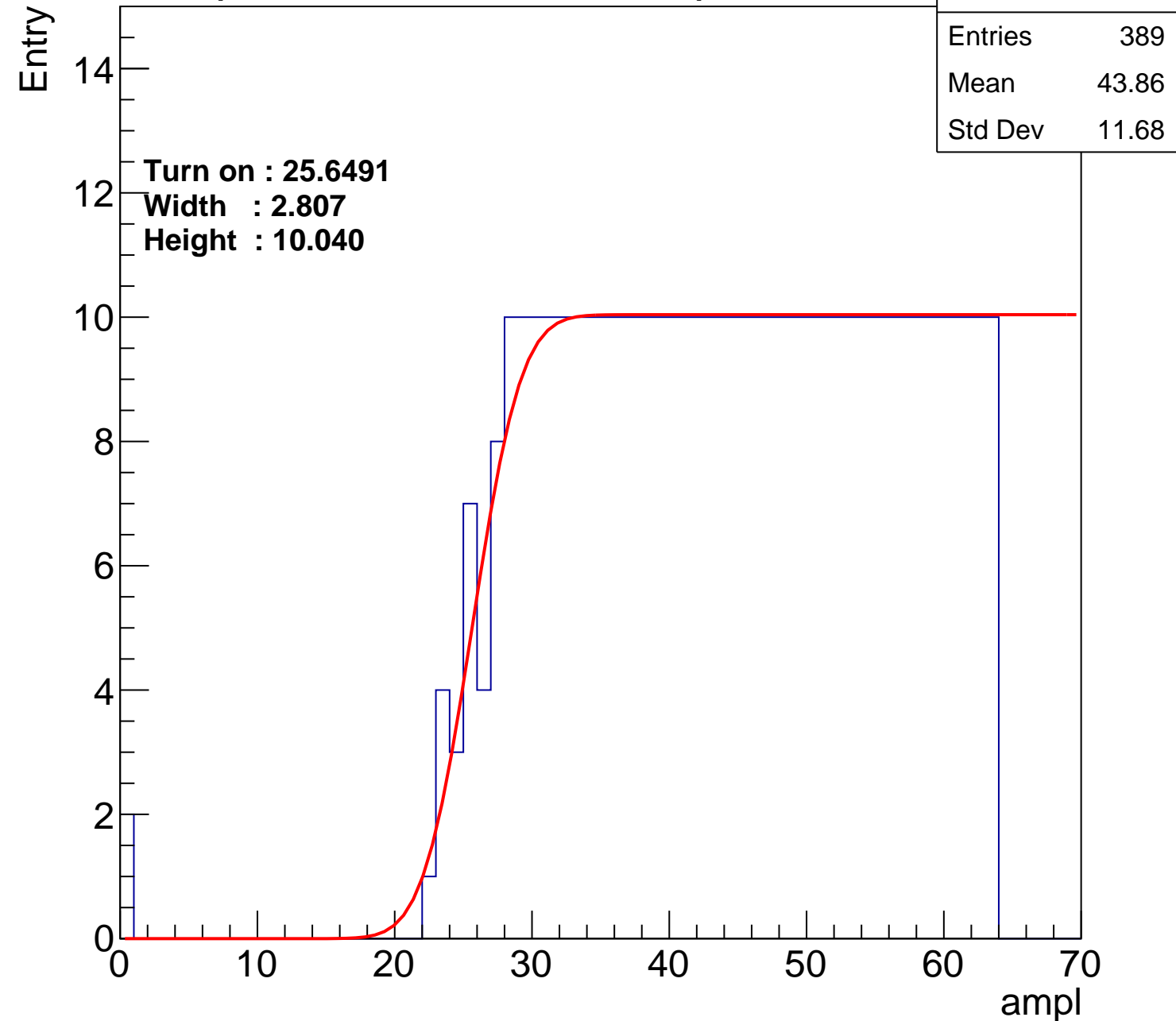
Width : 2.807

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.08
Std Dev	11.72

Turn on : 26.1163

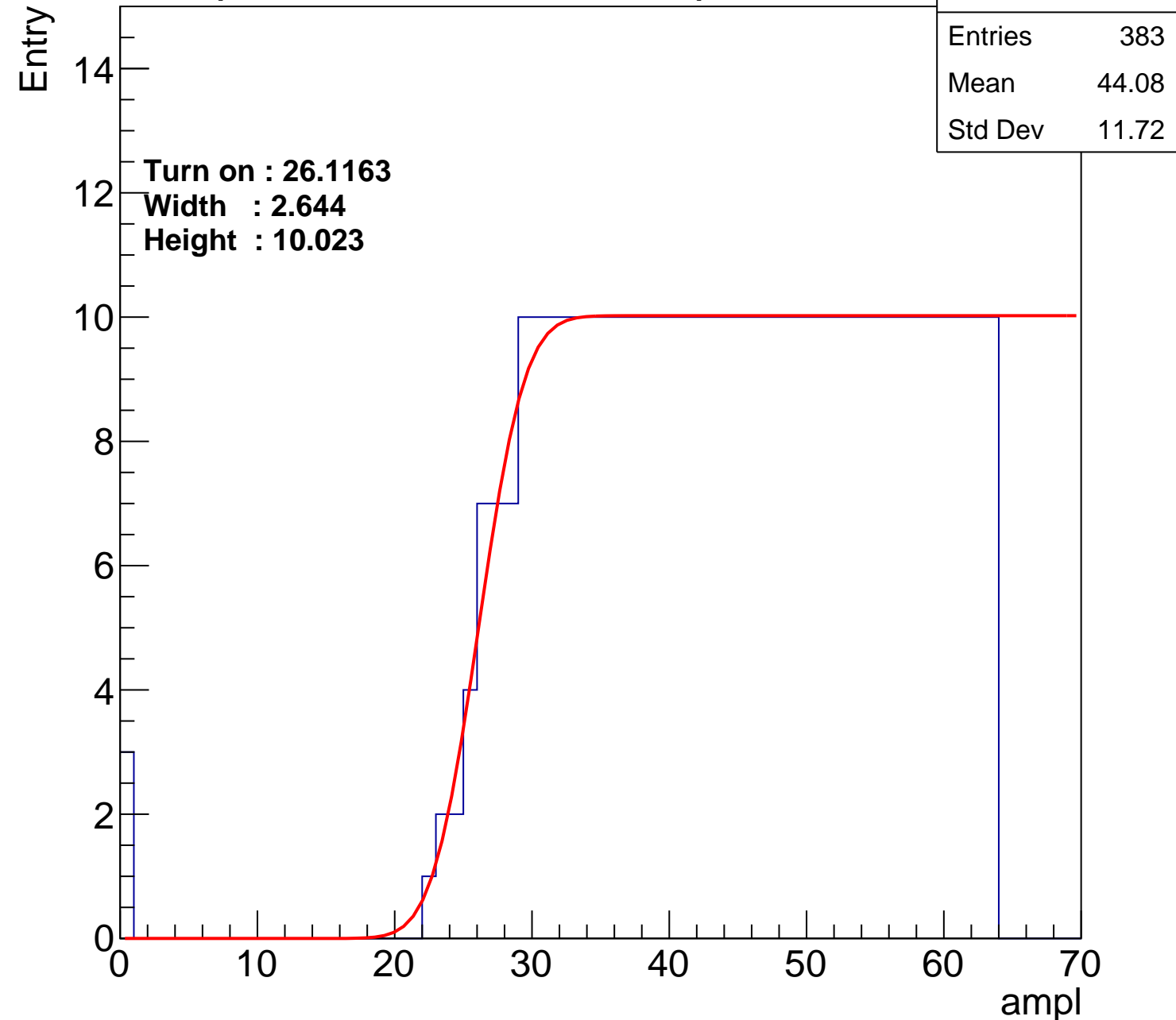
Width : 2.644

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.58
Std Dev	11.86

Turn on : 24.9734

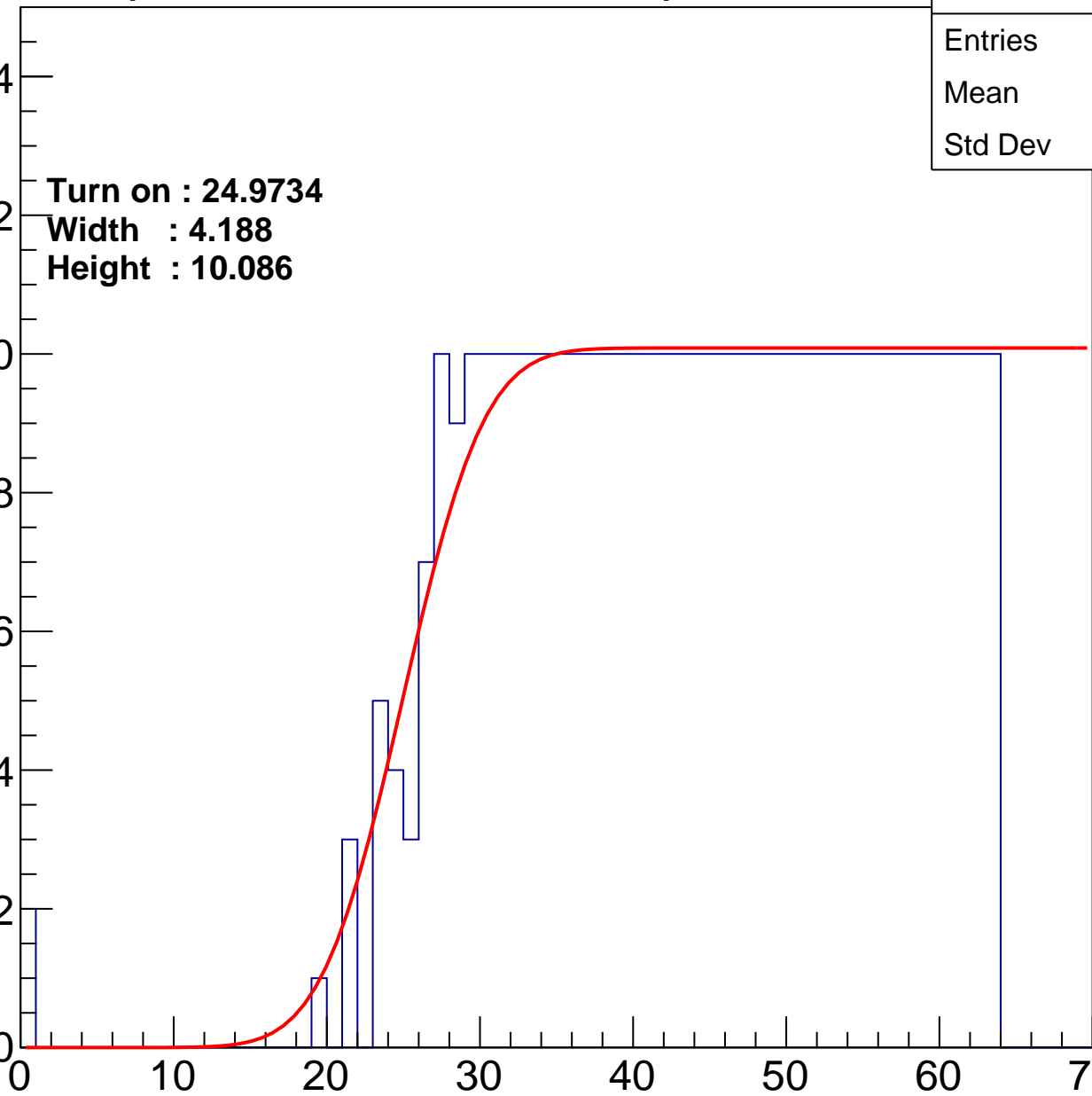
Width : 4.188

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch117

calib_packv5_042523_0143.root, FC#3, port B3

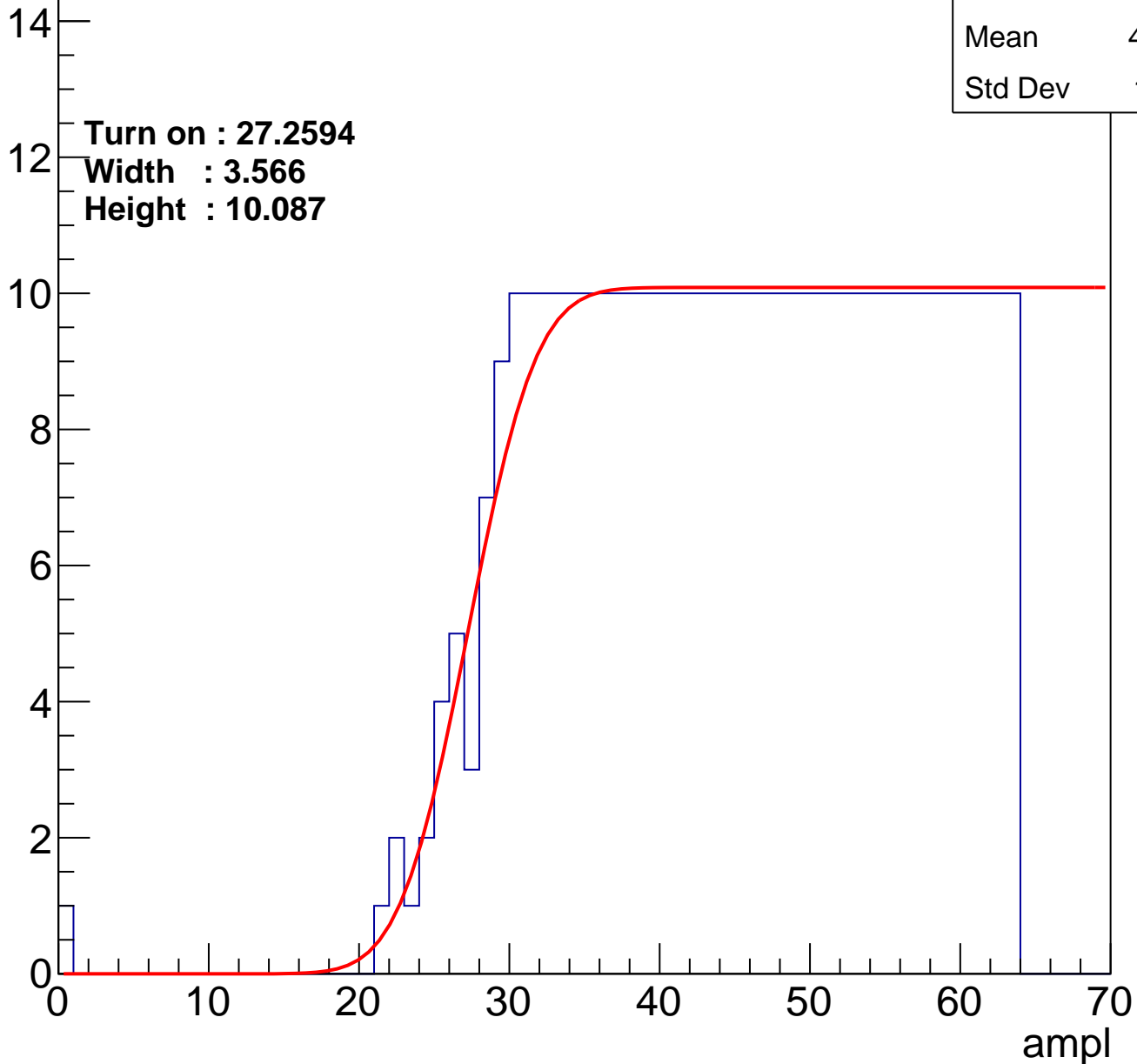
Entries	375
Mean	44.57
Std Dev	11.21

Turn on : 27.2594

Width : 3.566

Height : 10.087

Entry



B1L000S, U11-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.75
Std Dev	11.87

Turn on : 25.2708

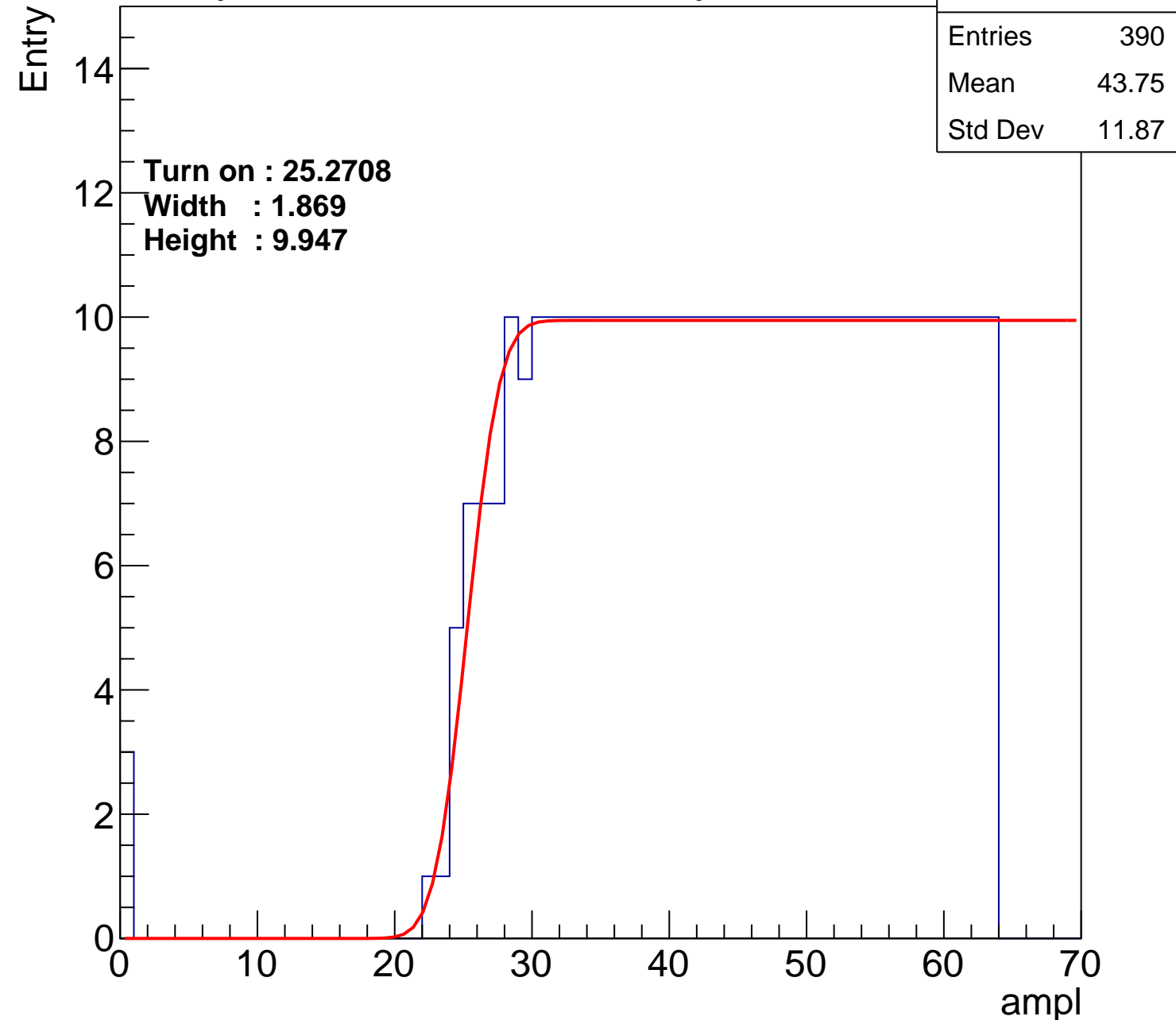
Width : 1.869

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 27.1704

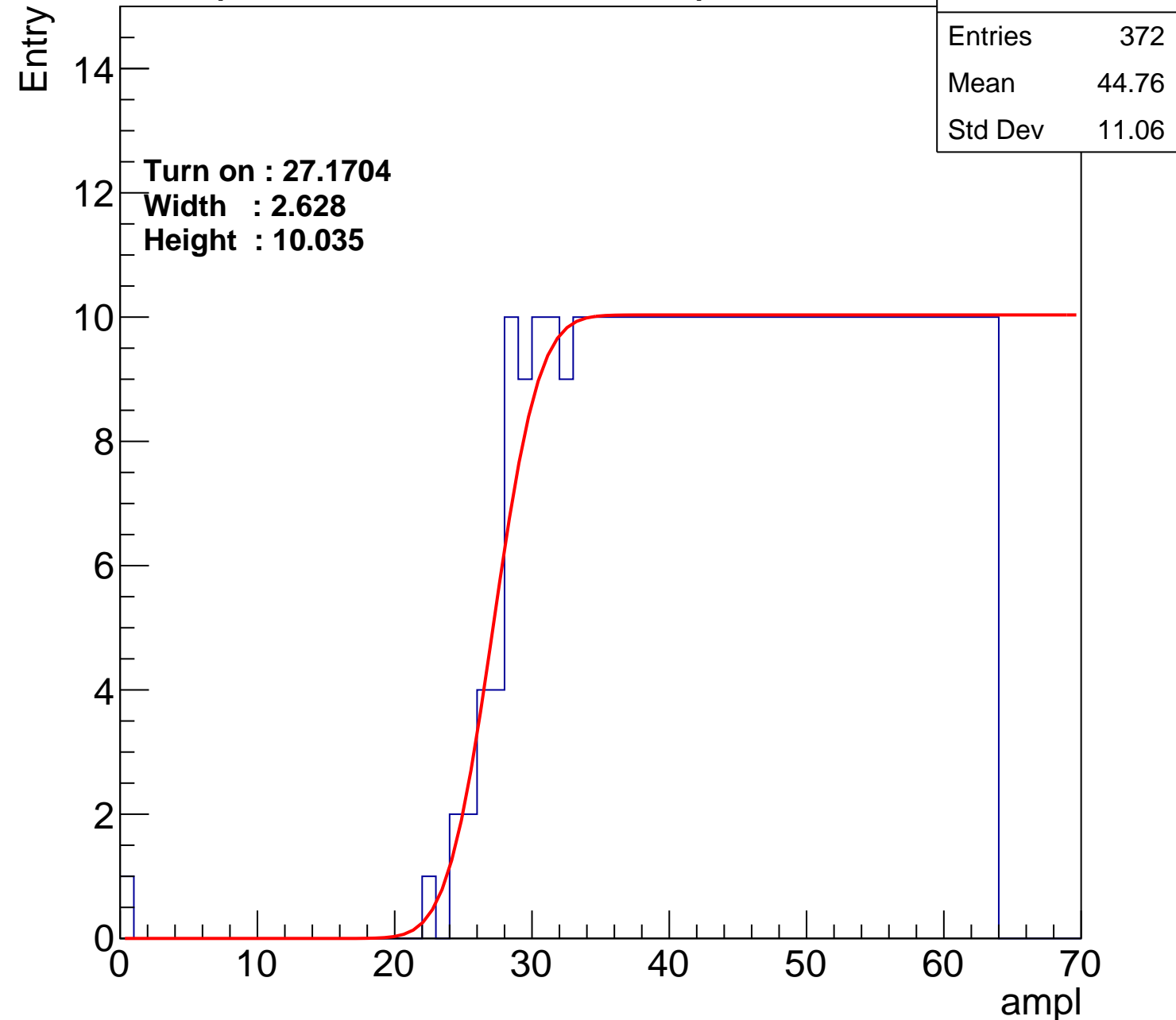
Width : 2.628

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.11
Std Dev	11.73

Turn on : 26.1456

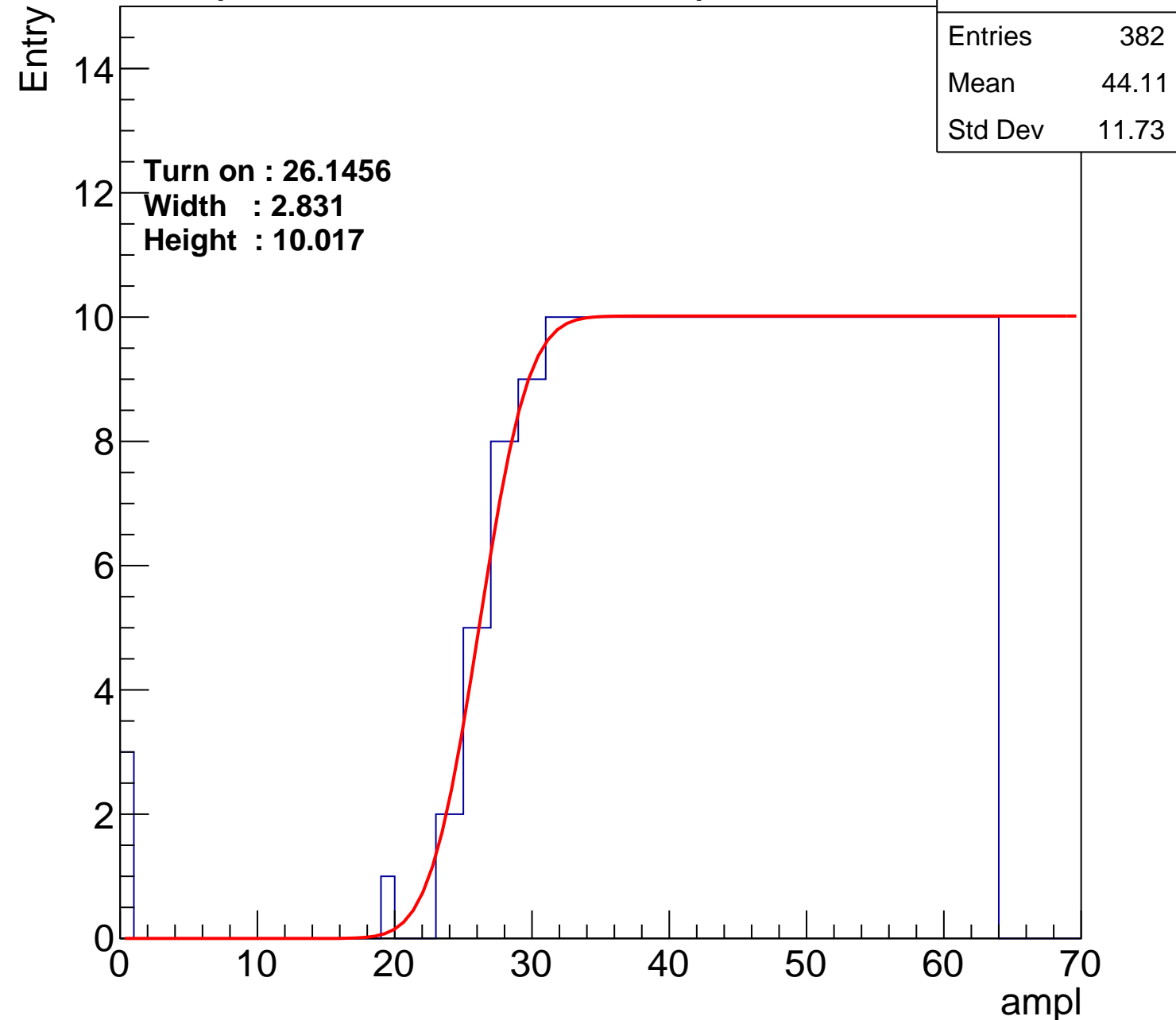
Width : 2.831

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch121

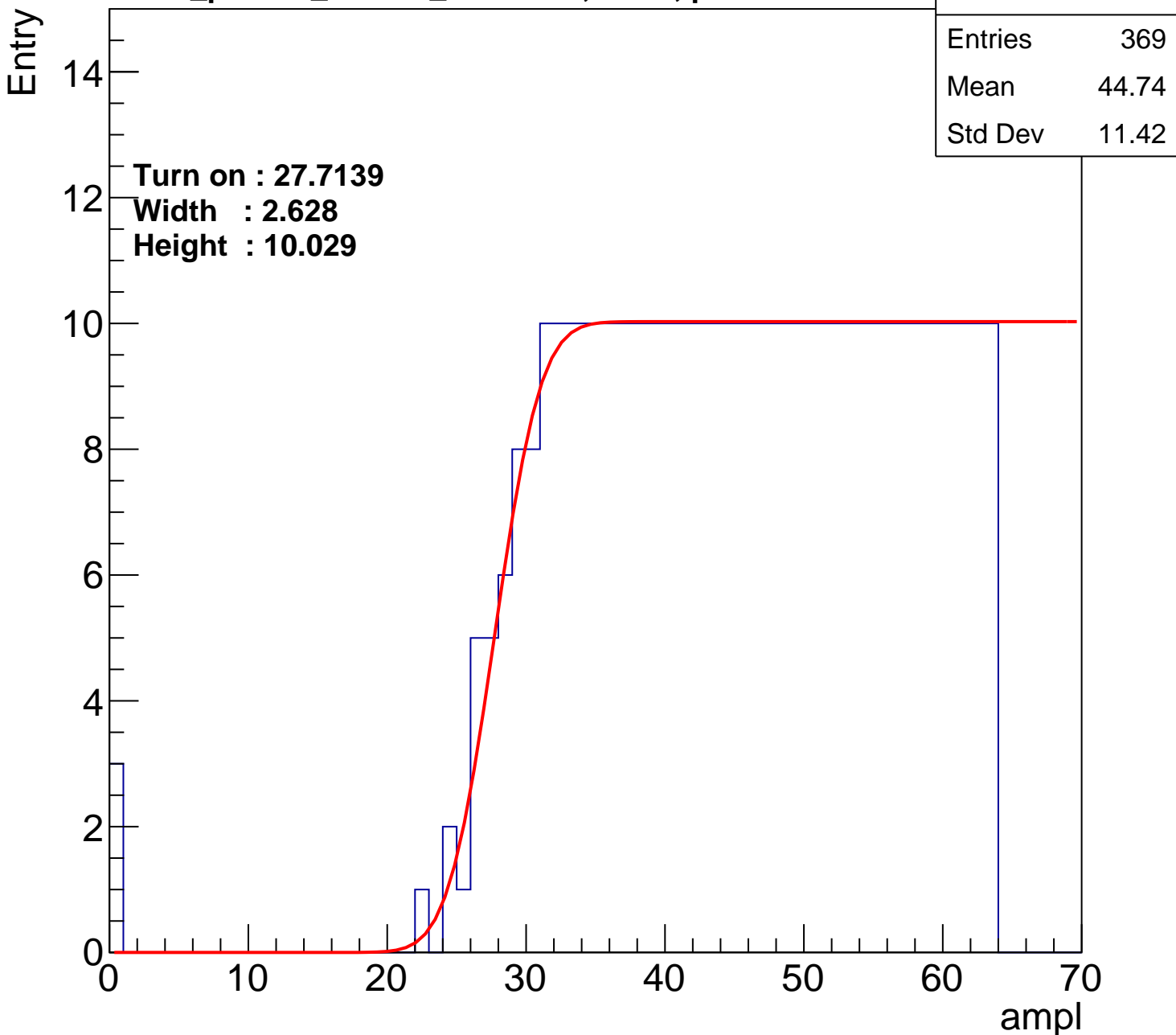
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 27.7139

Width : 2.628

Height : 10.029

Entries	369
Mean	44.74
Std Dev	11.42



B1L000S, U11-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.42
Std Dev	11.79

Turn on : 24.4274

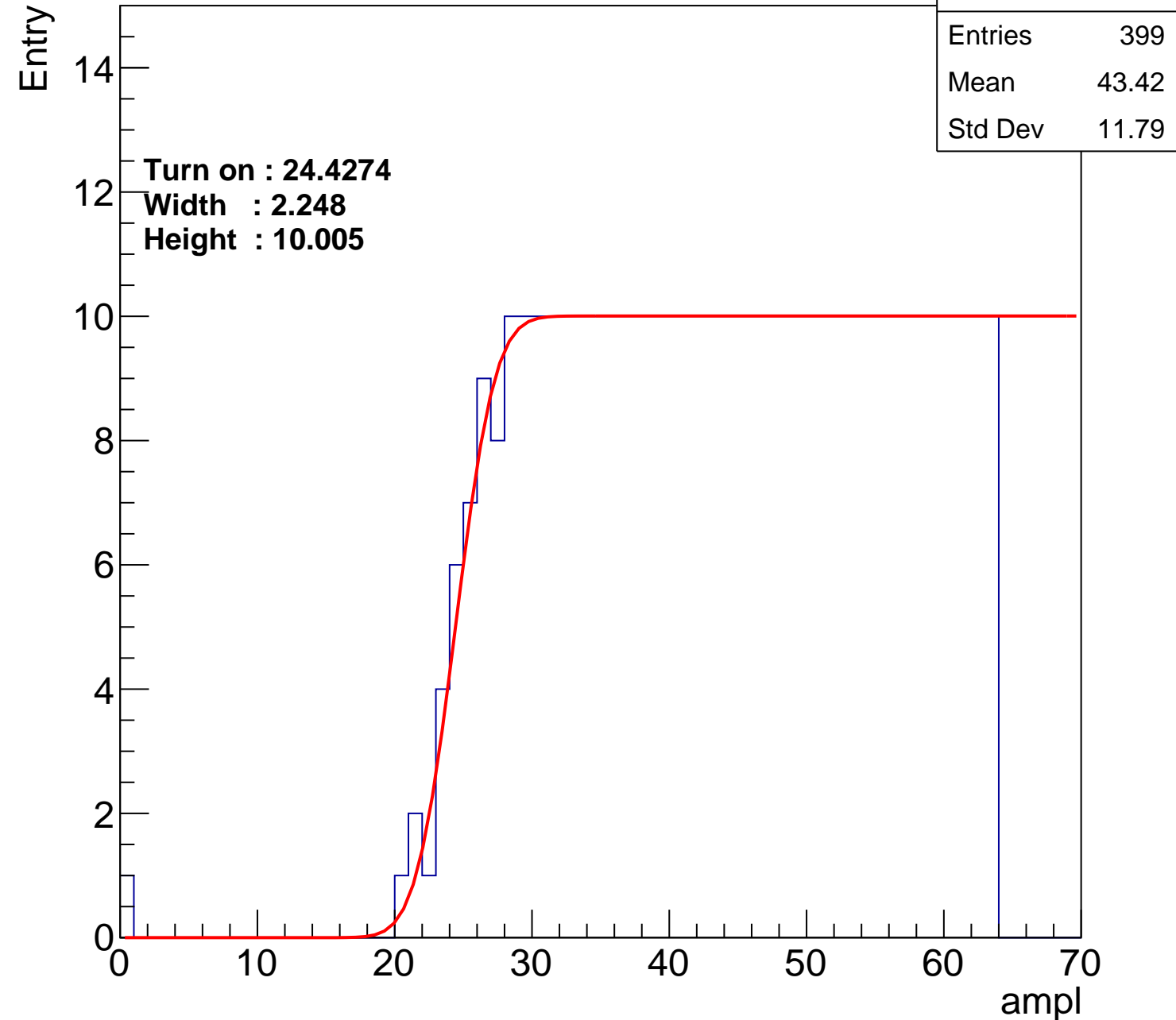
Width : 2.248

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch123

calib_packv5_042523_0143.root, FC#3, port B3

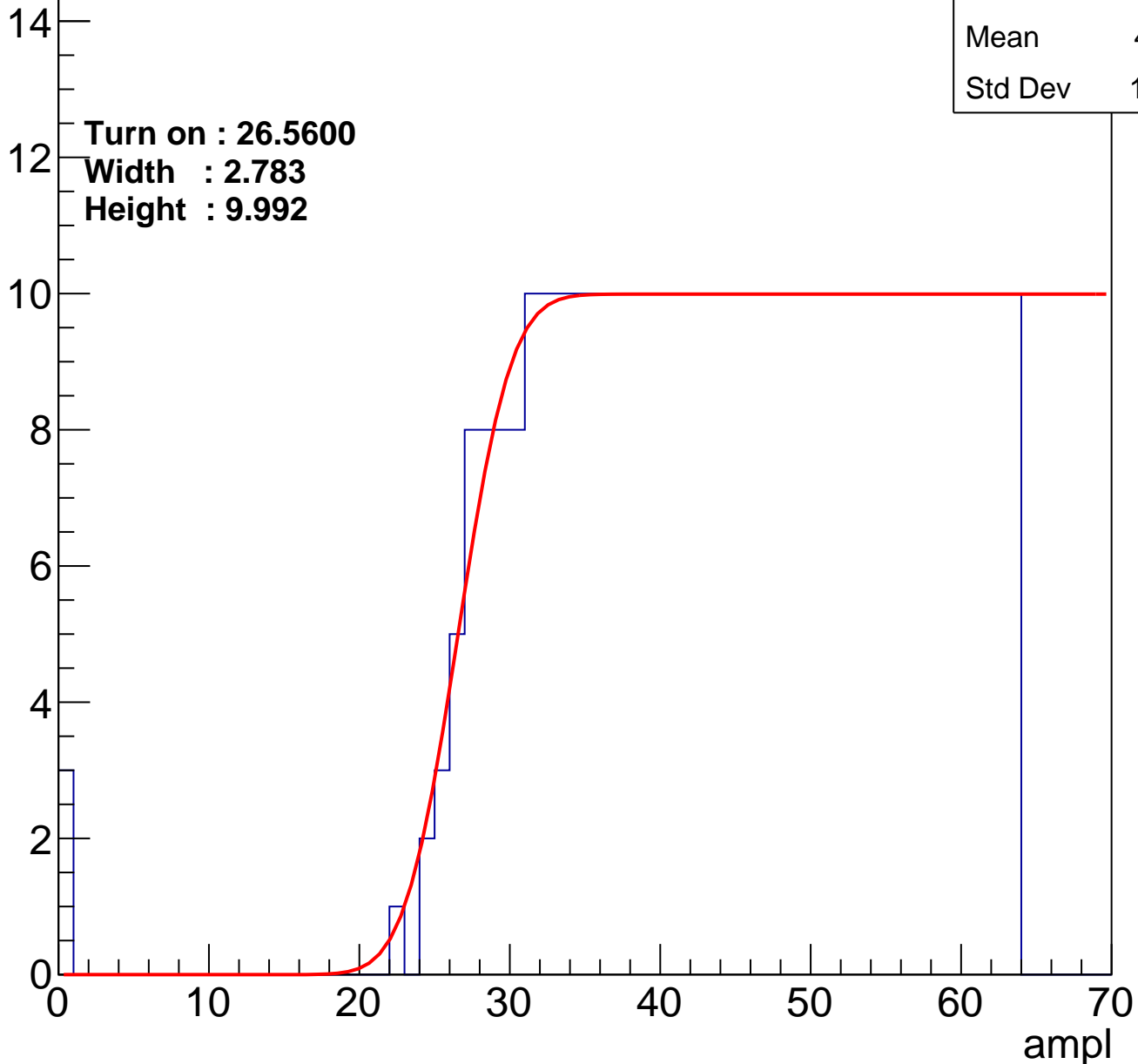
Entries	376
Mean	44.41
Std Dev	11.57

Turn on : 26.5600

Width : 2.783

Height : 9.992

Entry



B1L000S, U11-ch124

calib_packv5_042523_0143.root, FC#3, port B3

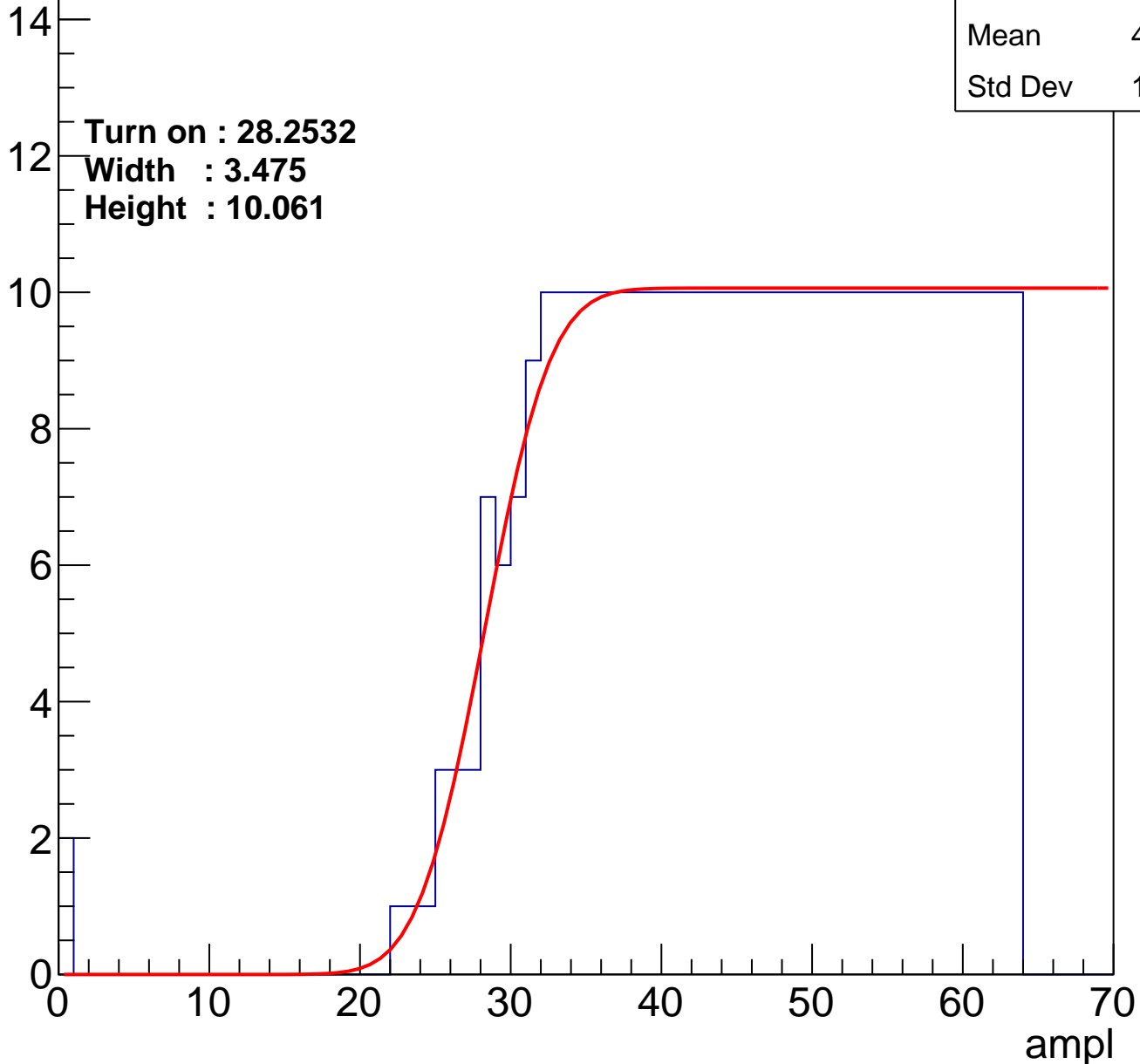
Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 28.2532

Width : 3.475

Height : 10.061

Entry



B1L000S, U11-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.1
Std Dev	11.62

Turn on : 26.1208

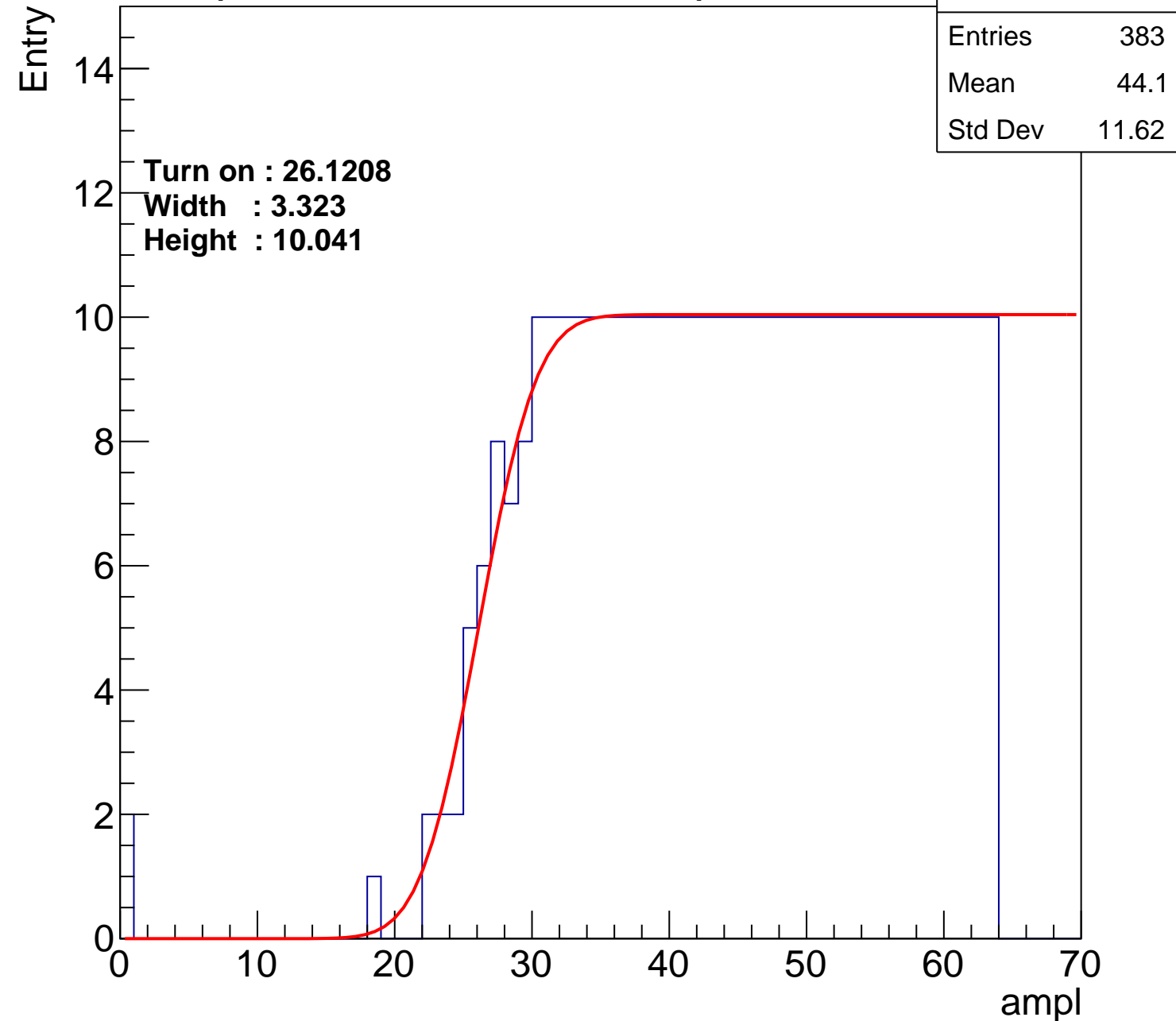
Width : 3.323

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.79
Std Dev	11.73

Turn on : 24.9798

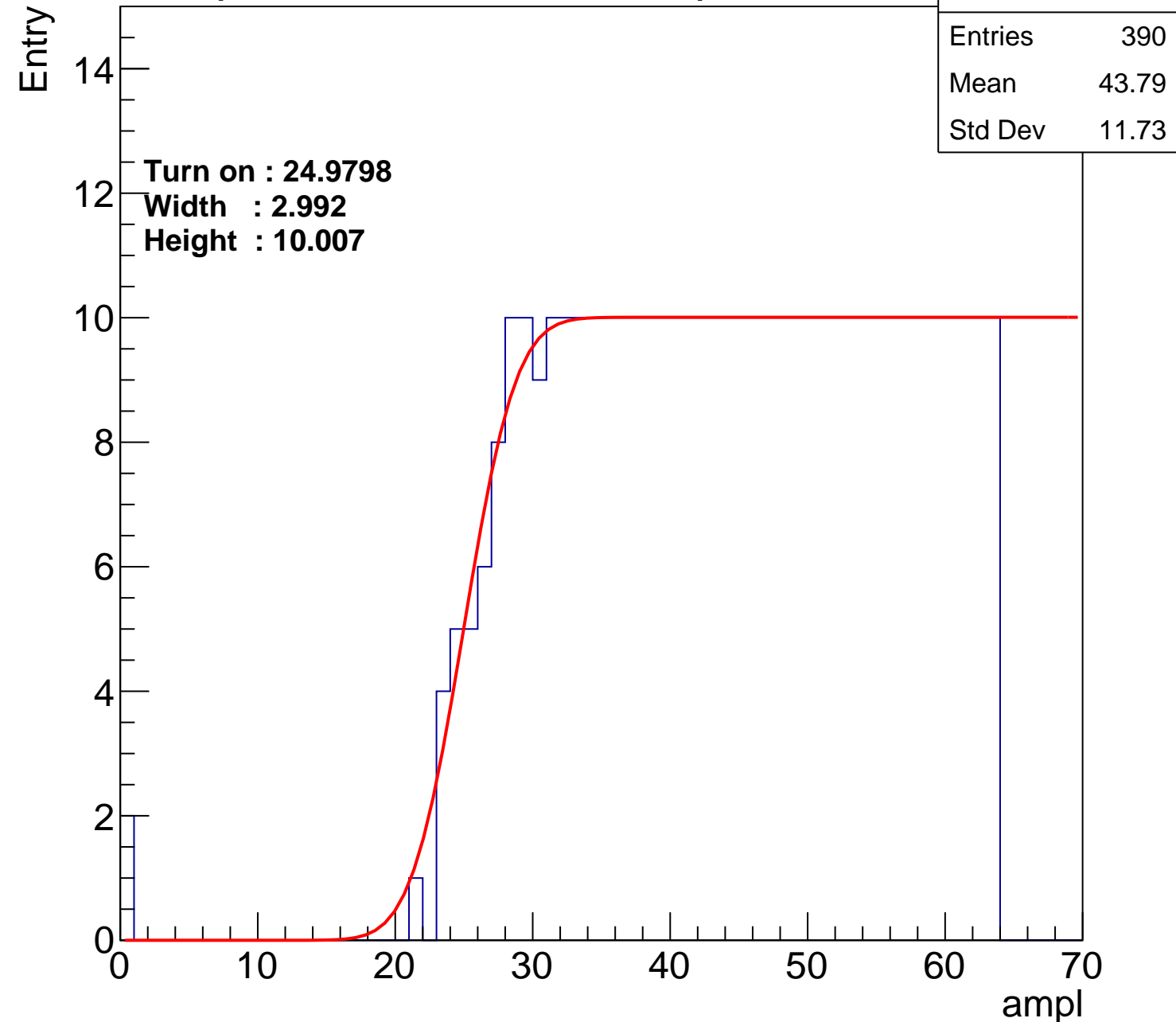
Width : 2.992

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U11-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
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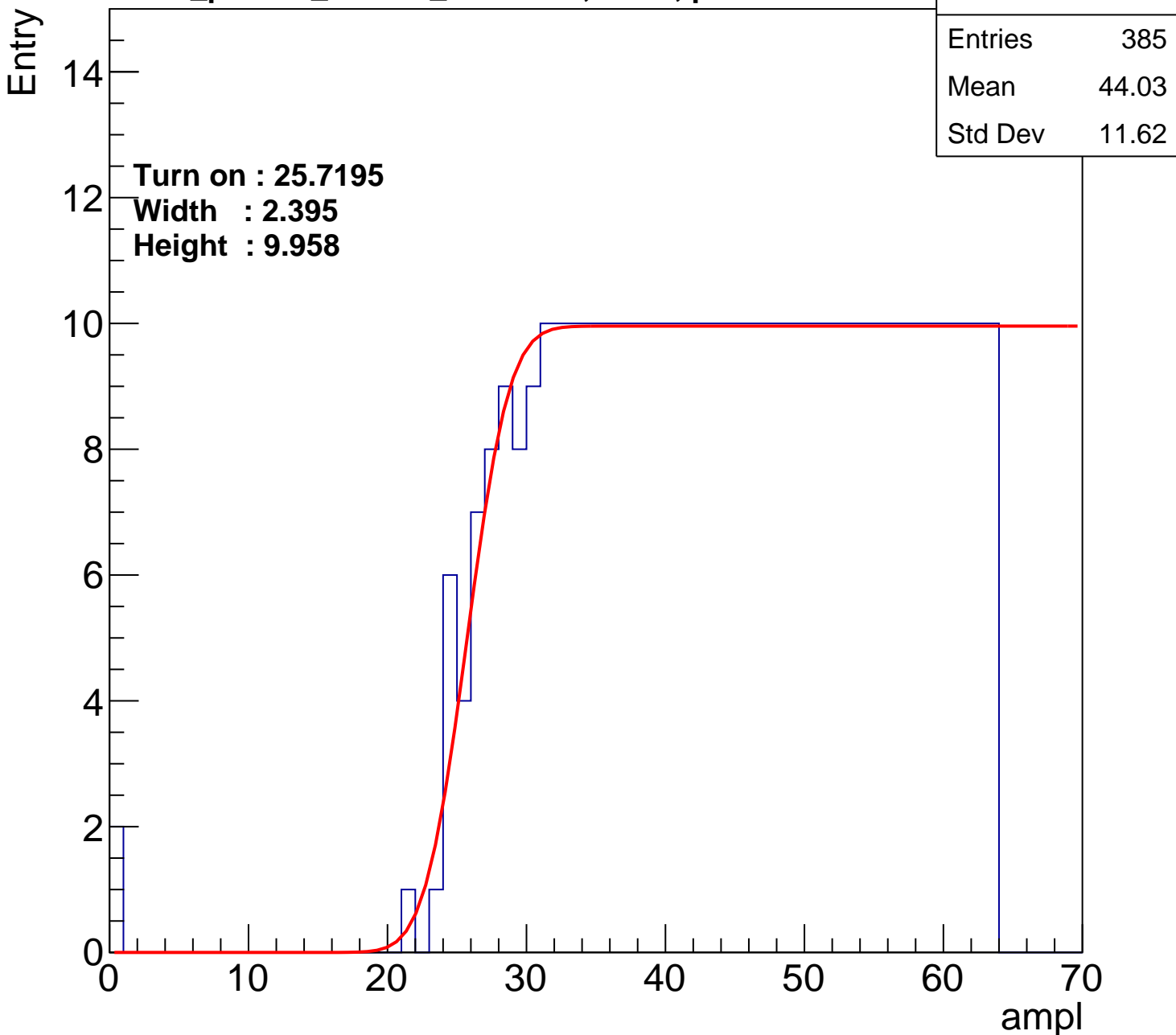
Mean	44.03
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Std Dev	11.62
---------	-------

Turn on : 25.7195

Width : 2.395

Height : 9.958



B1L000S, U11-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.7195

Width : 2.395

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl

