

B1L000S, U8-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.03
Std Dev	11.8

Turn on : 26.0797

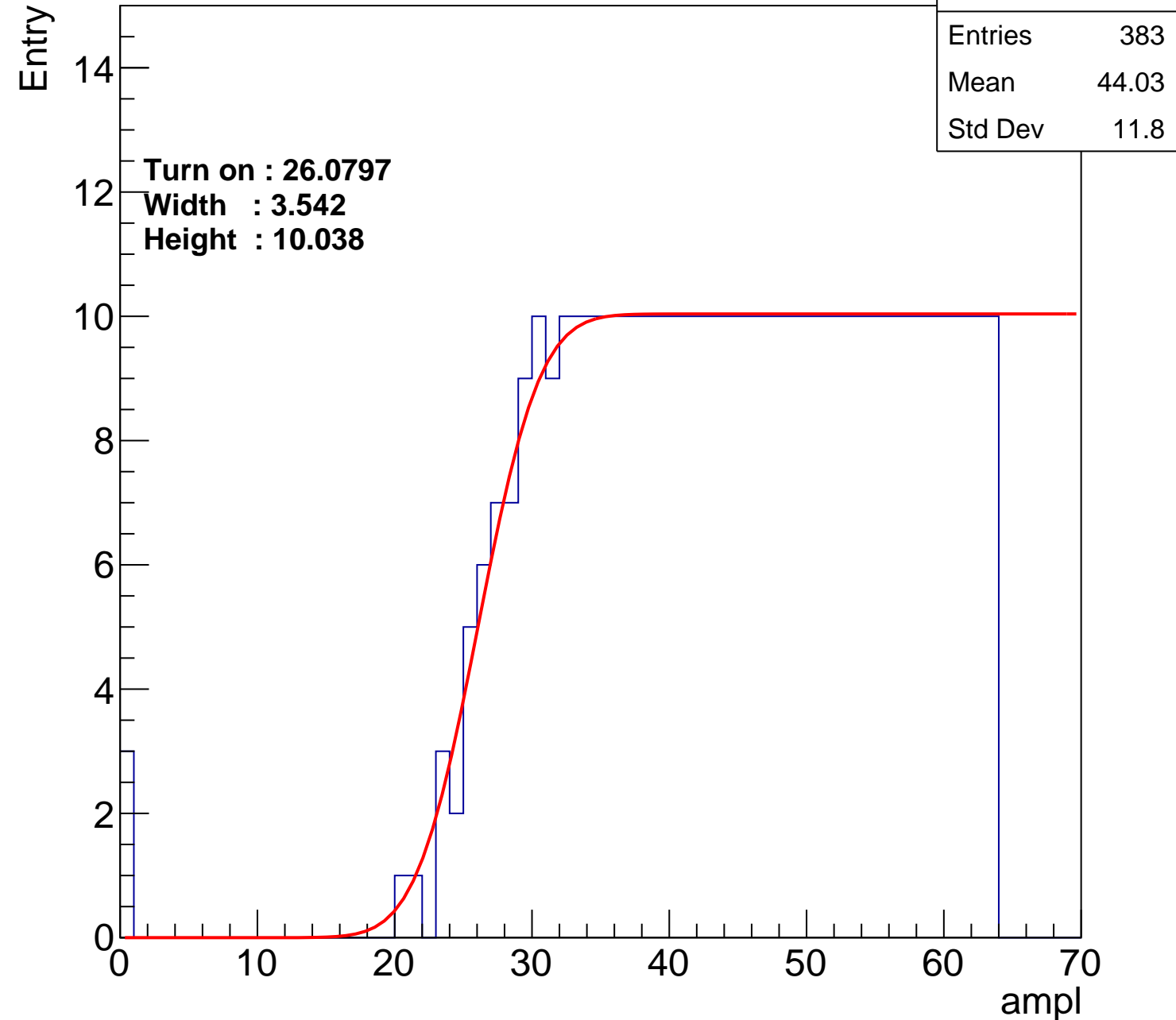
Width : 3.542

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.6
Std Dev	11.17

Turn on : 27.0743

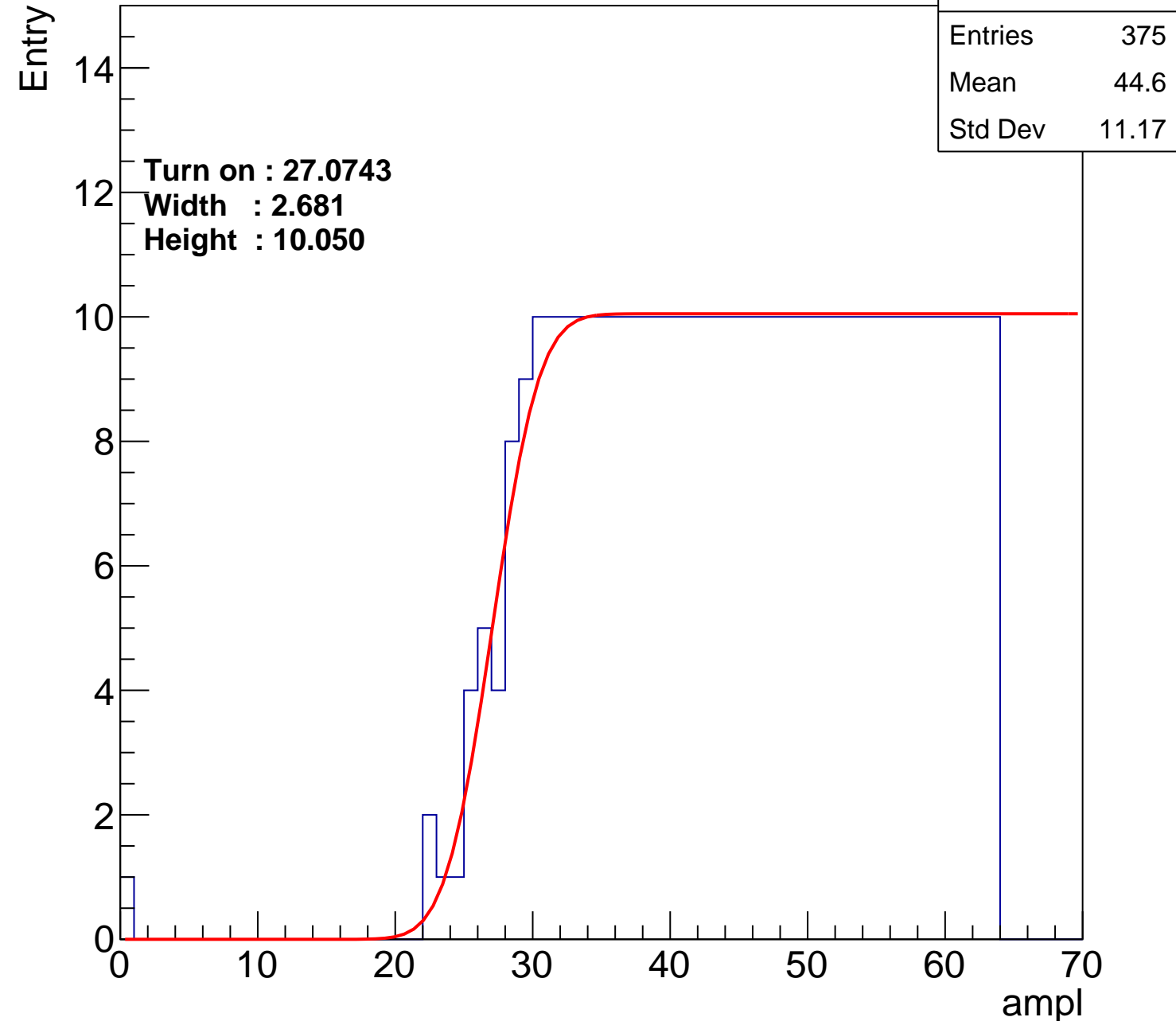
Width : 2.681

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.42
Std Dev	11.24

Turn on : 26.3300

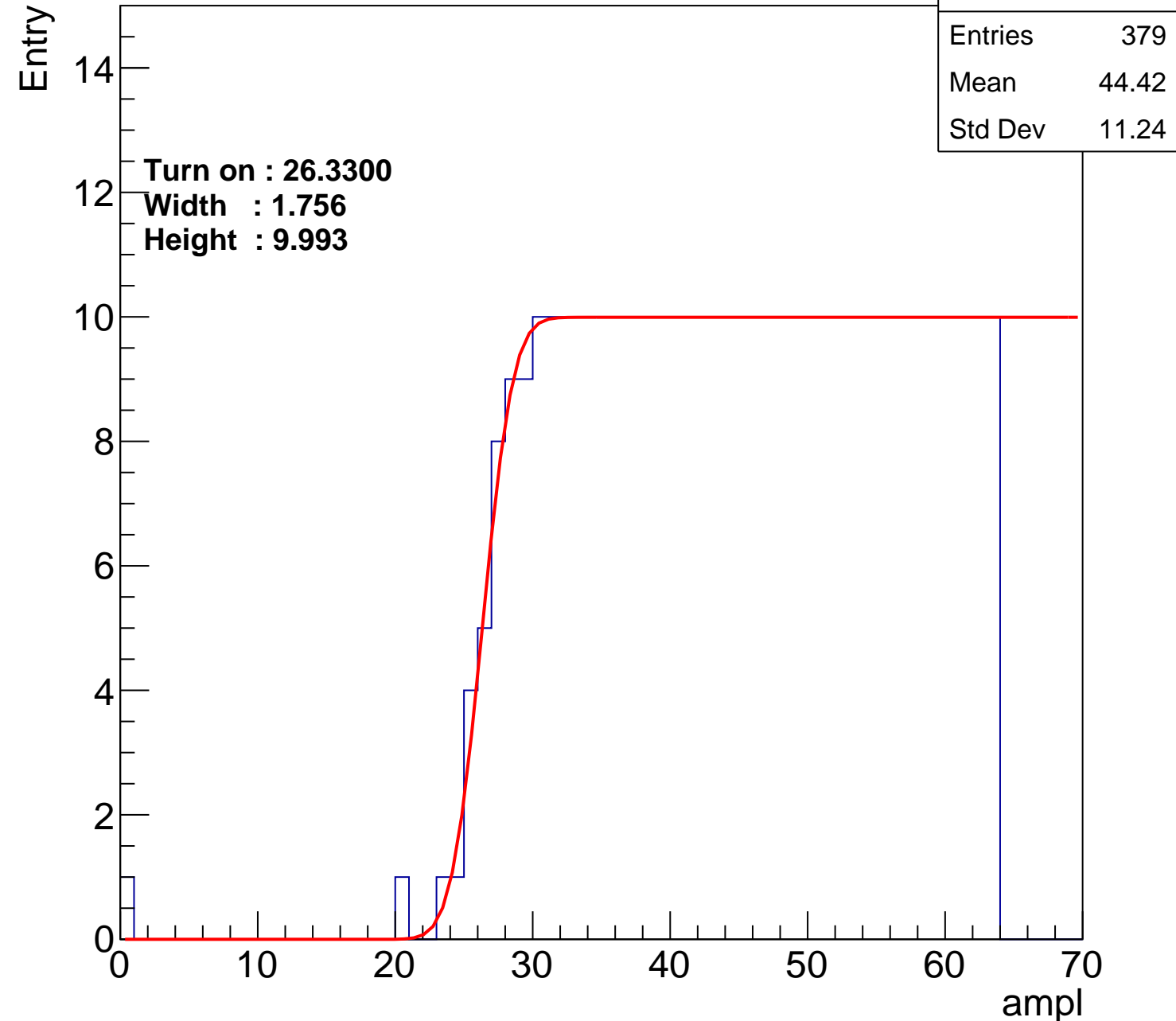
Width : 1.756

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch3

calib_packv5_042523_0143.root, FC#3, port B3

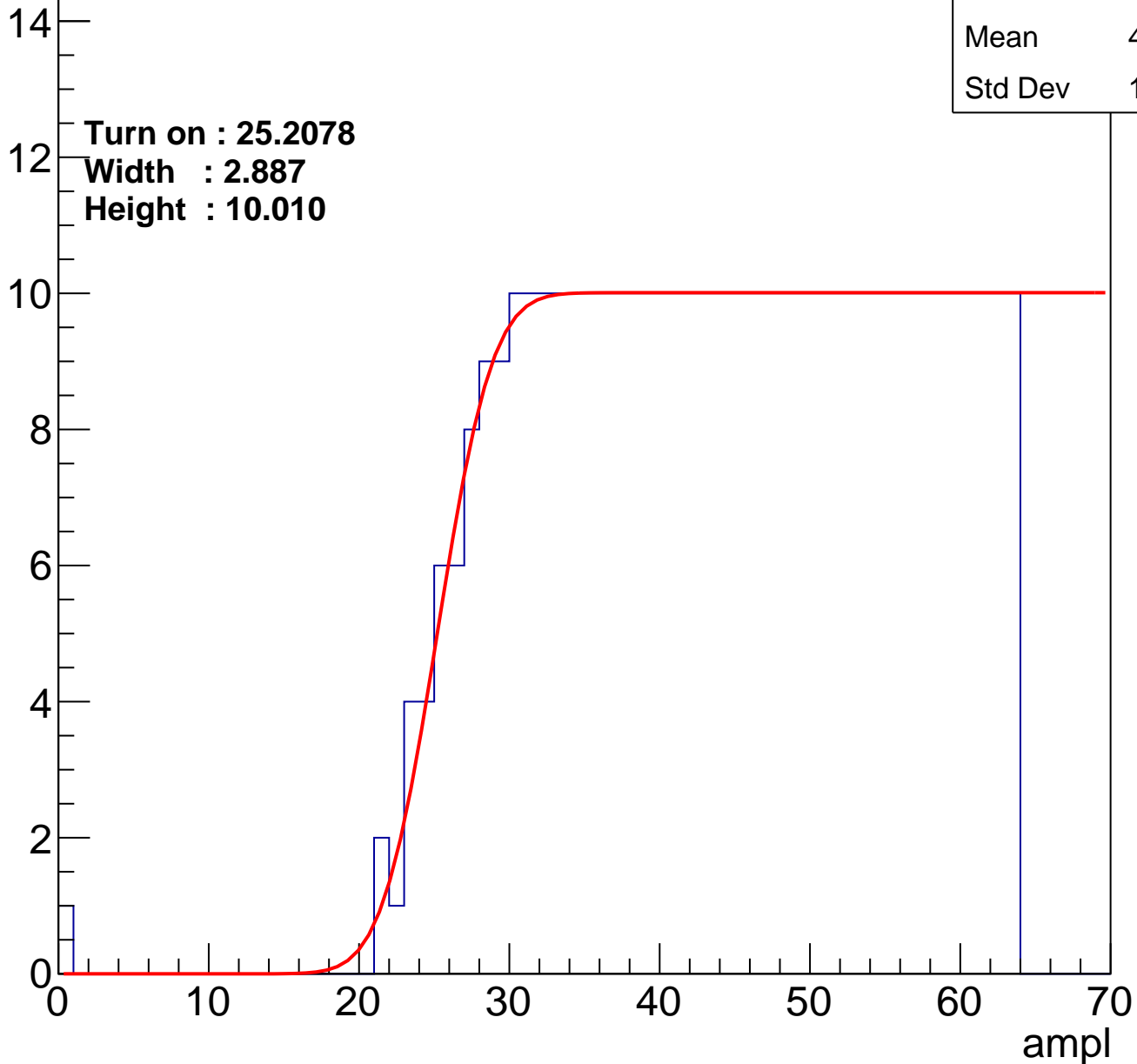
Entries	390
Mean	43.84
Std Dev	11.59

Turn on : 25.2078

Width : 2.887

Height : 10.010

Entry



B1L000S, U8-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.6
Std Dev	12

Turn on : 24.9430

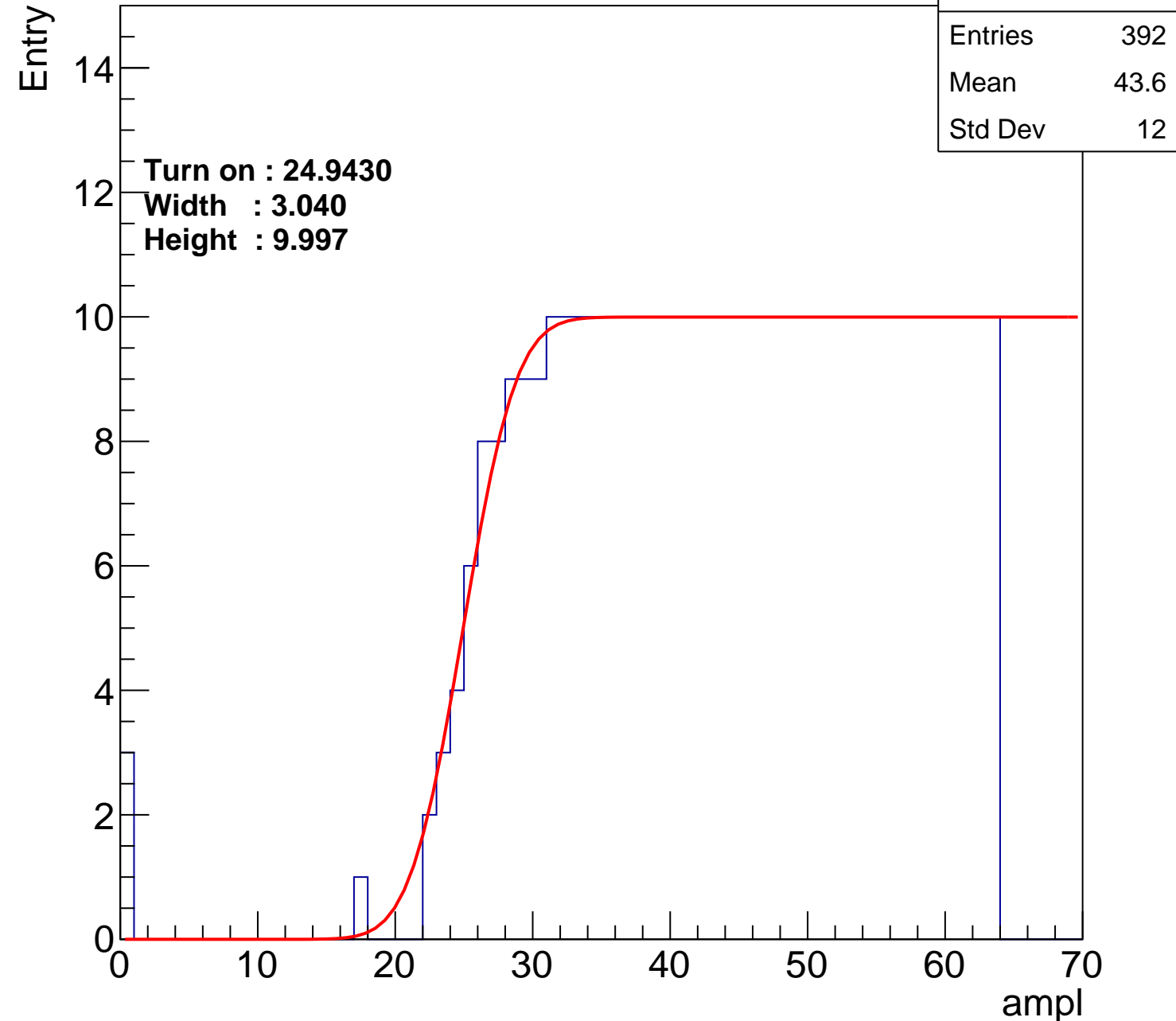
Width : 3.040

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.58
Std Dev	11.93

Turn on : 24.7303

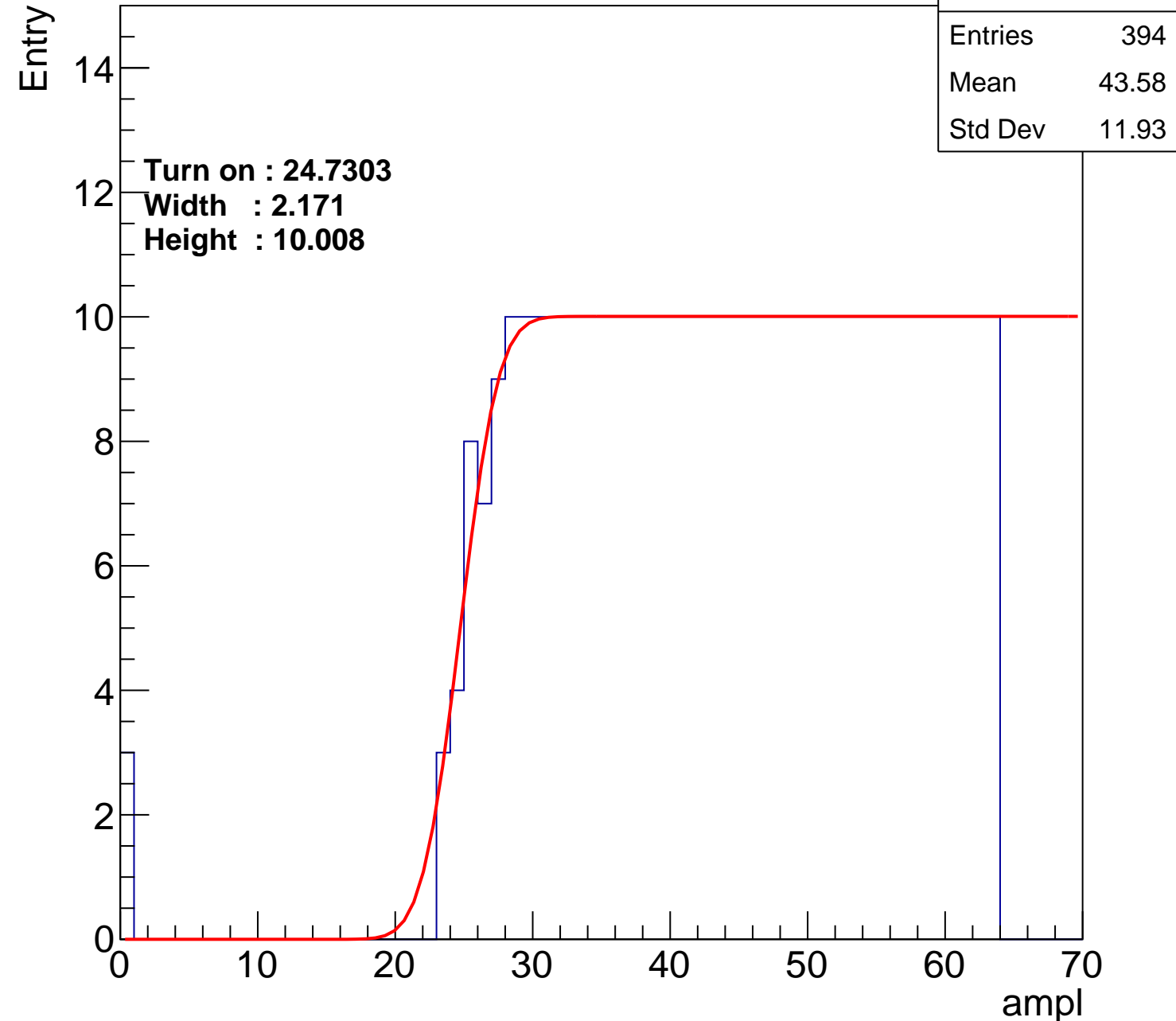
Width : 2.171

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.17
Std Dev	12.16

Turn on : 24.2977

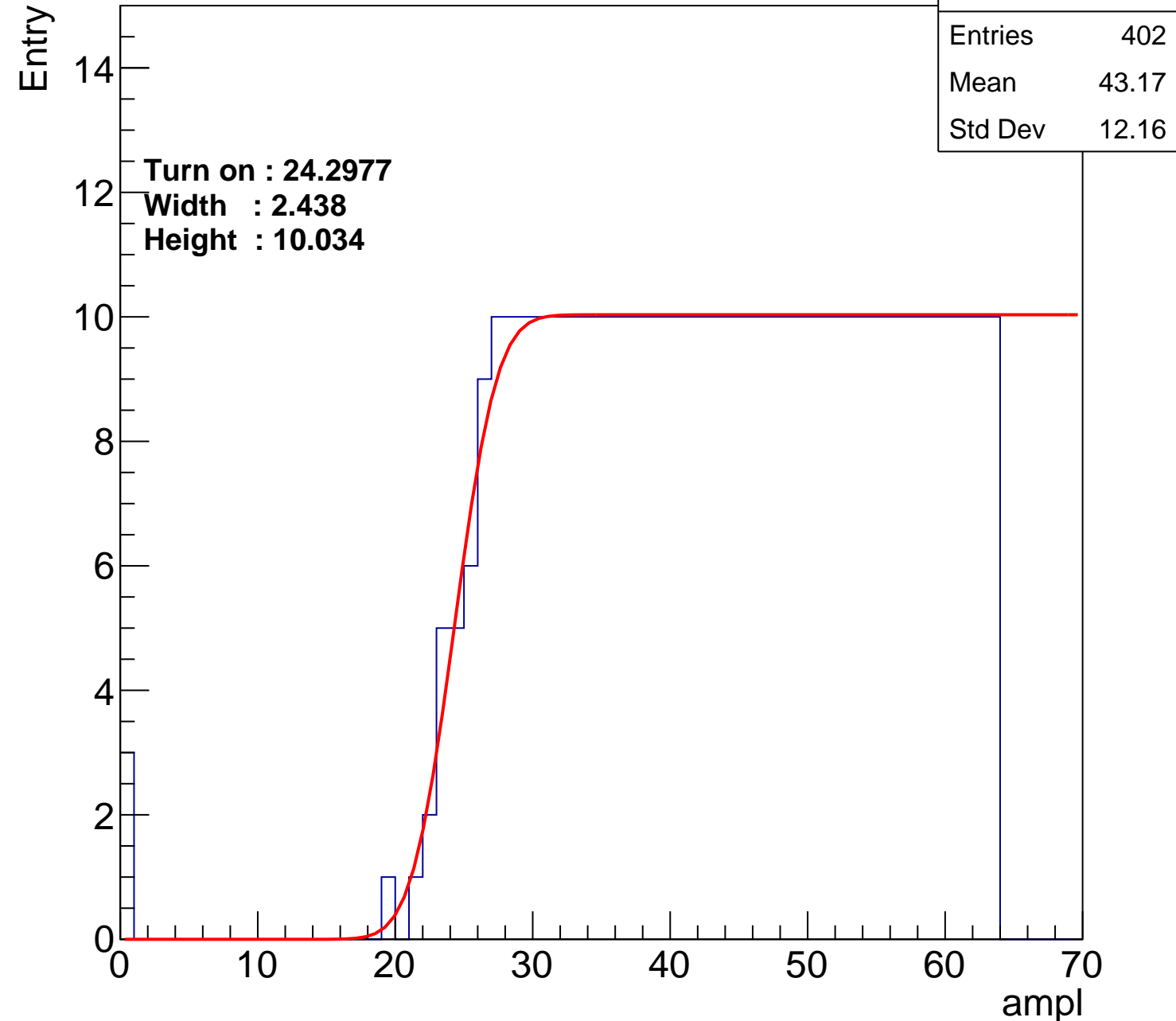
Width : 2.438

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.3
Std Dev	11.33

Turn on : 26.1819

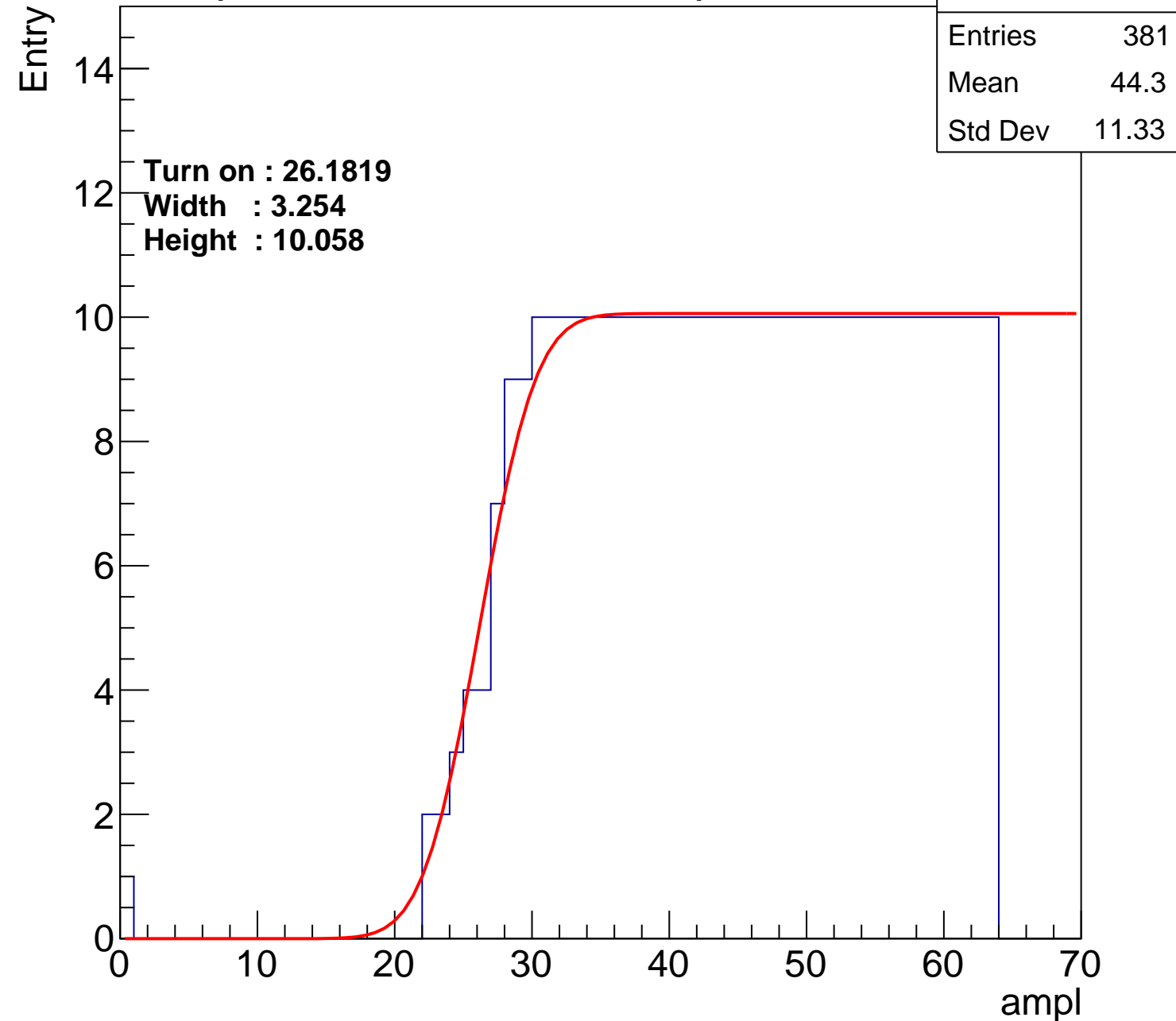
Width : 3.254

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch8

calib_packv5_042523_0143.root, FC#3, port B3

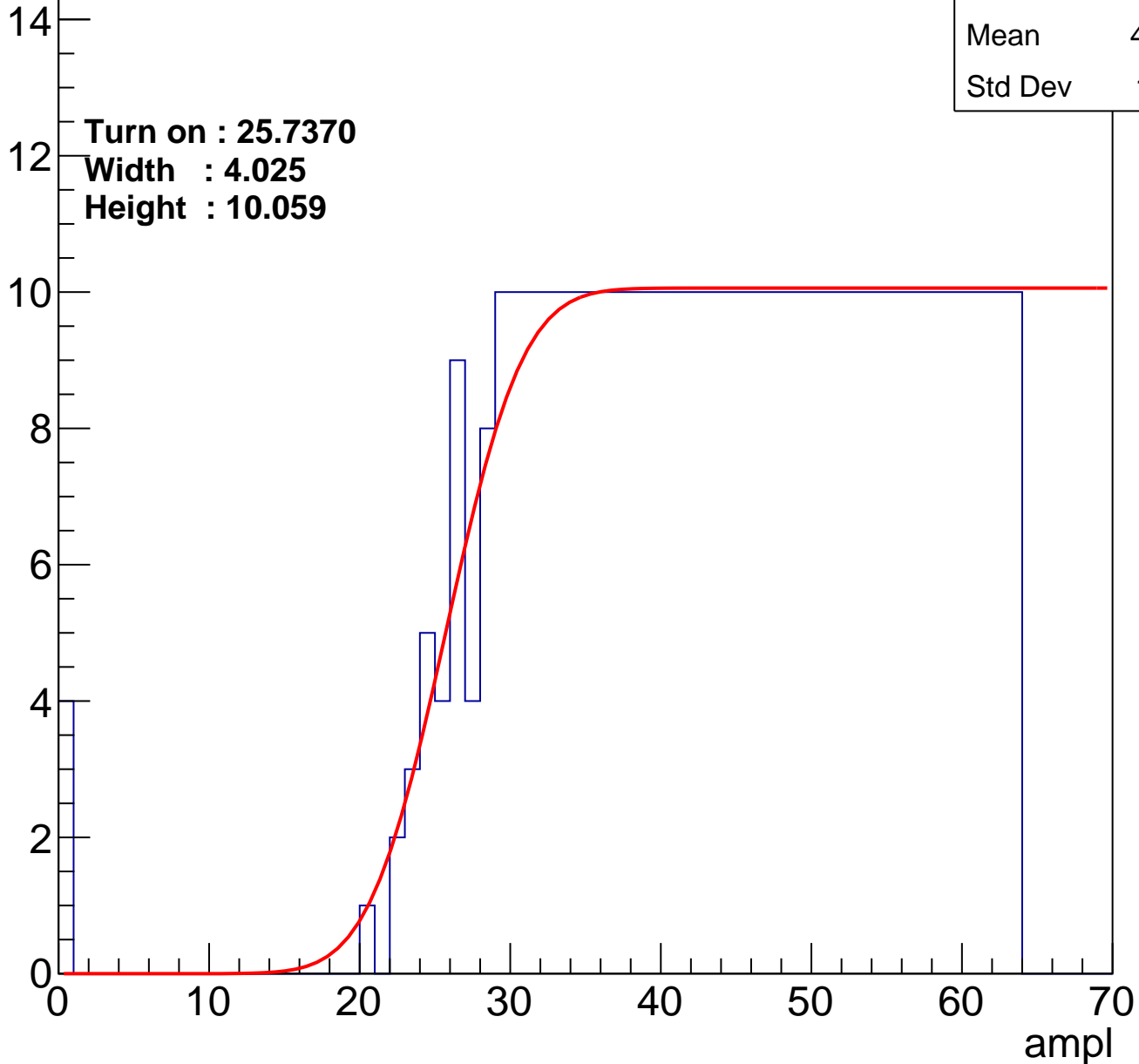
Entries	390
Mean	43.64
Std Dev	12.11

Turn on : 25.7370

Width : 4.025

Height : 10.059

Entry



B1L000S, U8-ch9

calib_packv5_042523_0143.root, FC#3, port B3

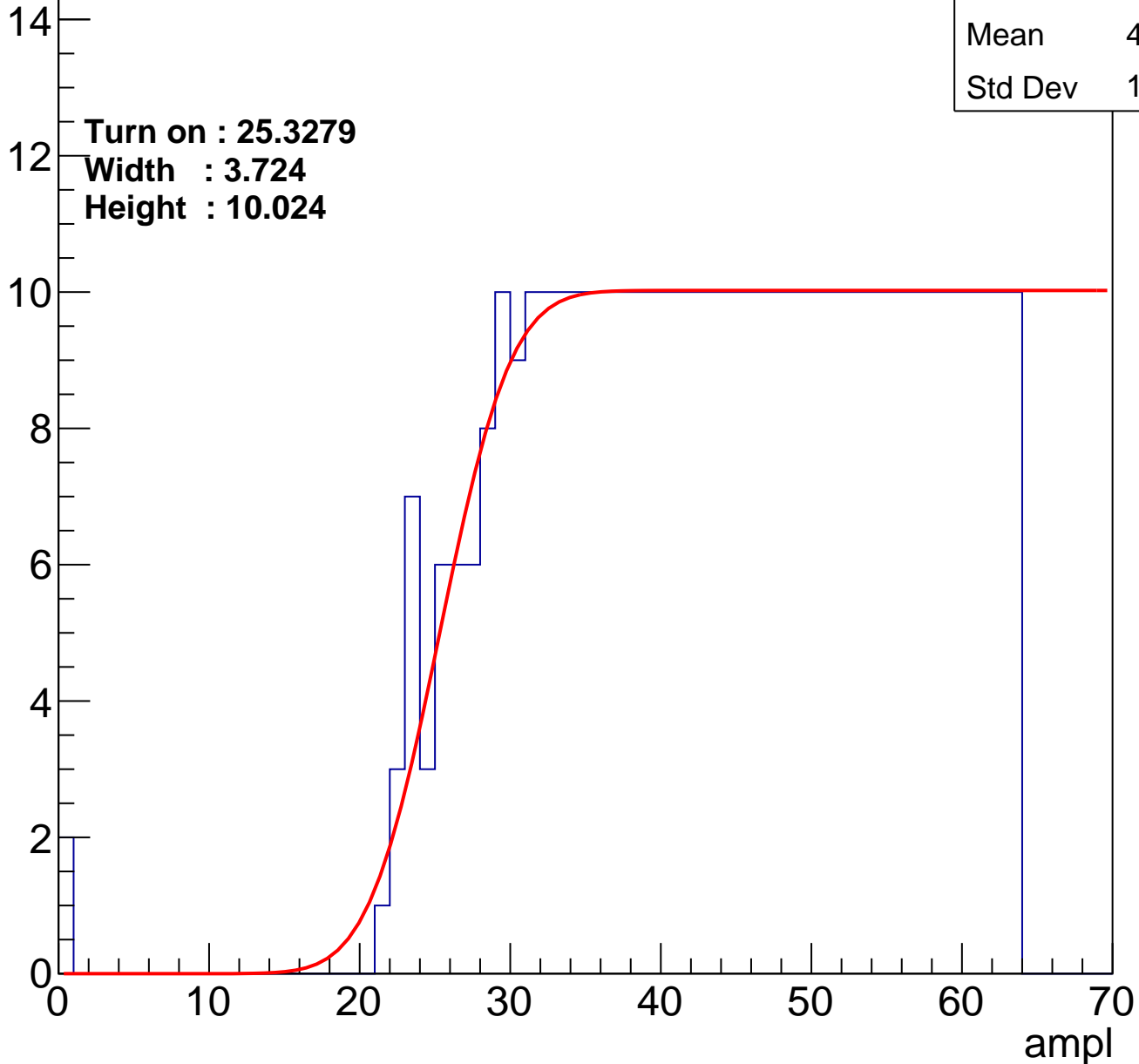
Entries	391
Mean	43.69
Std Dev	11.85

Turn on : 25.3279

Width : 3.724

Height : 10.024

Entry



B1L000S, U8-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.44
Std Dev	11.58

Turn on : 27.4338

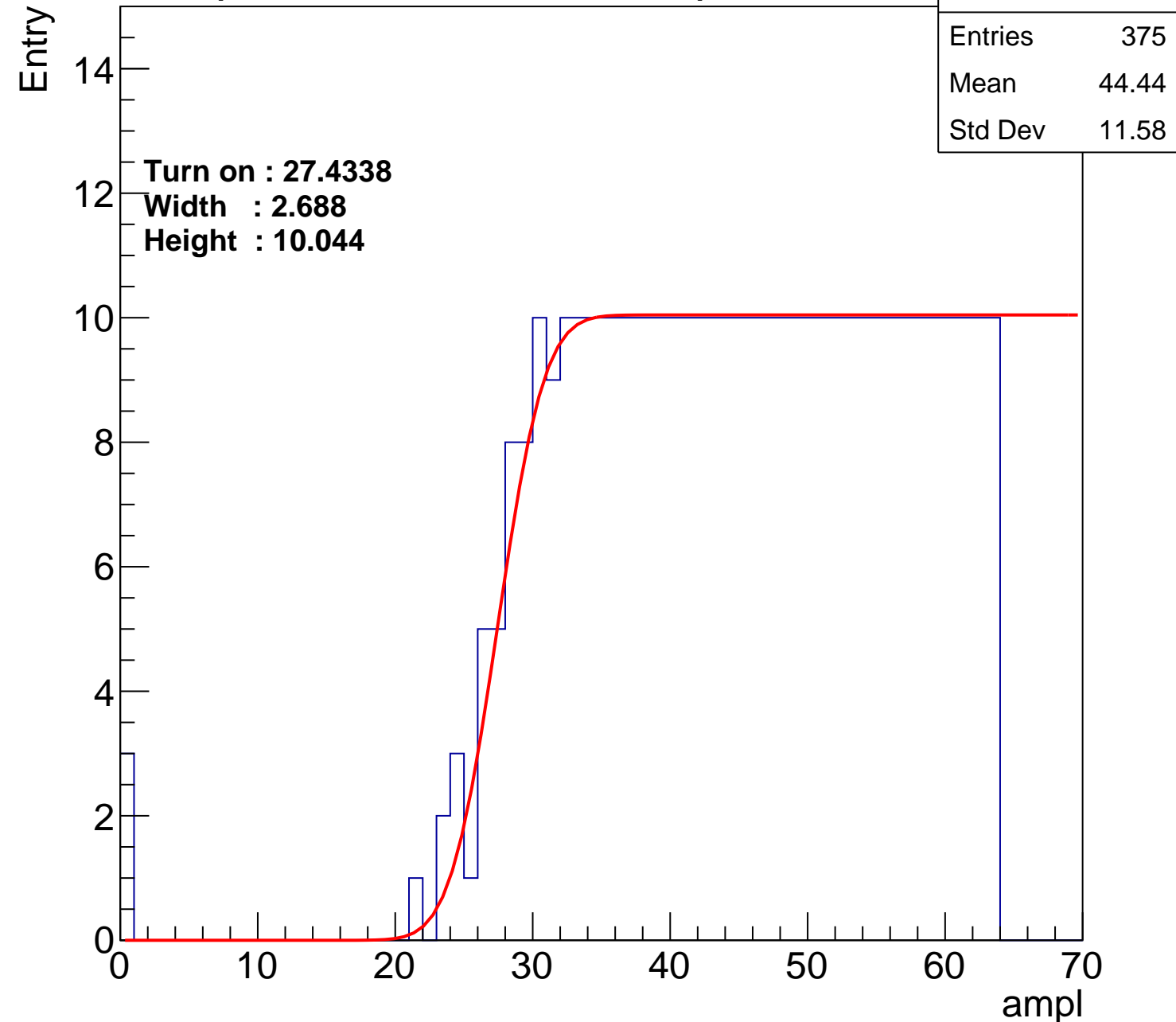
Width : 2.688

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch11

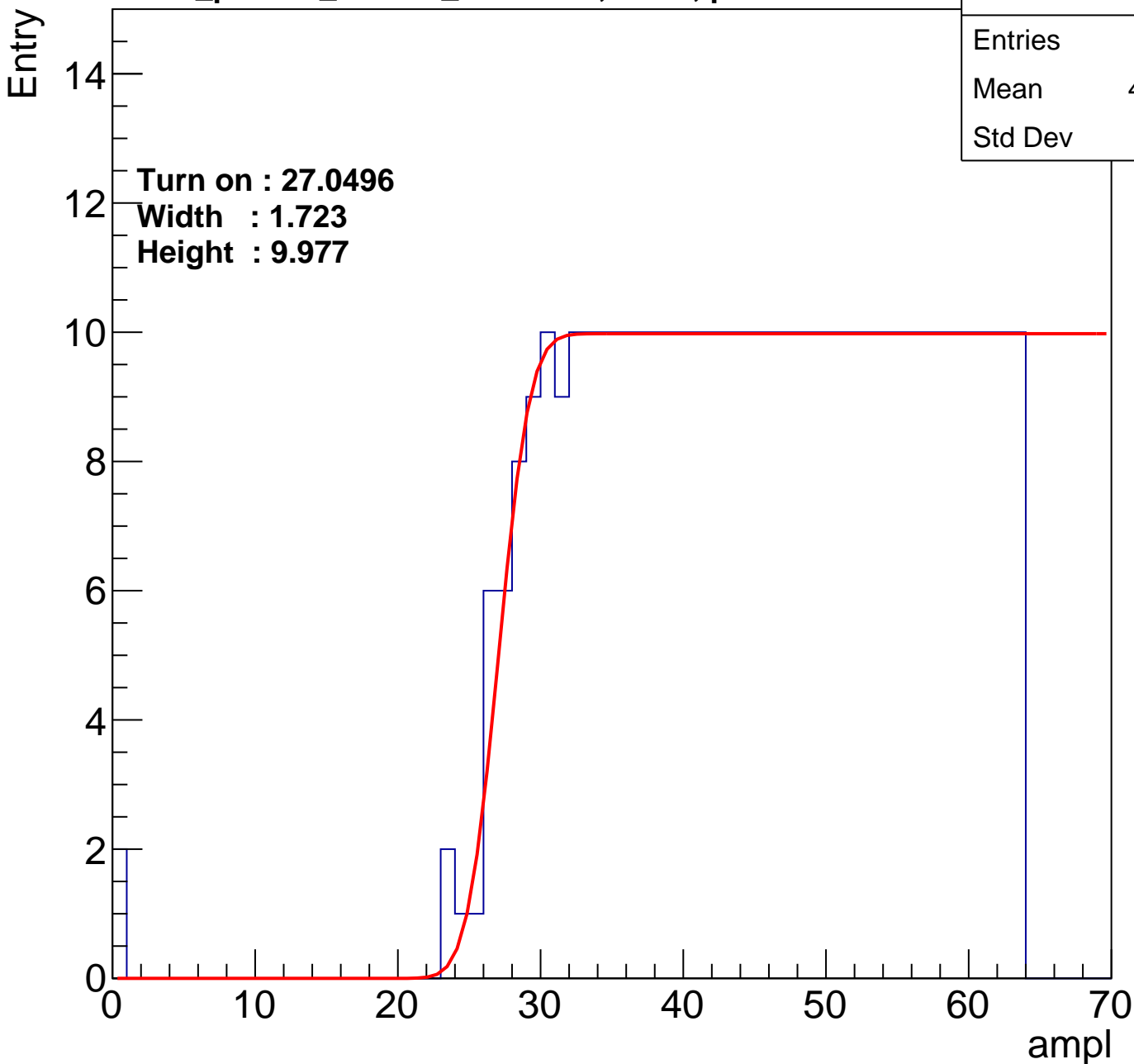
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 27.0496

Width : 1.723

Height : 9.977

Entries	374
Mean	44.59
Std Dev	11.31



B1L000S, U8-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.41
Std Dev	12.16

Turn on : 25.3576

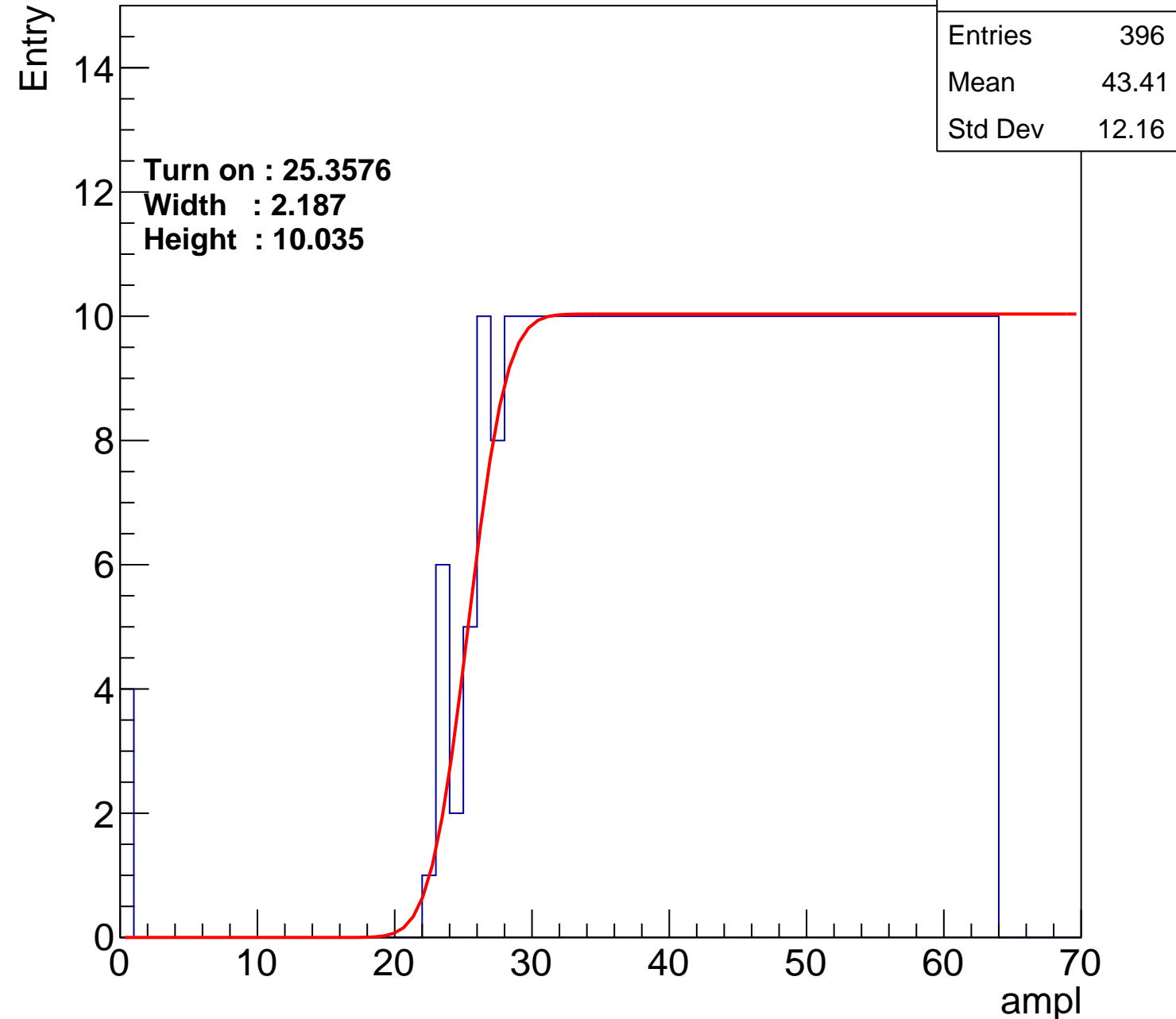
Width : 2.187

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.88
Std Dev	11.36

Turn on : 27.9671

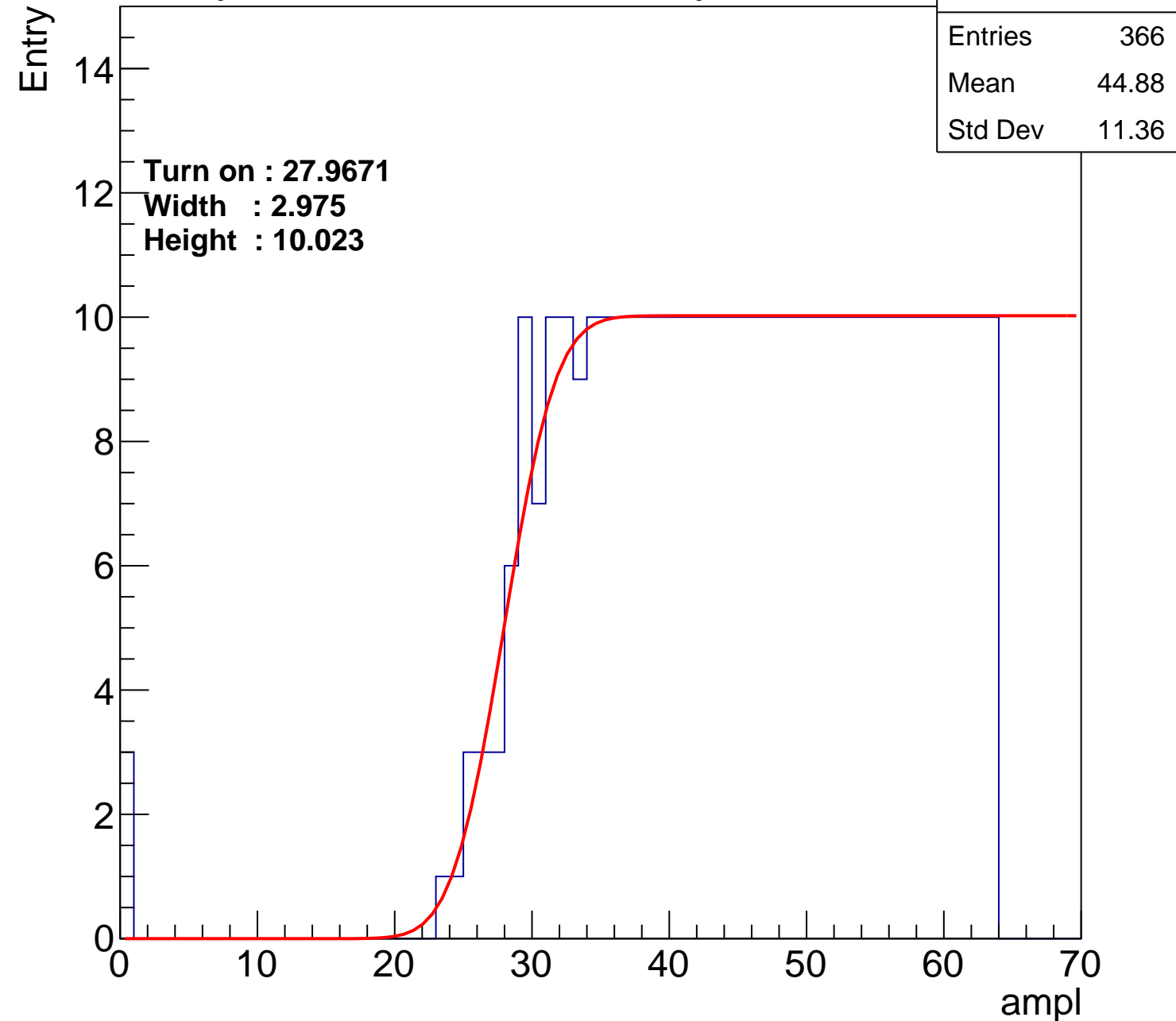
Width : 2.975

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch14

calib_packv5_042523_0143.root, FC#3, port B3

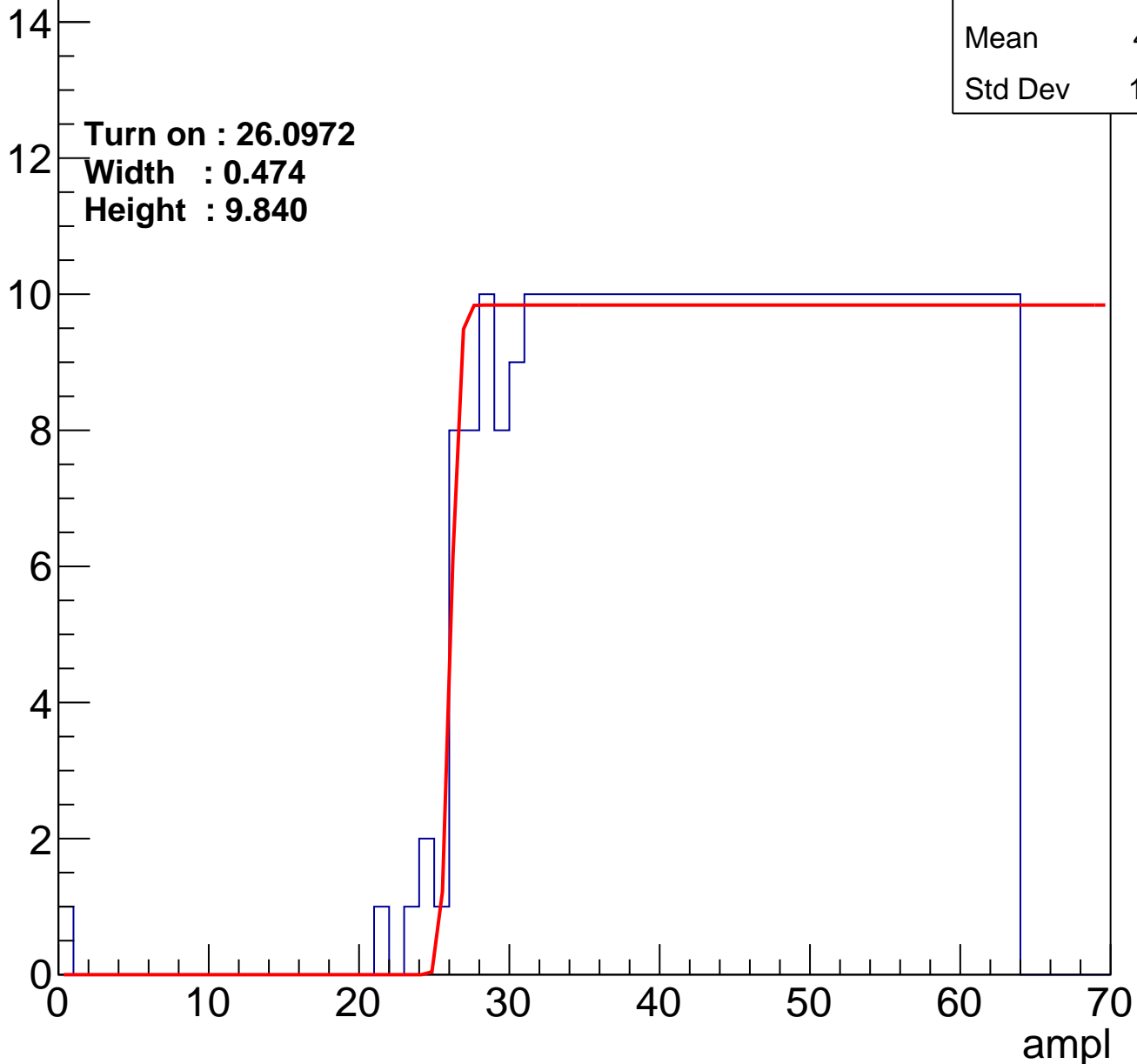
Entries	379
Mean	44.41
Std Dev	11.24

Turn on : 26.0972

Width : 0.474

Height : 9.840

Entry



B1L000S, U8-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.9
Std Dev	11.72

Turn on : 25.5910

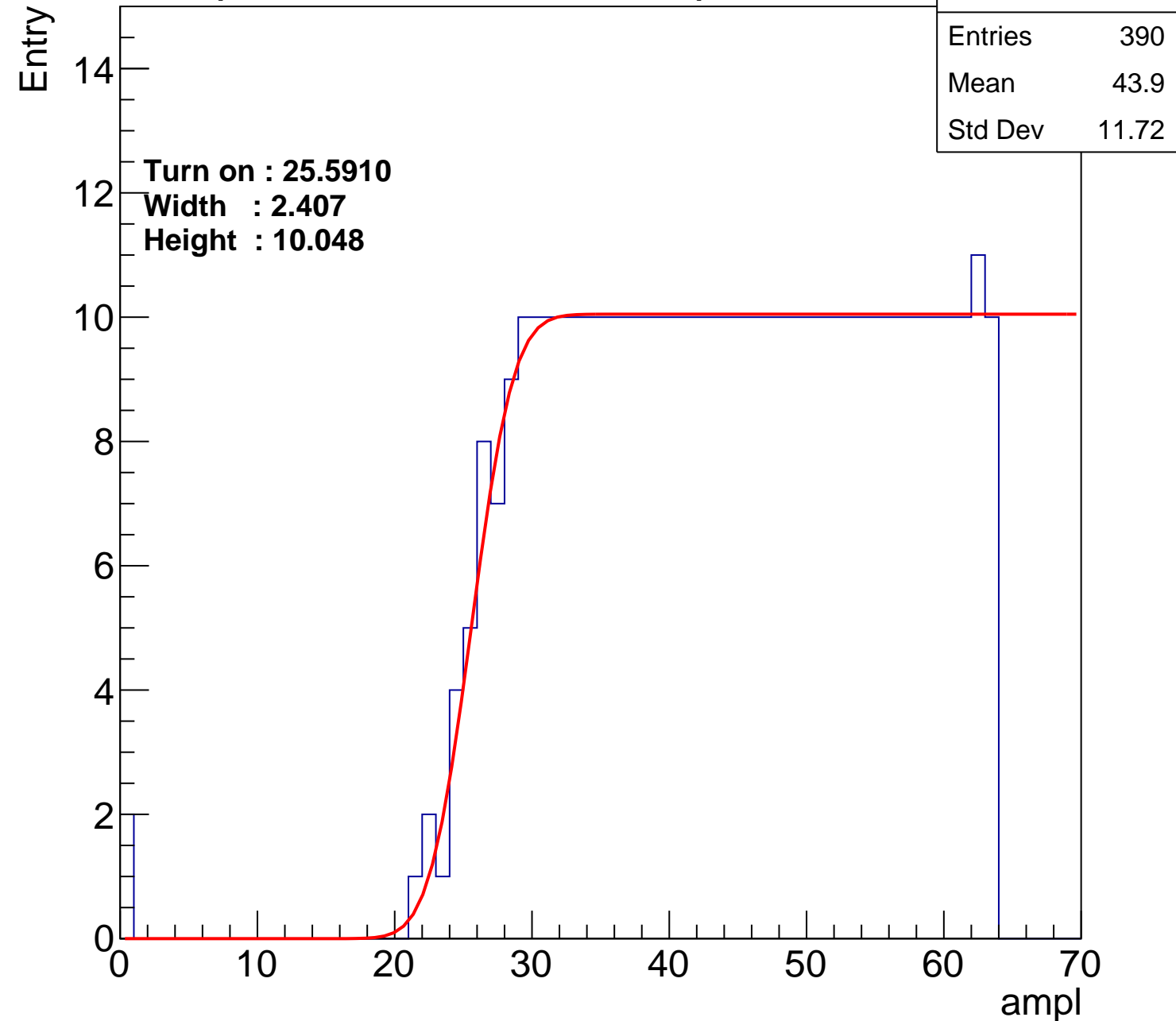
Width : 2.407

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.4216

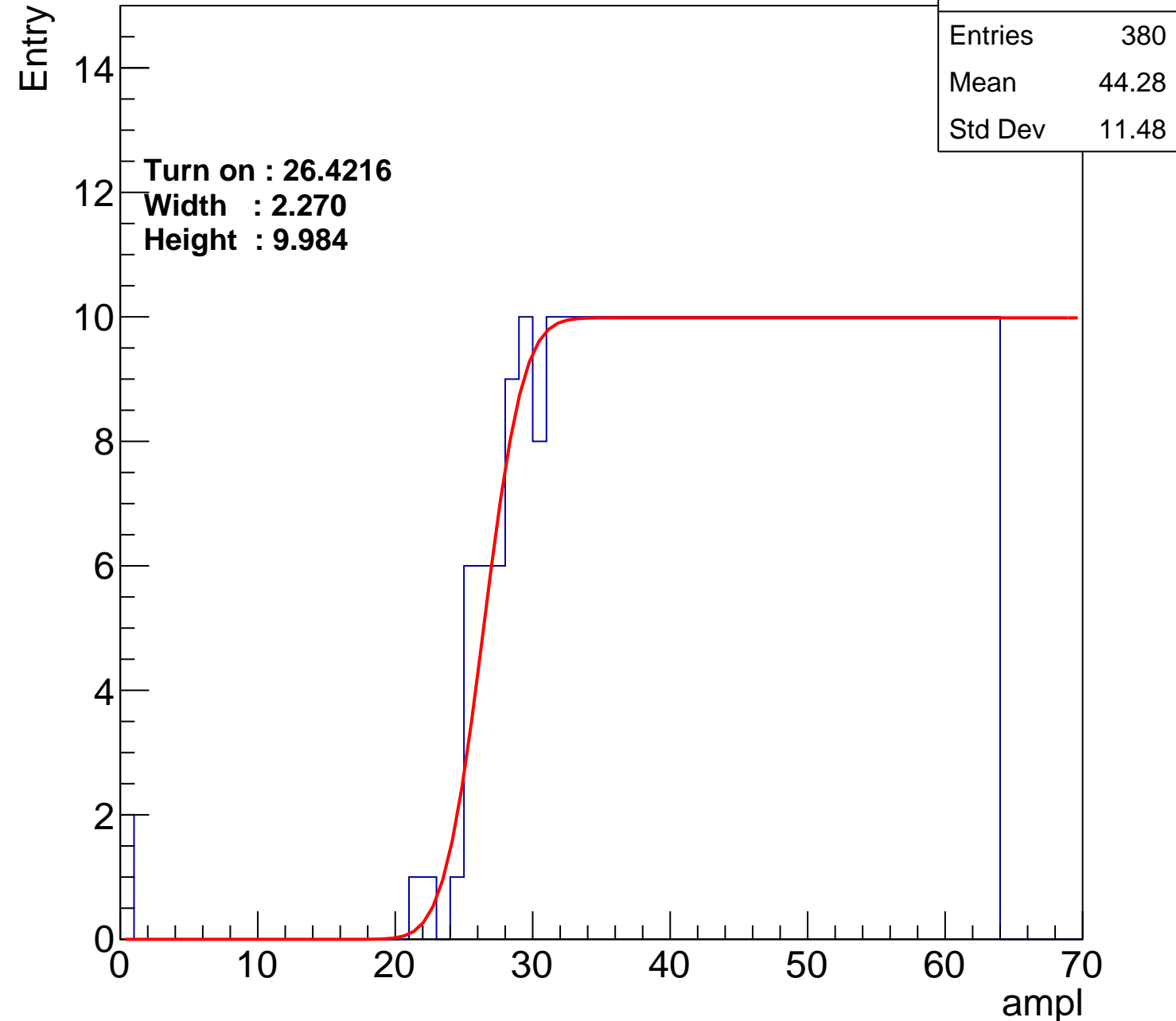
Width : 2.270

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	355
Mean	45.61
Std Dev	10.58

Turn on : 28.3013

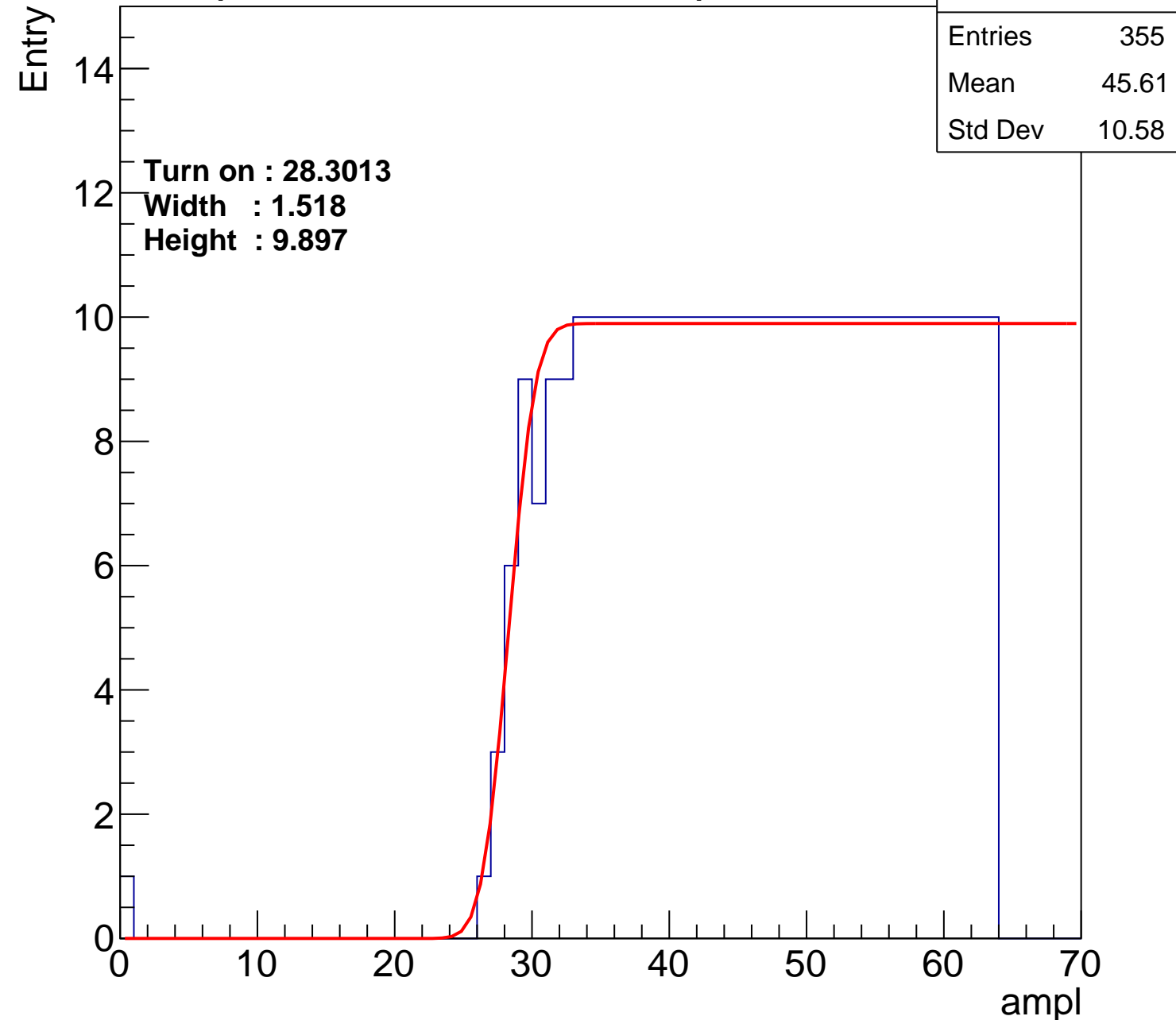
Width : 1.518

Height : 9.897

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.96
Std Dev	11.5

Turn on : 25.4857

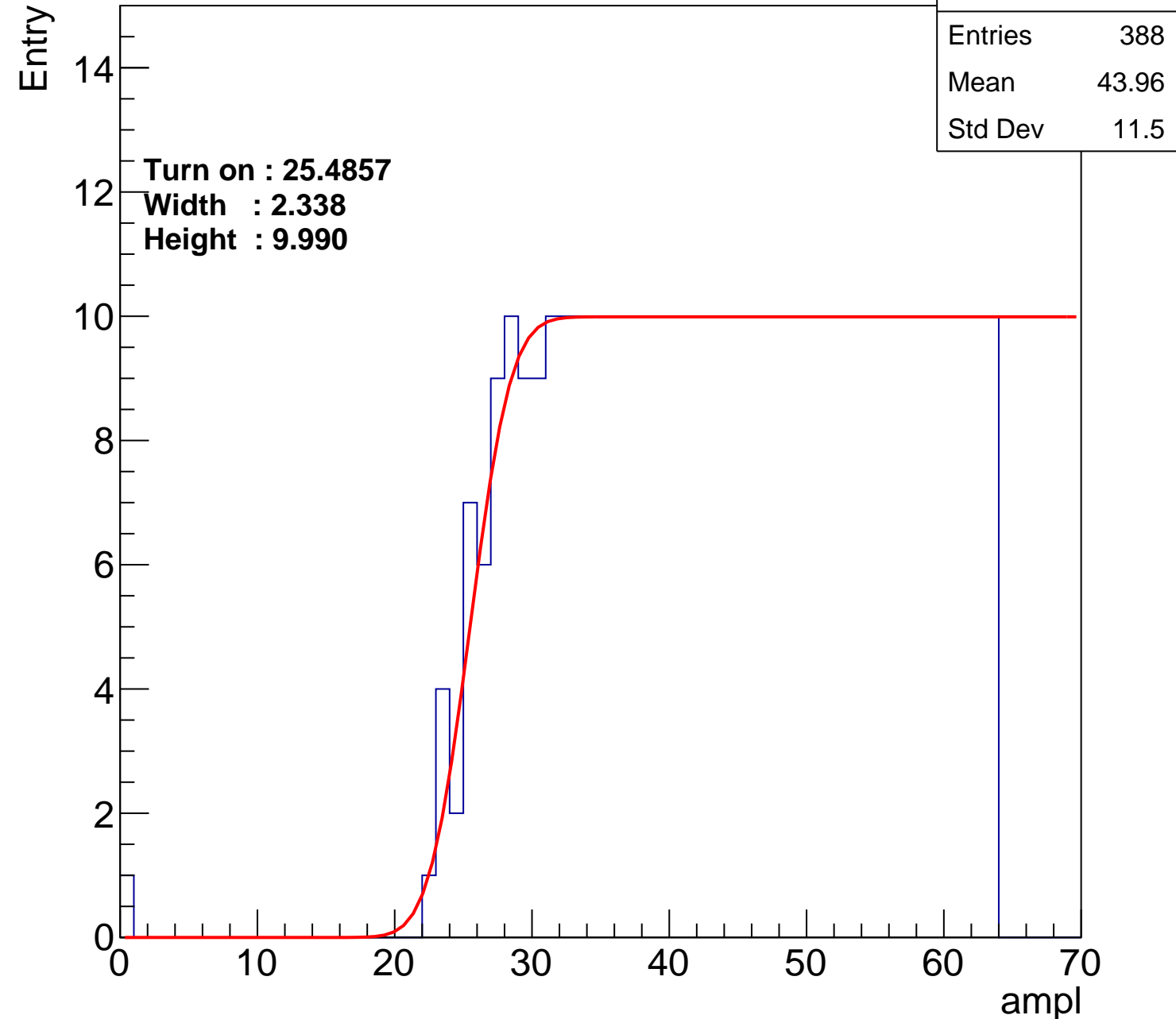
Width : 2.338

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch19

calib_packv5_042523_0143.root, FC#3, port B3

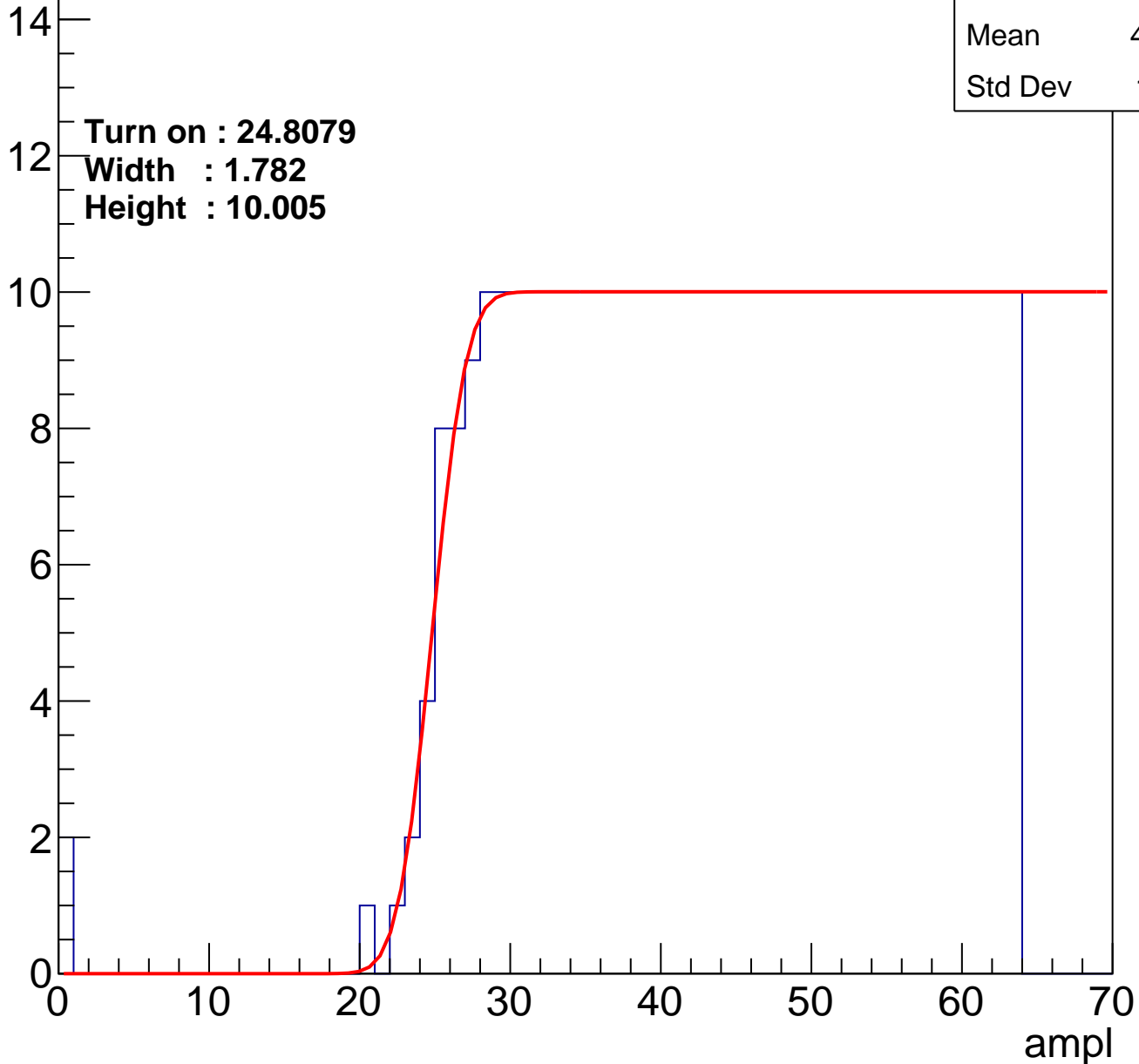
Entries	395
Mean	43.58
Std Dev	11.81

Turn on : 24.8079

Width : 1.782

Height : 10.005

Entry



B1L000S, U8-ch20

calib_packv5_042523_0143.root, FC#3, port B3

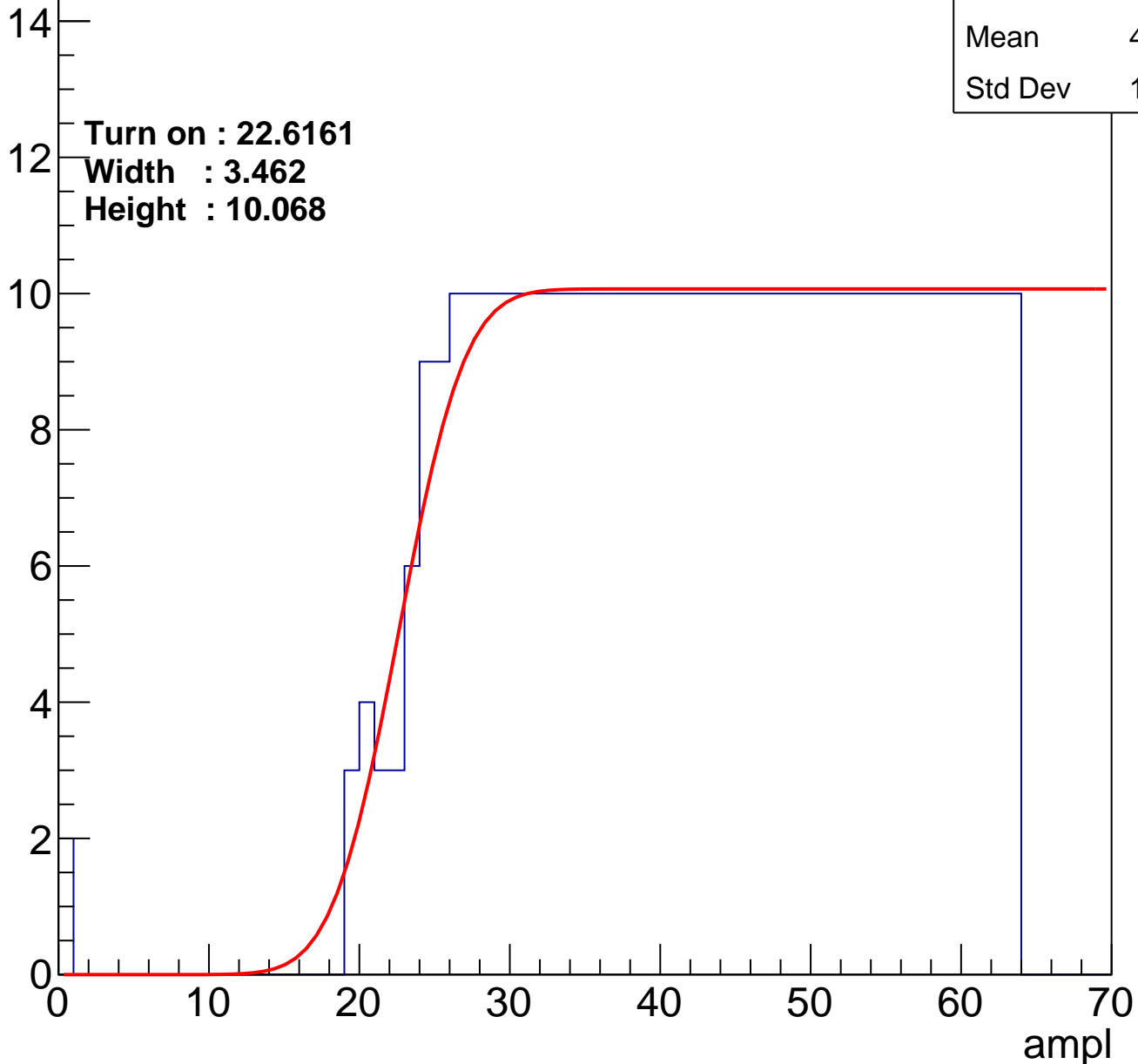
Entries	419
Mean	42.37
Std Dev	12.48

Turn on : 22.6161

Width : 3.462

Height : 10.068

Entry



B1L000S, U8-ch21

calib_packv5_042523_0143.root, FC#3, port B3

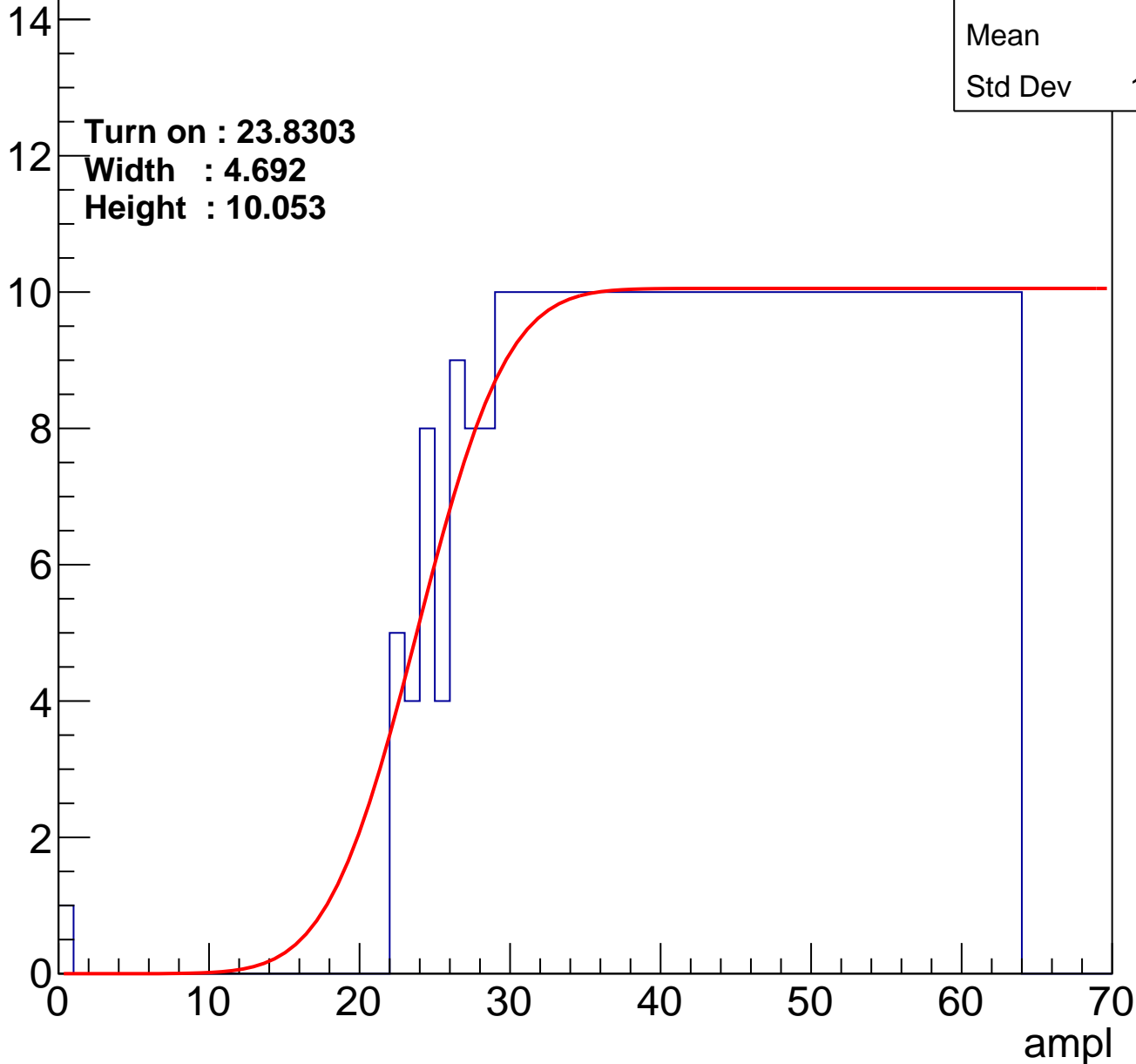
Entries	397
Mean	43.5
Std Dev	11.77

Turn on : 23.8303

Width : 4.692

Height : 10.053

Entry



B1L000S, U8-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	42.88
Std Dev	12.67

Turn on : 24.2364

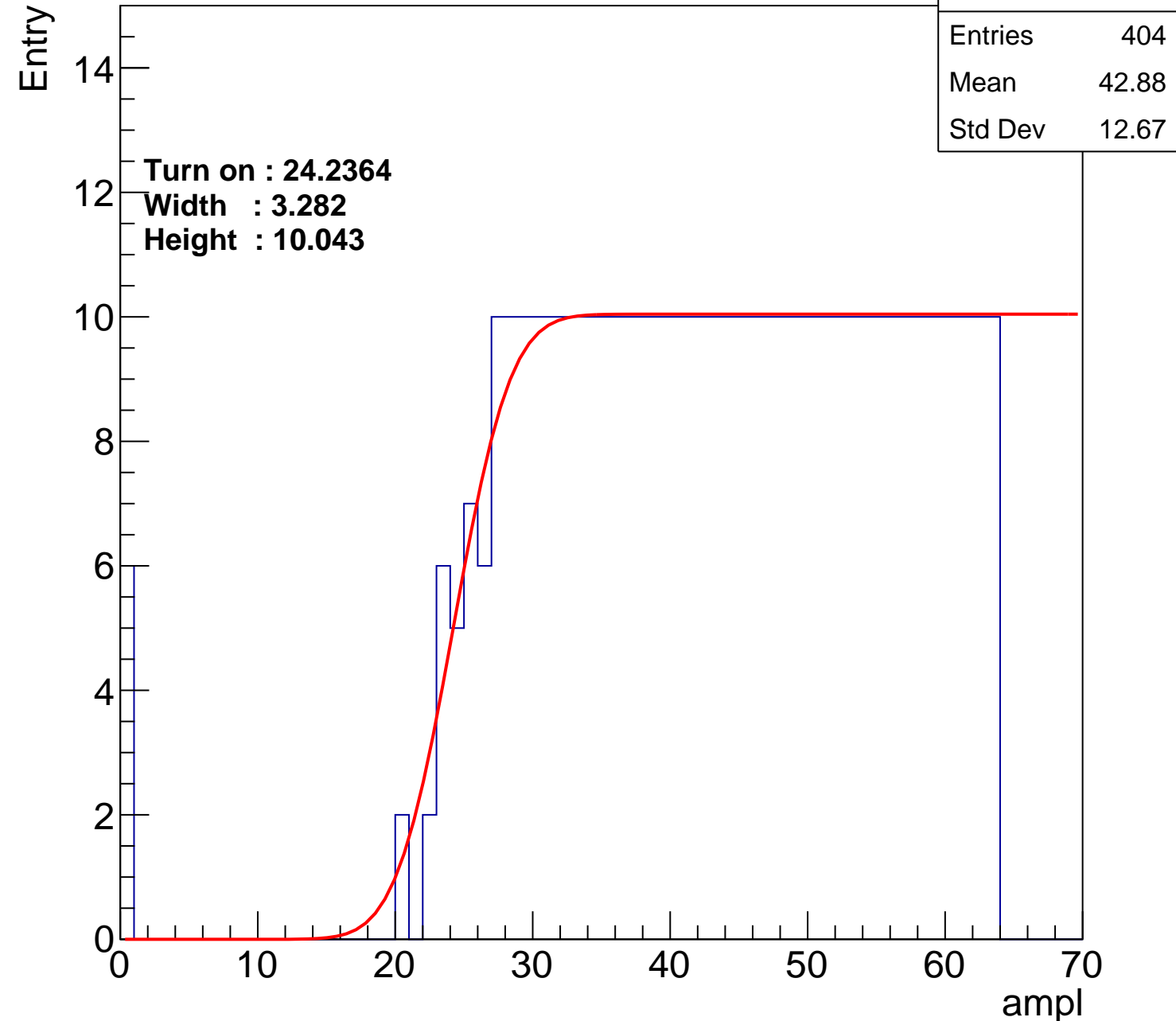
Width : 3.282

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.22
Std Dev	11.83

Turn on : 26.9610

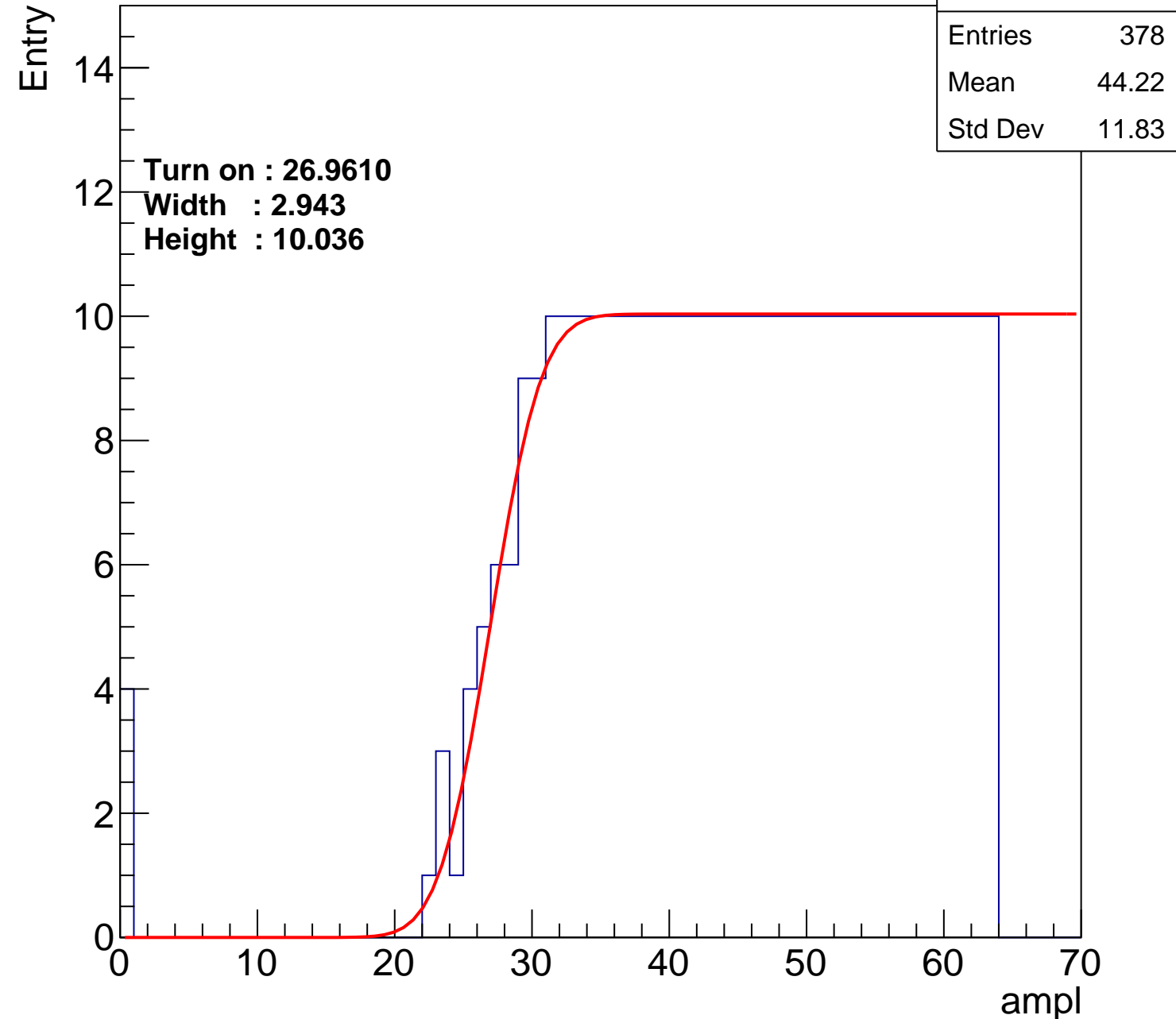
Width : 2.943

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.83
Std Dev	11.56

Turn on : 24.8052

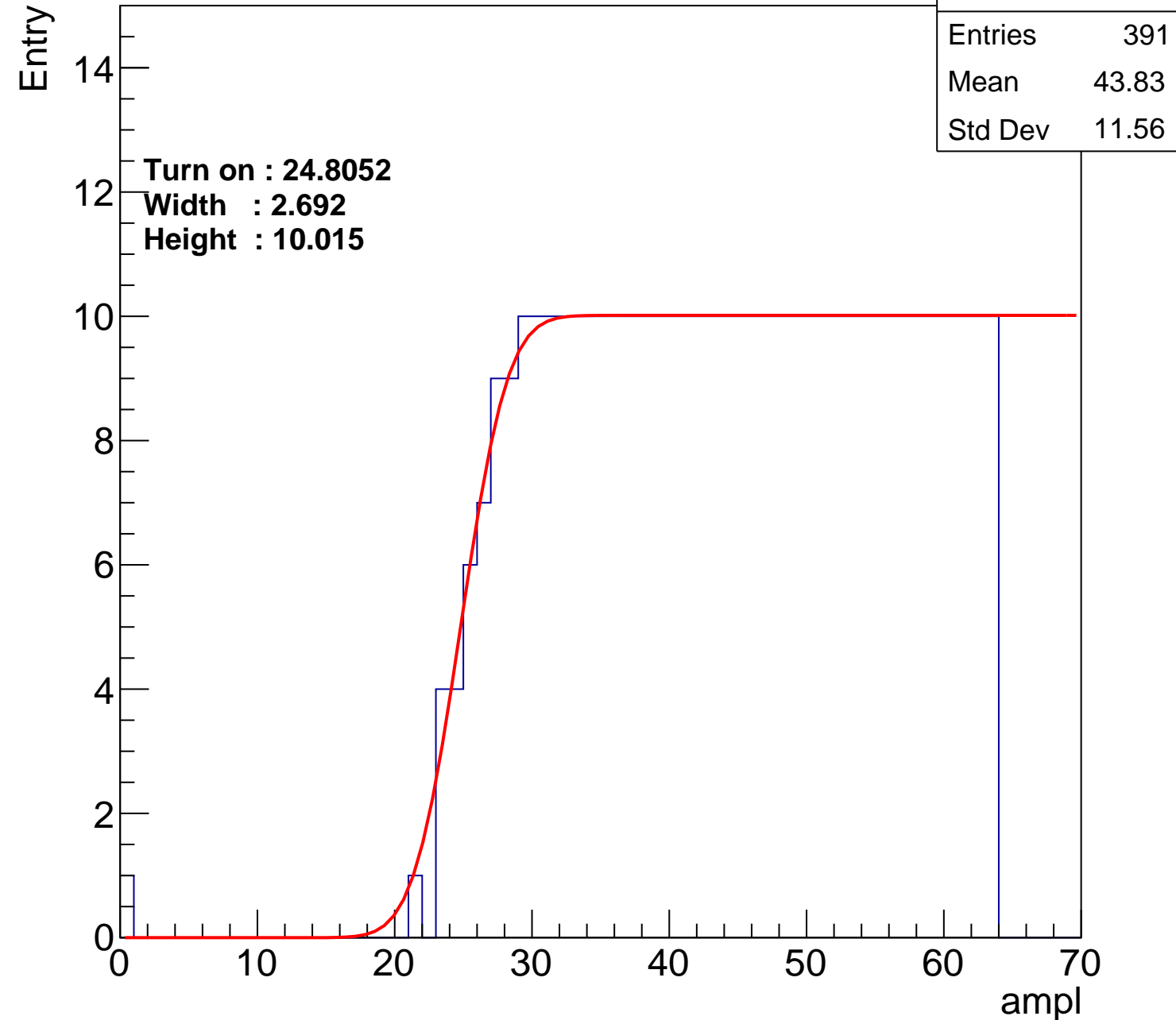
Width : 2.692

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.95
Std Dev	11.94

Turn on : 25.8802

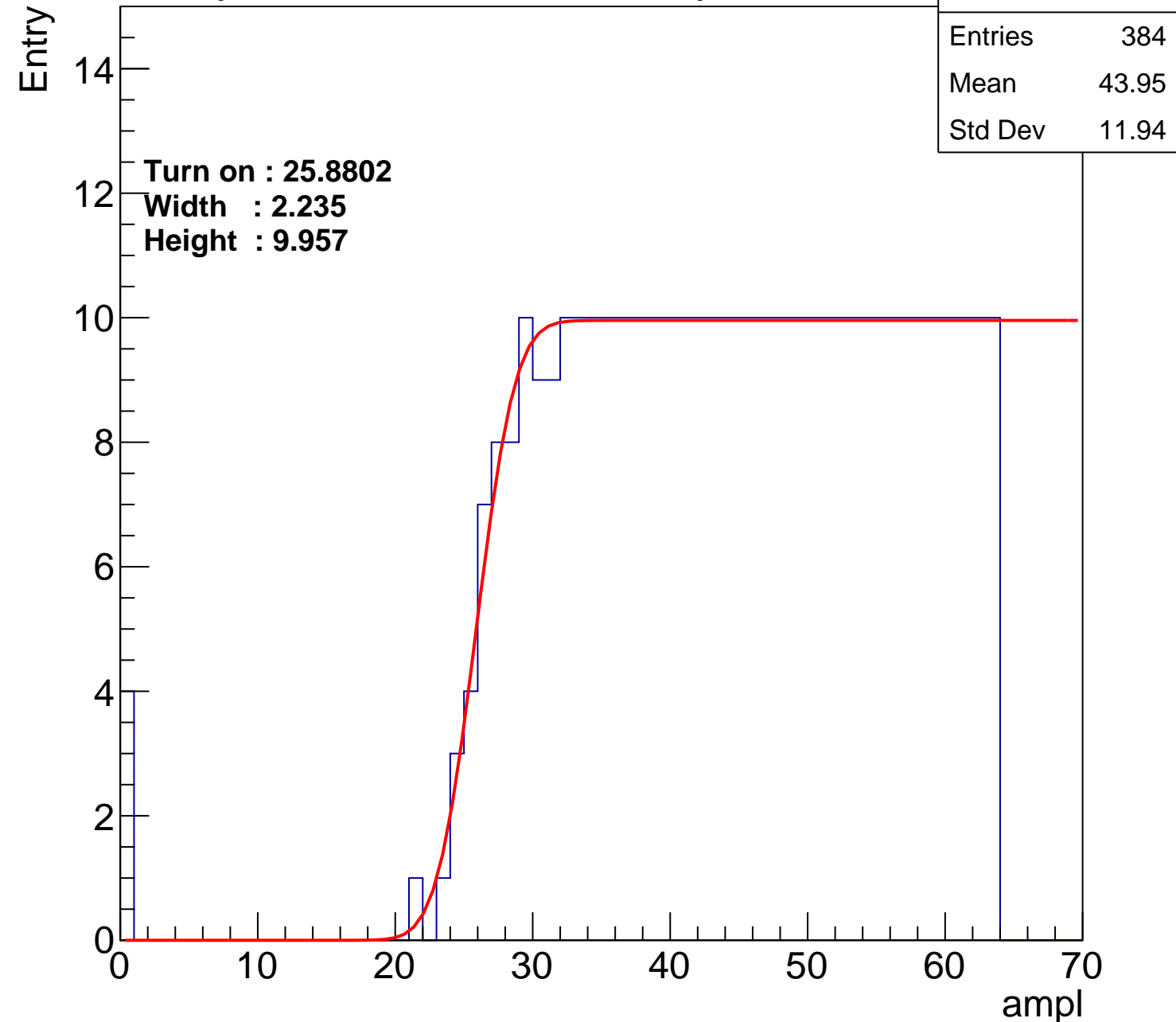
Width : 2.235

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.9
Std Dev	11.8

Turn on : 25.7223

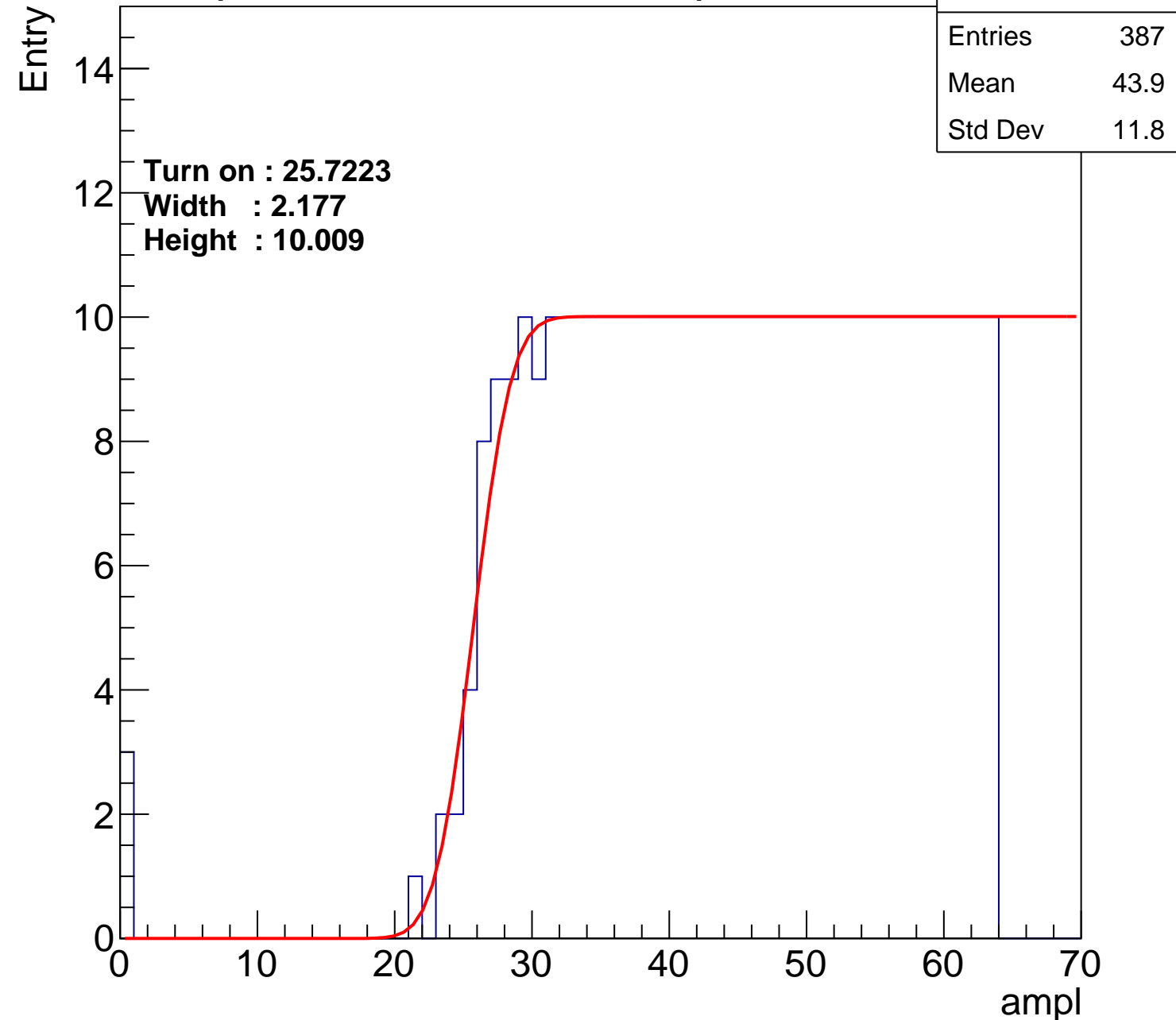
Width : 2.177

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch27

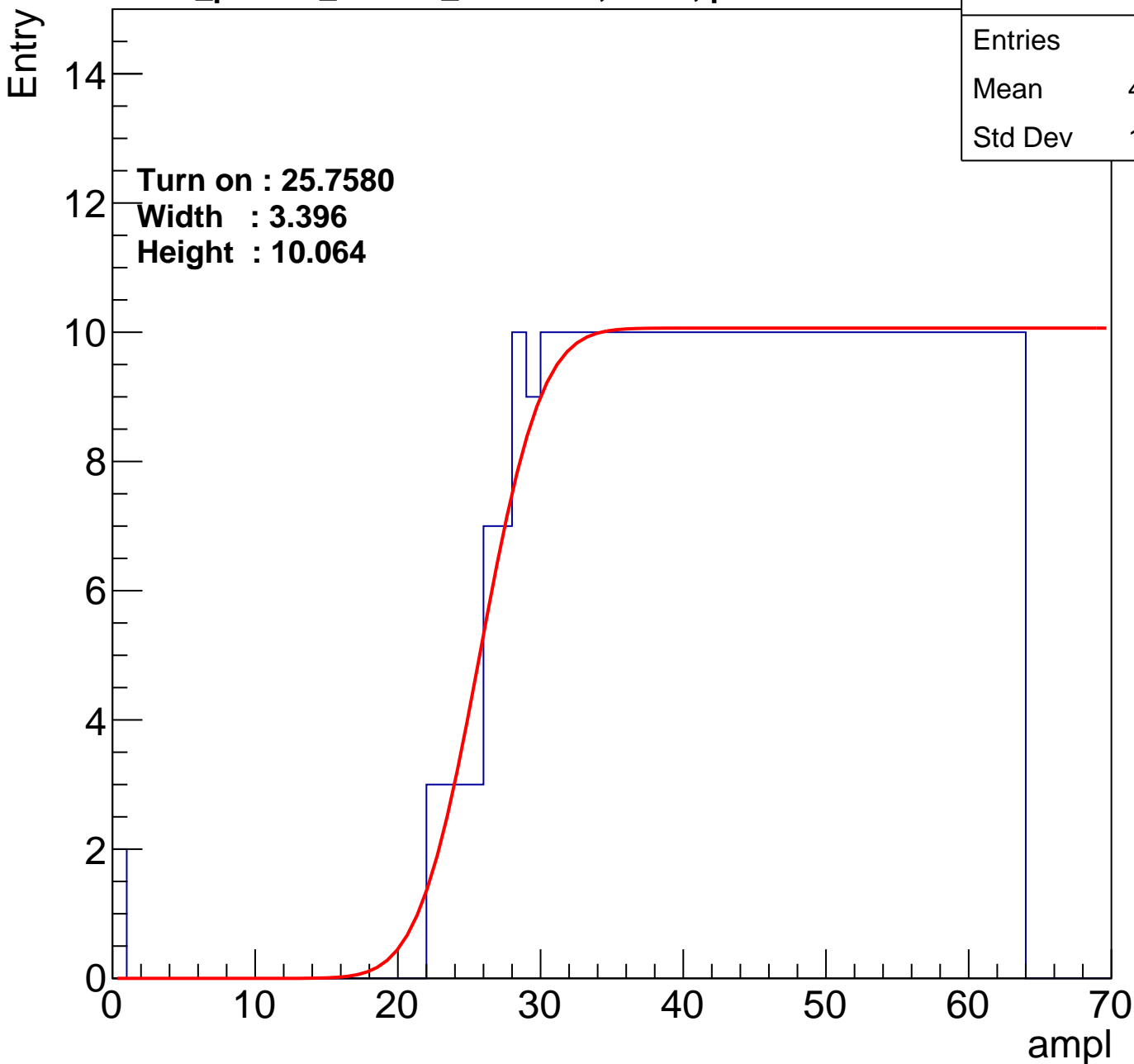
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 25.7580

Width : 3.396

Height : 10.064

Entries	387
Mean	43.94
Std Dev	11.66



B1L000S, U8-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.25
Std Dev	12.13

Turn on : 24.3473

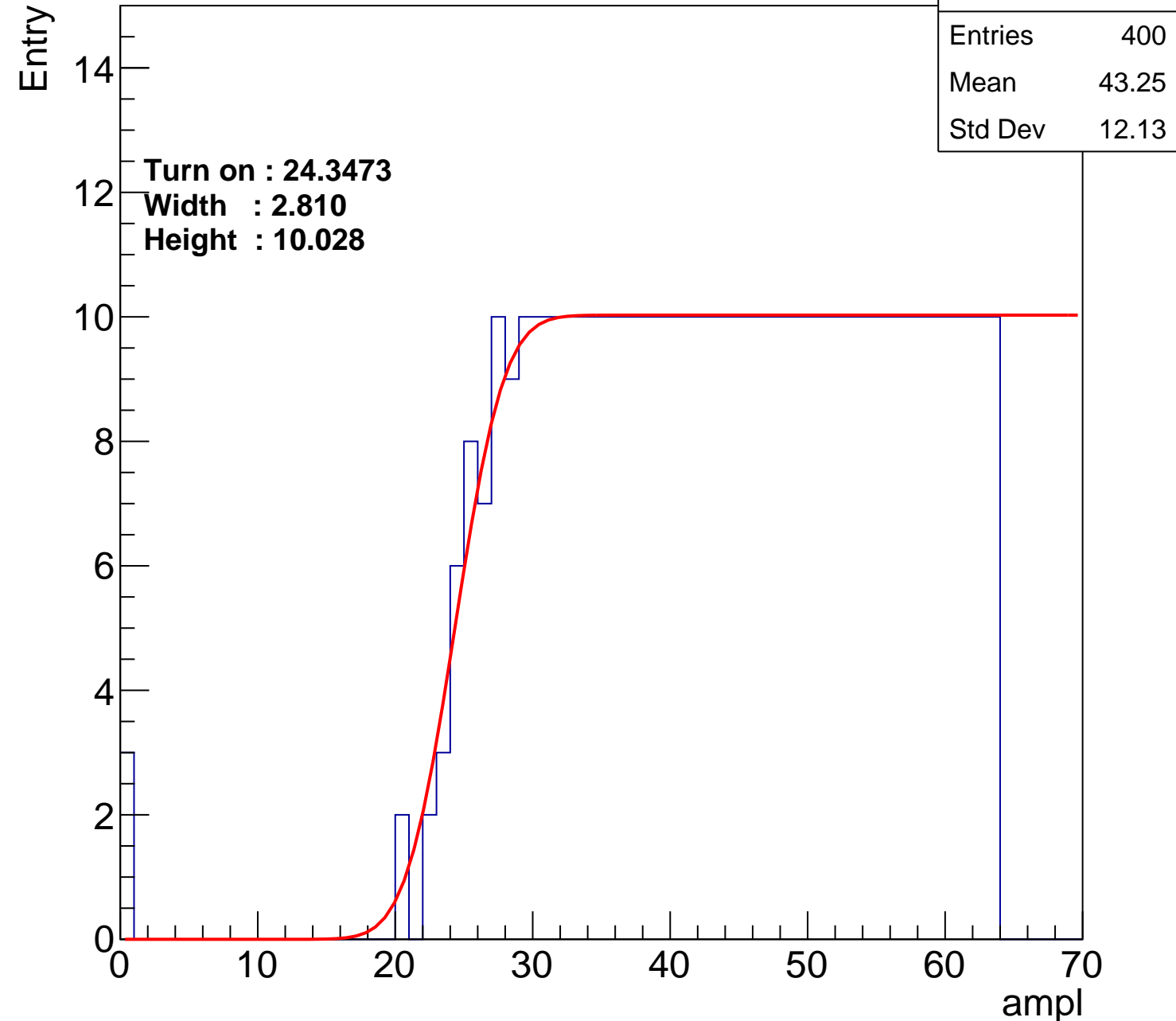
Width : 2.810

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.46
Std Dev	11.23

Turn on : 26.7703

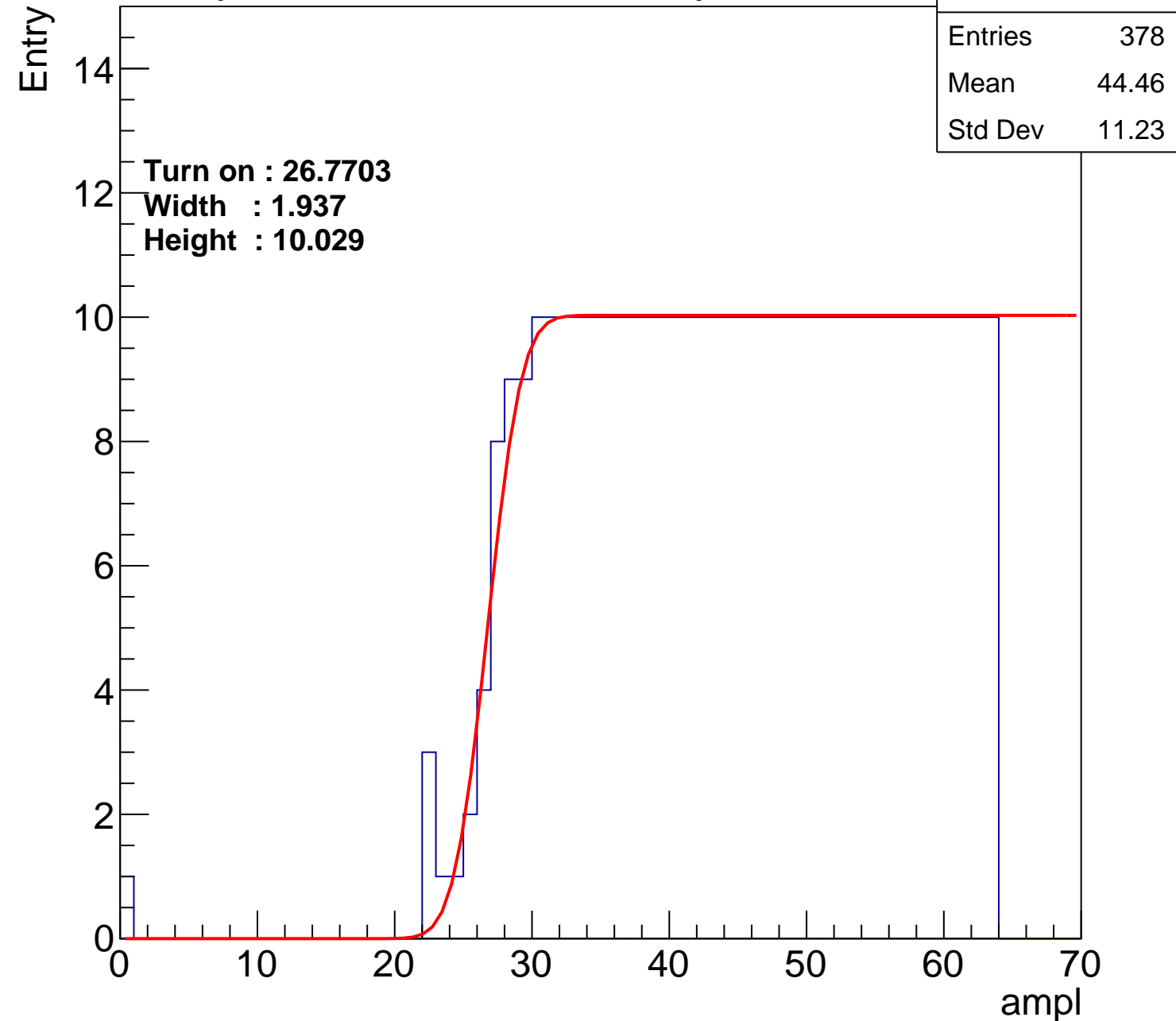
Width : 1.937

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	43.97
Std Dev	12.08

Turn on : 27.0438

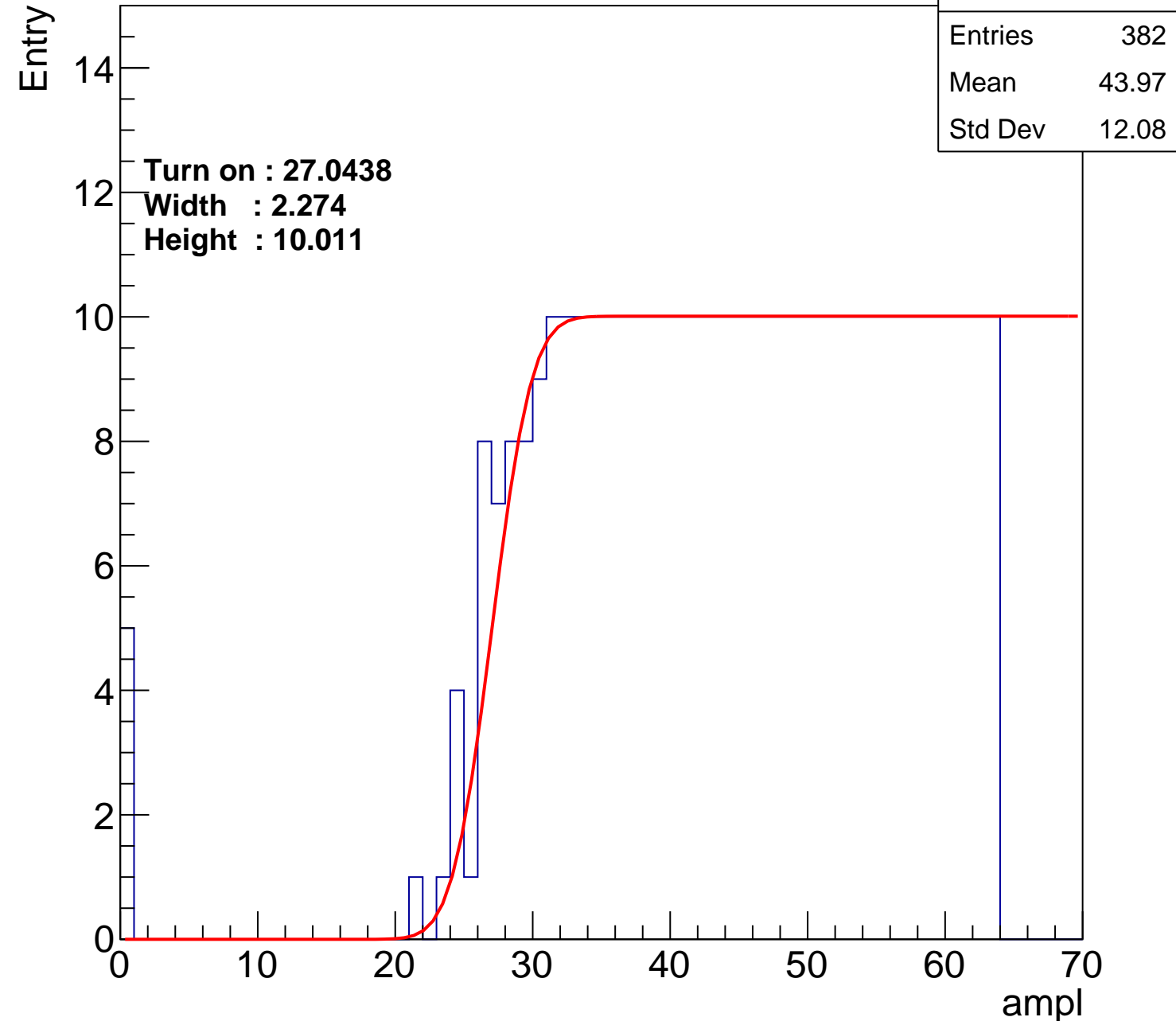
Width : 2.274

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.06
Std Dev	11.51

Turn on : 25.6153

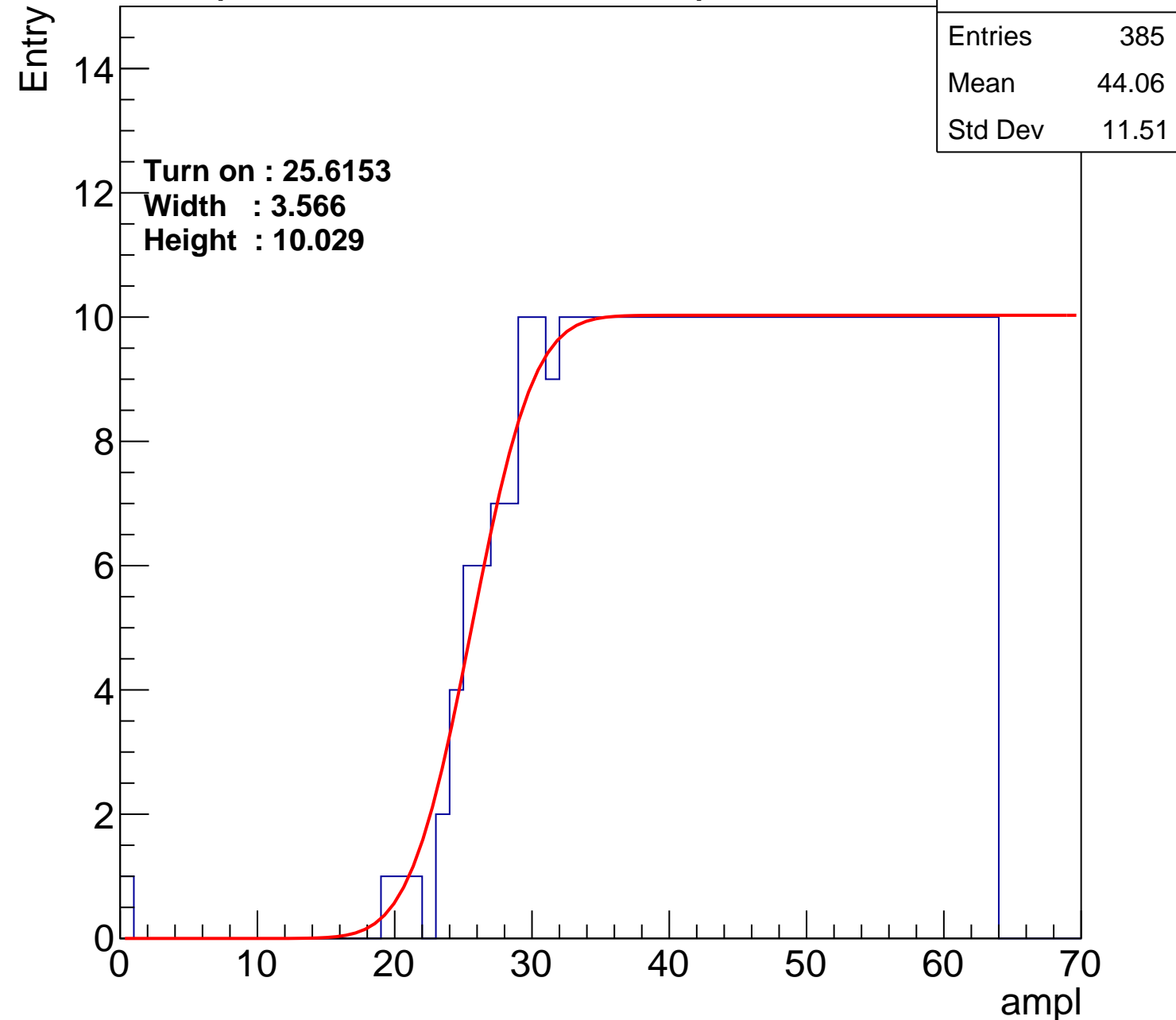
Width : 3.566

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.93
Std Dev	11.14

Turn on : 27.4336

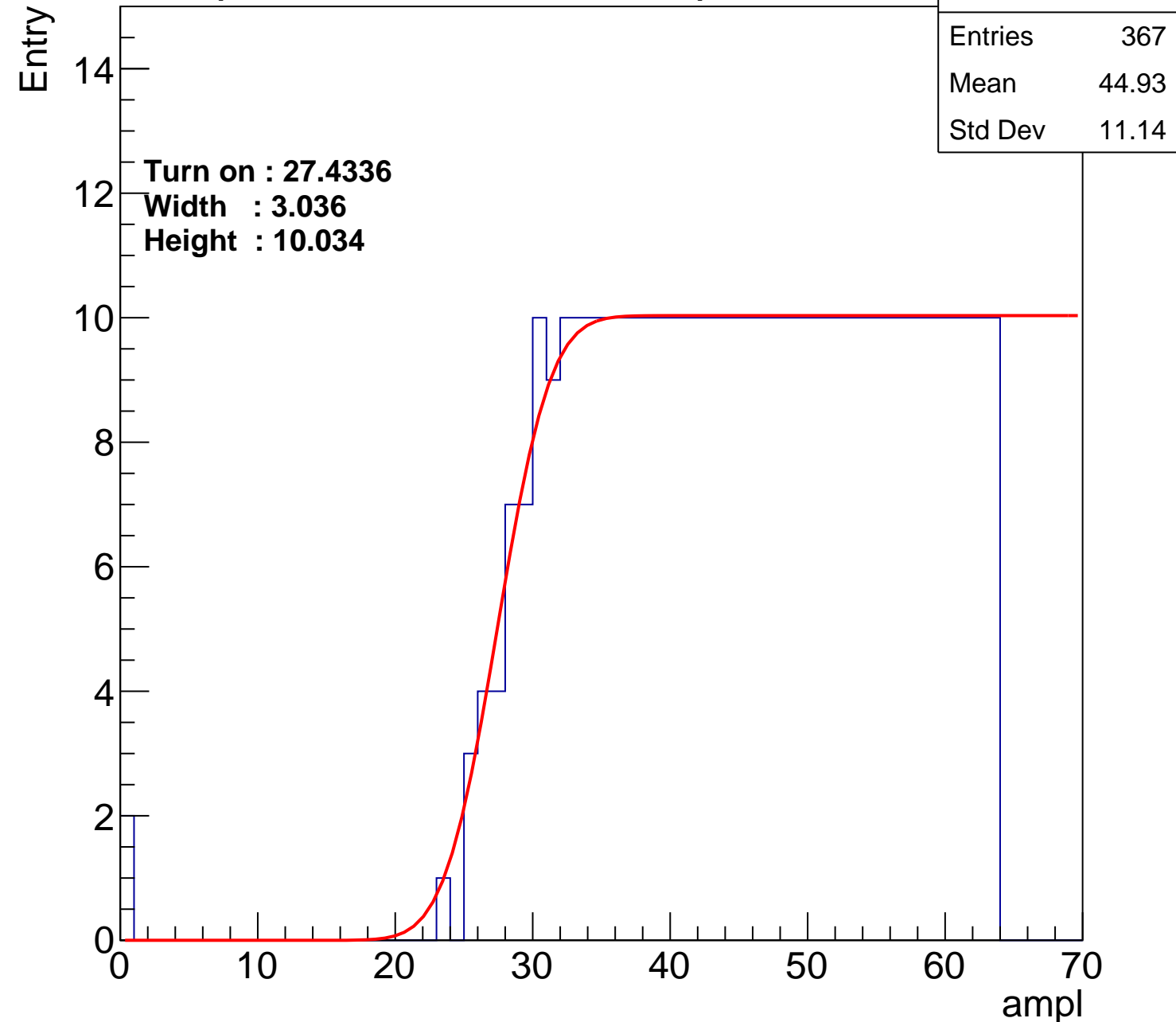
Width : 3.036

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.87
Std Dev	11.54

Turn on : 25.1610

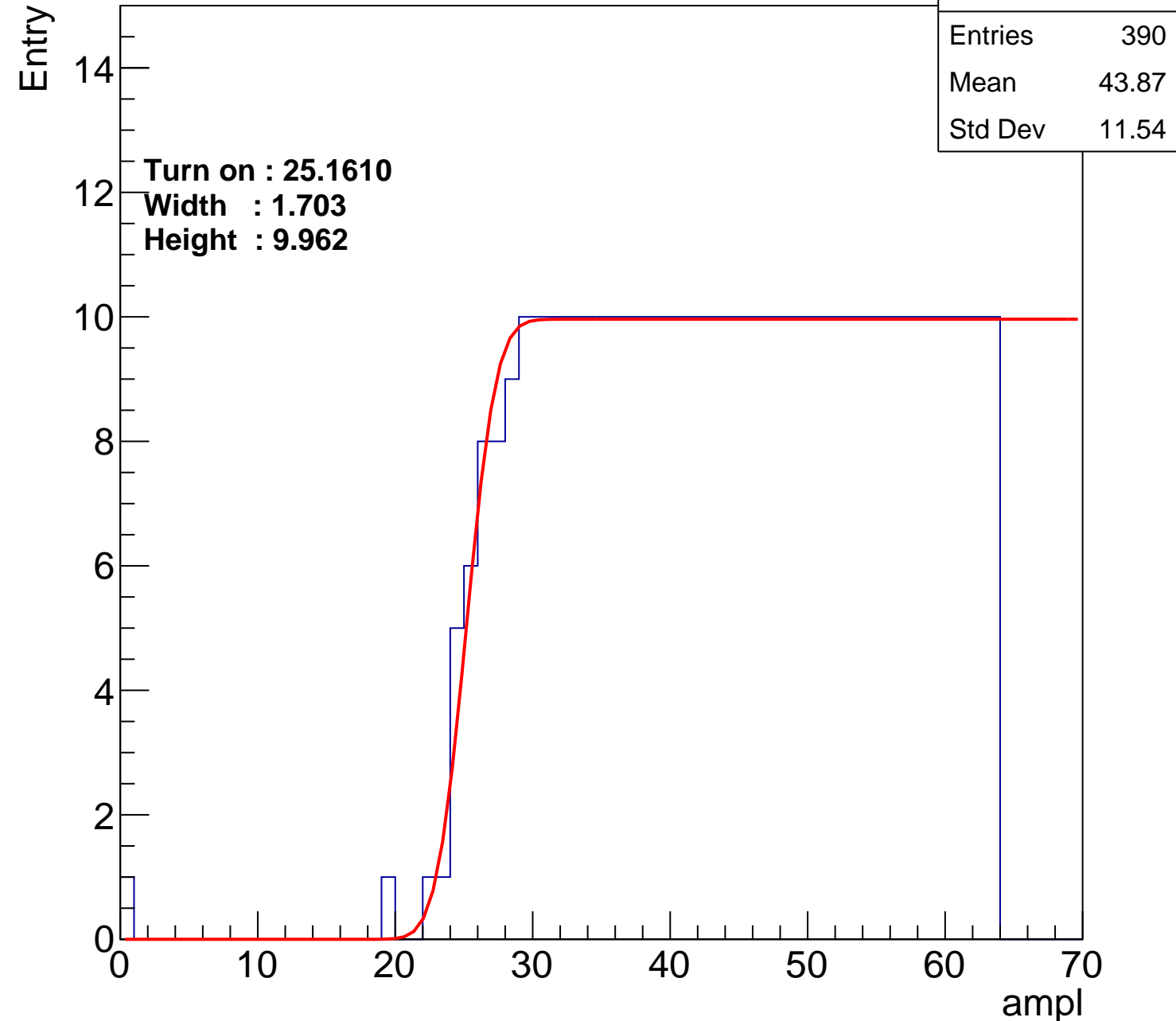
Width : 1.703

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch34

calib_packv5_042523_0143.root, FC#3, port B3

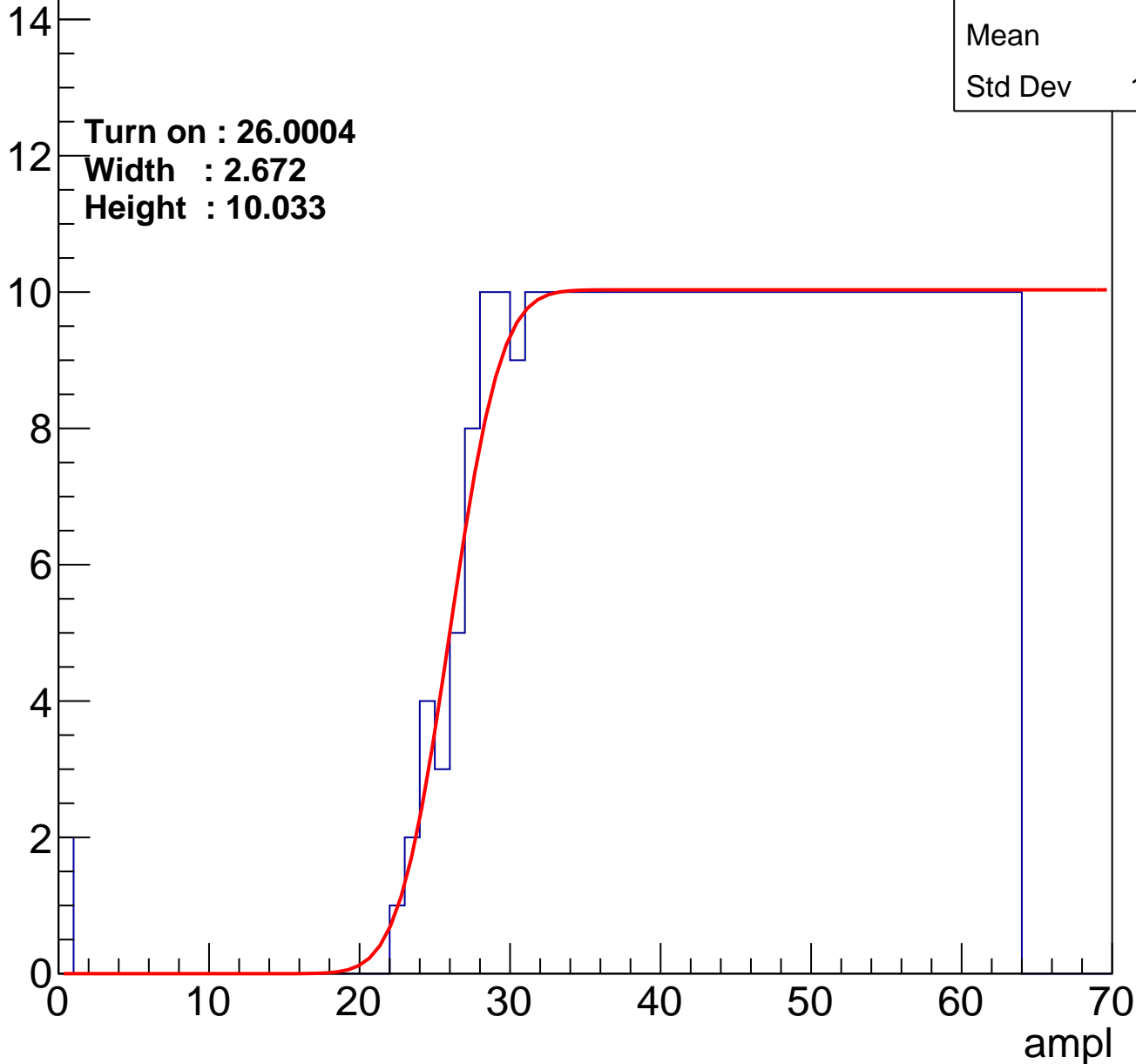
Entry

Entries	384
Mean	44.1
Std Dev	11.56

Turn on : 26.0004

Width : 2.672

Height : 10.033



B1L000S, U8-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.11
Std Dev	11.73

Turn on : 25.3559

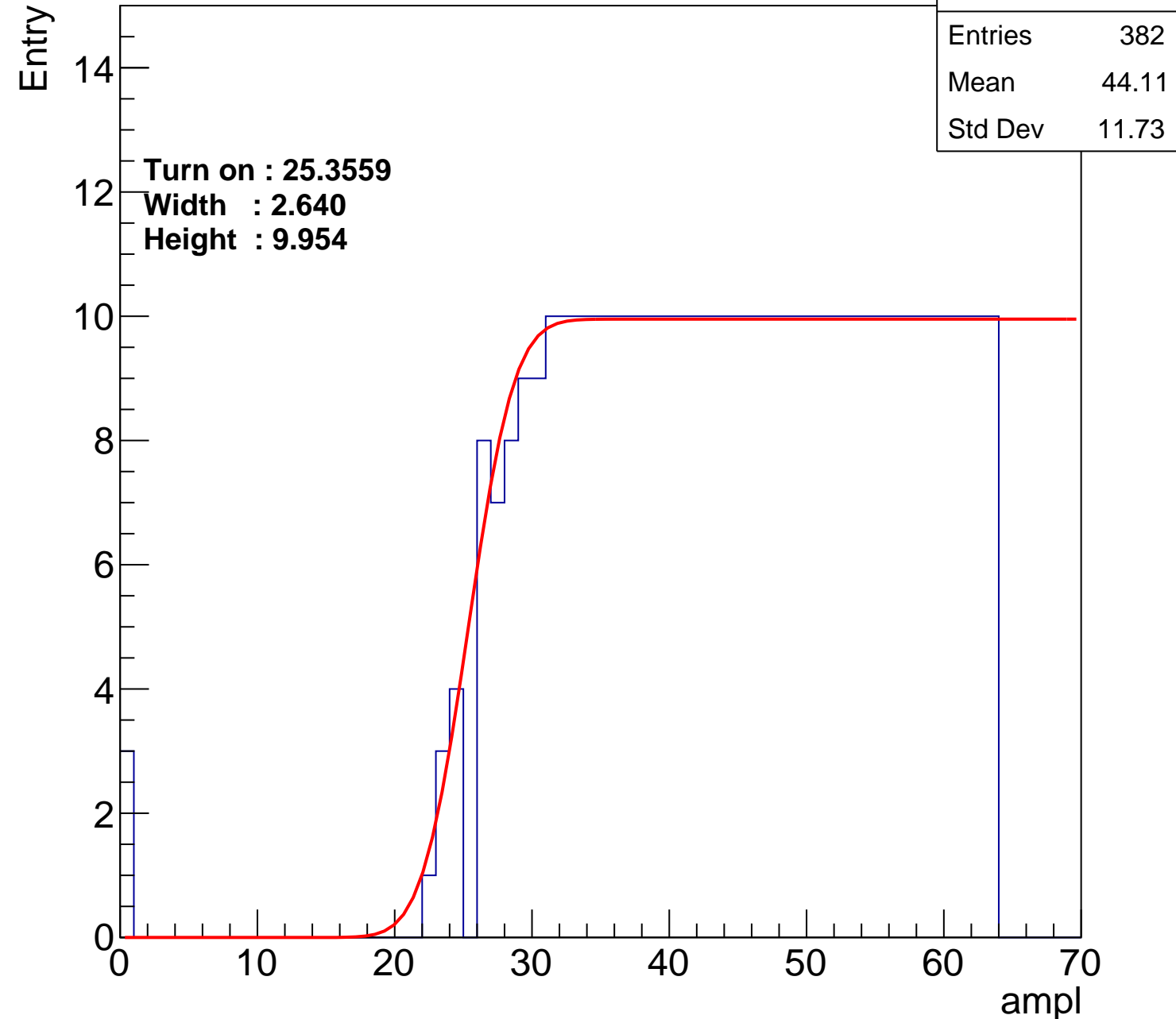
Width : 2.640

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.46
Std Dev	11.36

Turn on : 26.3742

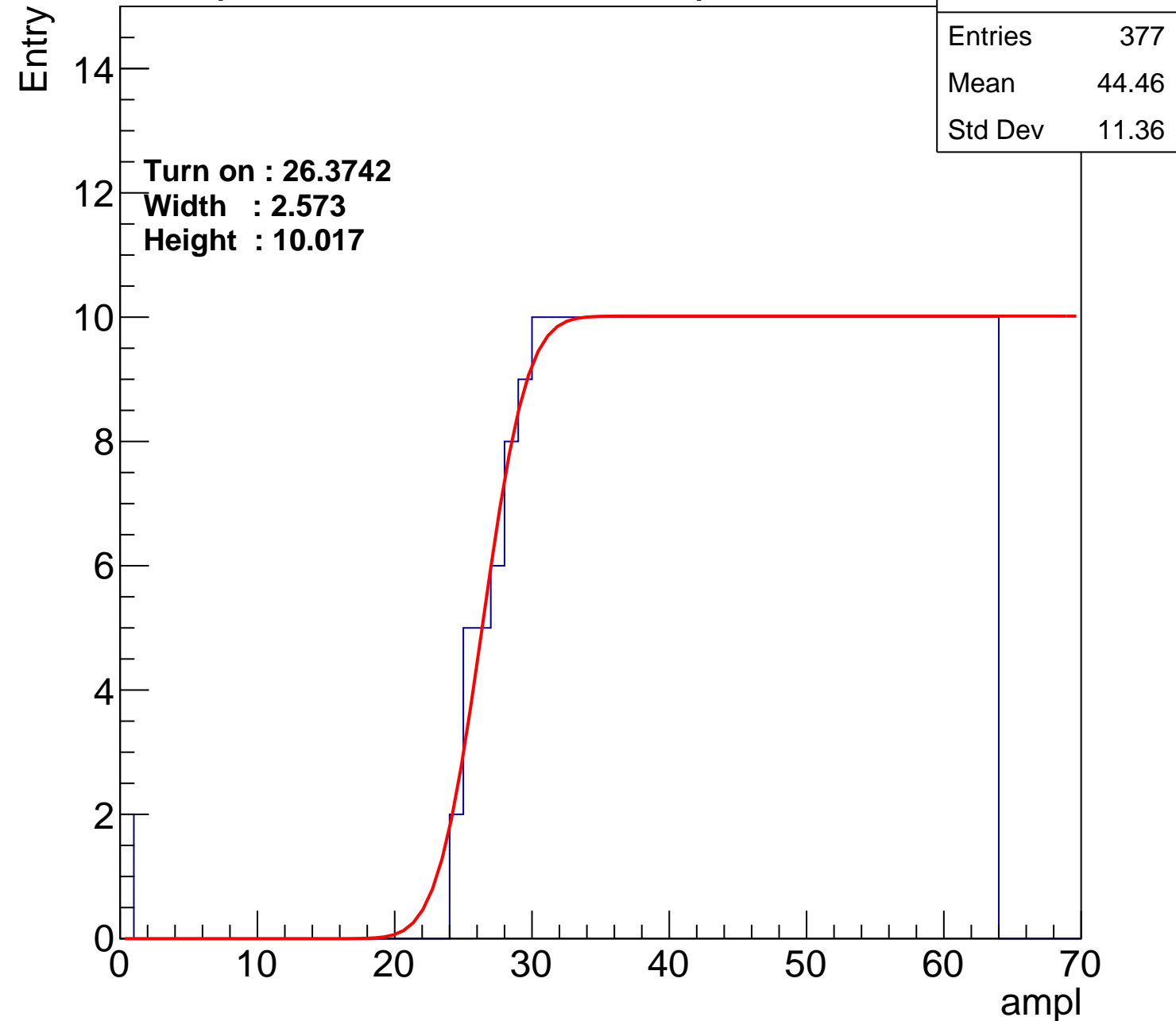
Width : 2.573

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.89
Std Dev	11.81

Turn on : 25.7443

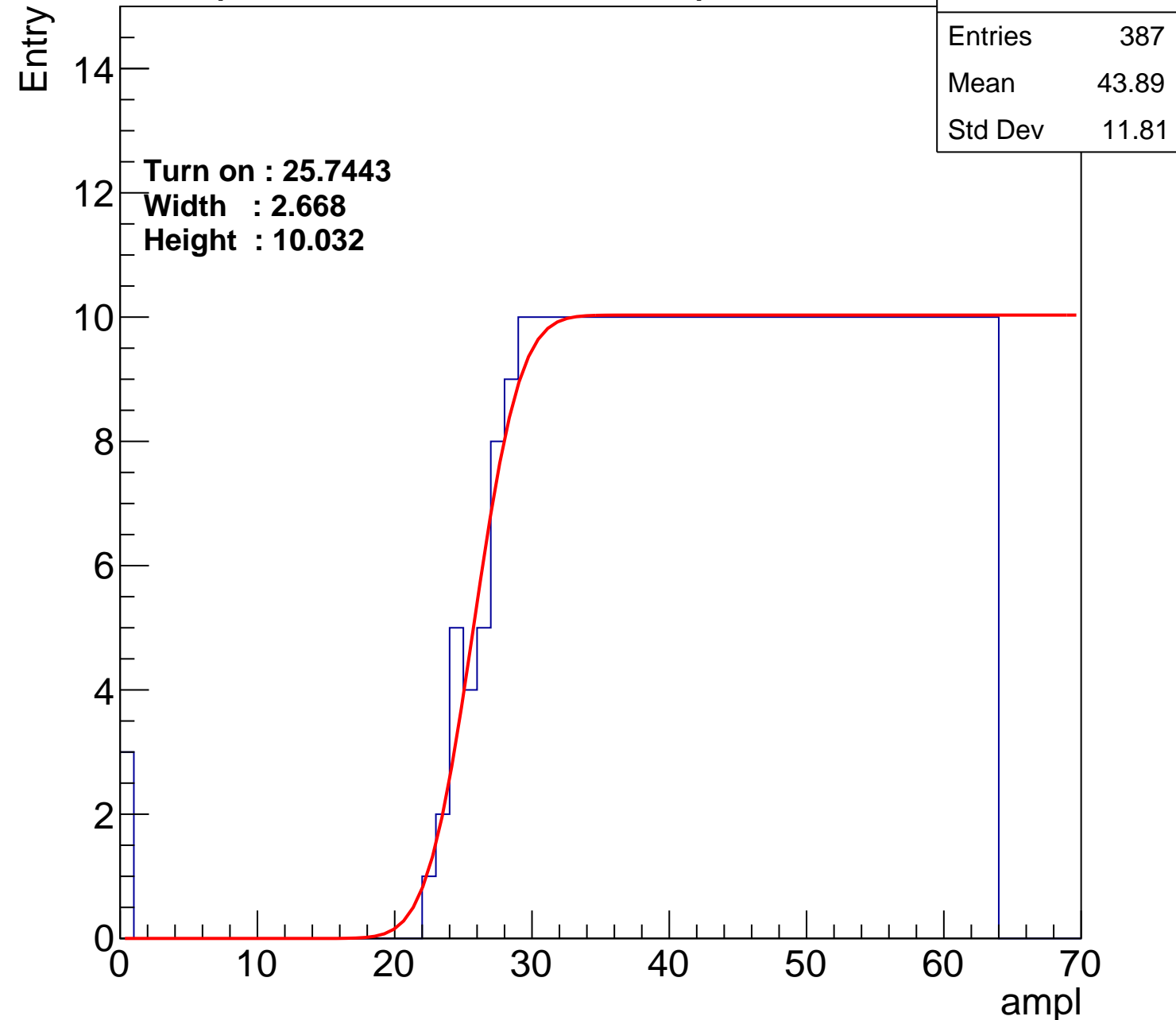
Width : 2.668

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 26.0108

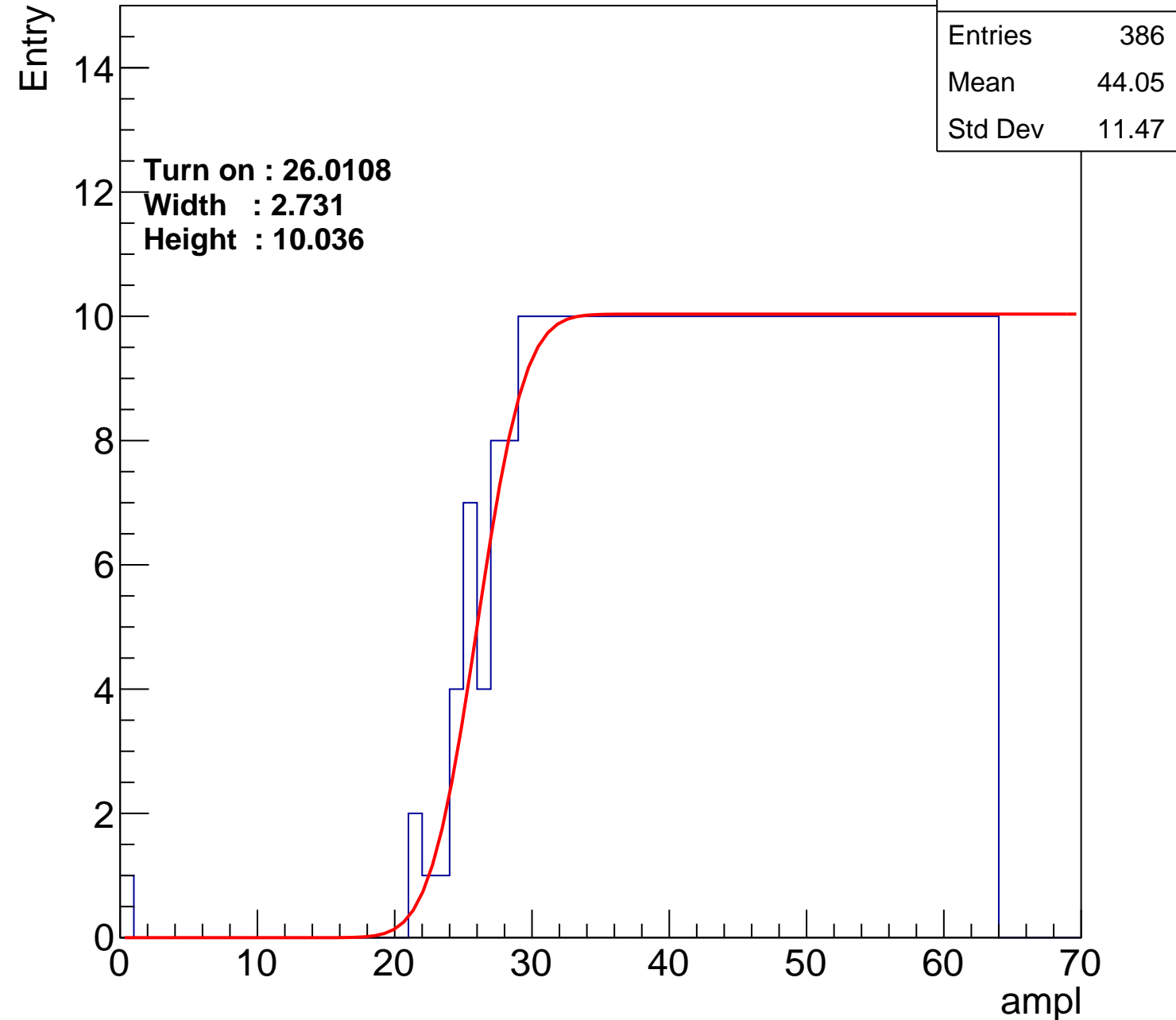
Width : 2.731

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch39

calib_packv5_042523_0143.root, FC#3, port B3

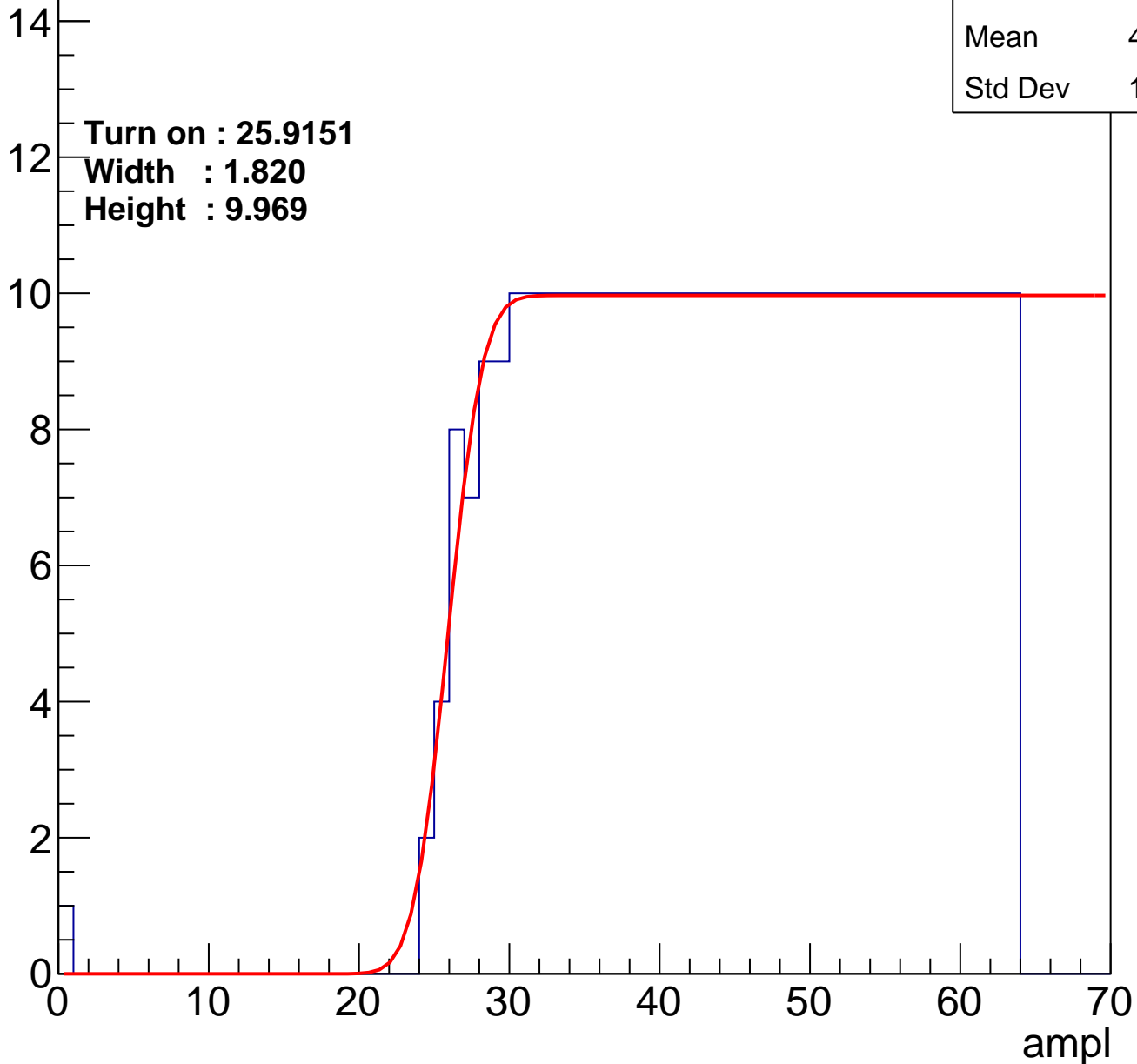
Entry

Entries	380
Mean	44.39
Std Dev	11.23

Turn on : 25.9151

Width : 1.820

Height : 9.969



B1L000S, U8-ch40

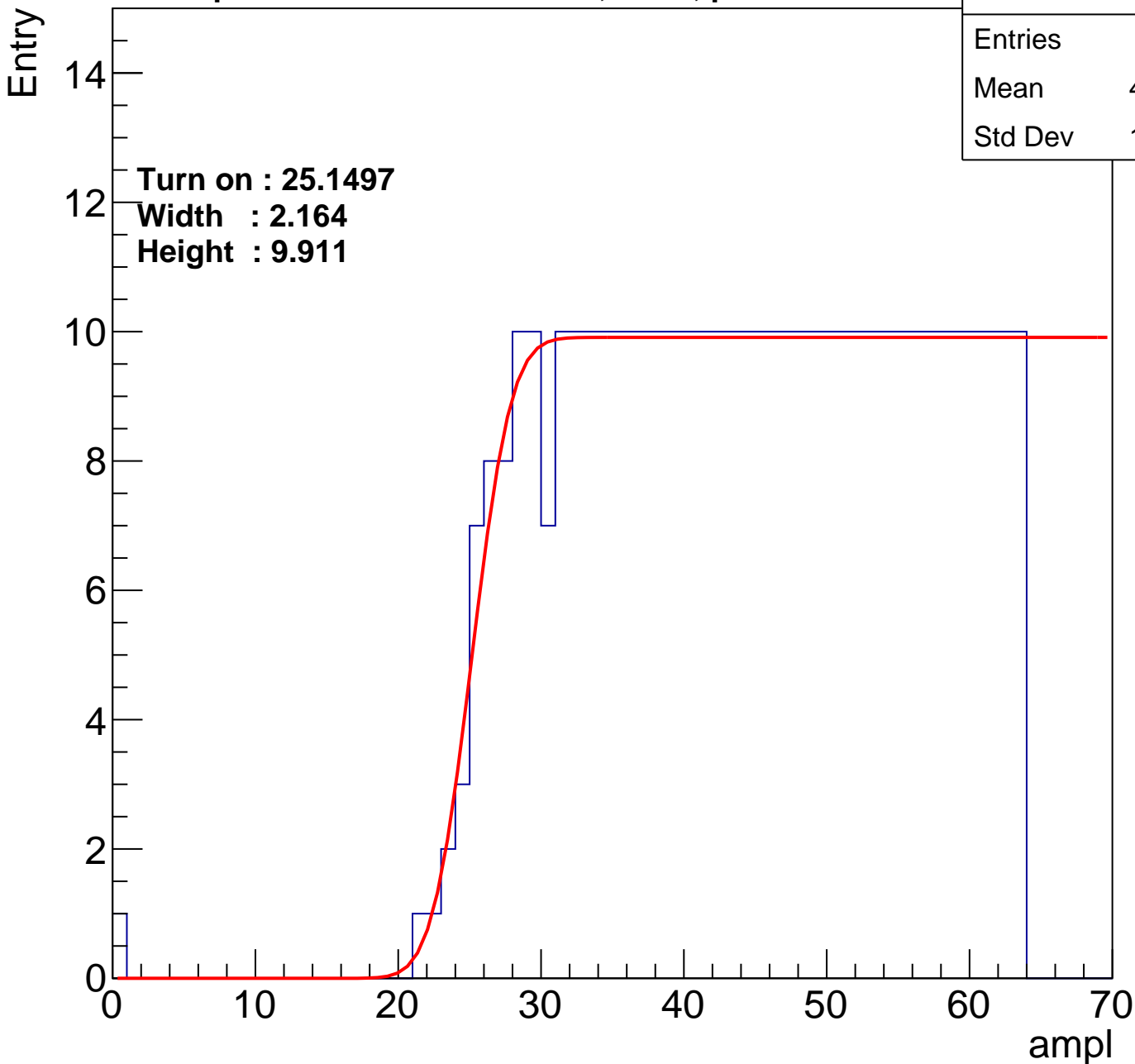
calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.94
Std Dev	11.52

Turn on : 25.1497

Width : 2.164

Height : 9.911



B1L000S, U8-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.8891

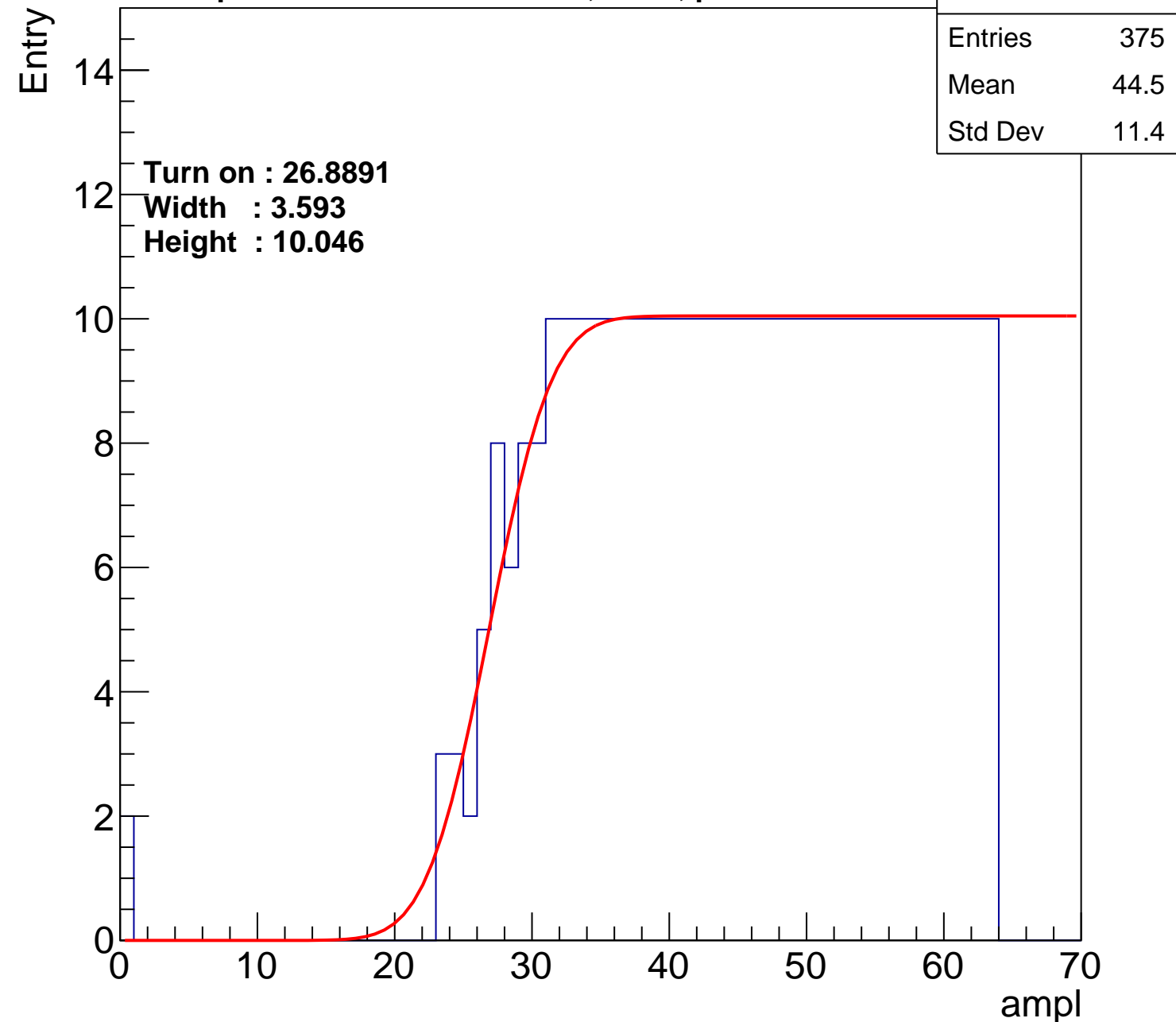
Width : 3.593

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.54
Std Dev	12.05

Turn on : 25.5146

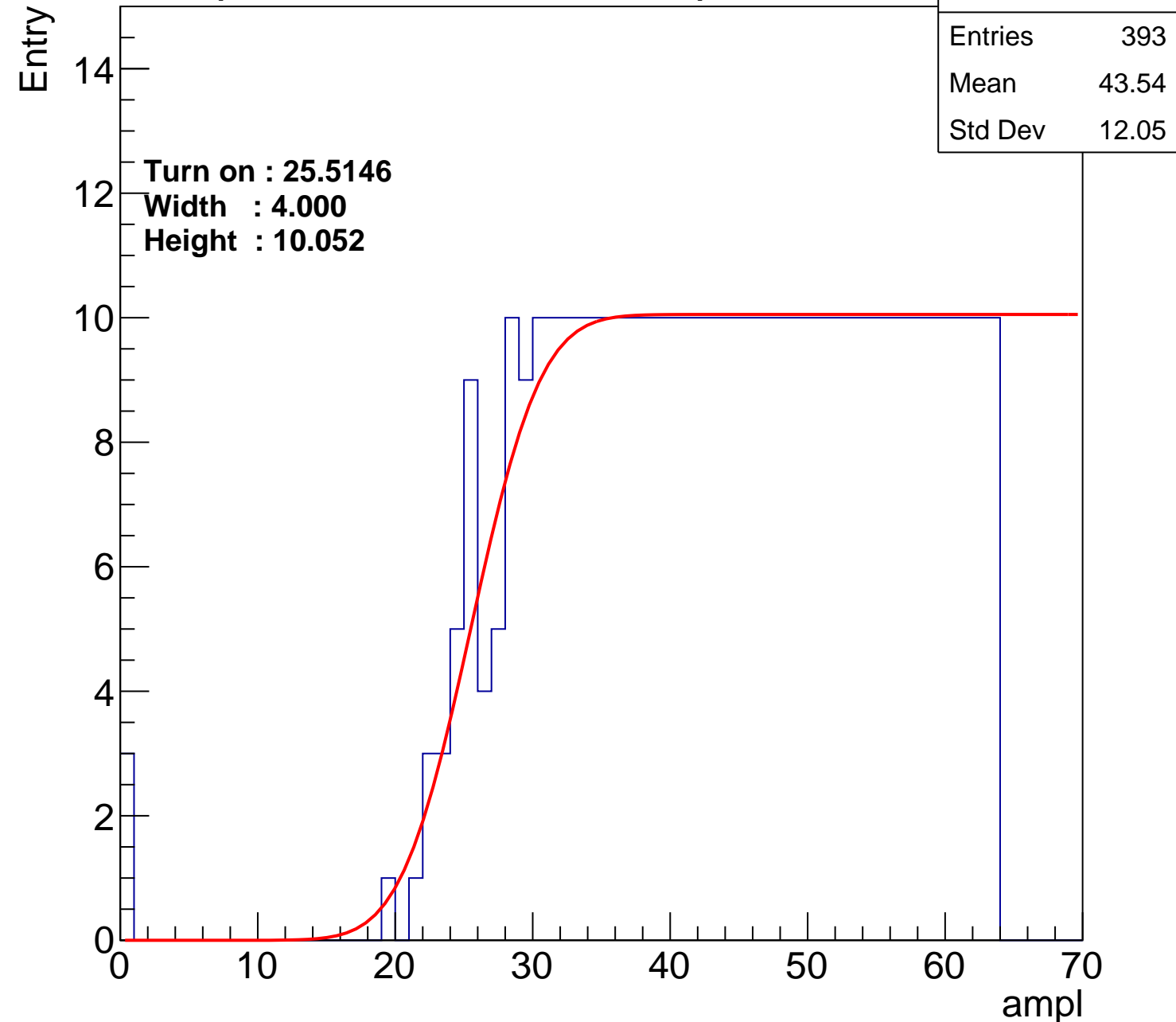
Width : 4.000

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.42
Std Dev	12.19

Turn on : 25.2579

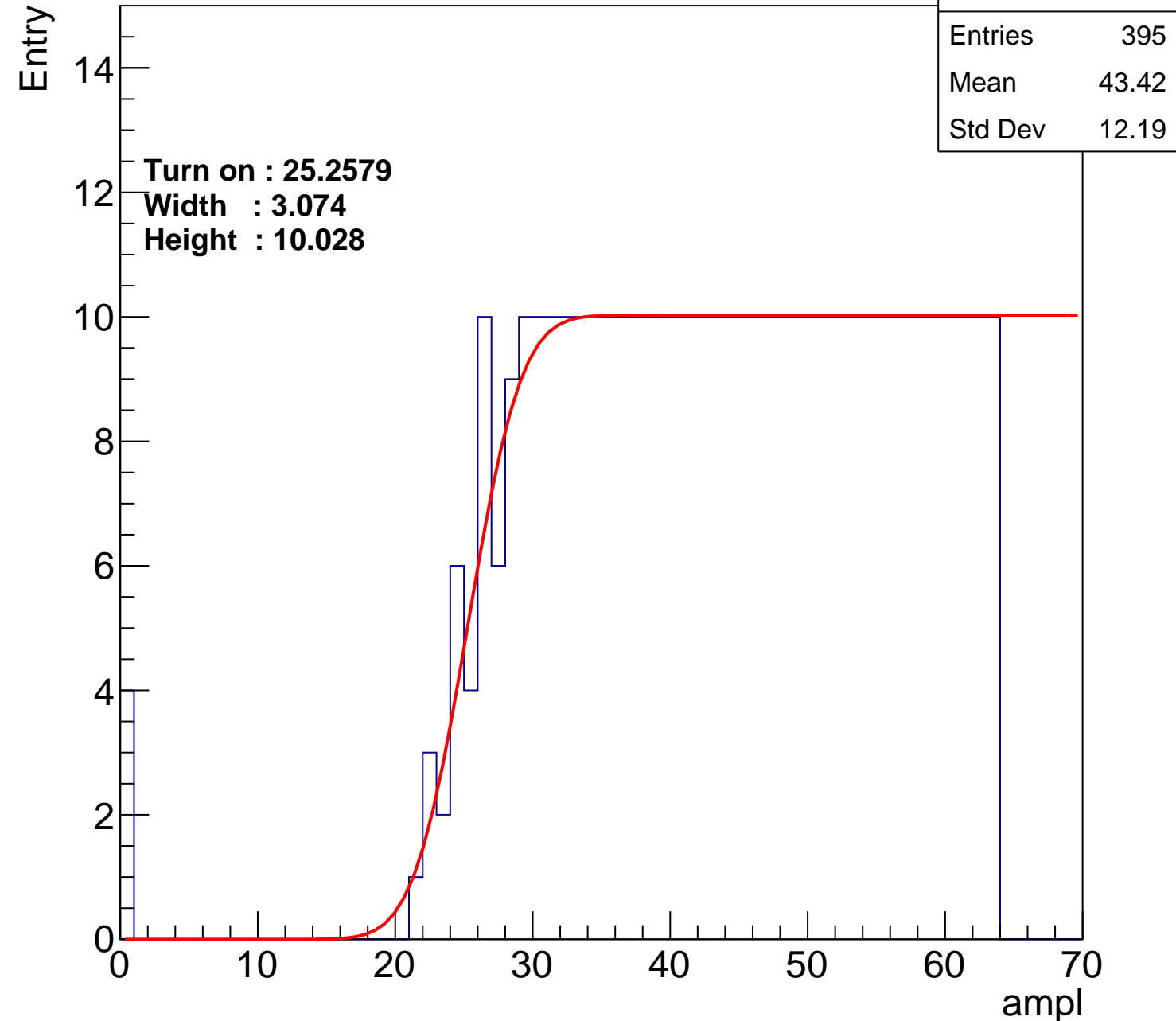
Width : 3.074

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.27
Std Dev	12.15

Turn on : 24.4934

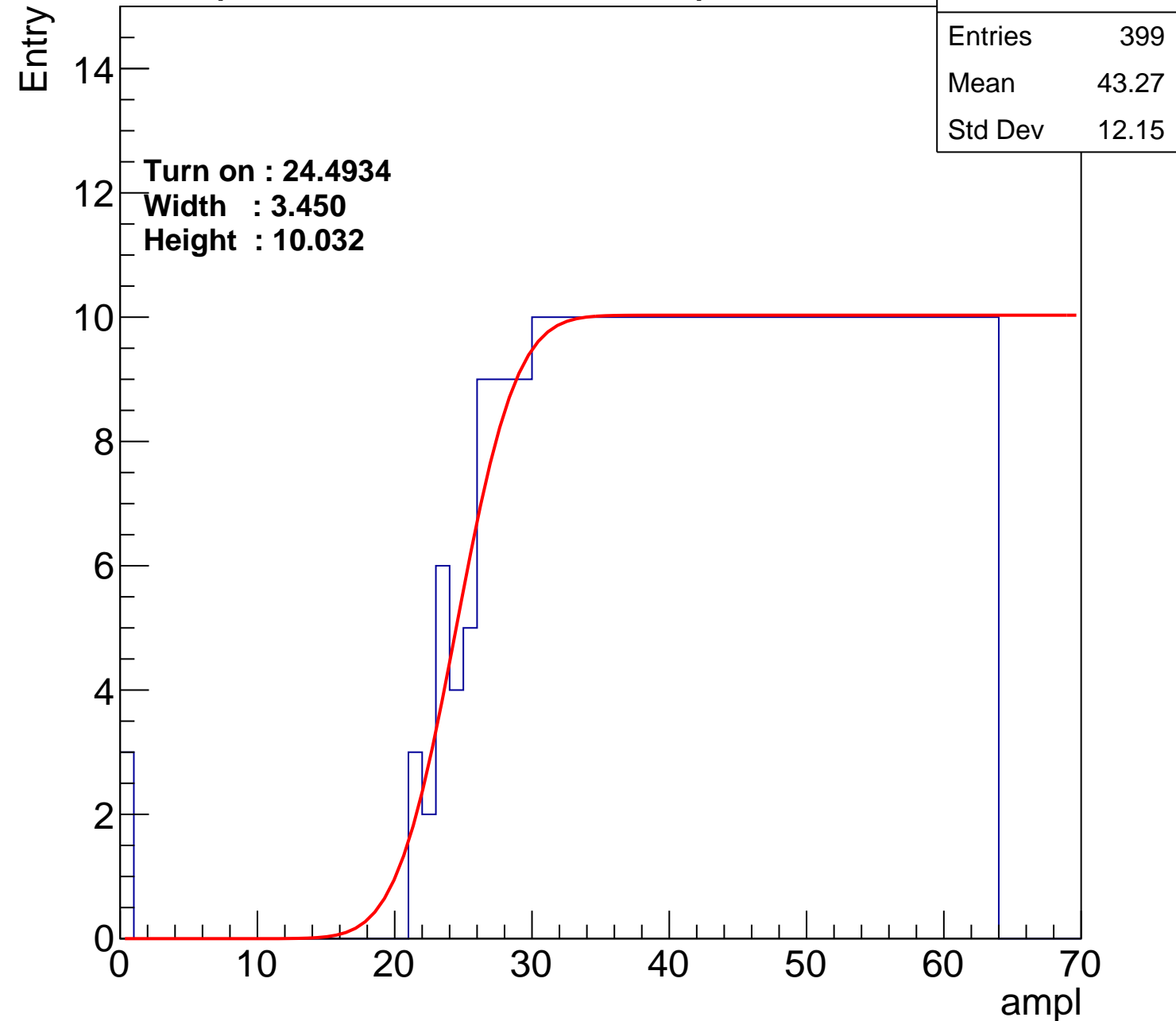
Width : 3.450

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.93
Std Dev	11.13

Turn on : 27.1788

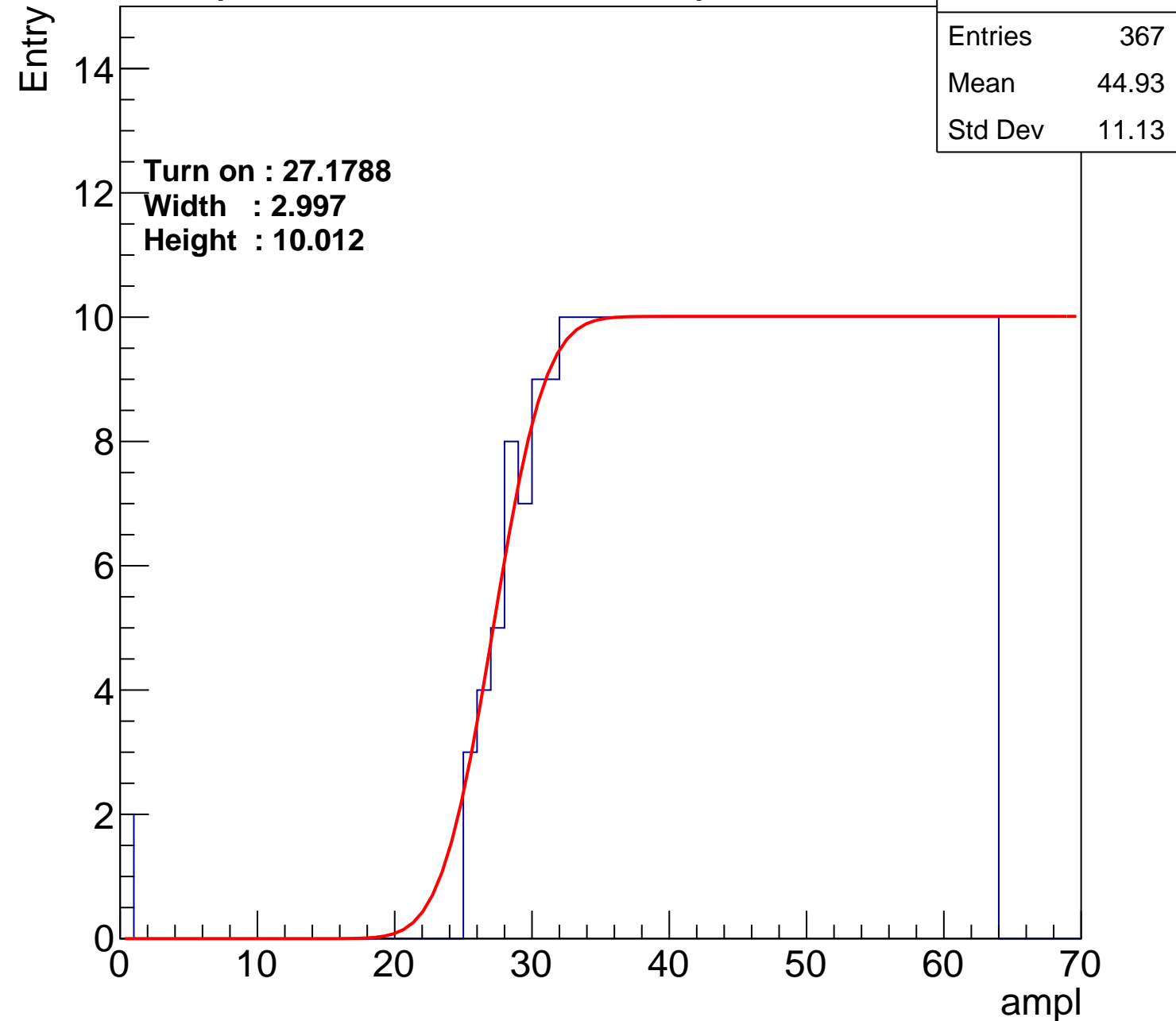
Width : 2.997

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.32
Std Dev	12.46

Turn on : 25.2467

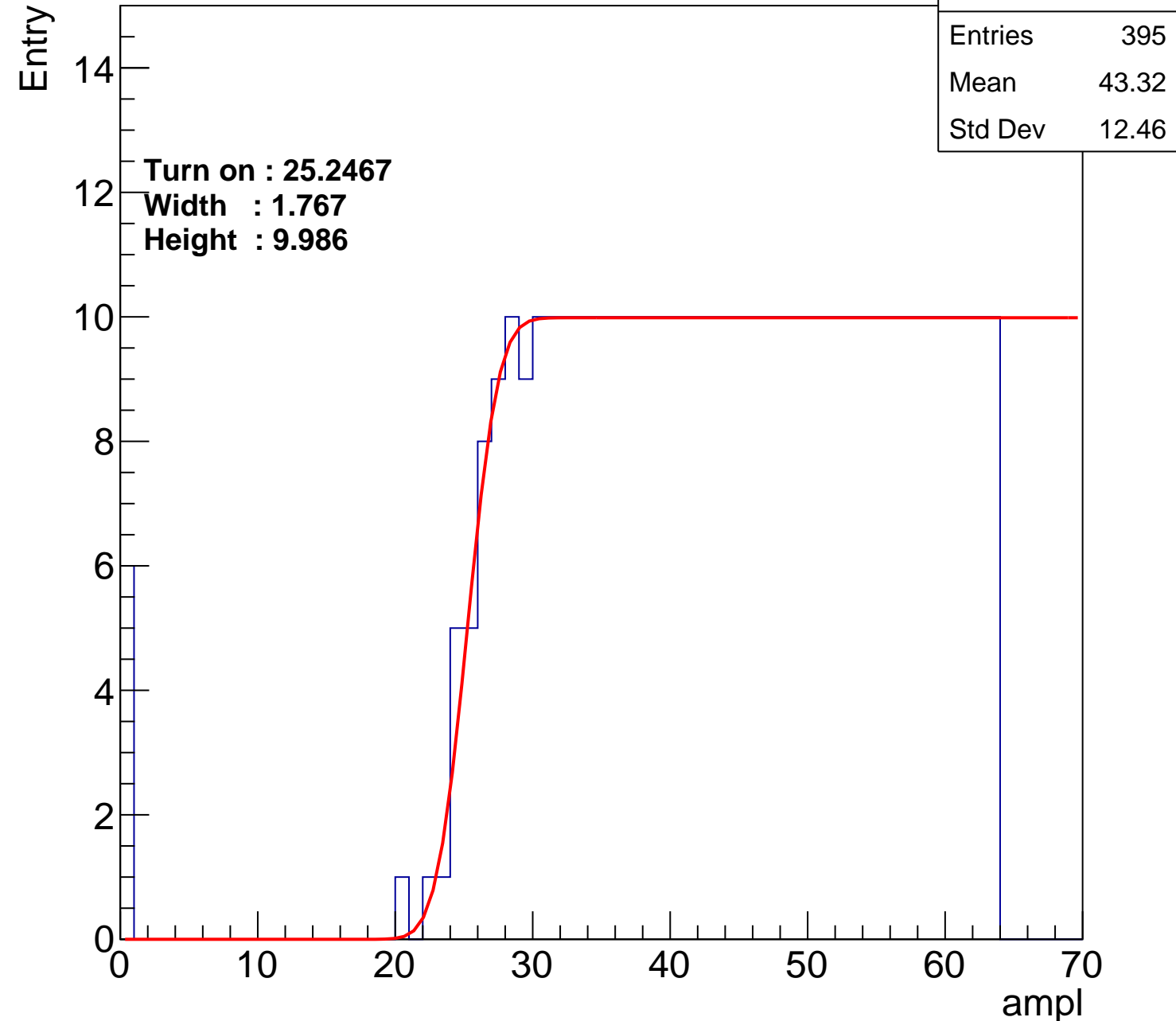
Width : 1.767

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.78
Std Dev	11.76

Turn on : 25.4876

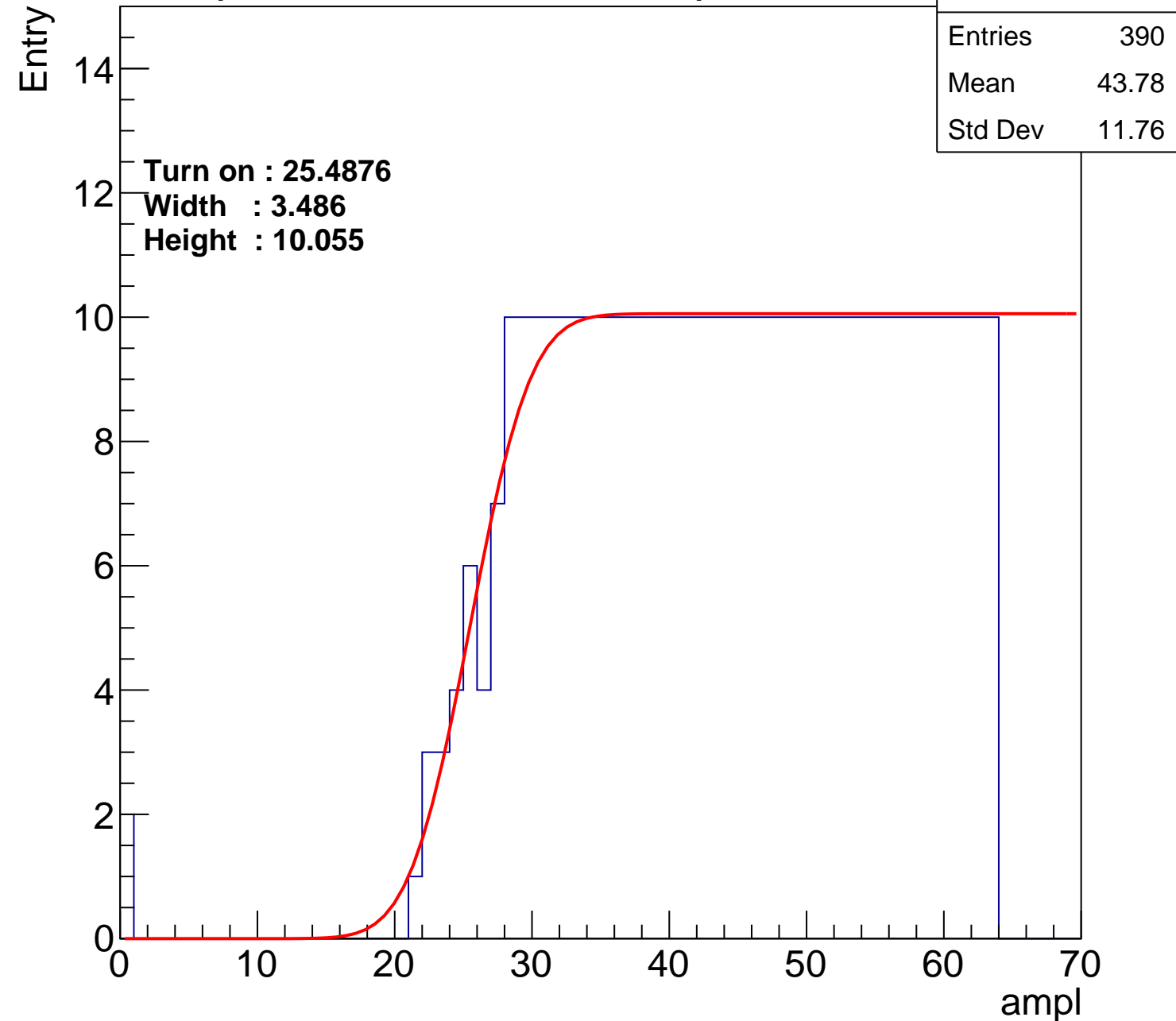
Width : 3.486

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.6
Std Dev	11.52

Turn on : 26.6477

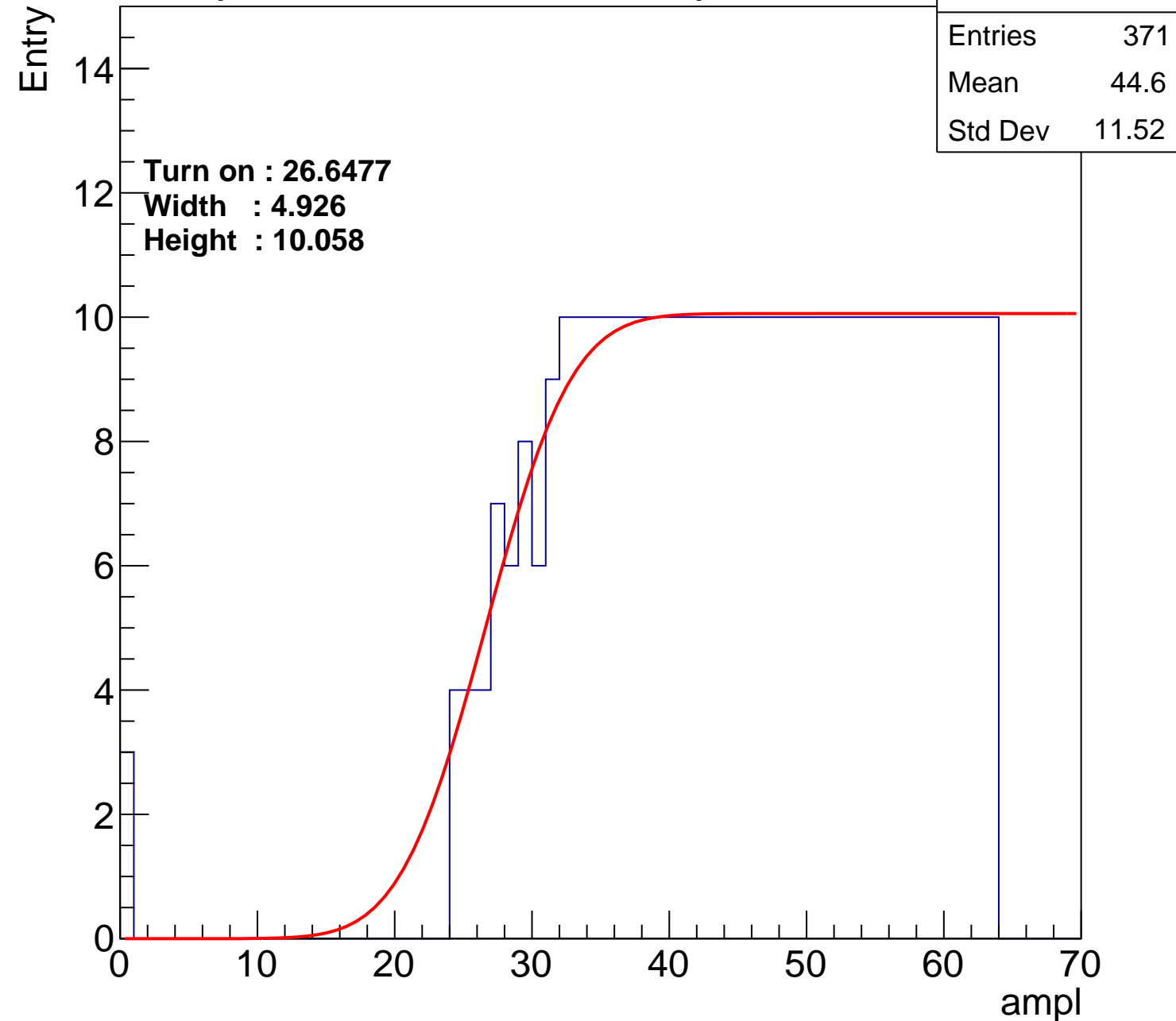
Width : 4.926

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch49

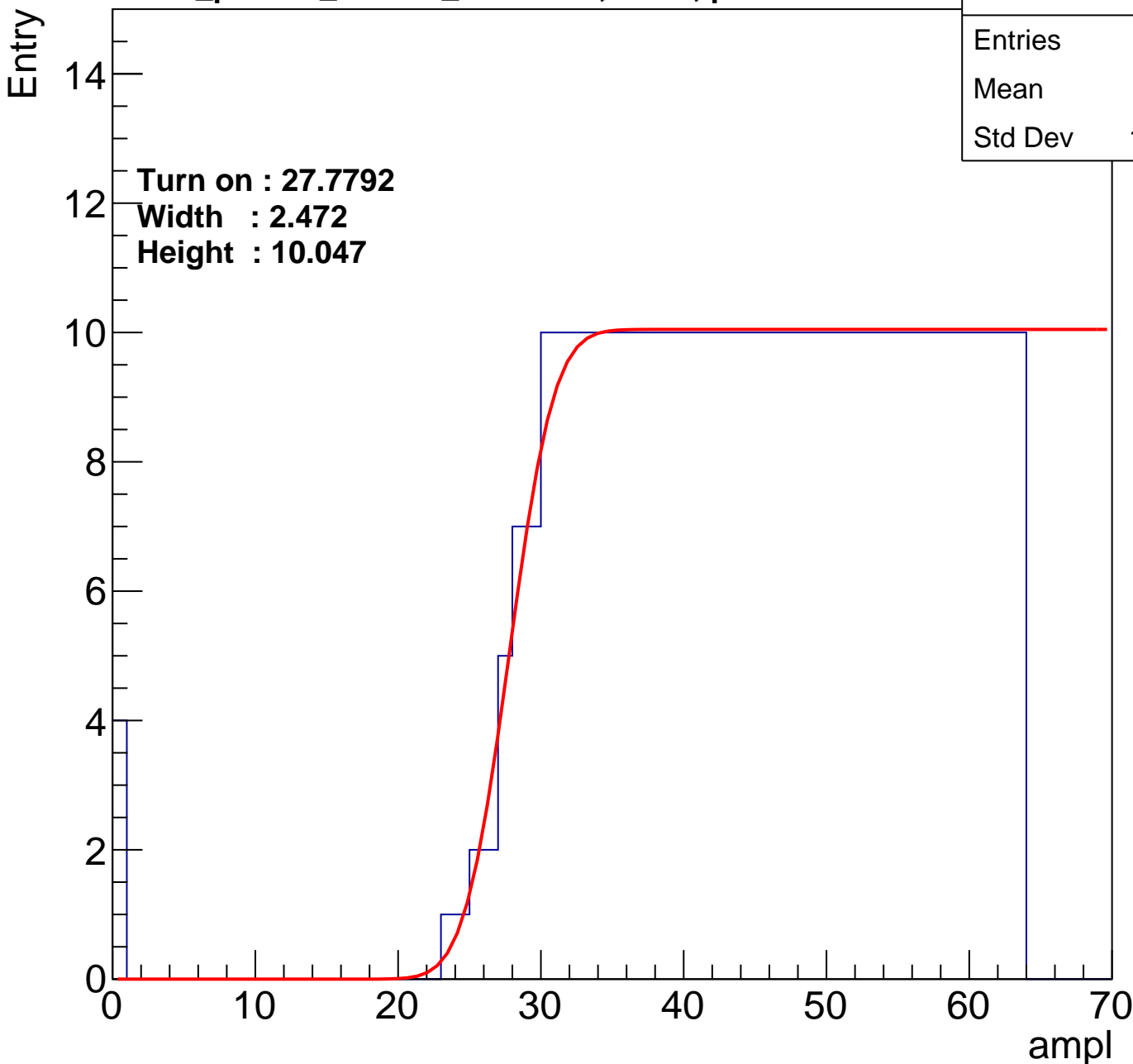
calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.7
Std Dev	11.57

Turn on : 27.7792

Width : 2.472

Height : 10.047



B1L000S, U8-ch50

calib_packv5_042523_0143.root, FC#3, port B3

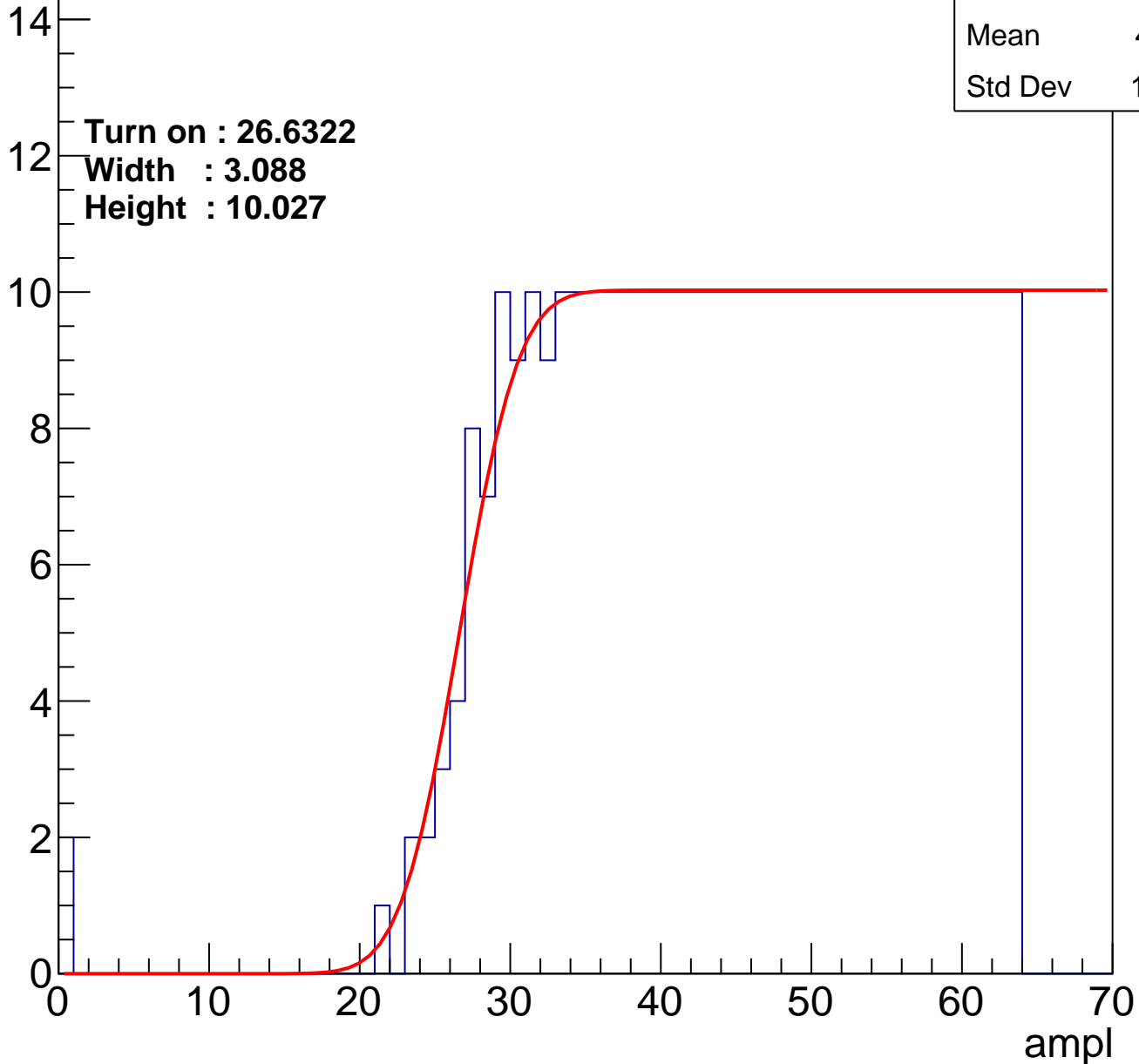
Entries	377
Mean	44.41
Std Dev	11.43

Turn on : 26.6322

Width : 3.088

Height : 10.027

Entry



B1L000S, U8-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.6
Std Dev	11.95

Turn on : 25.3230

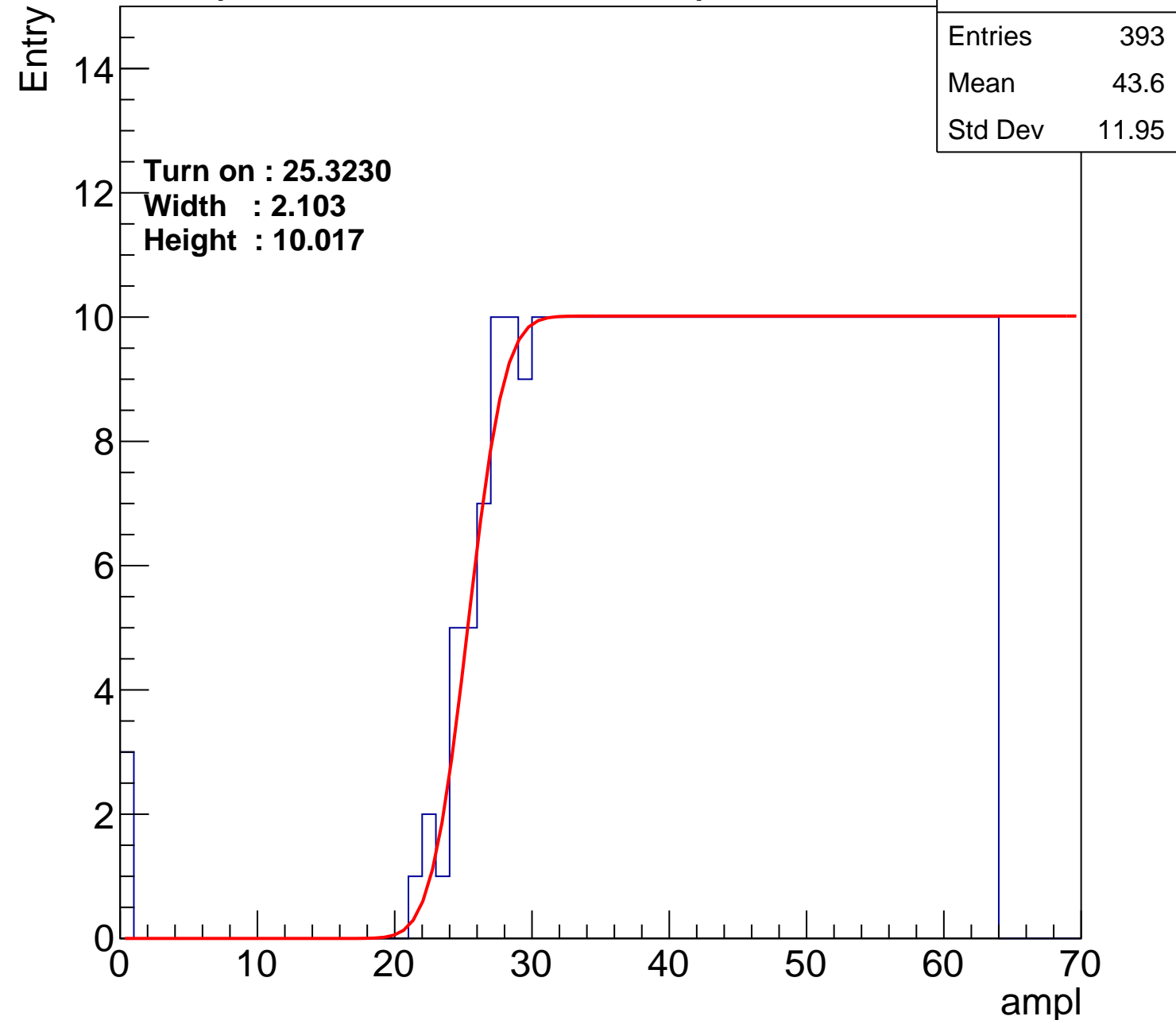
Width : 2.103

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 26.4632

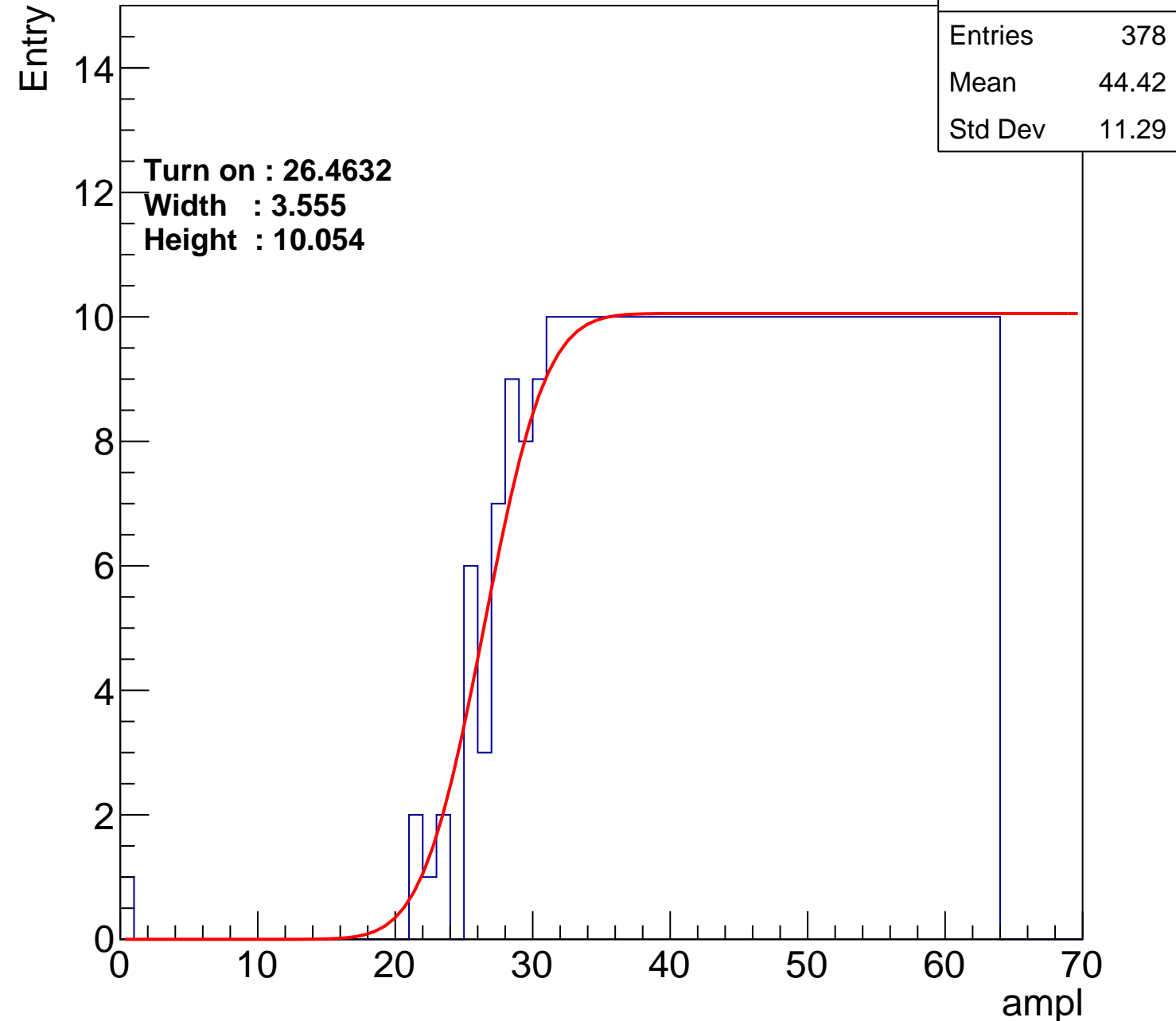
Width : 3.555

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.65
Std Dev	11.92

Turn on : 25.1670

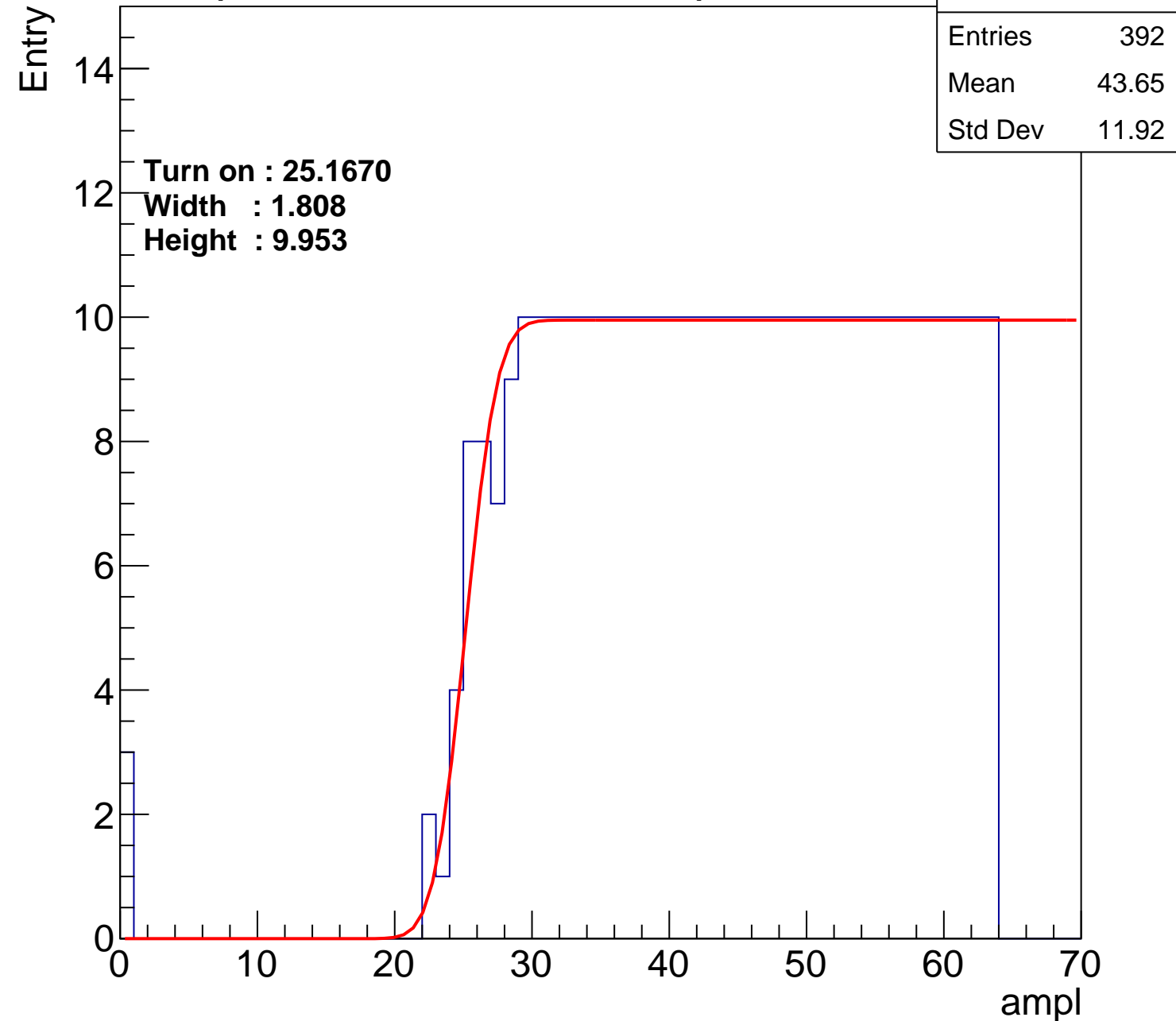
Width : 1.808

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.64
Std Dev	12.23

Turn on : 25.4192

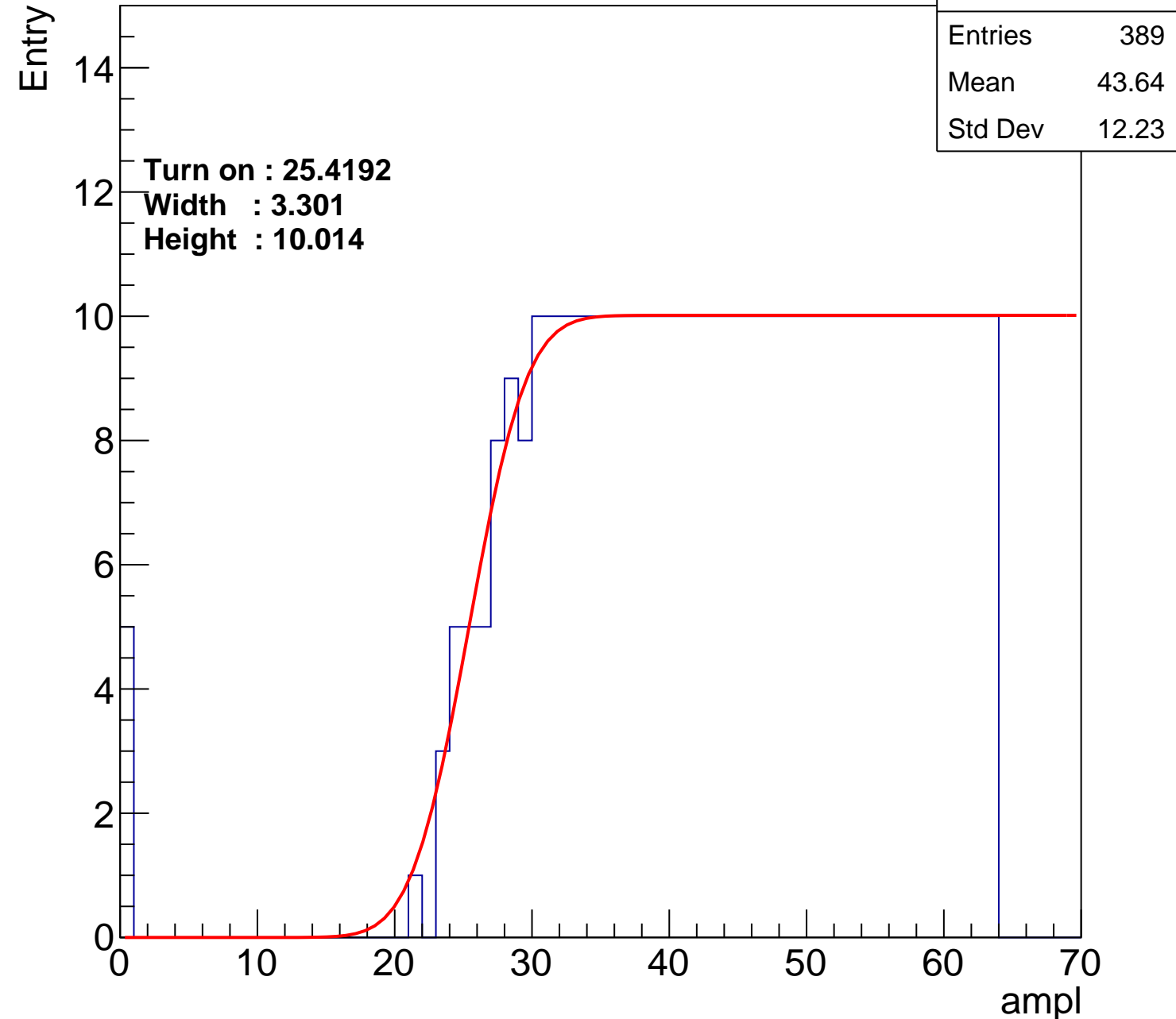
Width : 3.301

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.4914

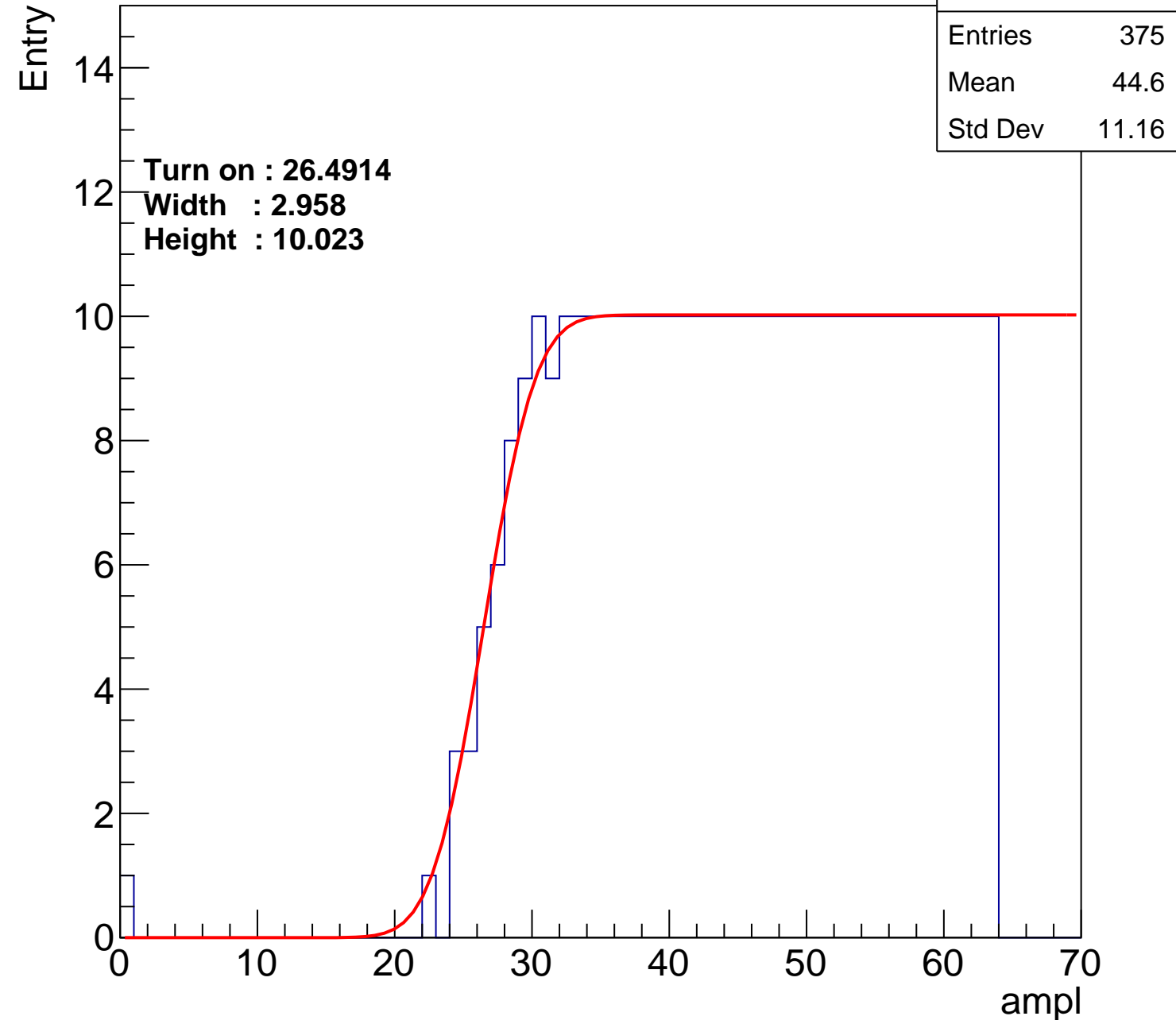
Width : 2.958

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch56

calib_packv5_042523_0143.root, FC#3, port B3

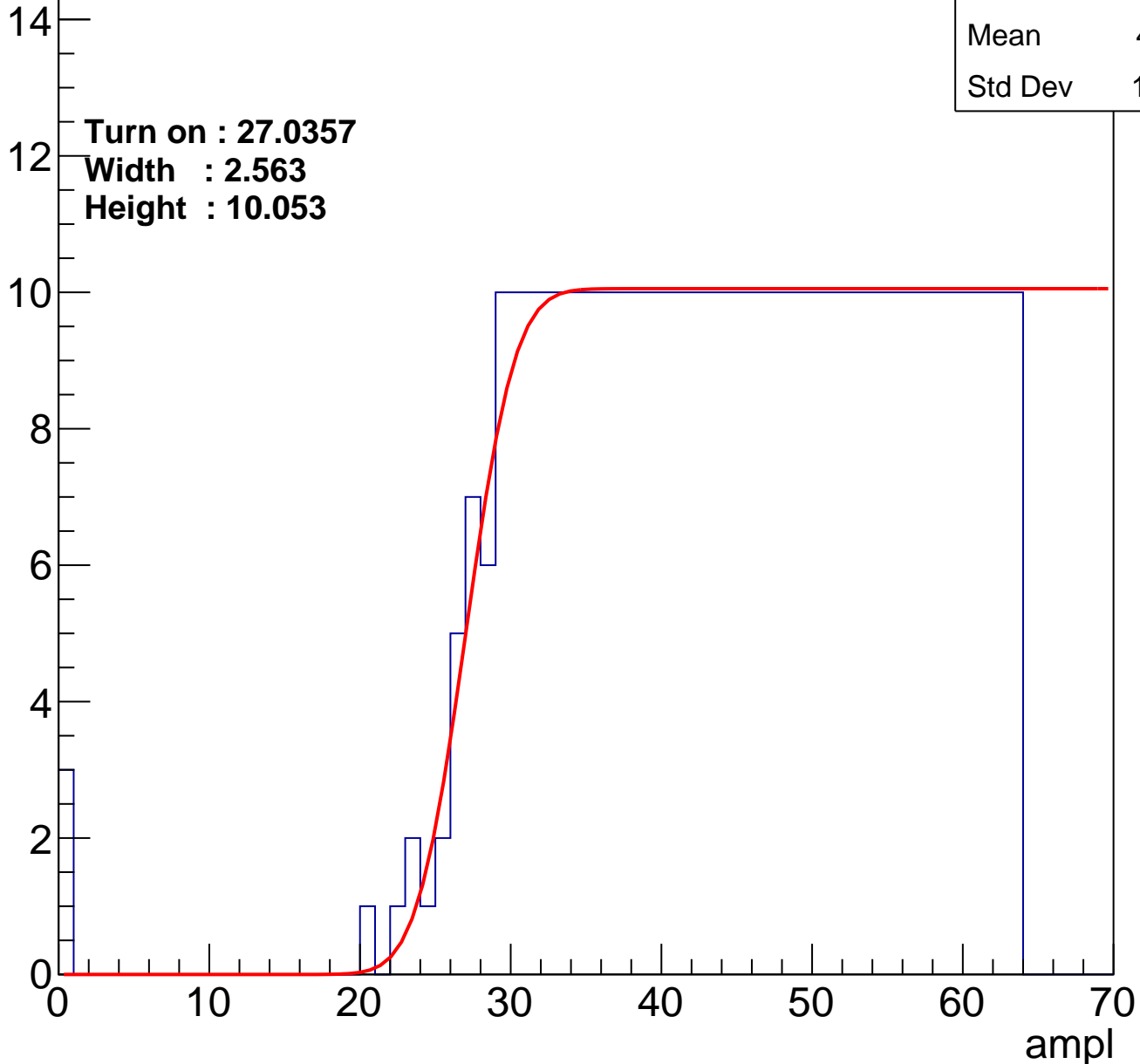
Entries	378
Mean	44.31
Std Dev	11.63

Turn on : 27.0357

Width : 2.563

Height : 10.053

Entry



B1L000S, U8-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 26.4177

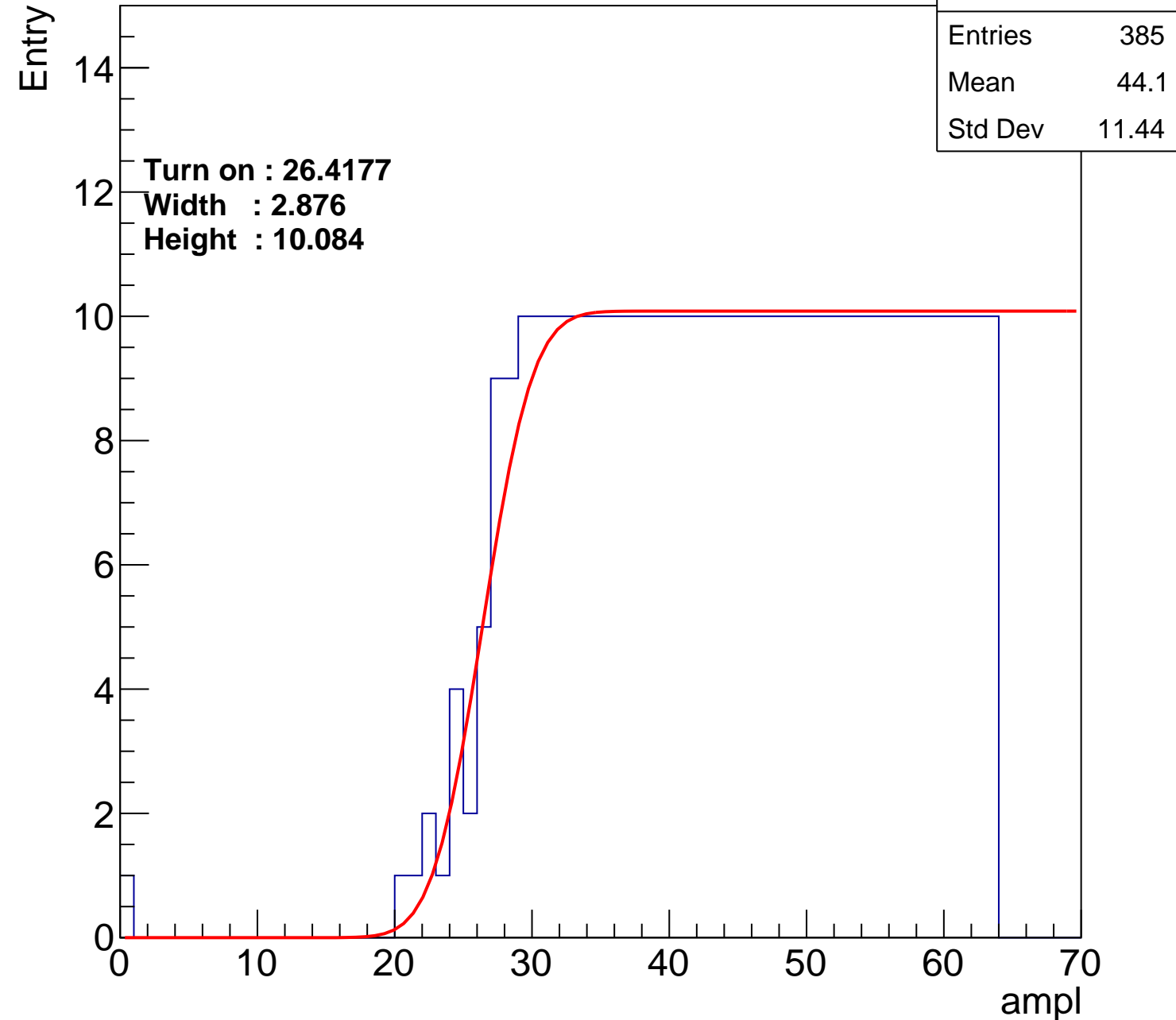
Width : 2.876

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.66
Std Dev	11.78

Turn on : 24.8536

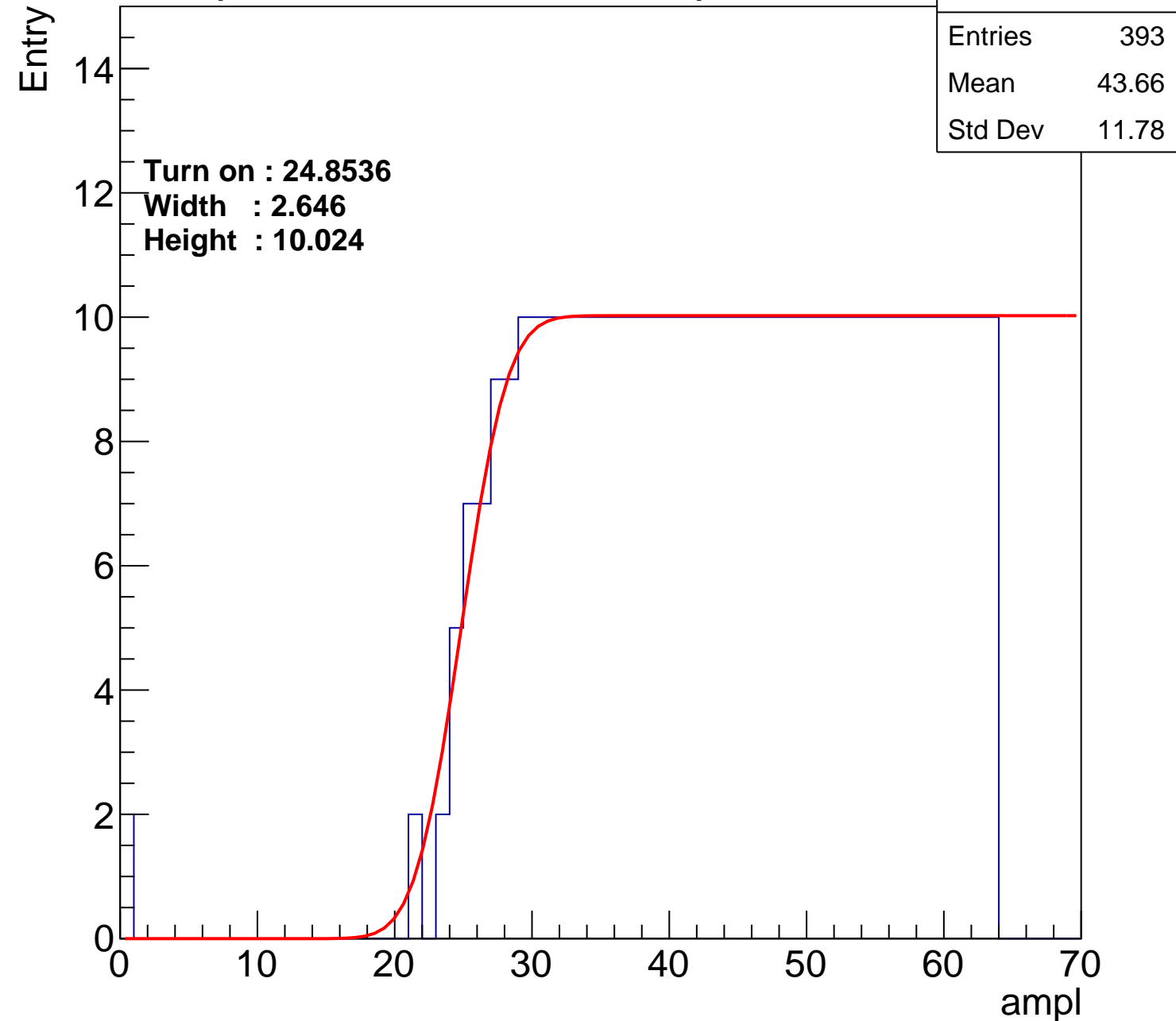
Width : 2.646

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44
Std Dev	11.97

Turn on : 26.7560

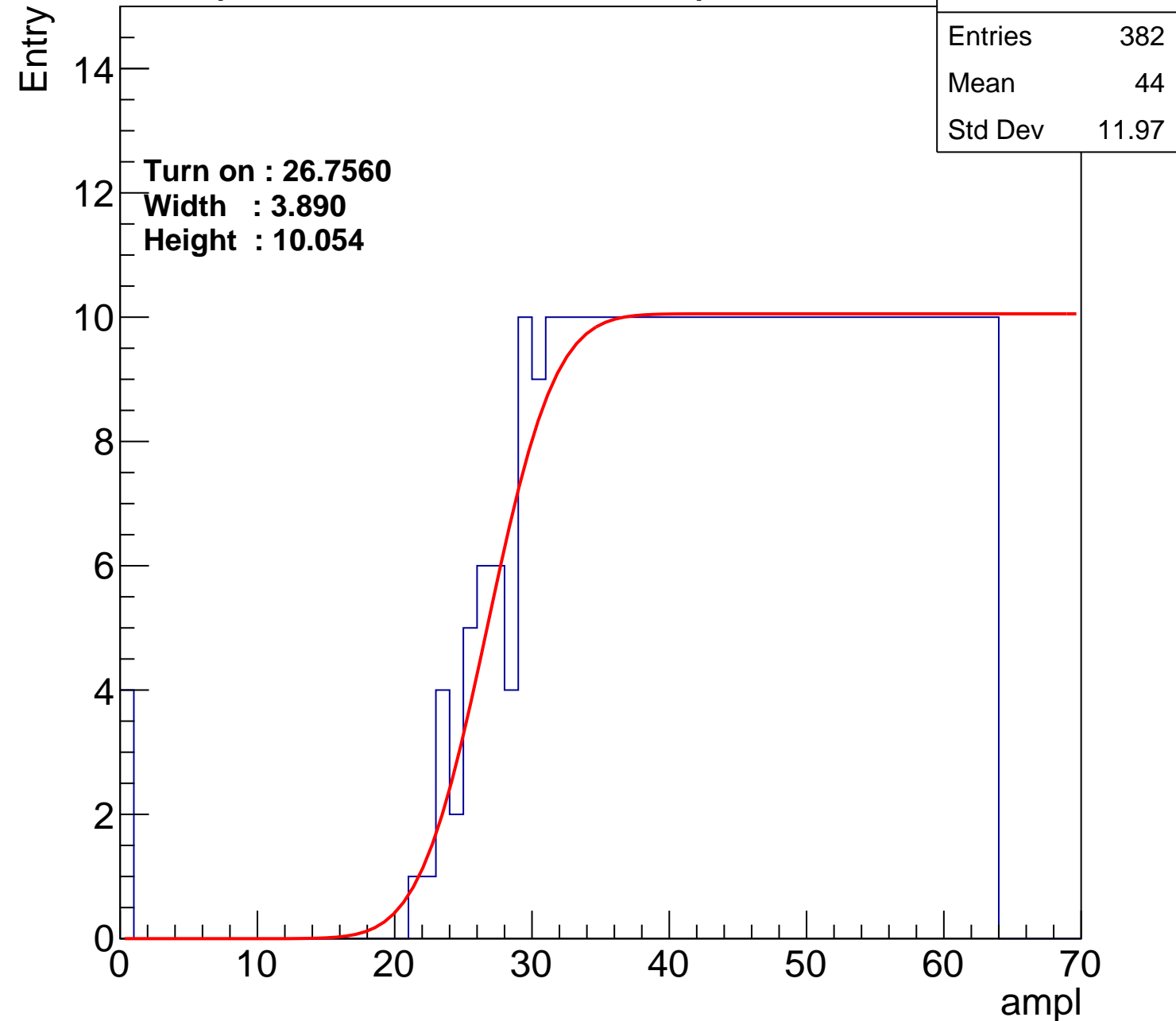
Width : 3.890

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch60

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.36
Std Dev	11.47

Turn on : 26.6166

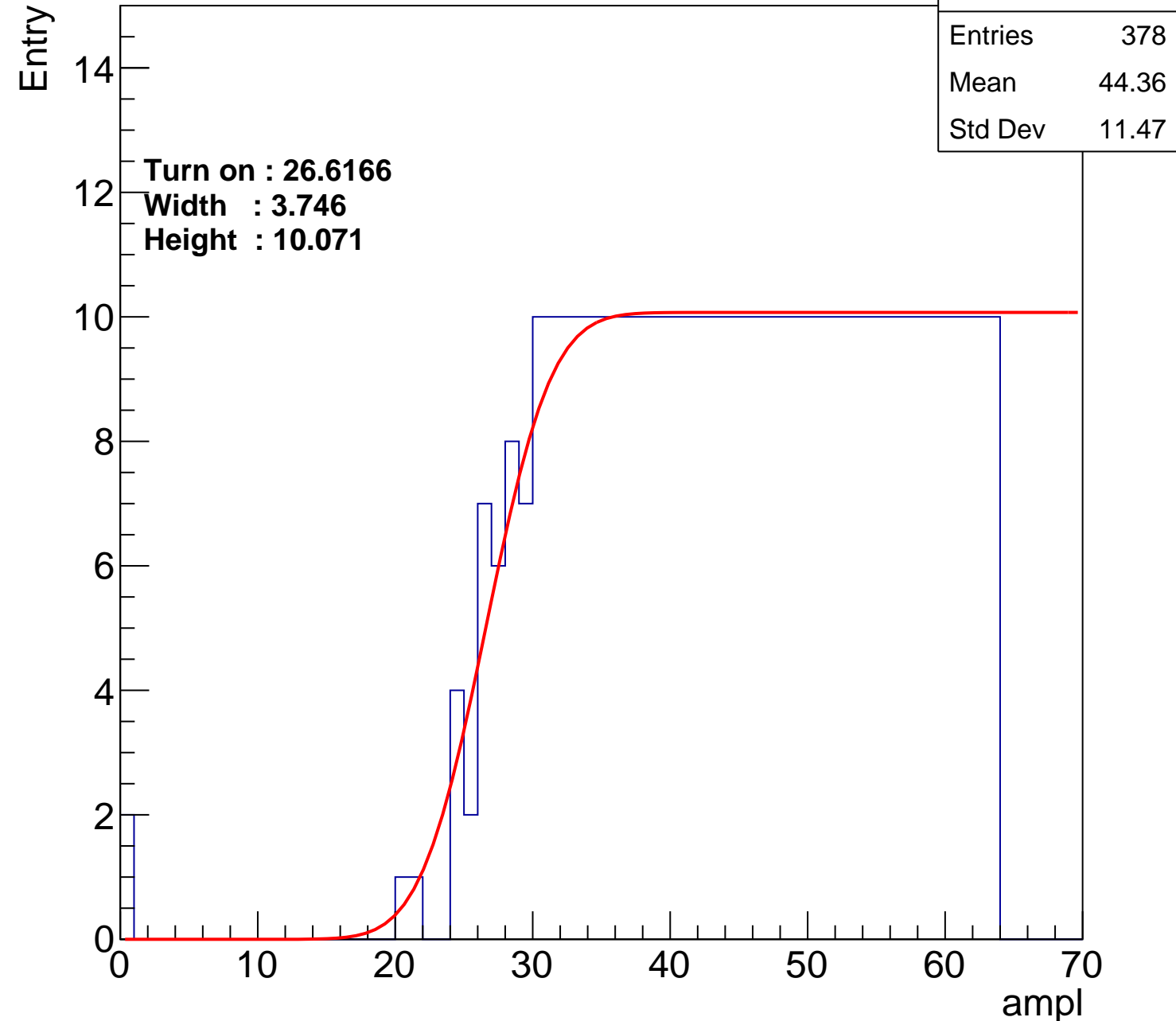
Width : 3.746

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.2
Std Dev	11.68

Turn on : 26.4658

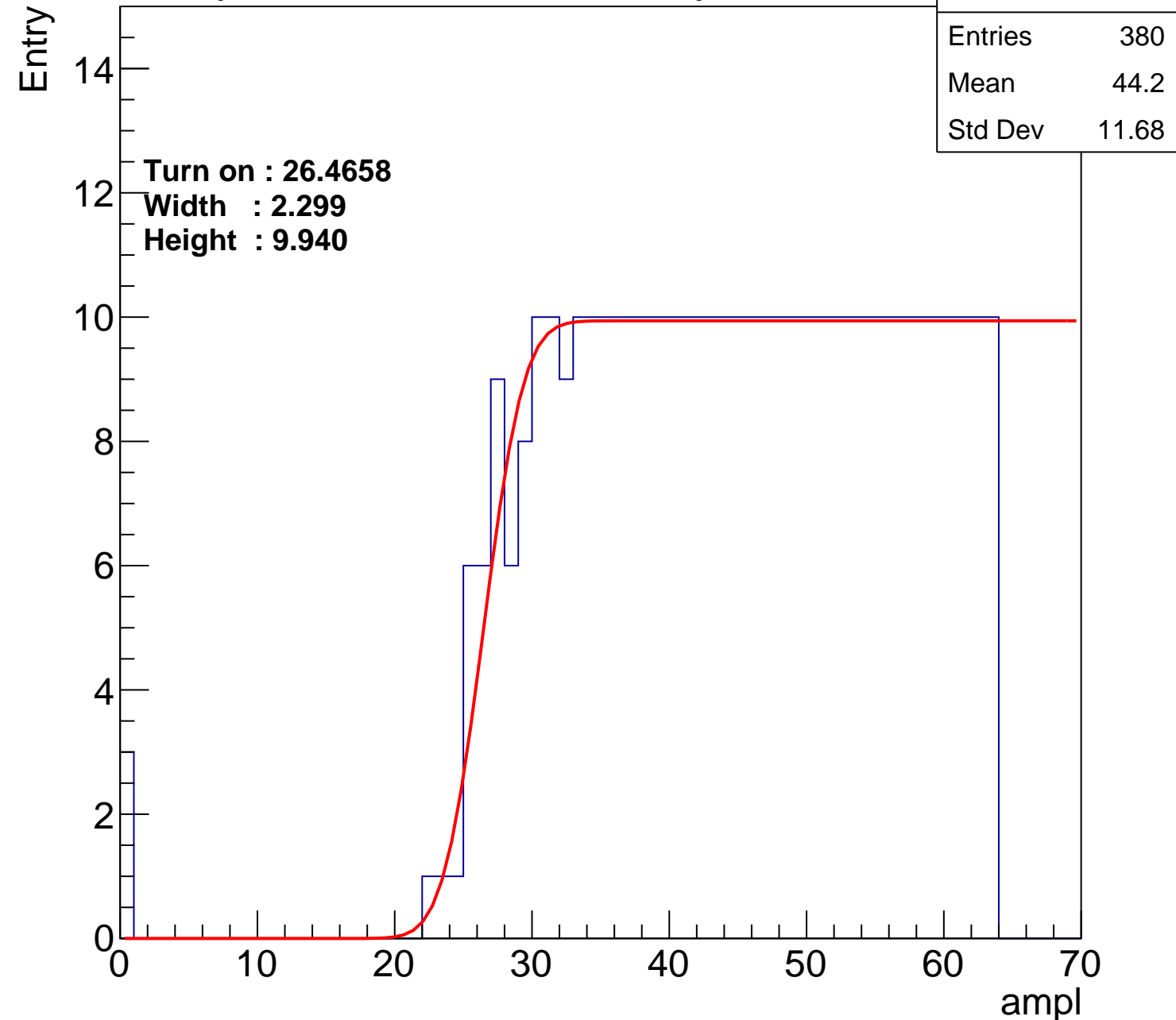
Width : 2.299

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.03
Std Dev	12.59

Turn on : 24.0244

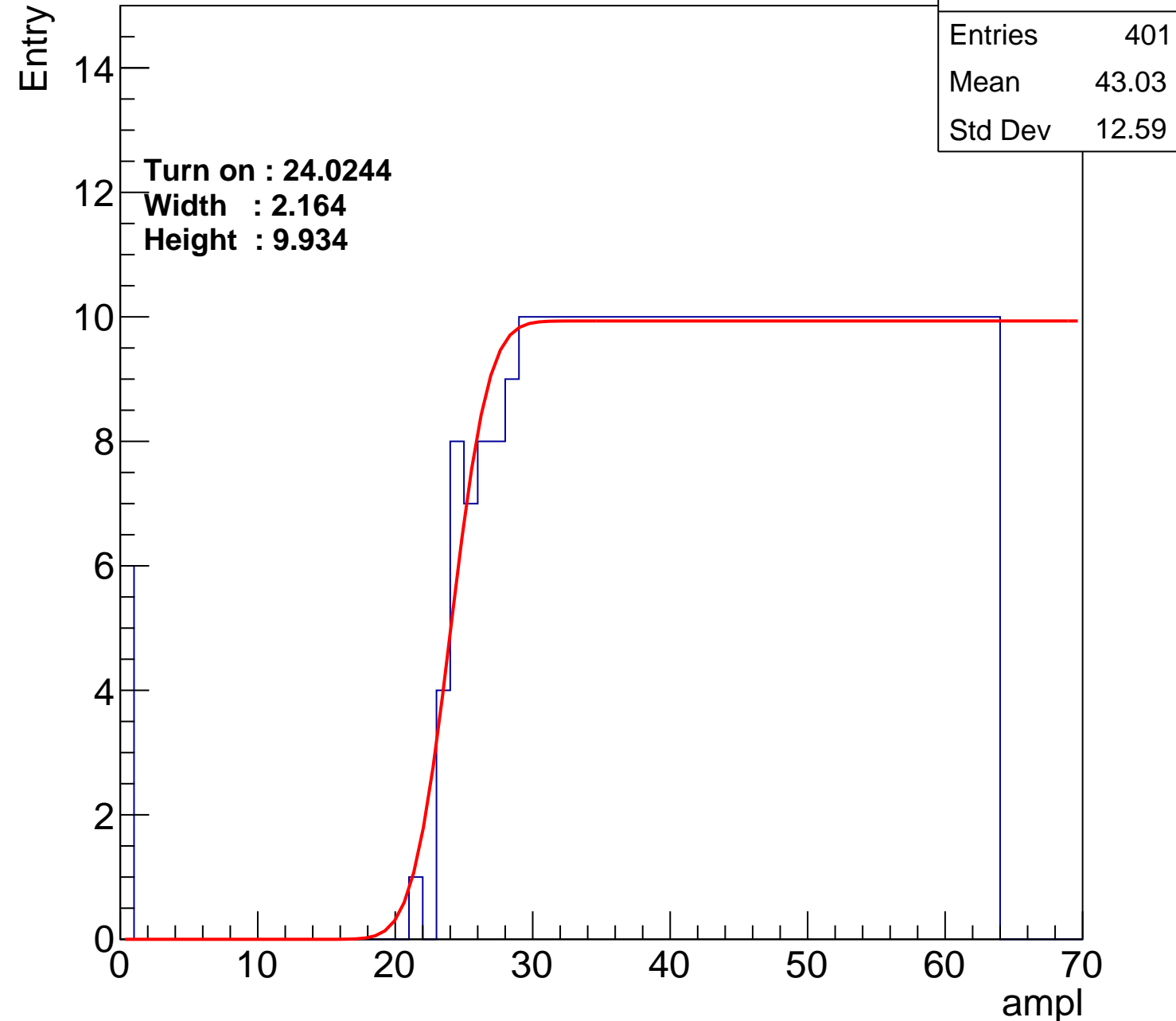
Width : 2.164

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.24
Std Dev	11.5

Turn on : 26.2338

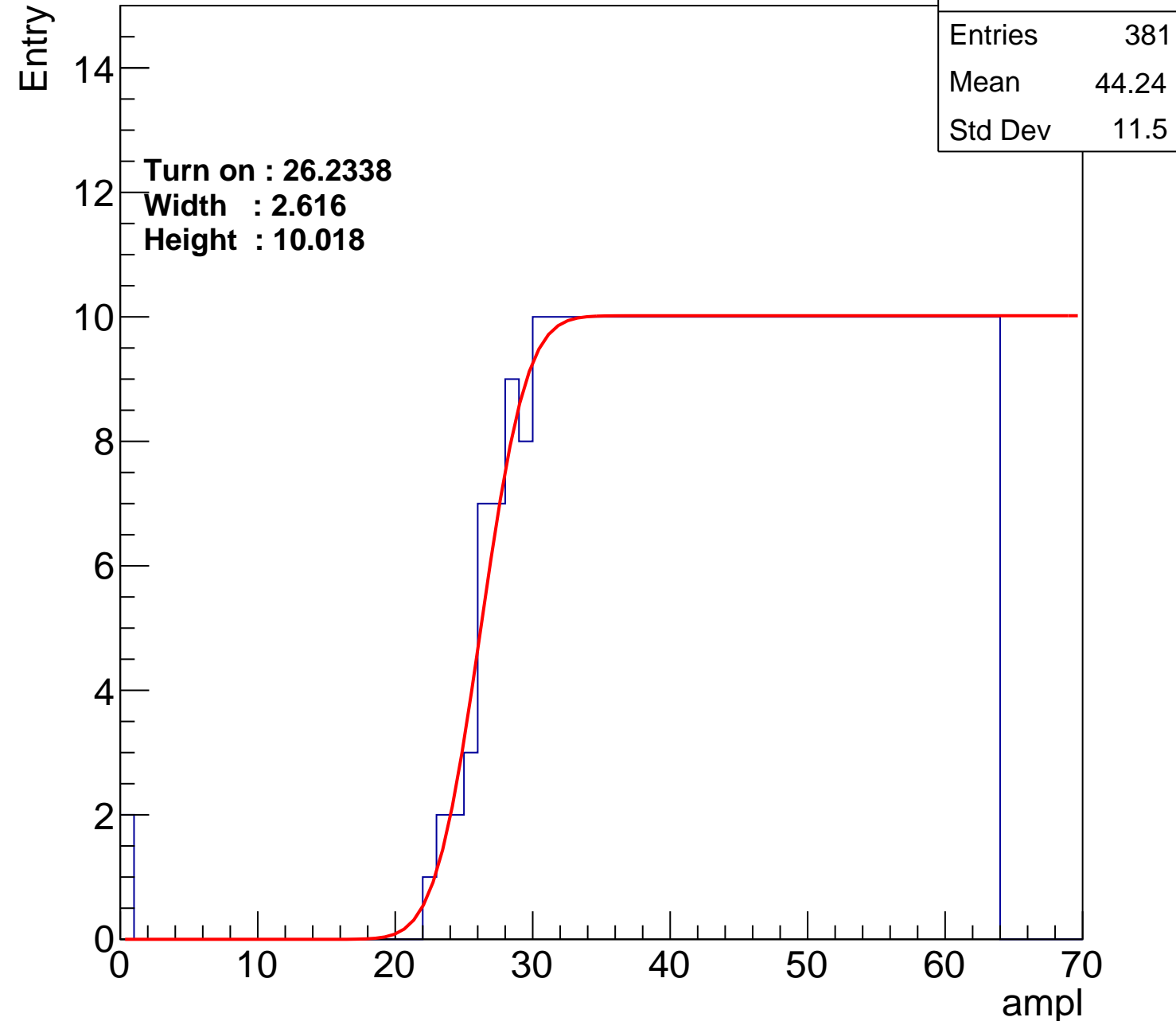
Width : 2.616

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.63
Std Dev	11.84

Turn on : 25.5300

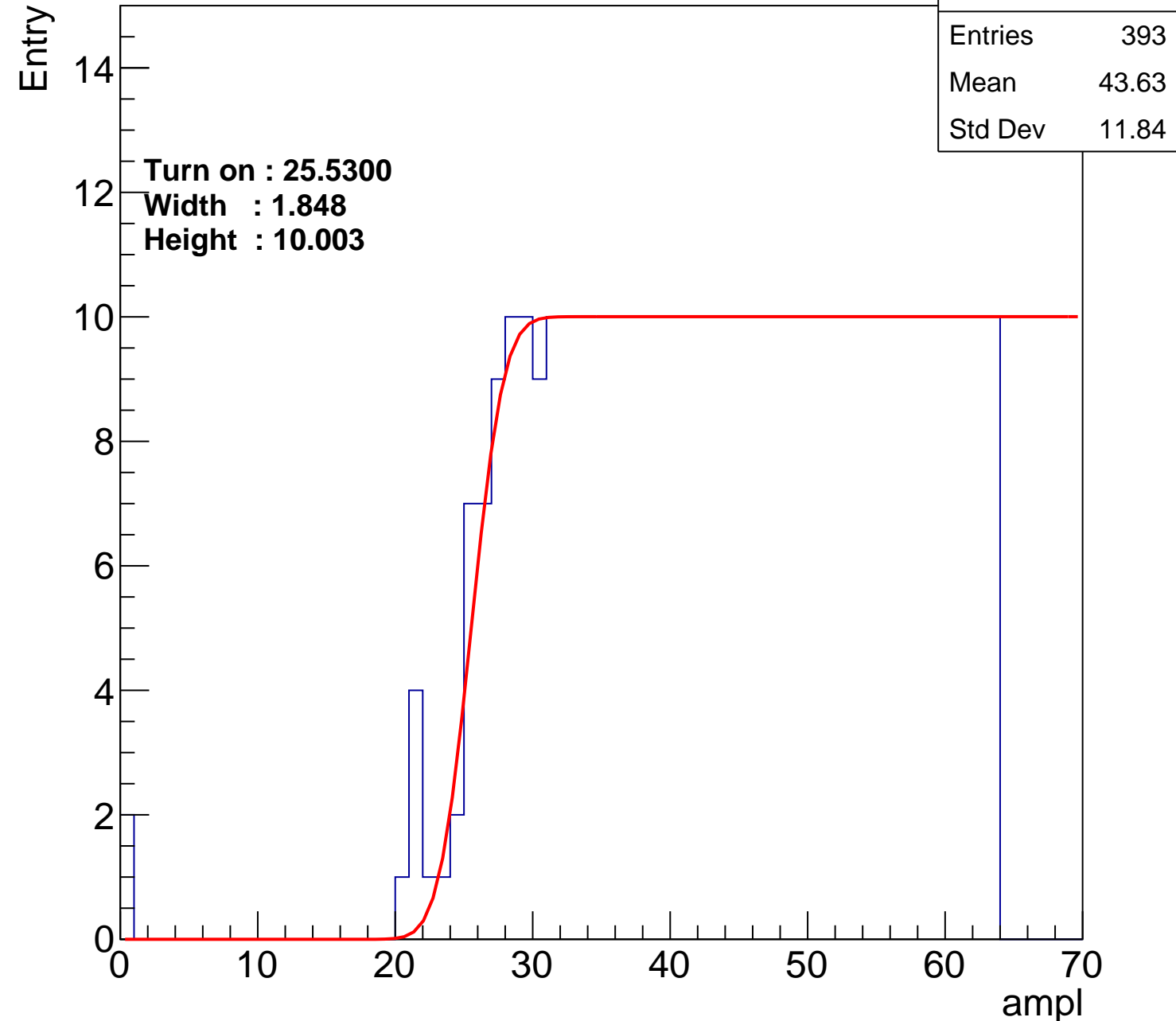
Width : 1.848

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch65

calib_packv5_042523_0143.root, FC#3, port B3

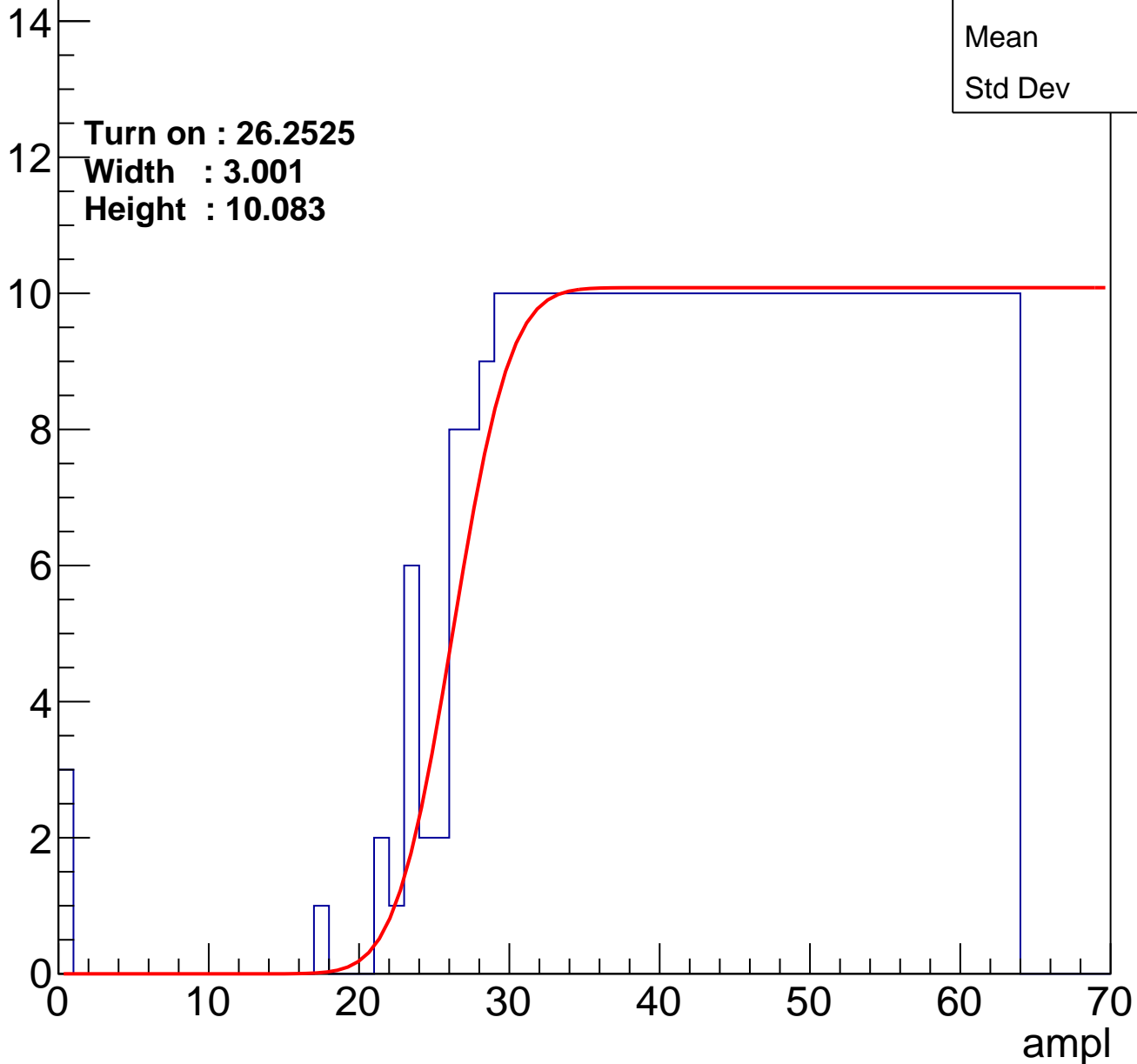
Entries	392
Mean	43.6
Std Dev	12

Turn on : 26.2525

Width : 3.001

Height : 10.083

Entry



B1L000S, U8-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.87
Std Dev	11.19

Turn on : 28.2253

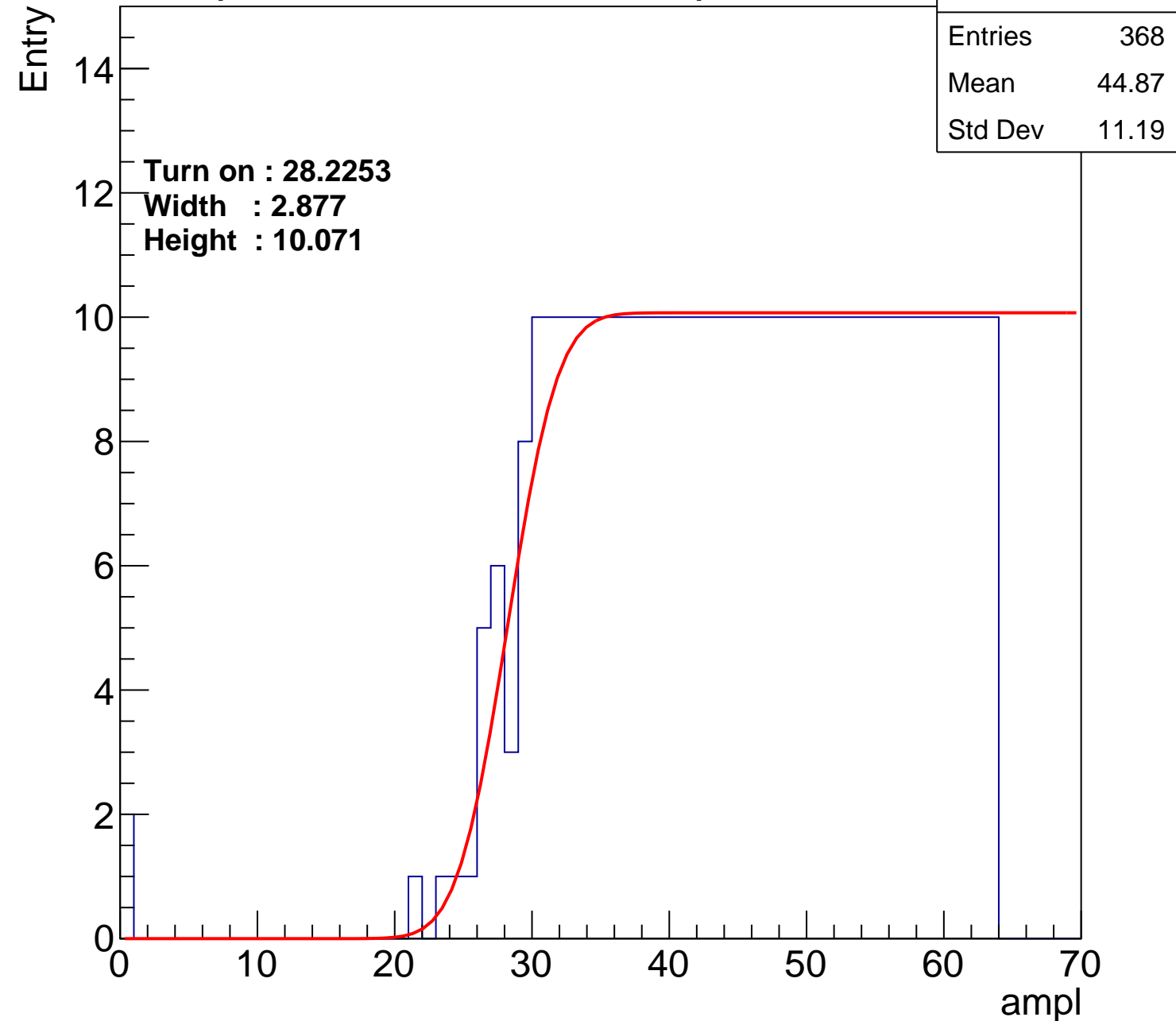
Width : 2.877

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.2491

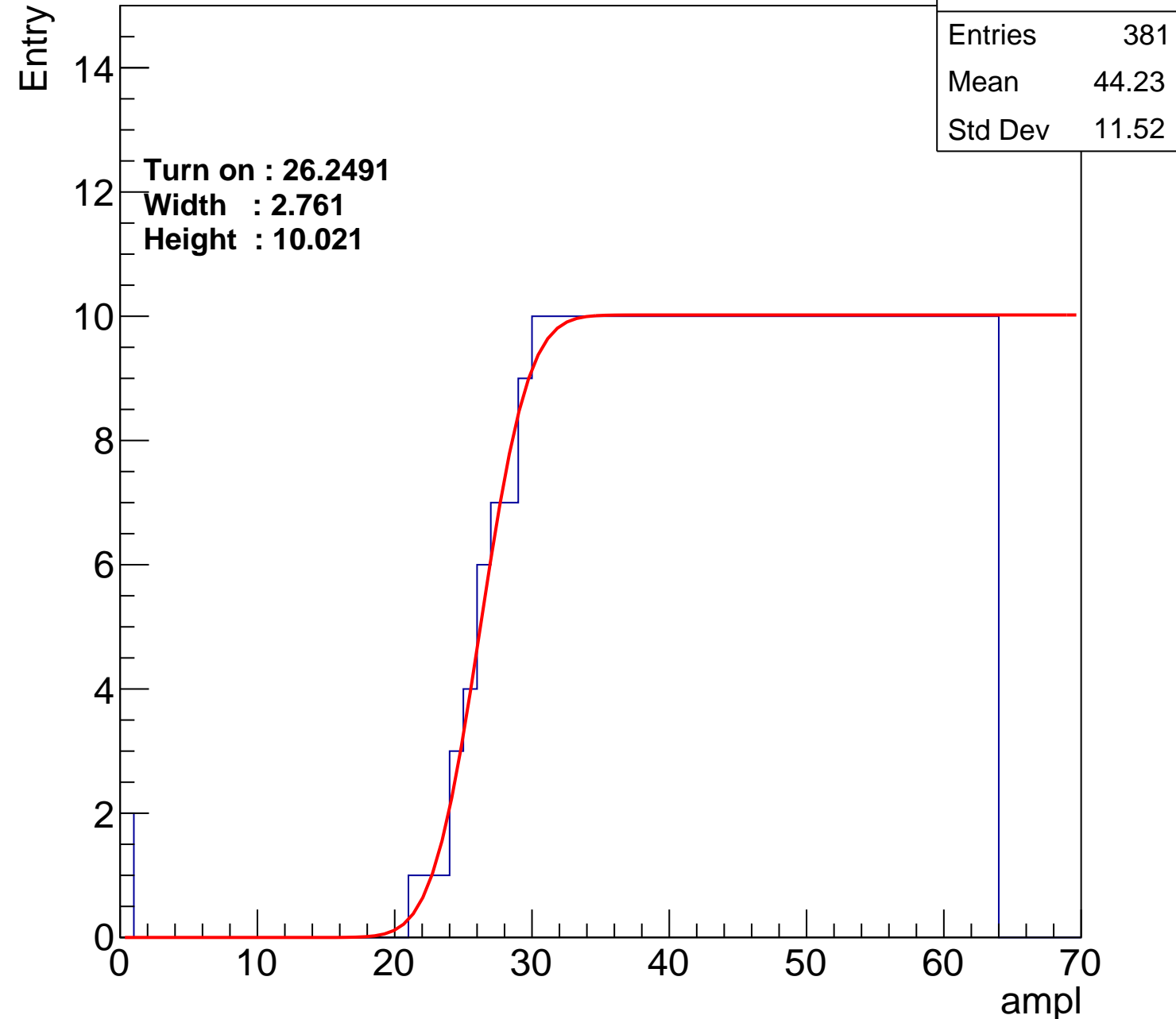
Width : 2.761

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.55
Std Dev	12.54

Turn on : 25.7519

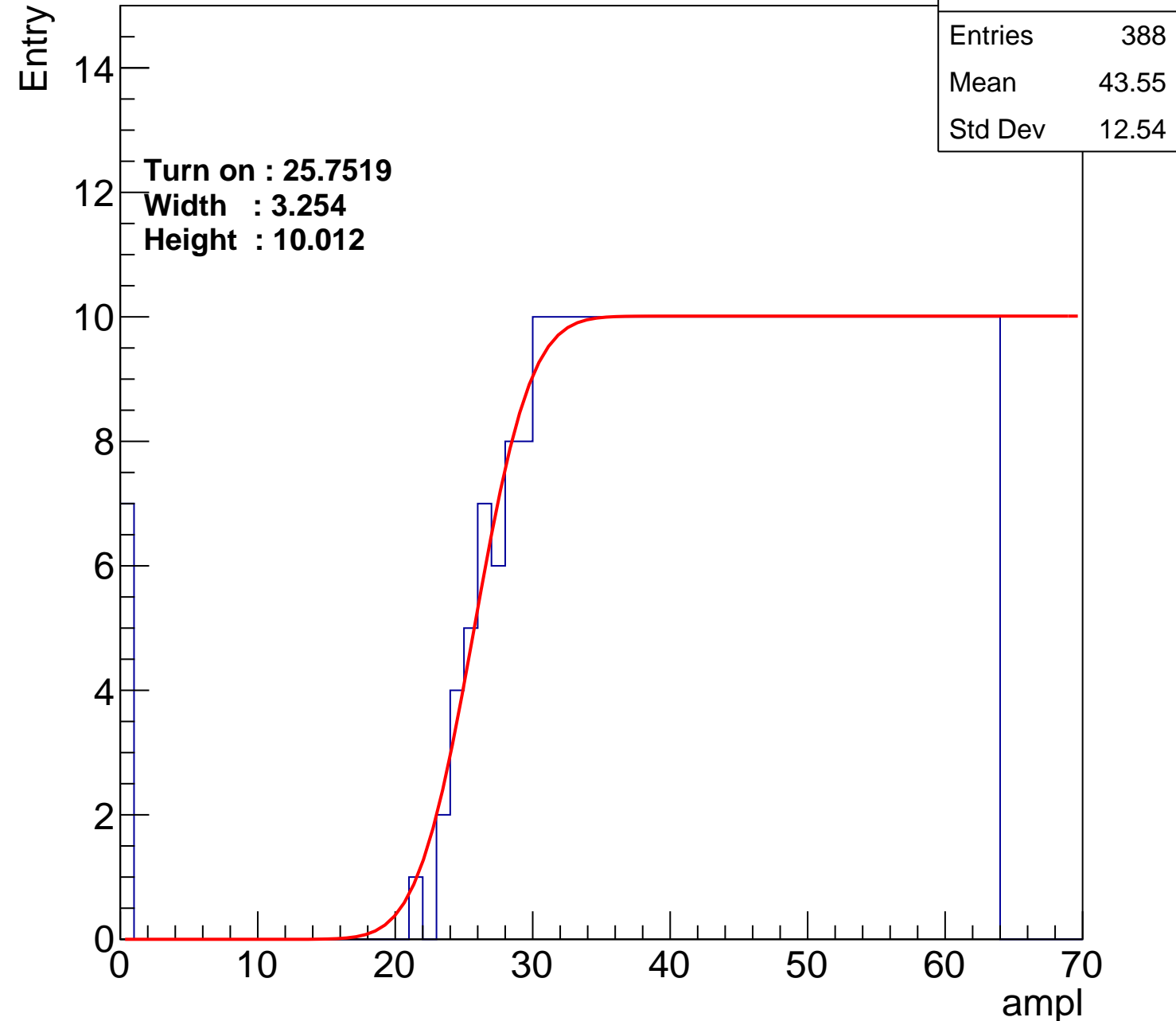
Width : 3.254

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch69

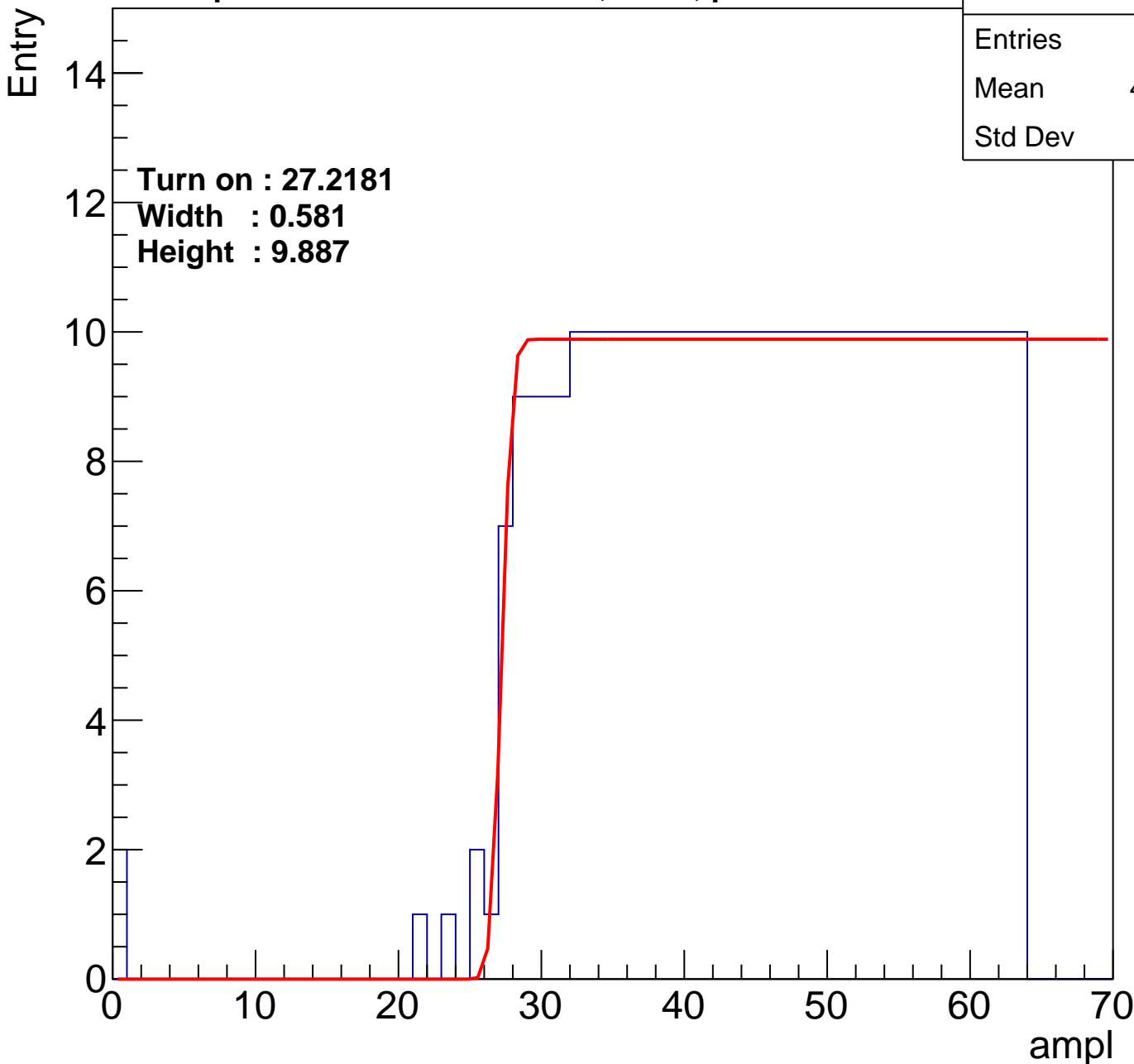
calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.79
Std Dev	11.21

Turn on : 27.2181

Width : 0.581

Height : 9.887



B1L000S, U8-ch70

calib_packv5_042523_0143.root, FC#3, port B3

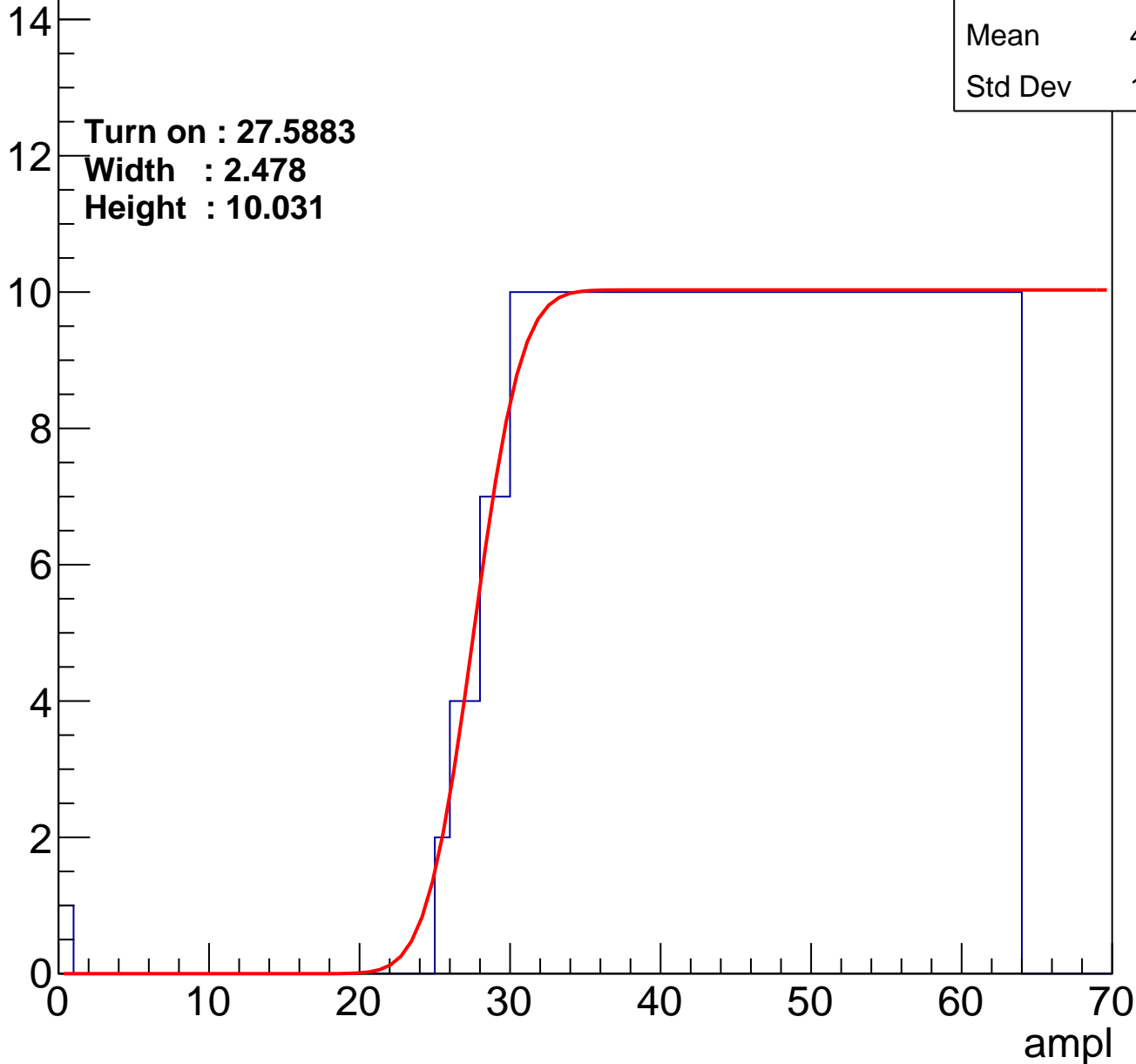
Entries	365
Mean	45.13
Std Dev	10.84

Turn on : 27.5883

Width : 2.478

Height : 10.031

Entry



B1L000S, U8-ch71

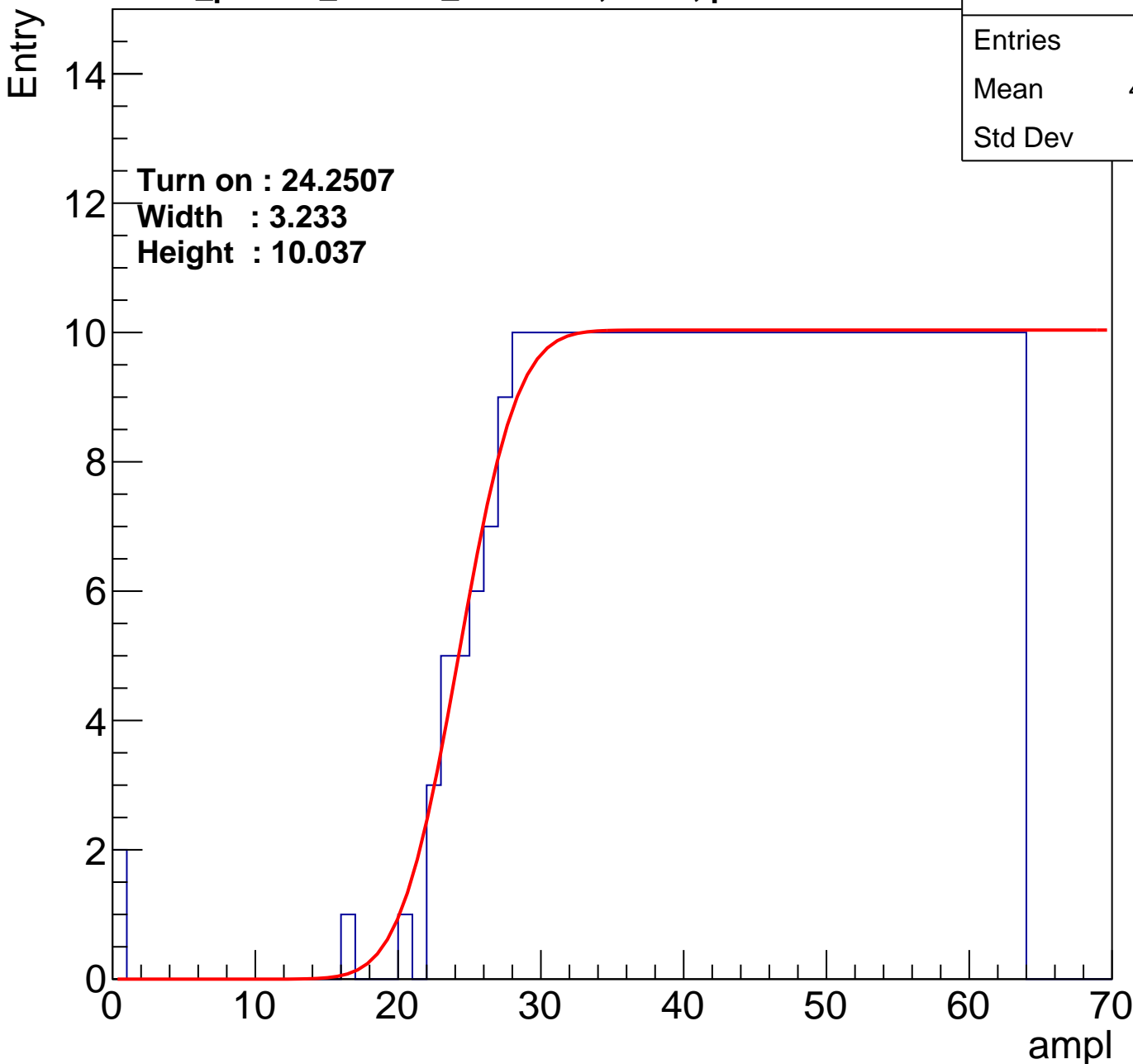
calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.34
Std Dev	12

Turn on : 24.2507

Width : 3.233

Height : 10.037



B1L000S, U8-ch72

calib_packv5_042523_0143.root, FC#3, port B3

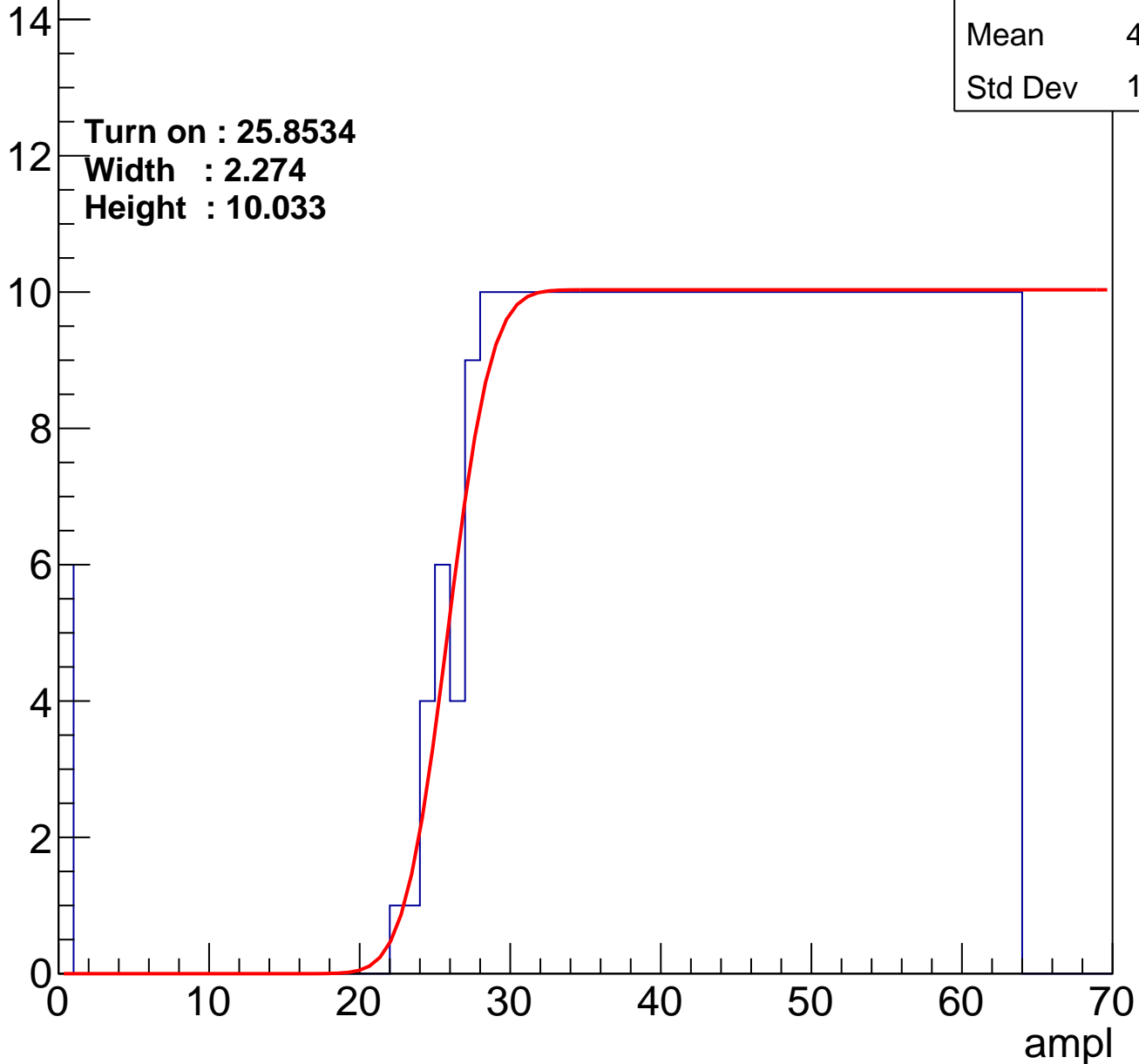
Entries	391
Mean	43.52
Std Dev	12.36

Turn on : 25.8534

Width : 2.274

Height : 10.033

Entry



B1L000S, U8-ch73

calib_packv5_042523_0143.root, FC#3, port B3

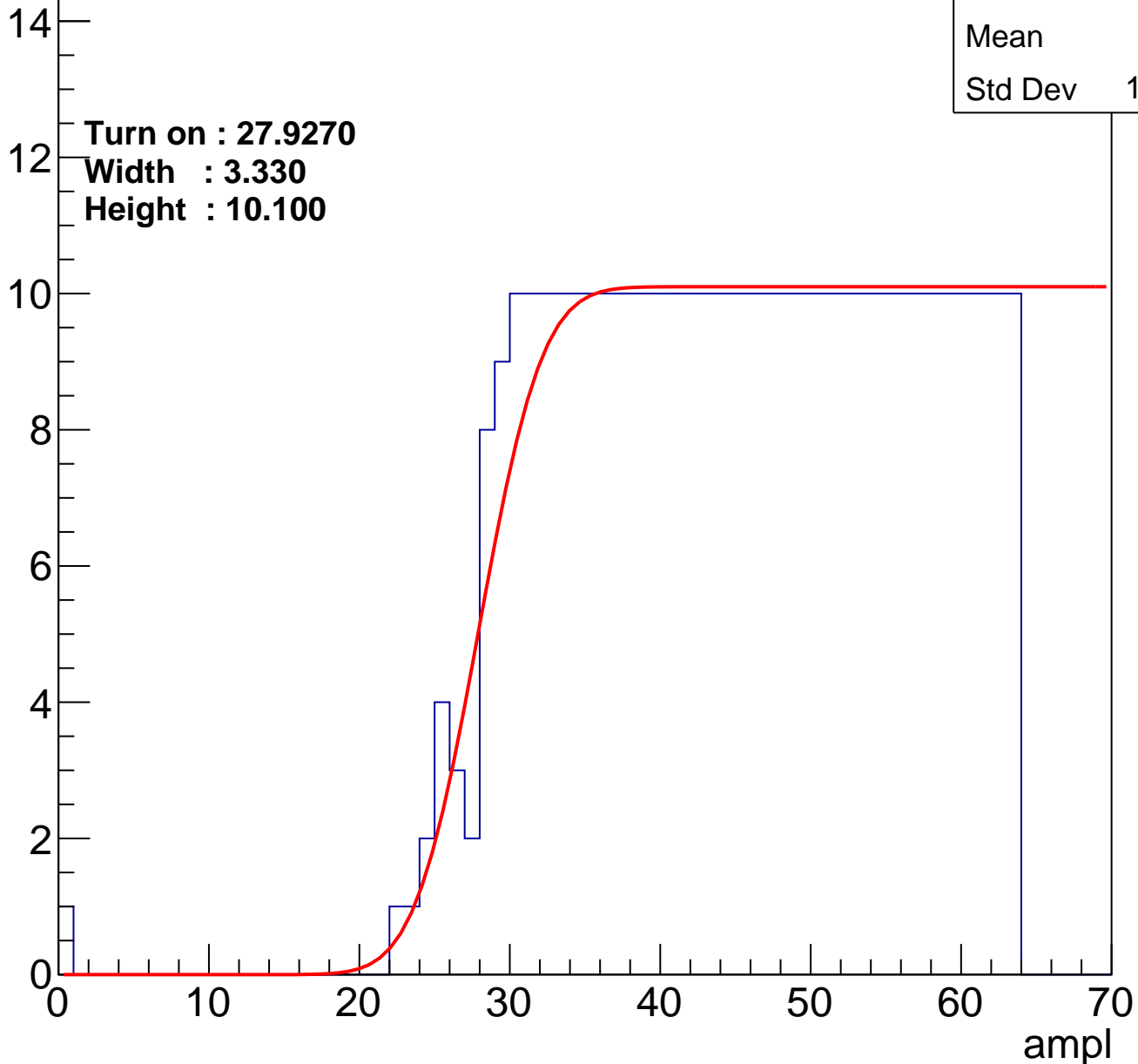
Entries	371
Mean	44.8
Std Dev	11.06

Turn on : 27.9270

Width : 3.330

Height : 10.100

Entry



B1L000S, U8-ch74

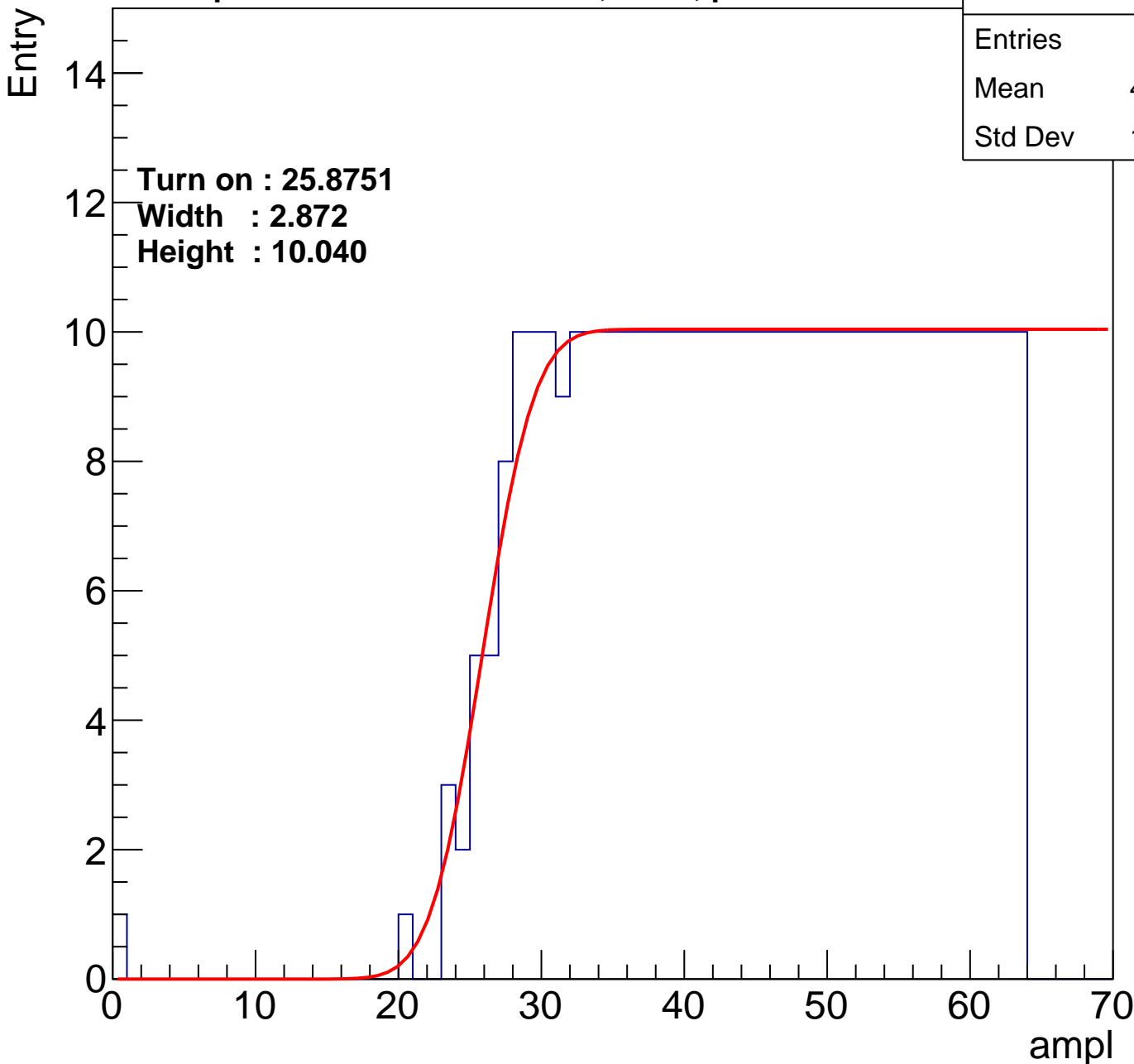
calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.16
Std Dev	11.39

Turn on : 25.8751

Width : 2.872

Height : 10.040



B1L000S, U8-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.35
Std Dev	11.42

Turn on : 26.2842

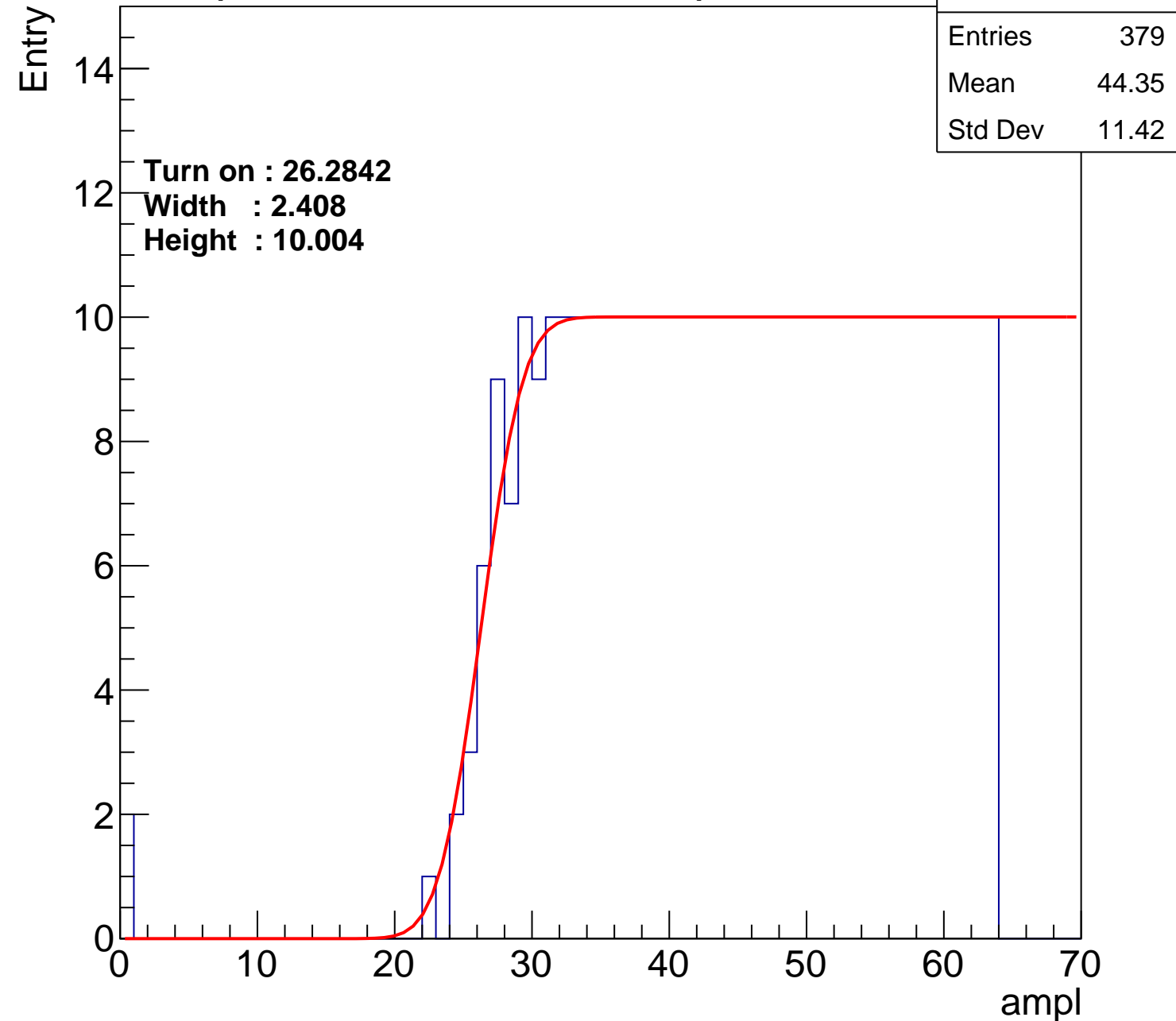
Width : 2.408

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch76

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.31
Std Dev	12.47

Turn on : 24.9072

Width : 1.928

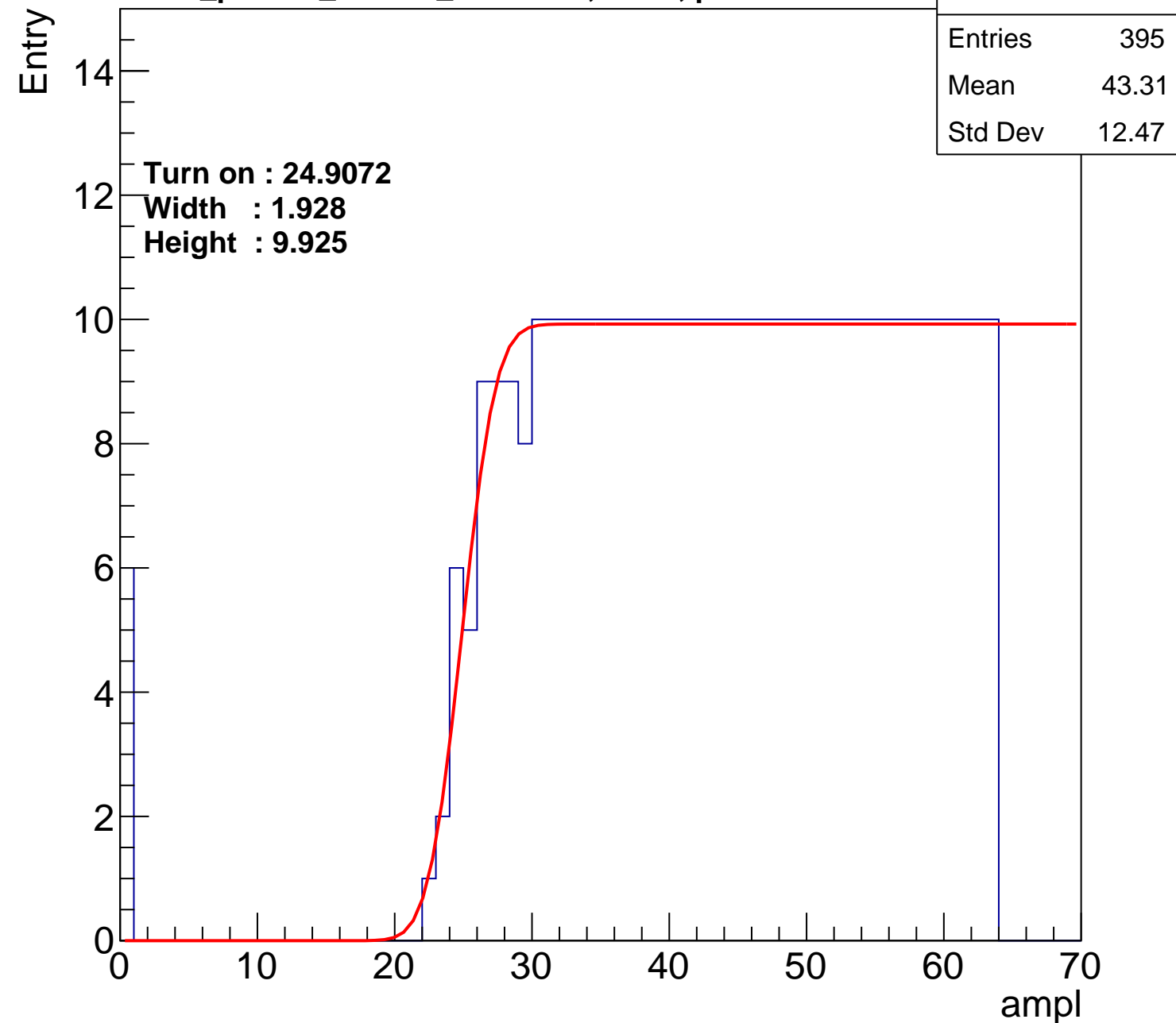
Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U8-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 25.1763

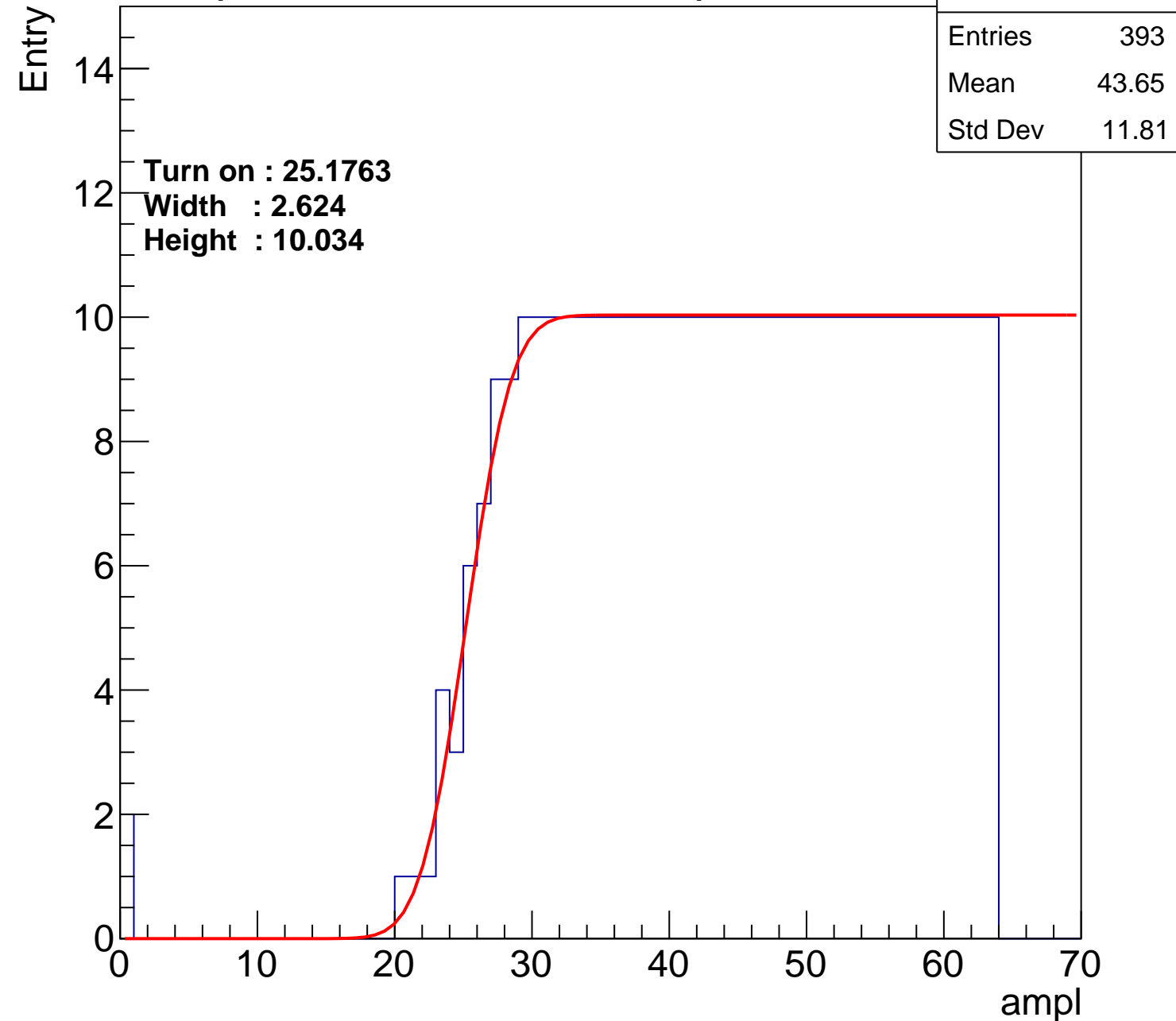
Width : 2.624

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch78

calib_packv5_042523_0143.root, FC#3, port B3

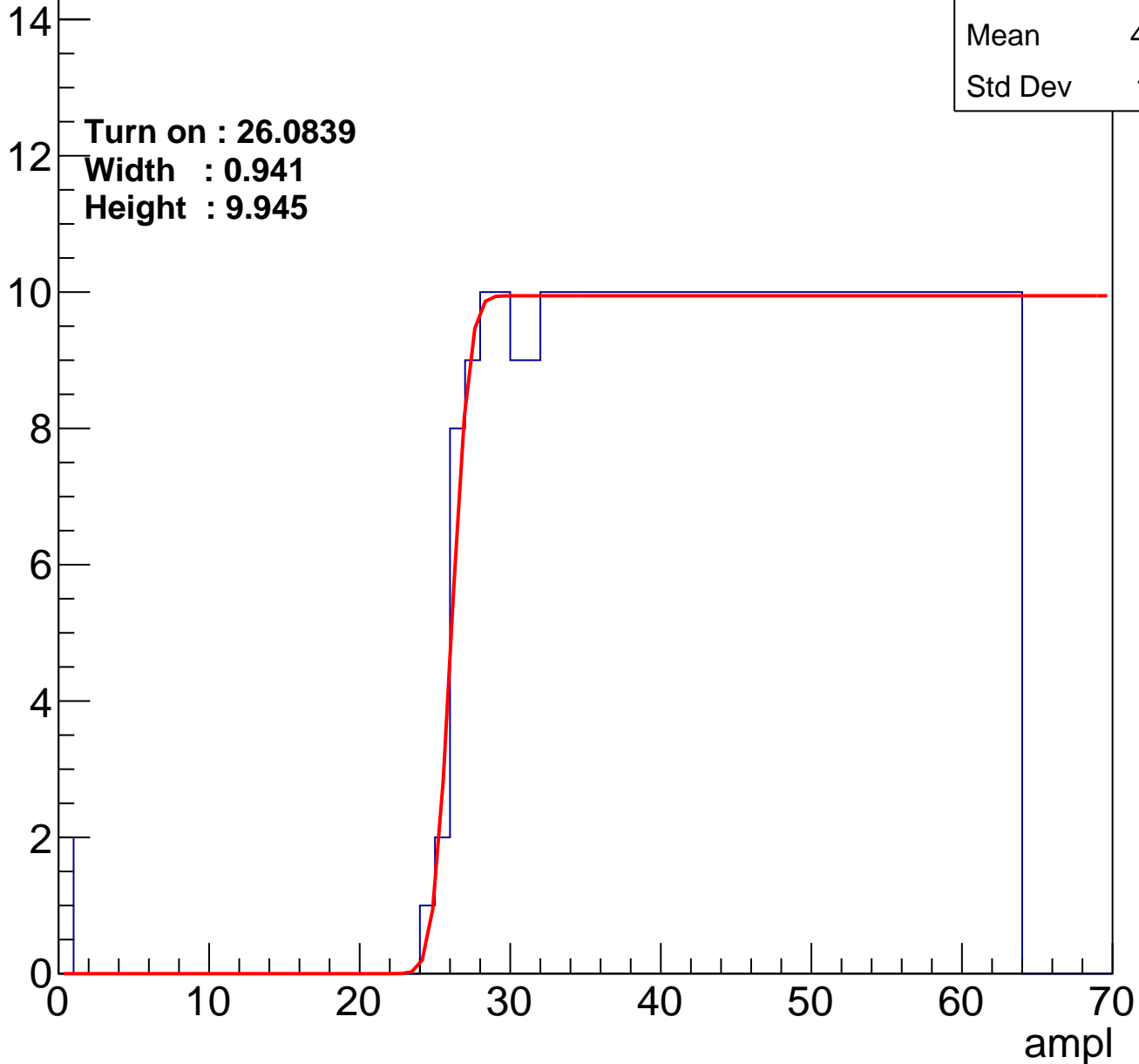
Entries	380
Mean	44.33
Std Dev	11.41

Turn on : 26.0839

Width : 0.941

Height : 9.945

Entry



B1L000S, U8-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.54
Std Dev	12

Turn on : 25.2026

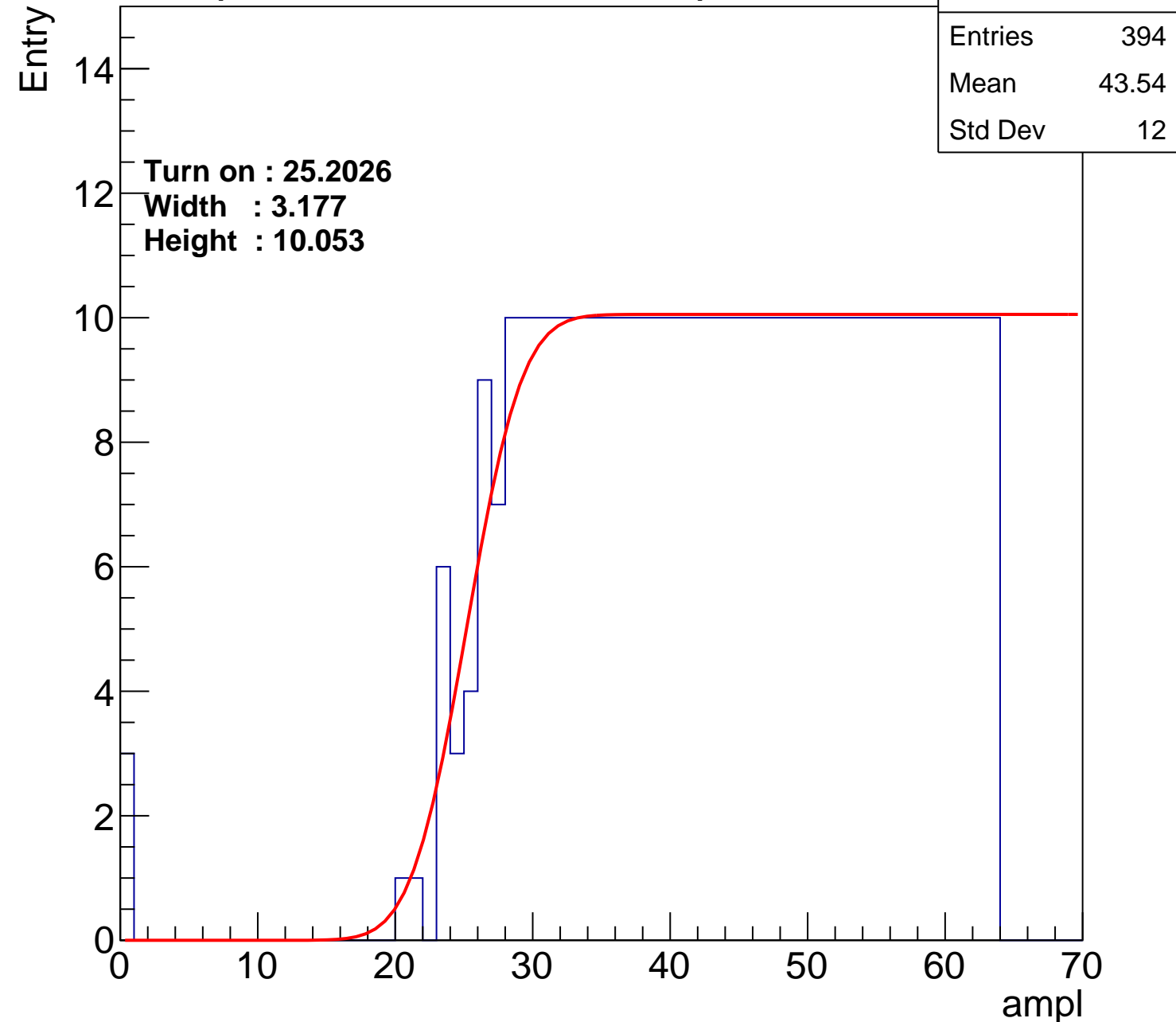
Width : 3.177

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.96
Std Dev	11.28

Turn on : 28.1955

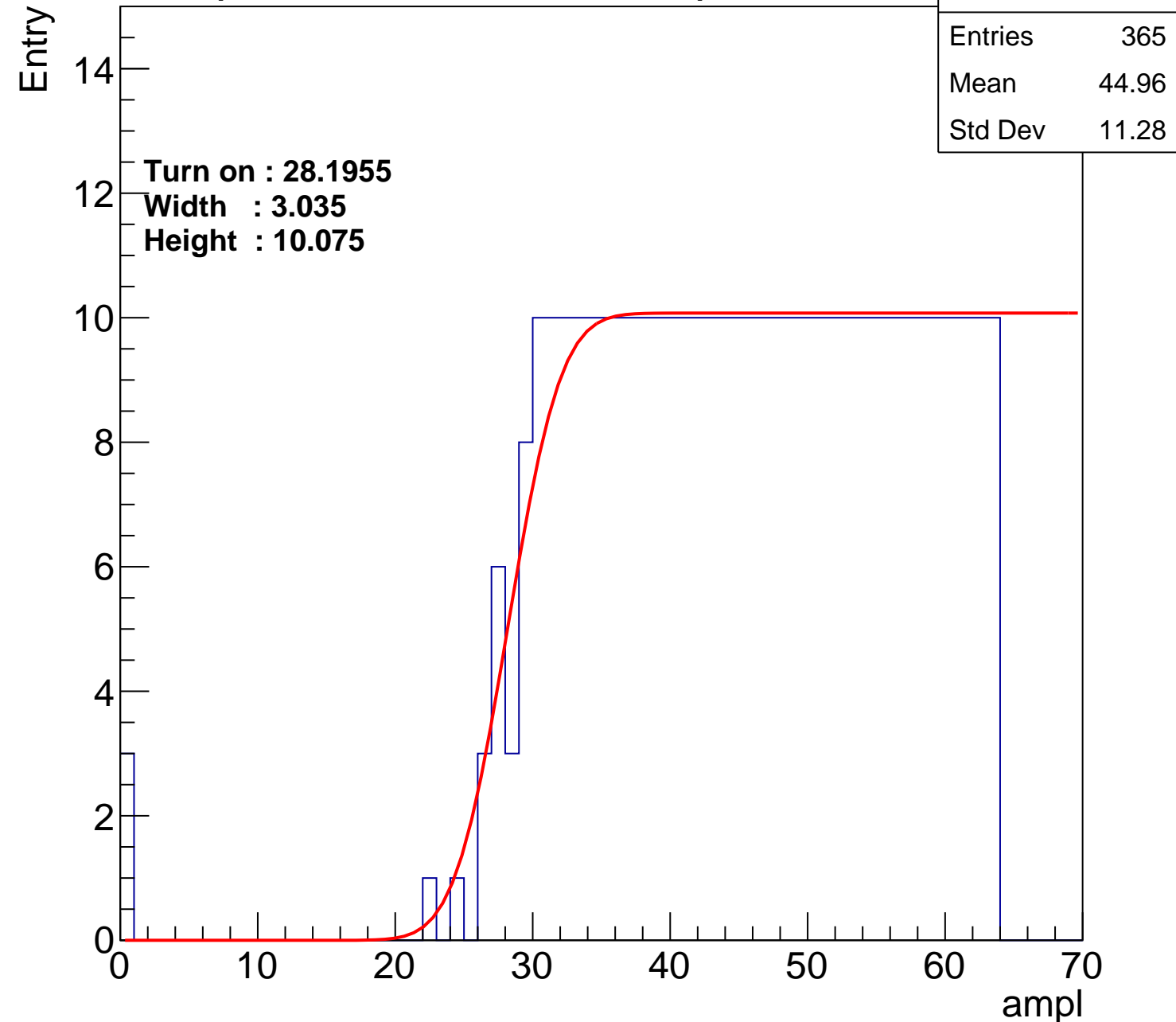
Width : 3.035

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch81

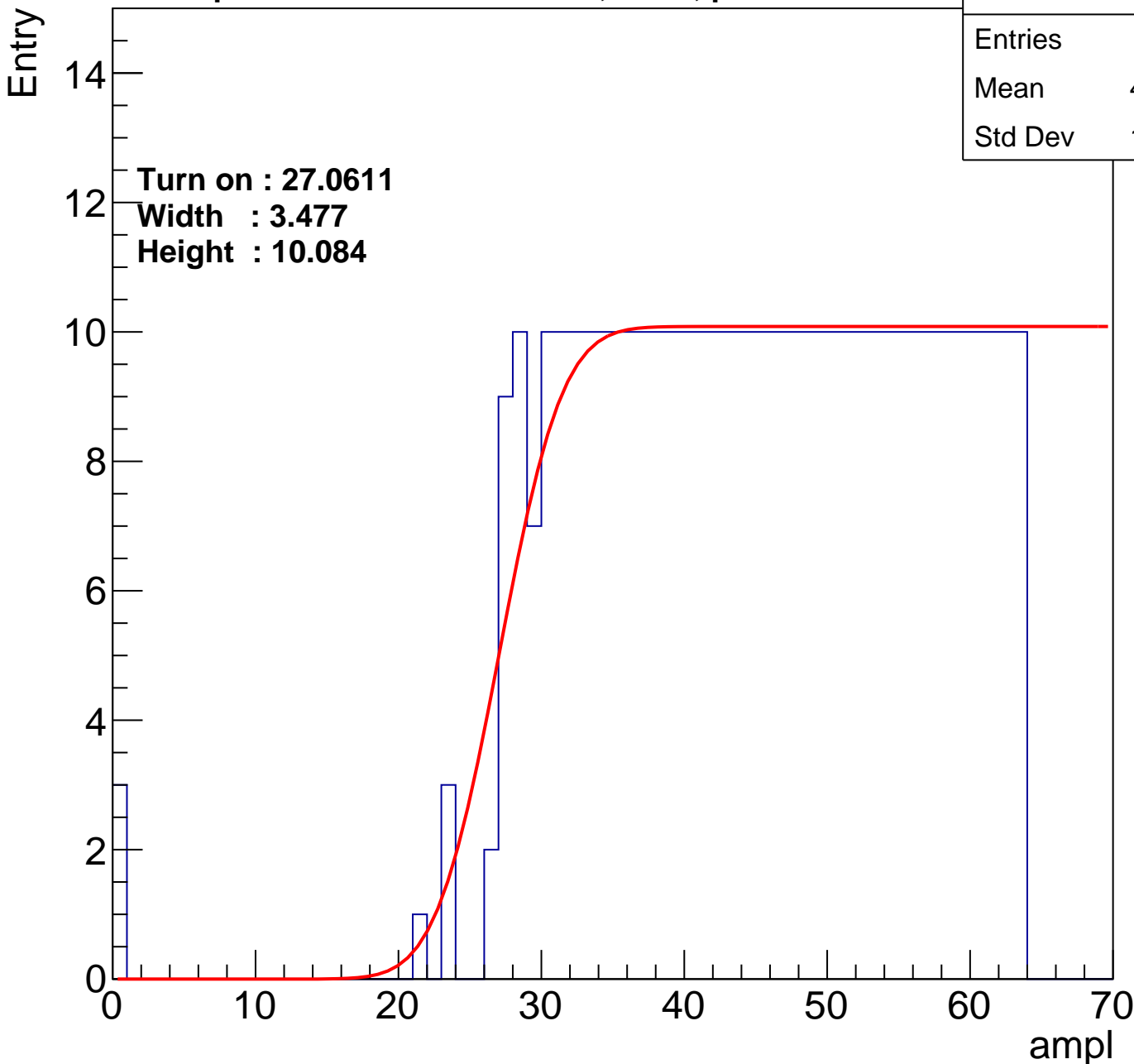
calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.47
Std Dev	11.52

Turn on : 27.0611

Width : 3.477

Height : 10.084



B1L000S, U8-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.27
Std Dev	11.63

Turn on : 26.3065

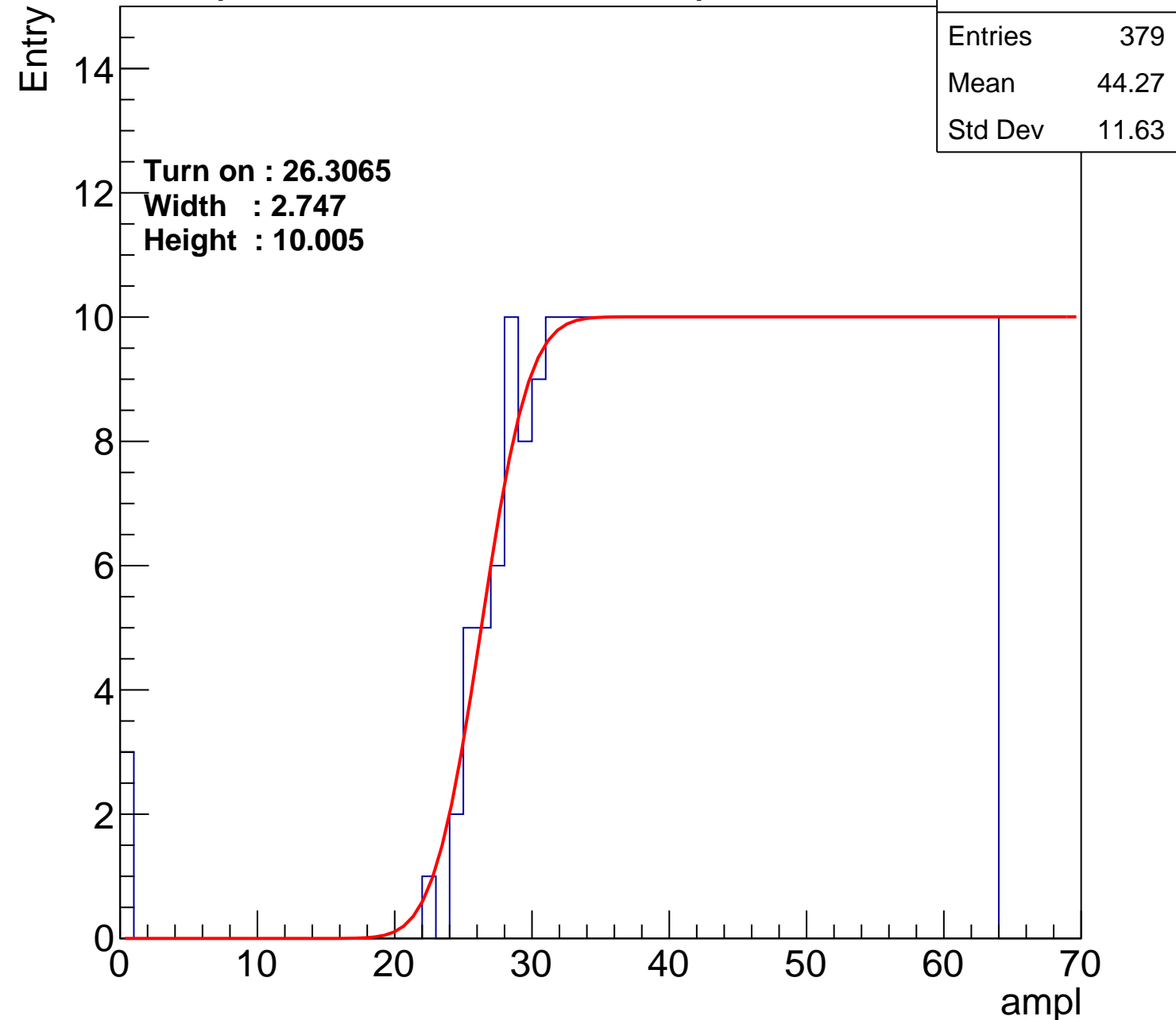
Width : 2.747

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.53
Std Dev	11.15

Turn on : 26.2004

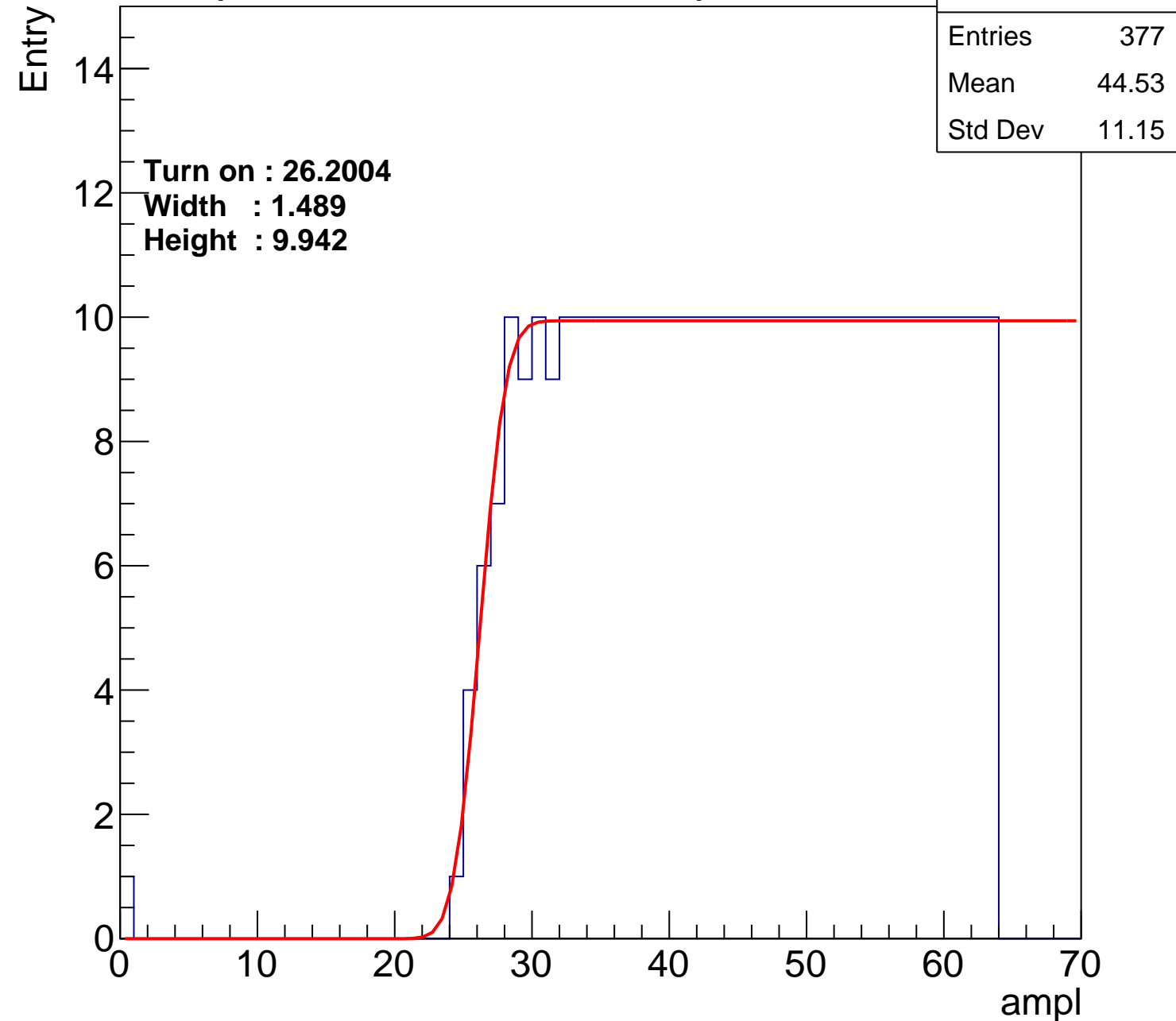
Width : 1.489

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch84

calib_packv5_042523_0143.root, FC#3, port B3

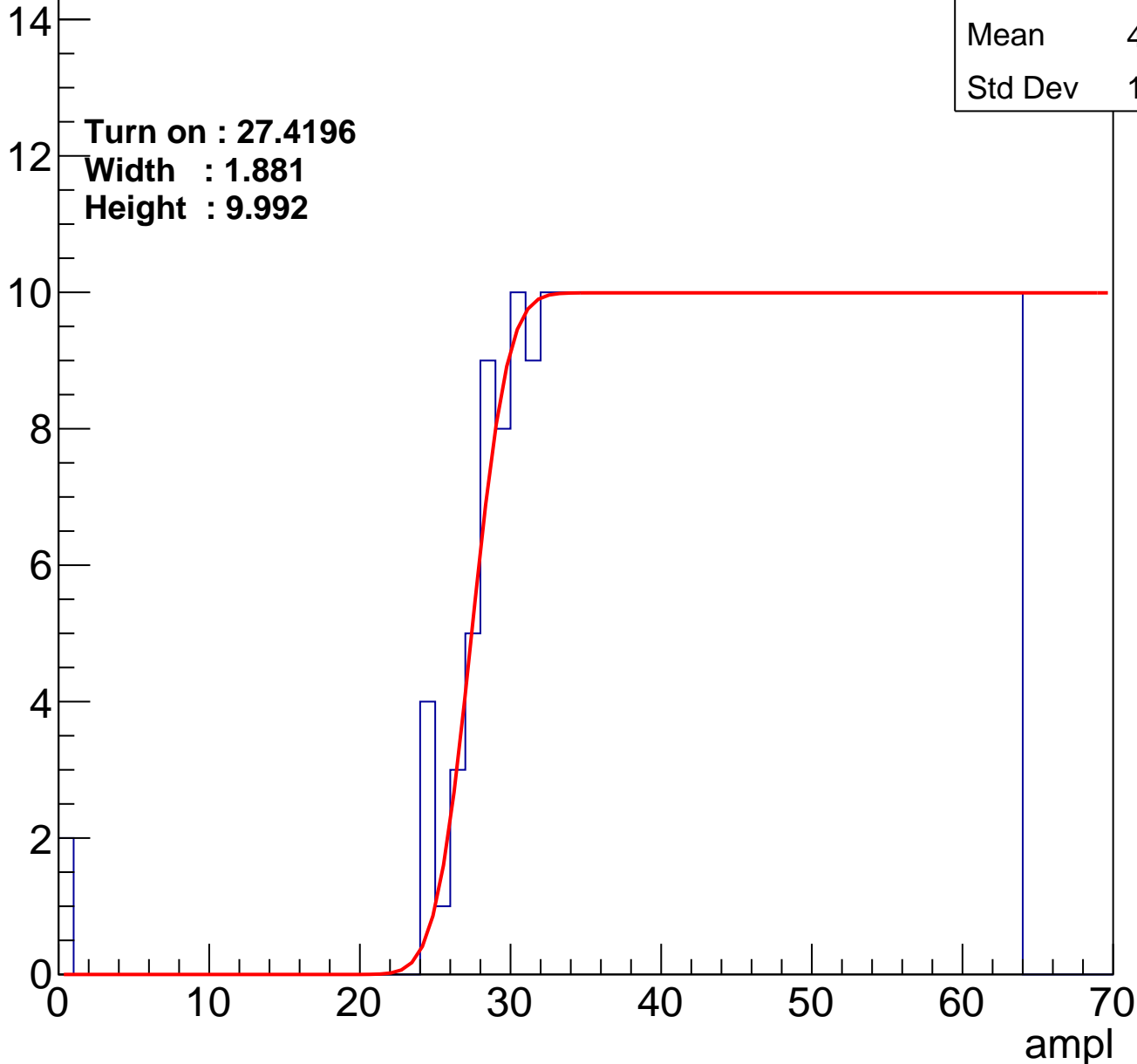
Entry

Entries	371
Mean	44.74
Std Dev	11.24

Turn on : 27.4196

Width : 1.881

Height : 9.992



B1L000S, U8-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.02
Std Dev	11.51

Turn on : 25.8413

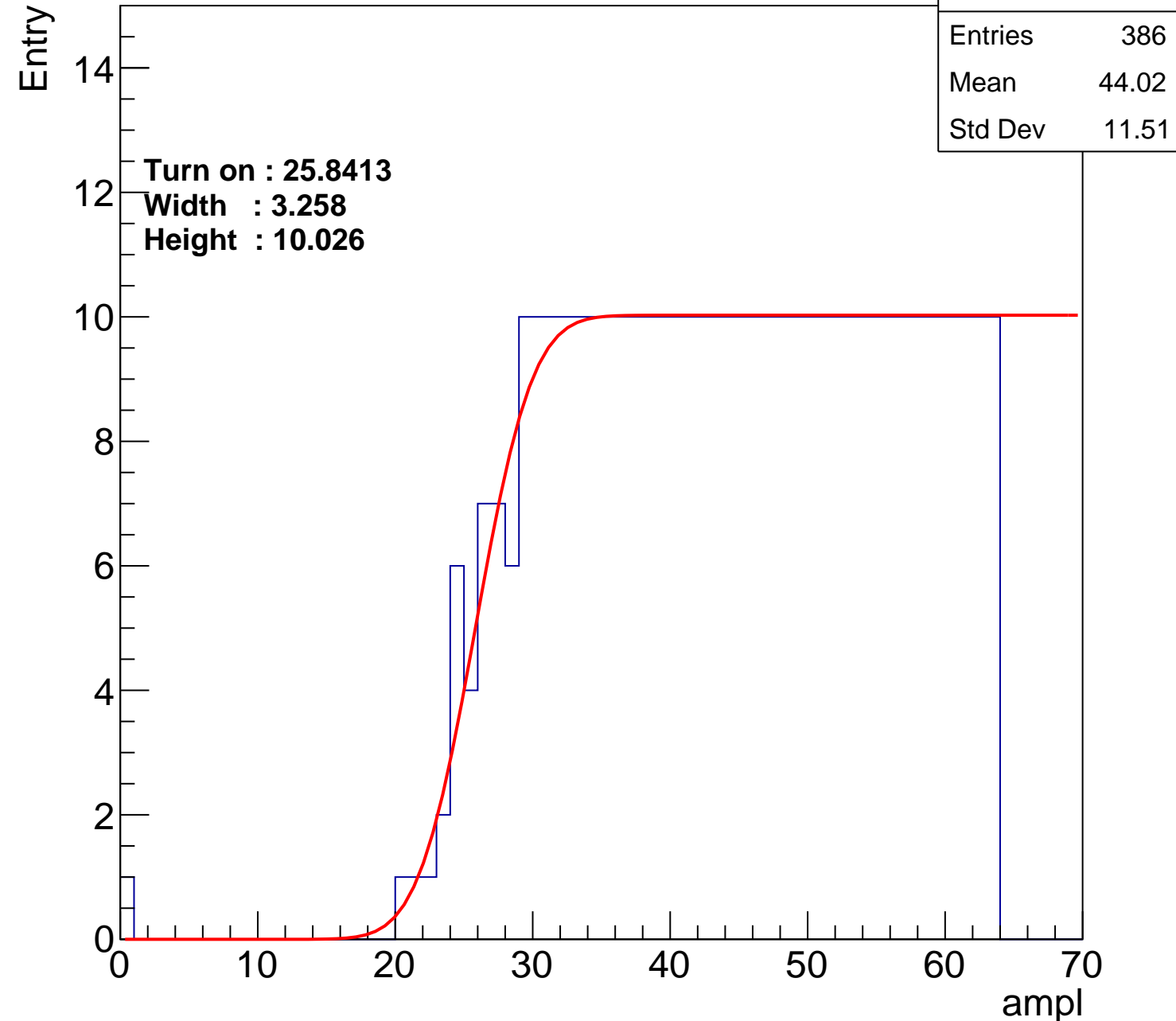
Width : 3.258

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.34
Std Dev	12.22

Turn on : 24.8742

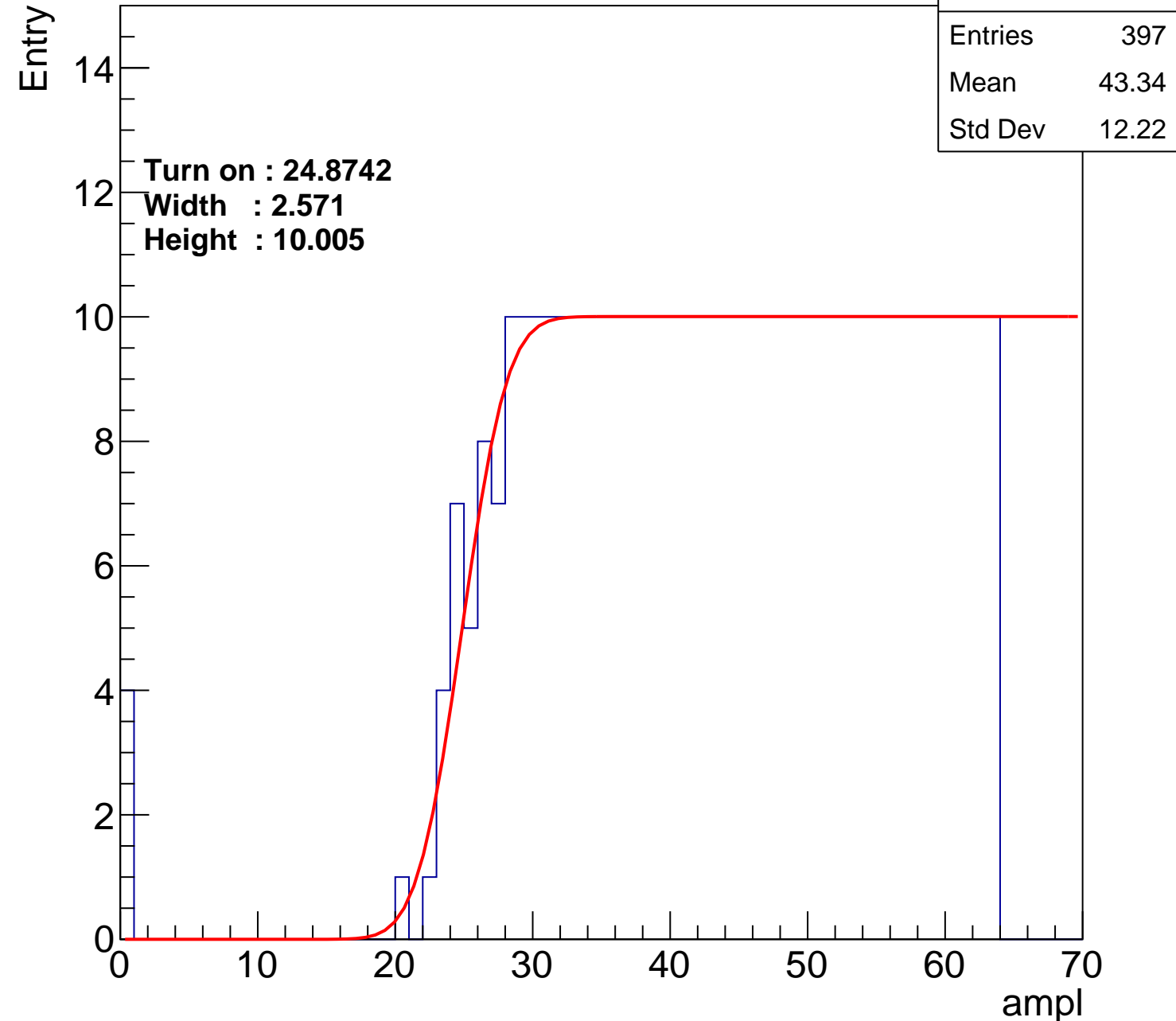
Width : 2.571

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 25.1835

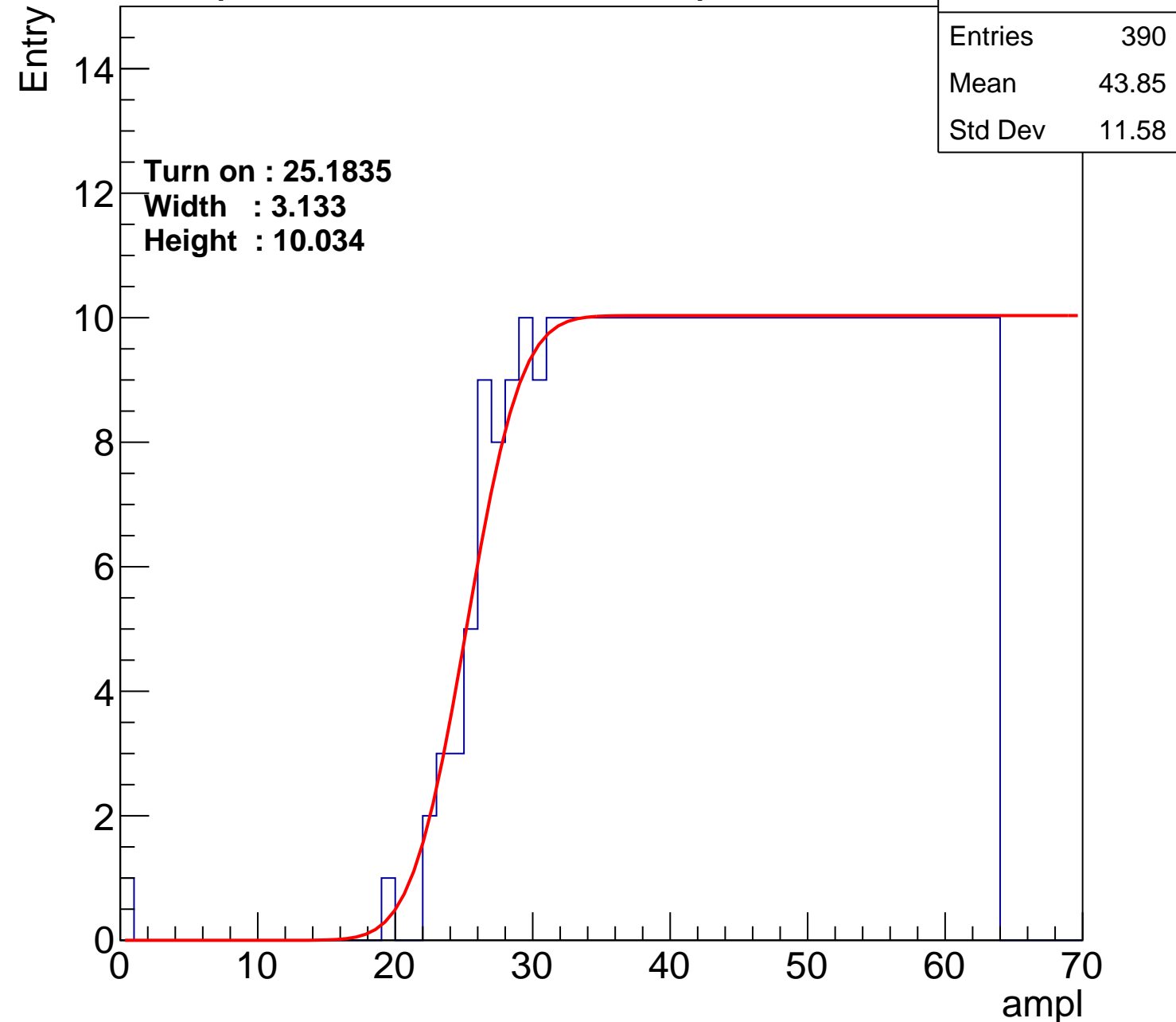
Width : 3.133

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.0323

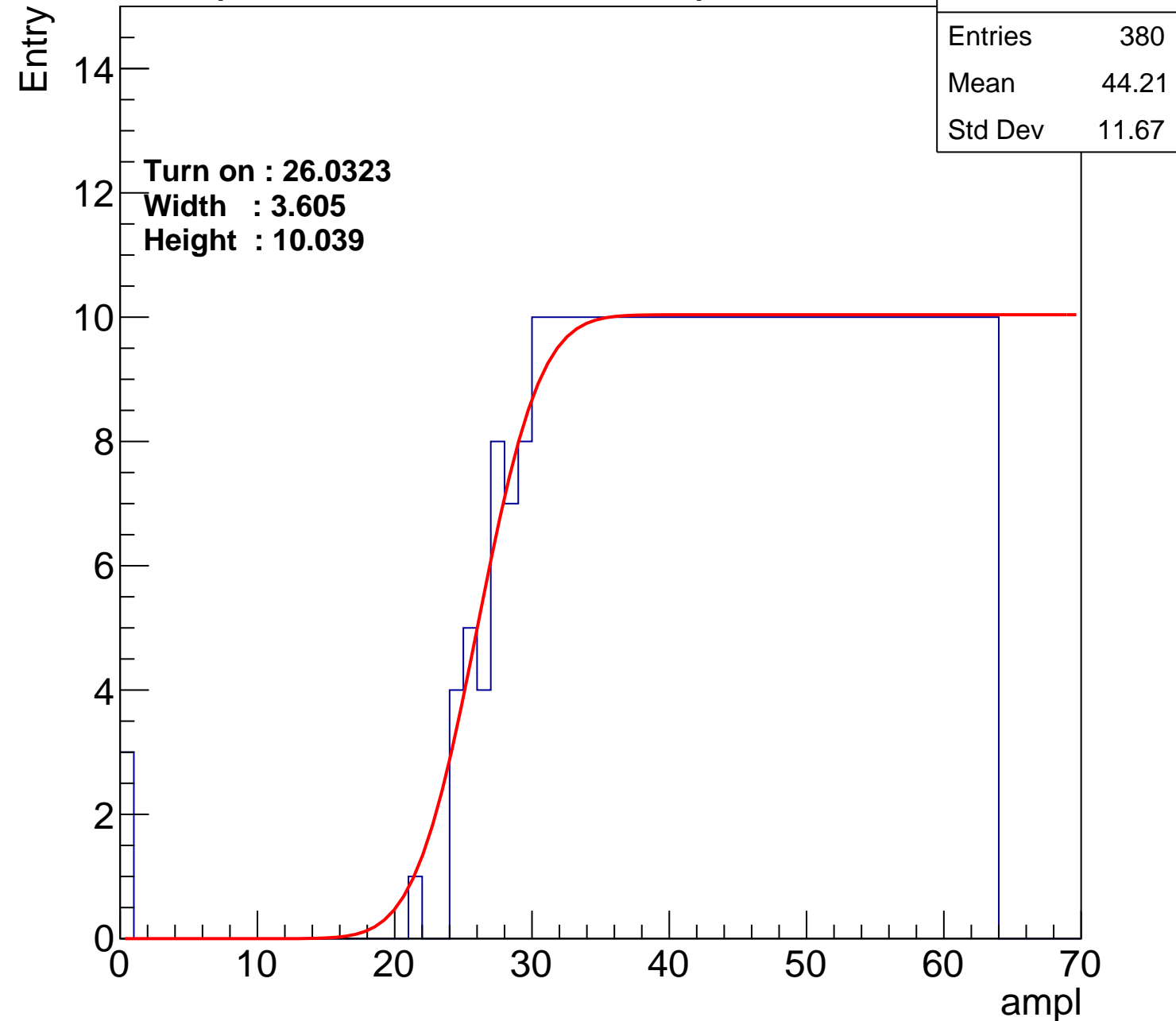
Width : 3.605

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.86
Std Dev	12.09

Turn on : 25.9896

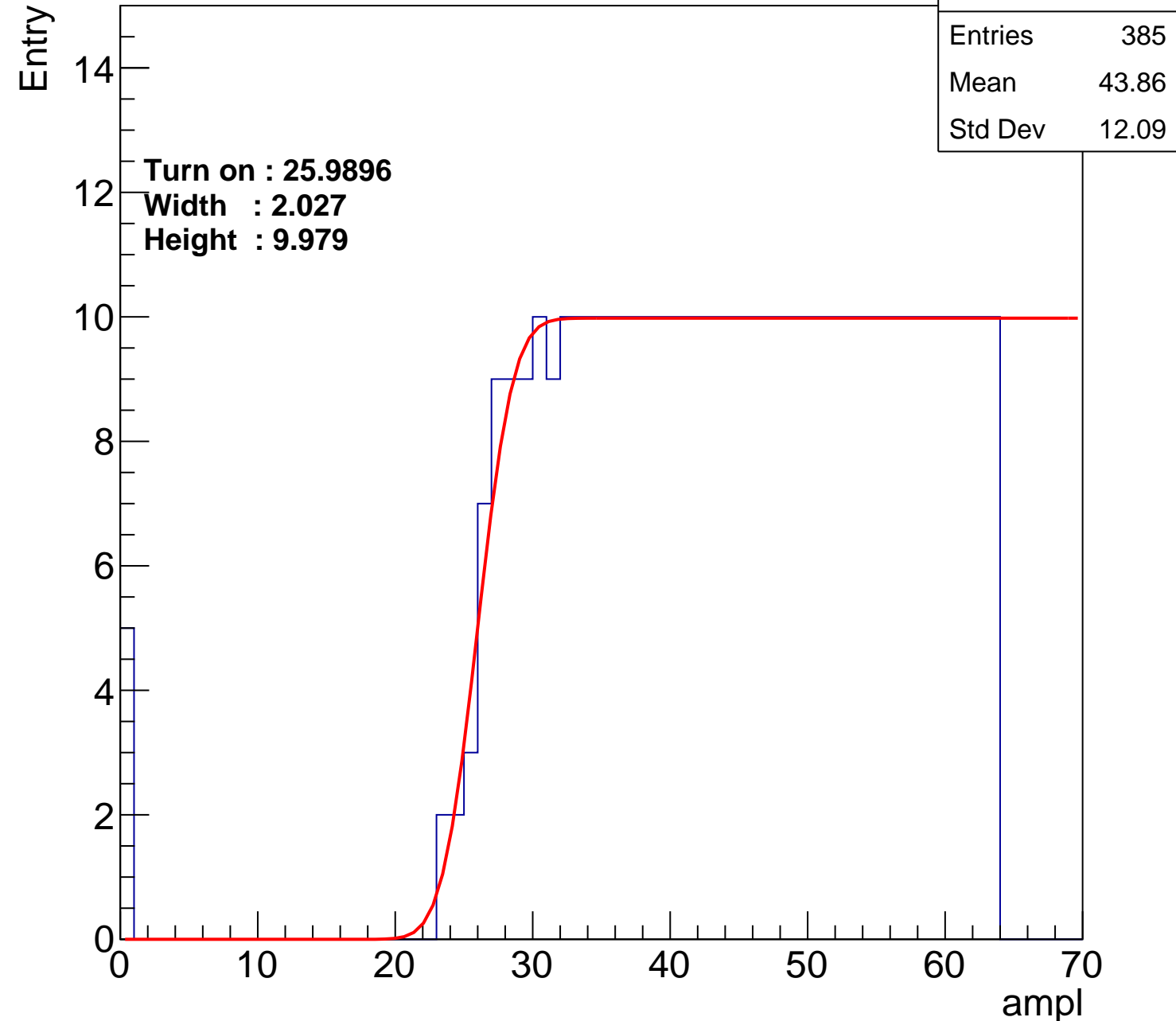
Width : 2.027

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.12
Std Dev	11.53

Turn on : 25.8786

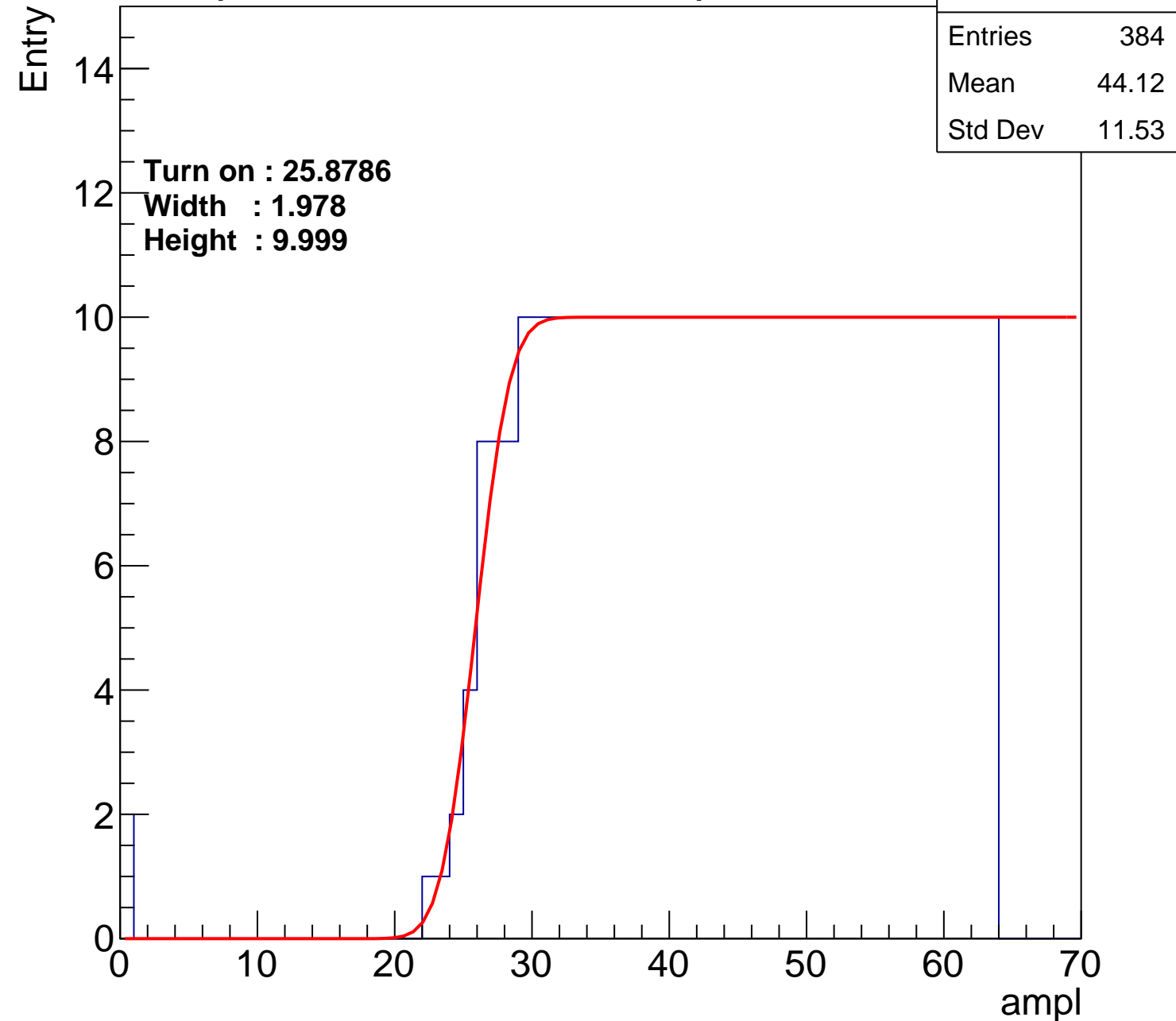
Width : 1.978

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.7
Std Dev	11.62

Turn on : 27.5134

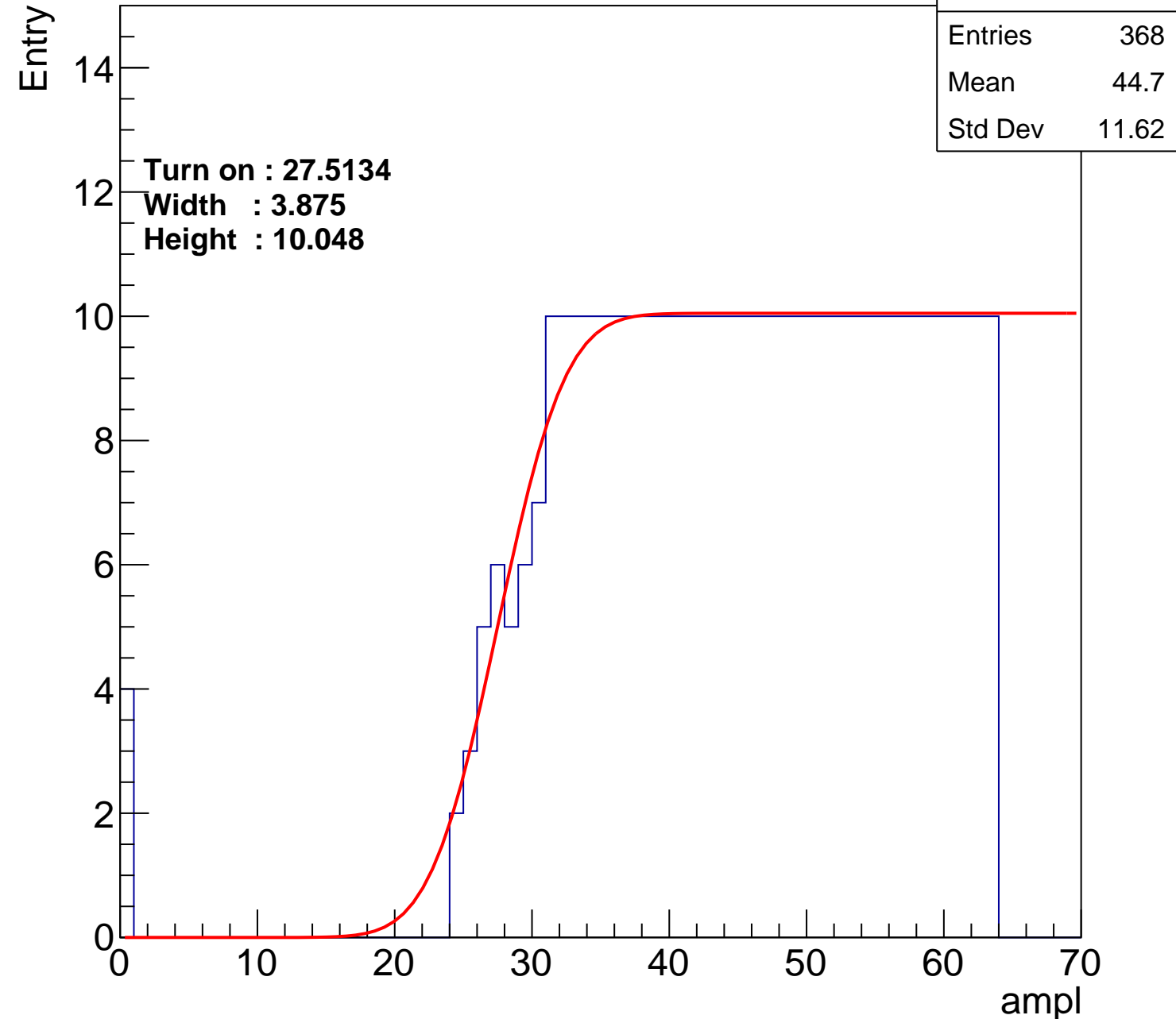
Width : 3.875

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.2114

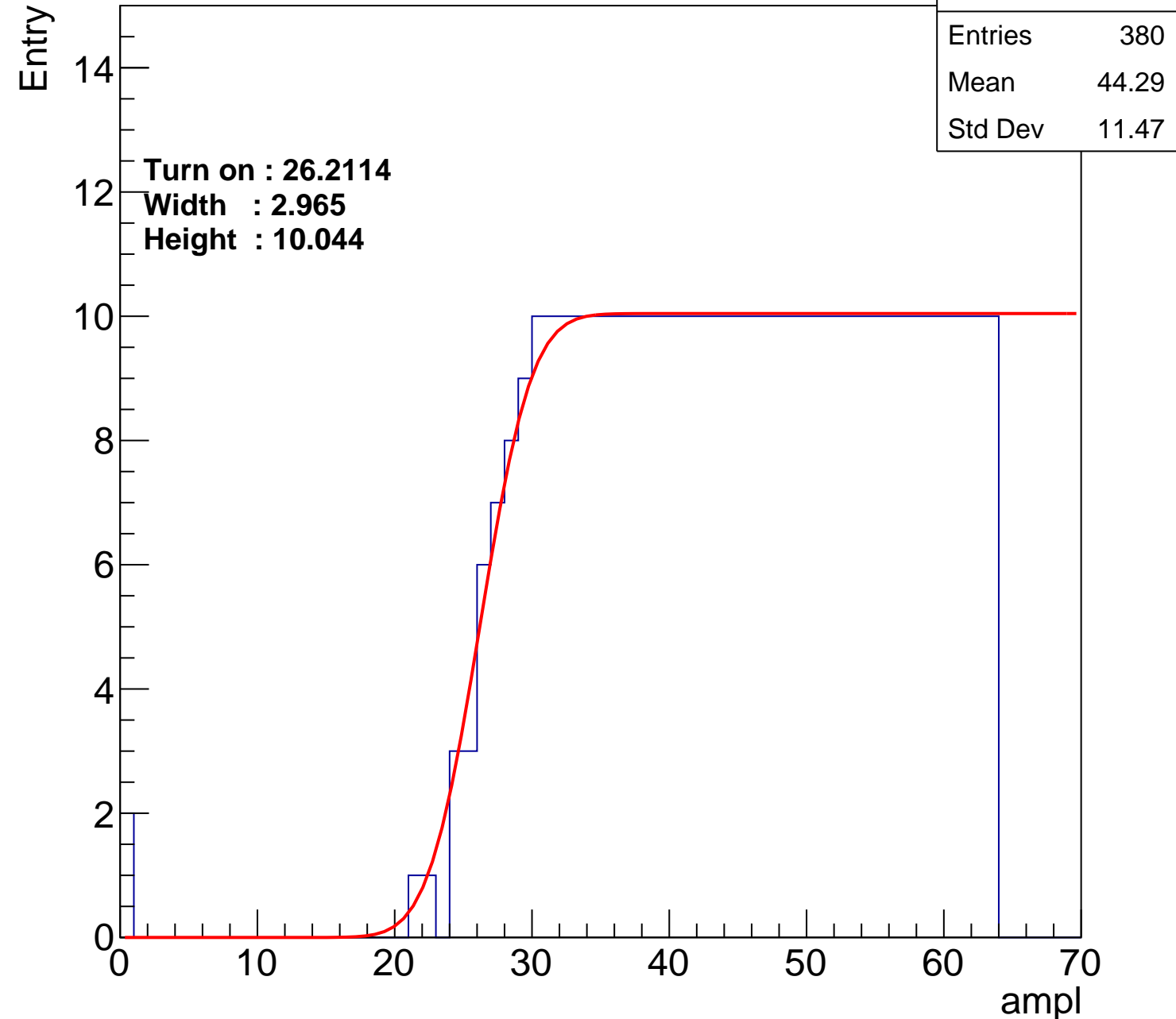
Width : 2.965

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch93

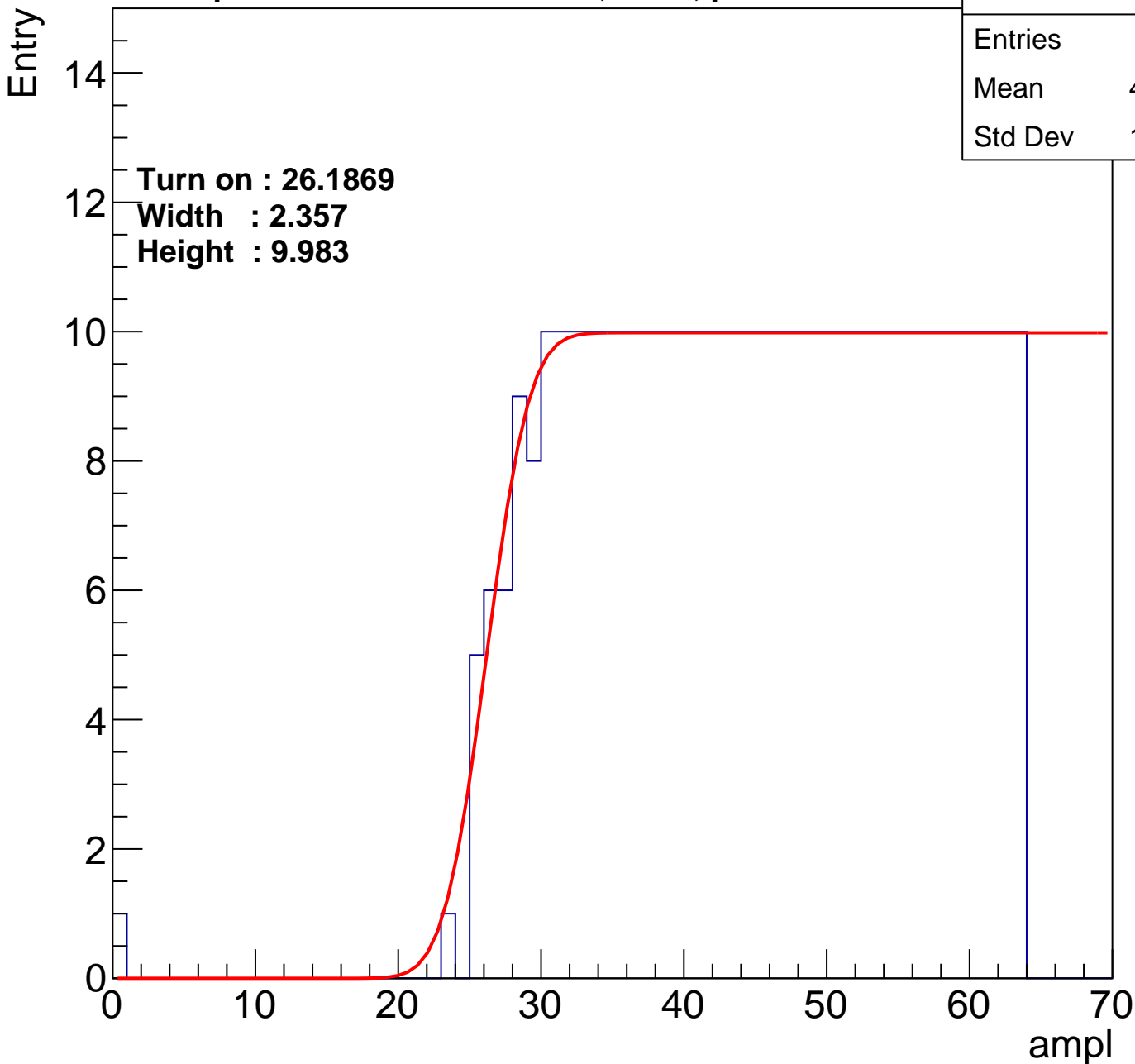
calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.57
Std Dev	11.14

Turn on : 26.1869

Width : 2.357

Height : 9.983



B1L000S, U8-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 26.7794

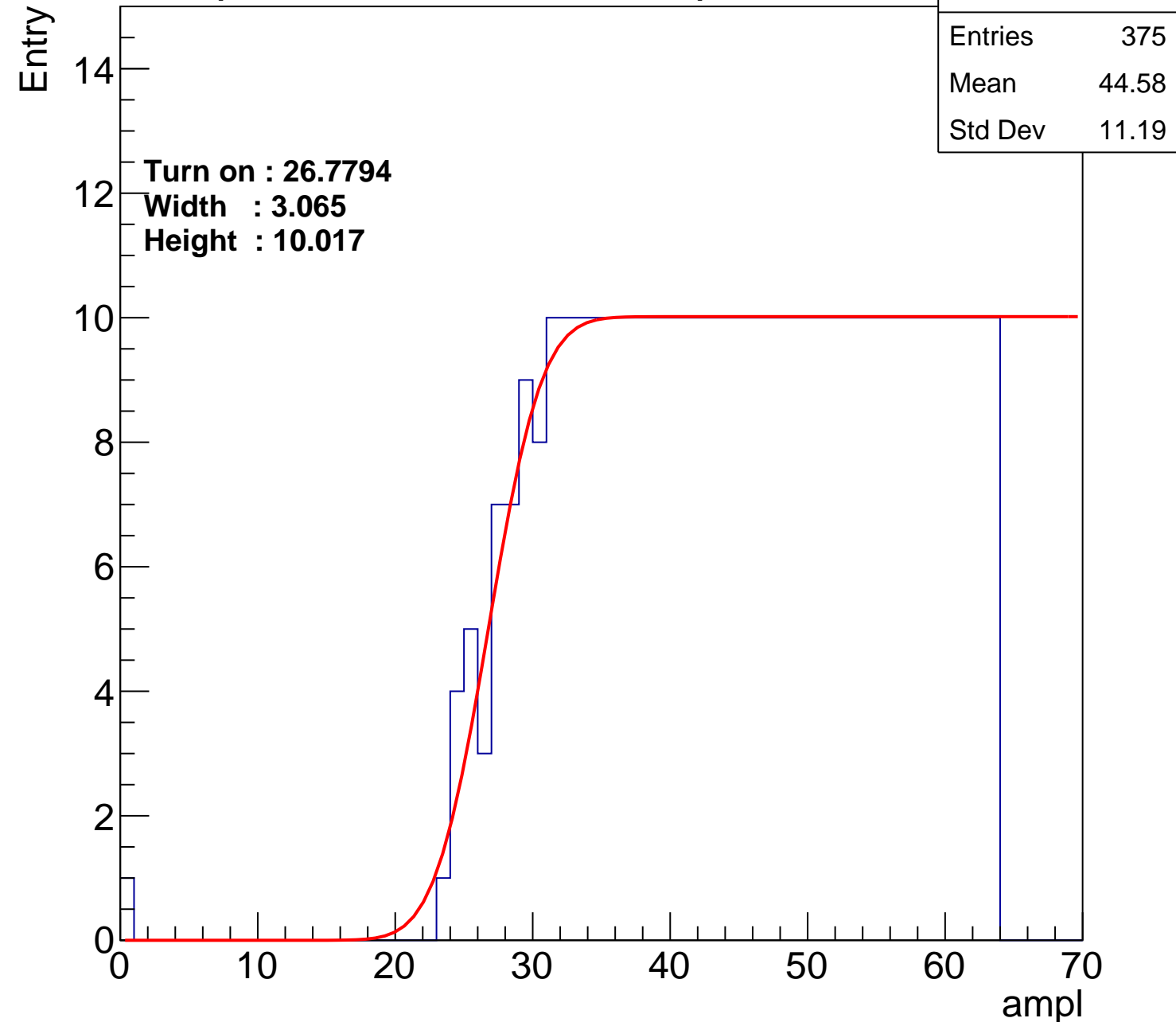
Width : 3.065

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch95

calib_packv5_042523_0143.root, FC#3, port B3

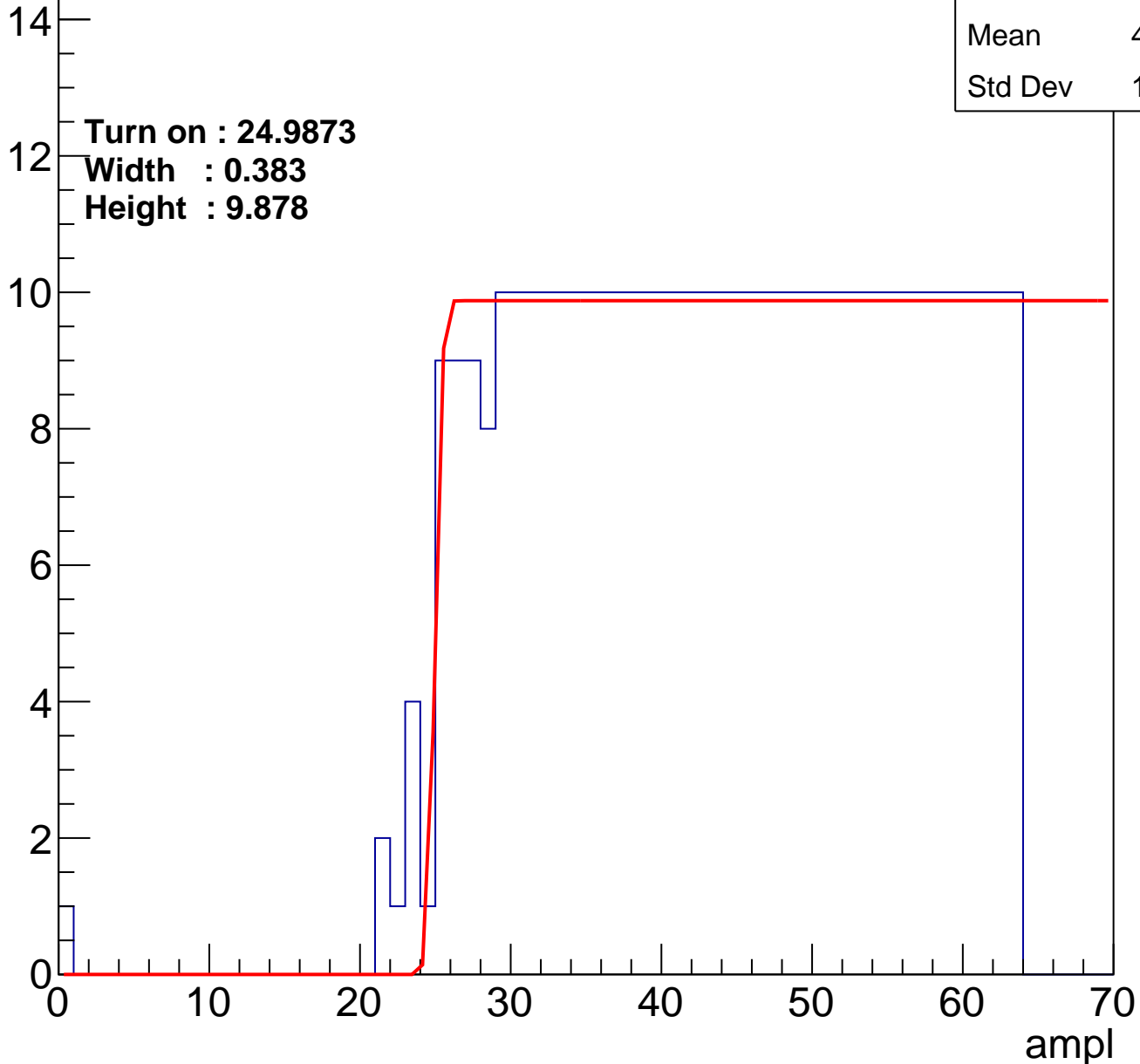
Entries	394
Mean	43.67
Std Dev	11.65

Turn on : 24.9873

Width : 0.383

Height : 9.878

Entry



B1L000S, U8-ch96

calib_packv5_042523_0143.root, FC#3, port B3

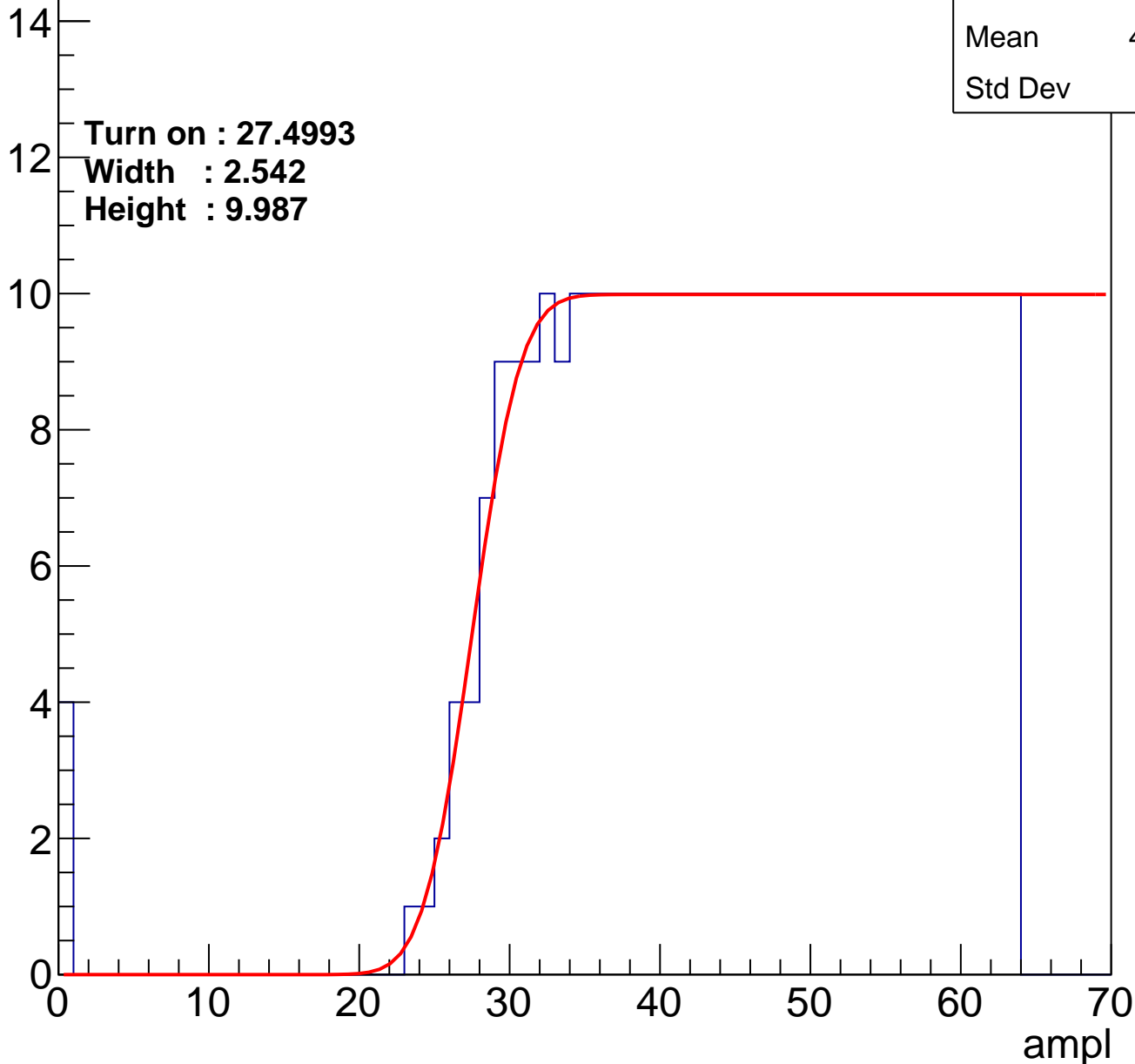
Entry

Entries	369
Mean	44.67
Std Dev	11.61

Turn on : 27.4993

Width : 2.542

Height : 9.987



B1L000S, U8-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.81
Std Dev	11.23

Turn on : 27.3919

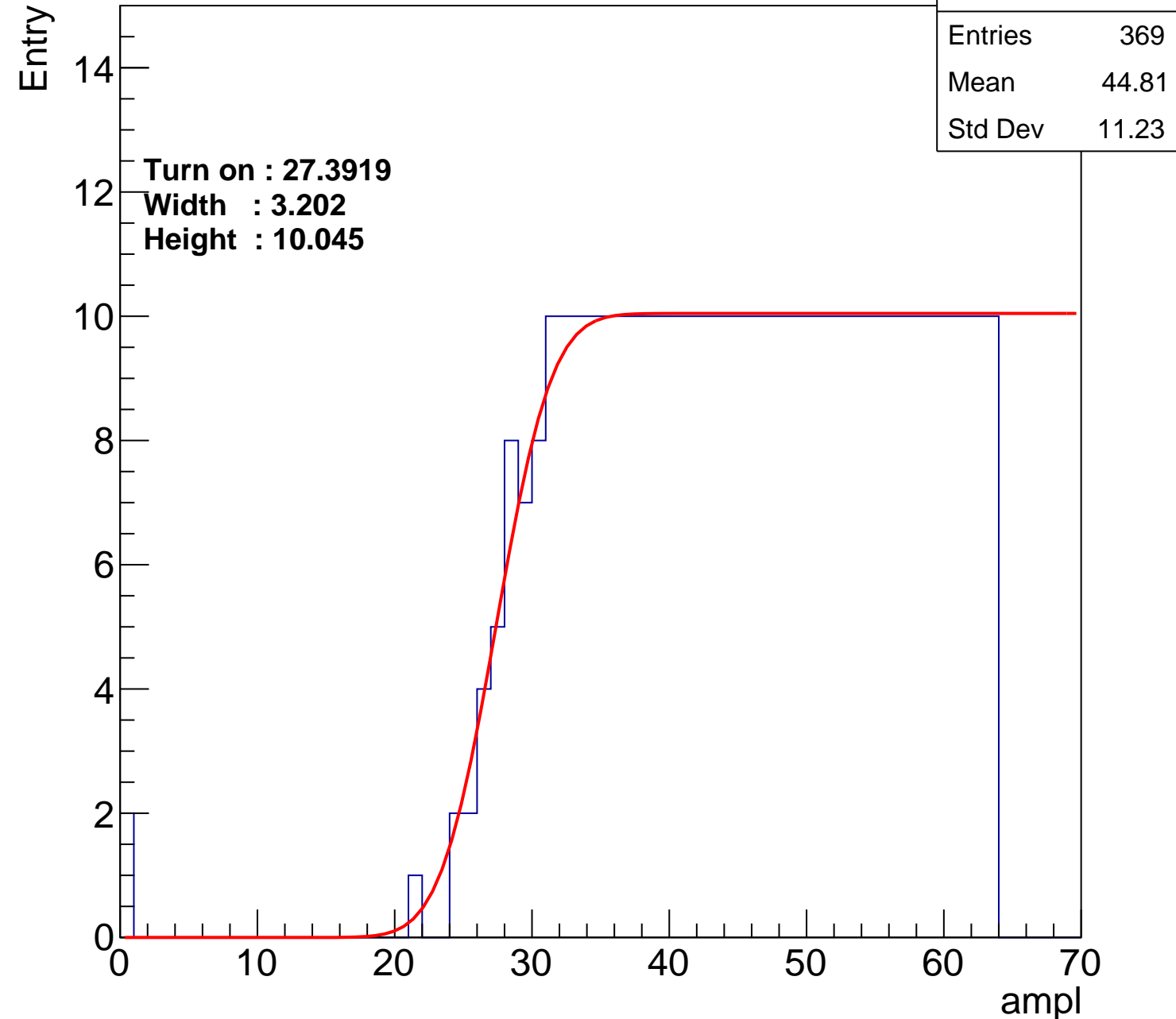
Width : 3.202

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.79
Std Dev	12.03

Turn on : 26.2395

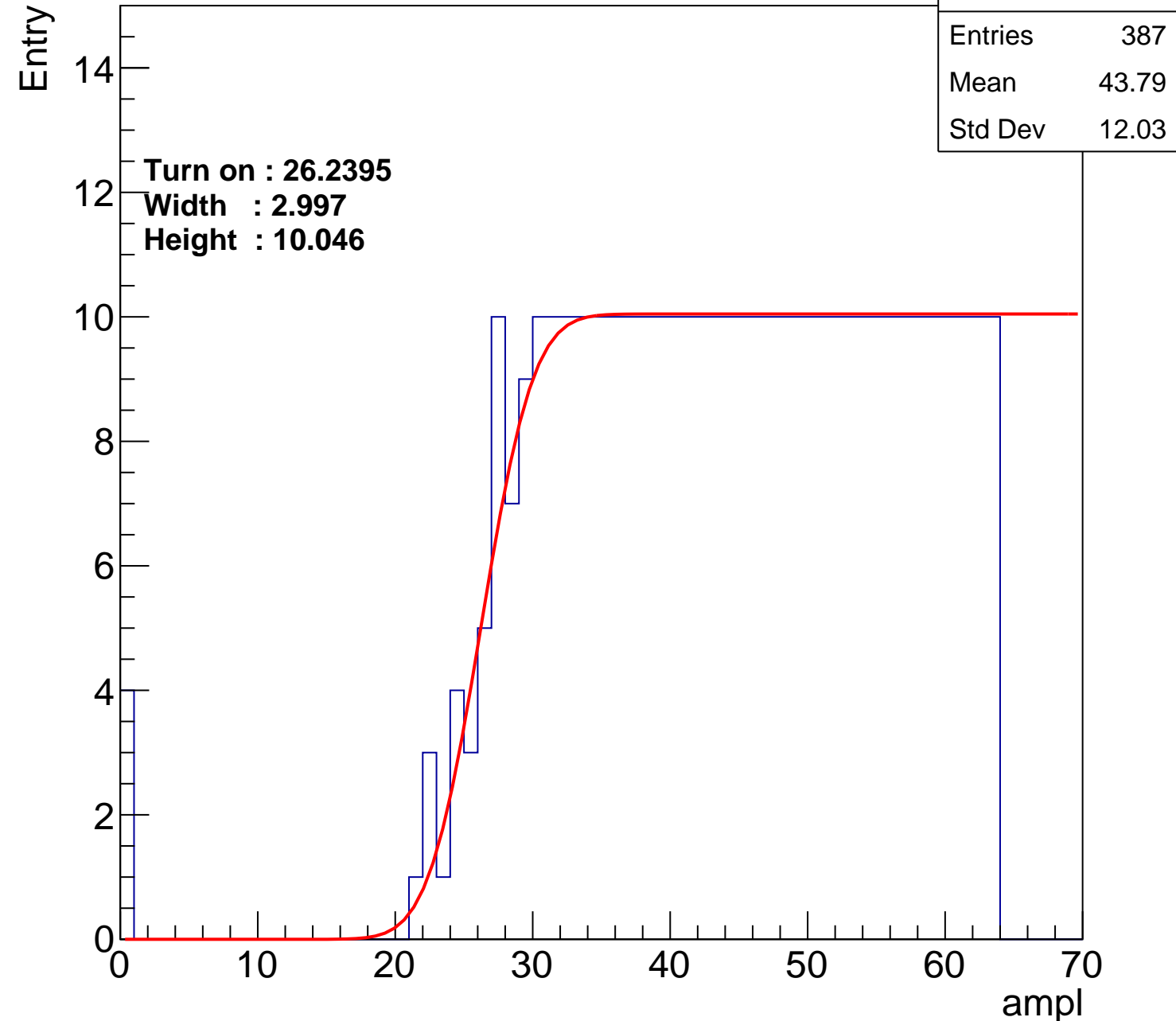
Width : 2.997

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch99

calib_packv5_042523_0143.root, FC#3, port B3

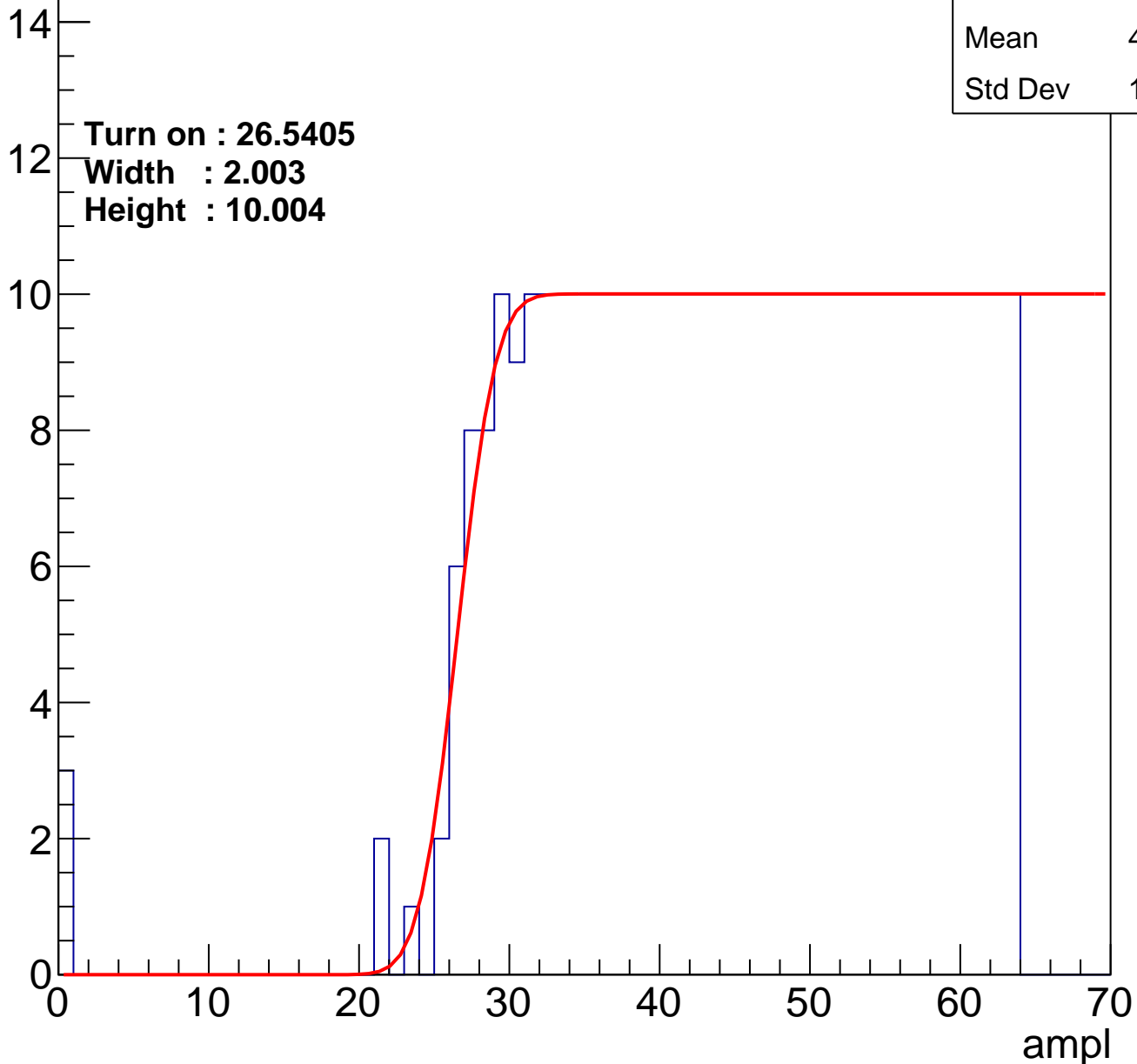
Entries	379
Mean	44.28
Std Dev	11.62

Turn on : 26.5405

Width : 2.003

Height : 10.004

Entry



B1L000S, U8-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	356
Mean	45.44
Std Dev	10.92

Turn on : 28.4282

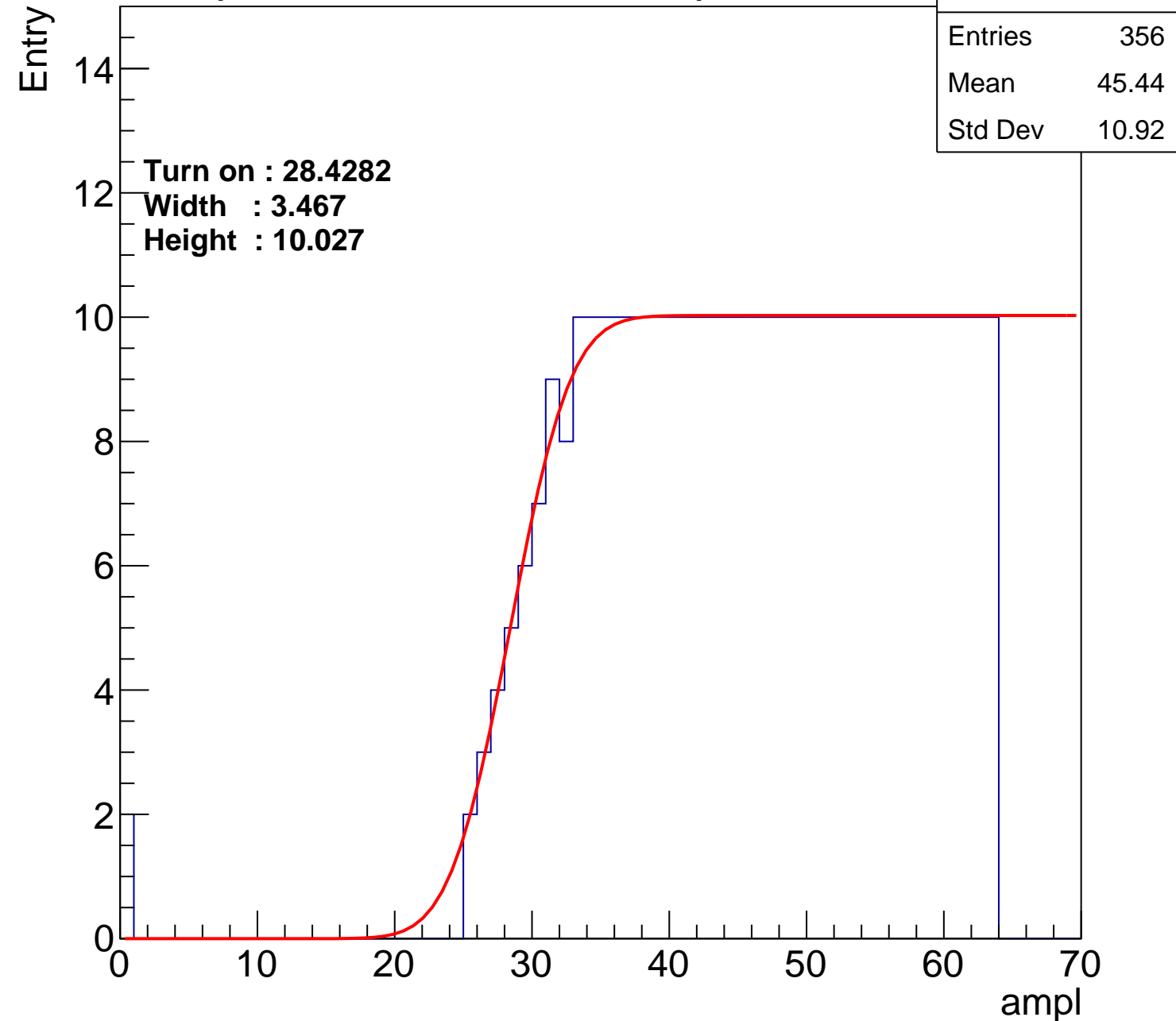
Width : 3.467

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.49
Std Dev	11.9

Turn on : 24.7132

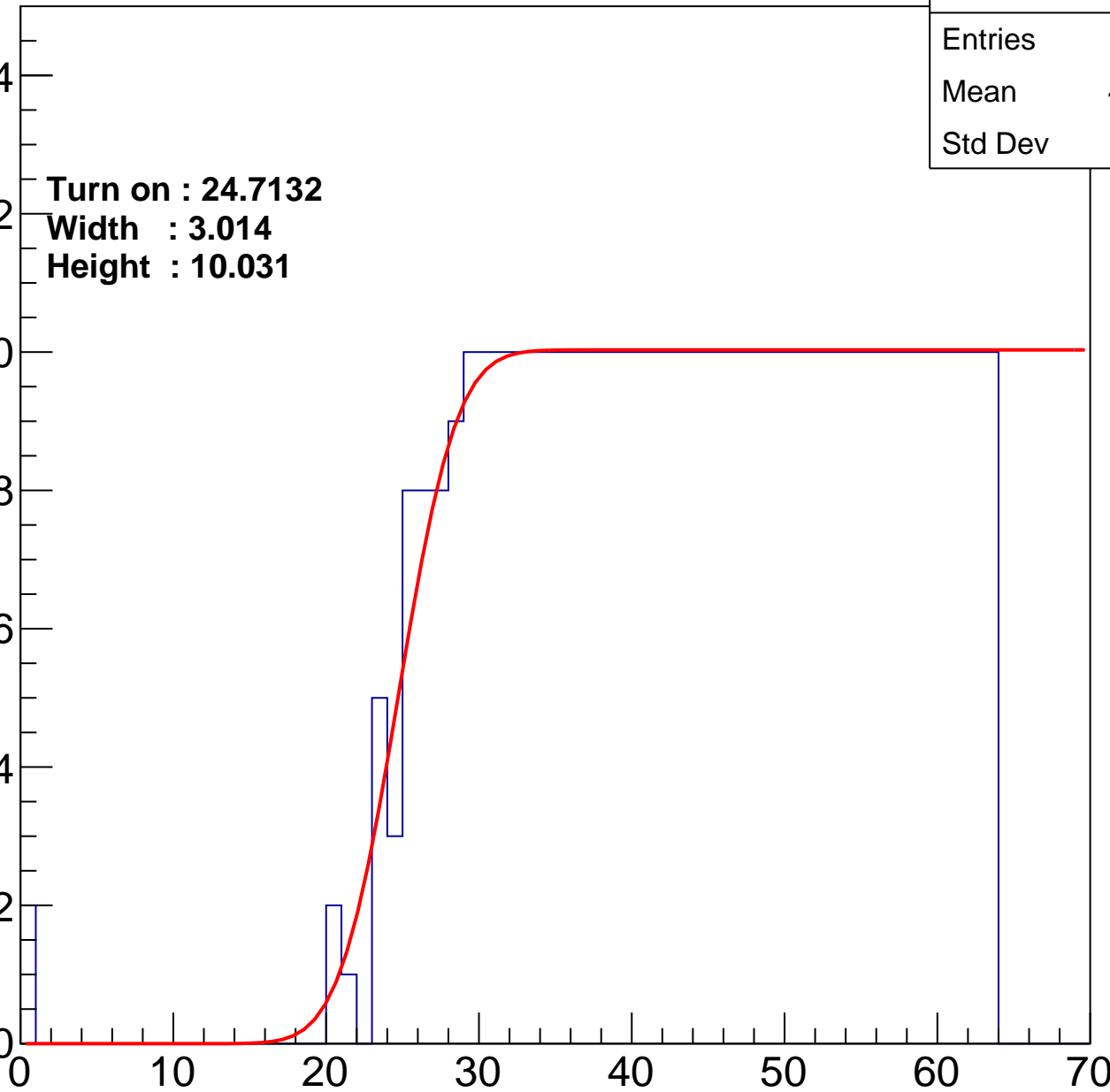
Width : 3.014

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.4
Std Dev	11.39

Turn on : 26.3184

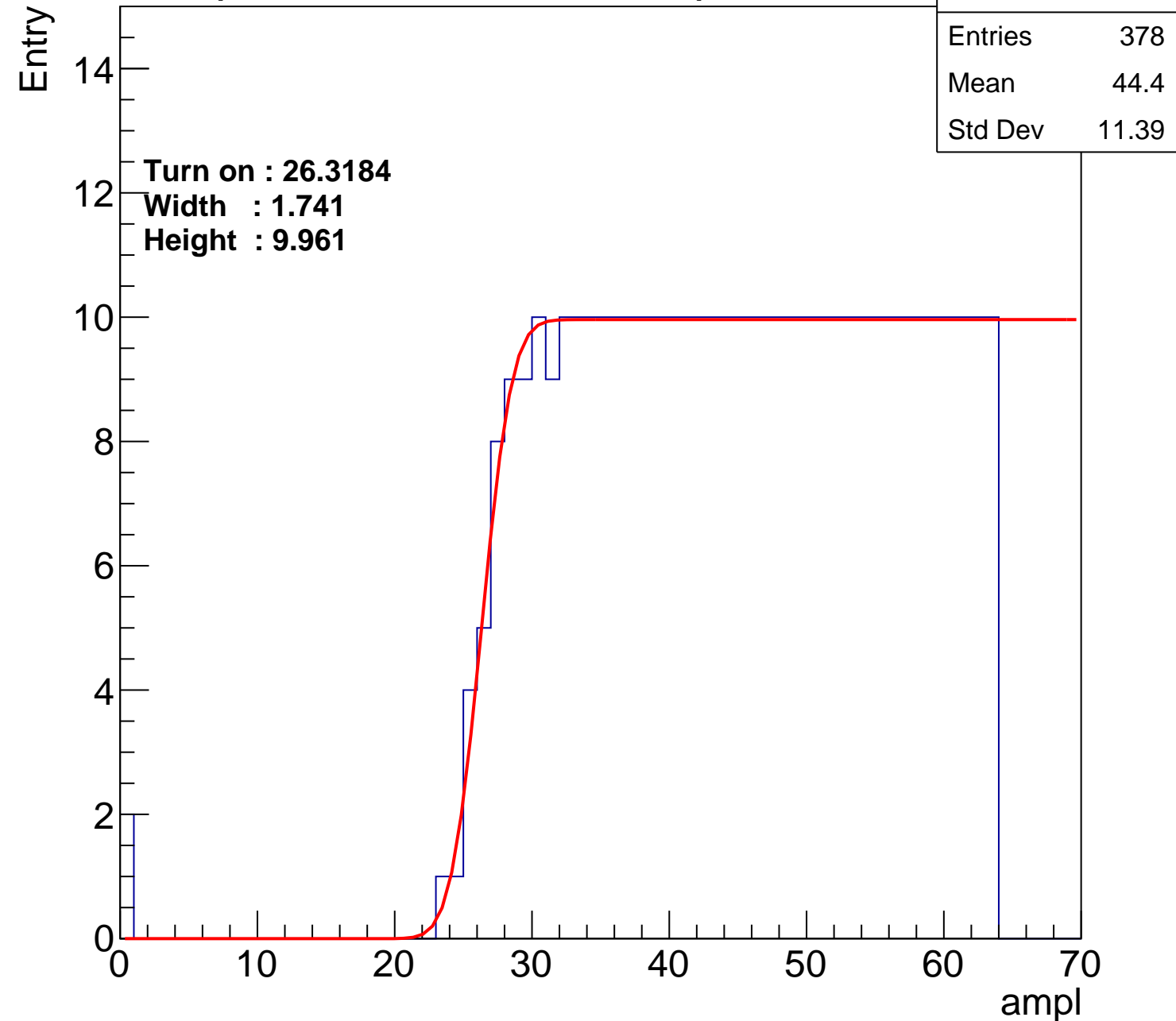
Width : 1.741

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	44.99
Std Dev	11.29

Turn on : 28.0966

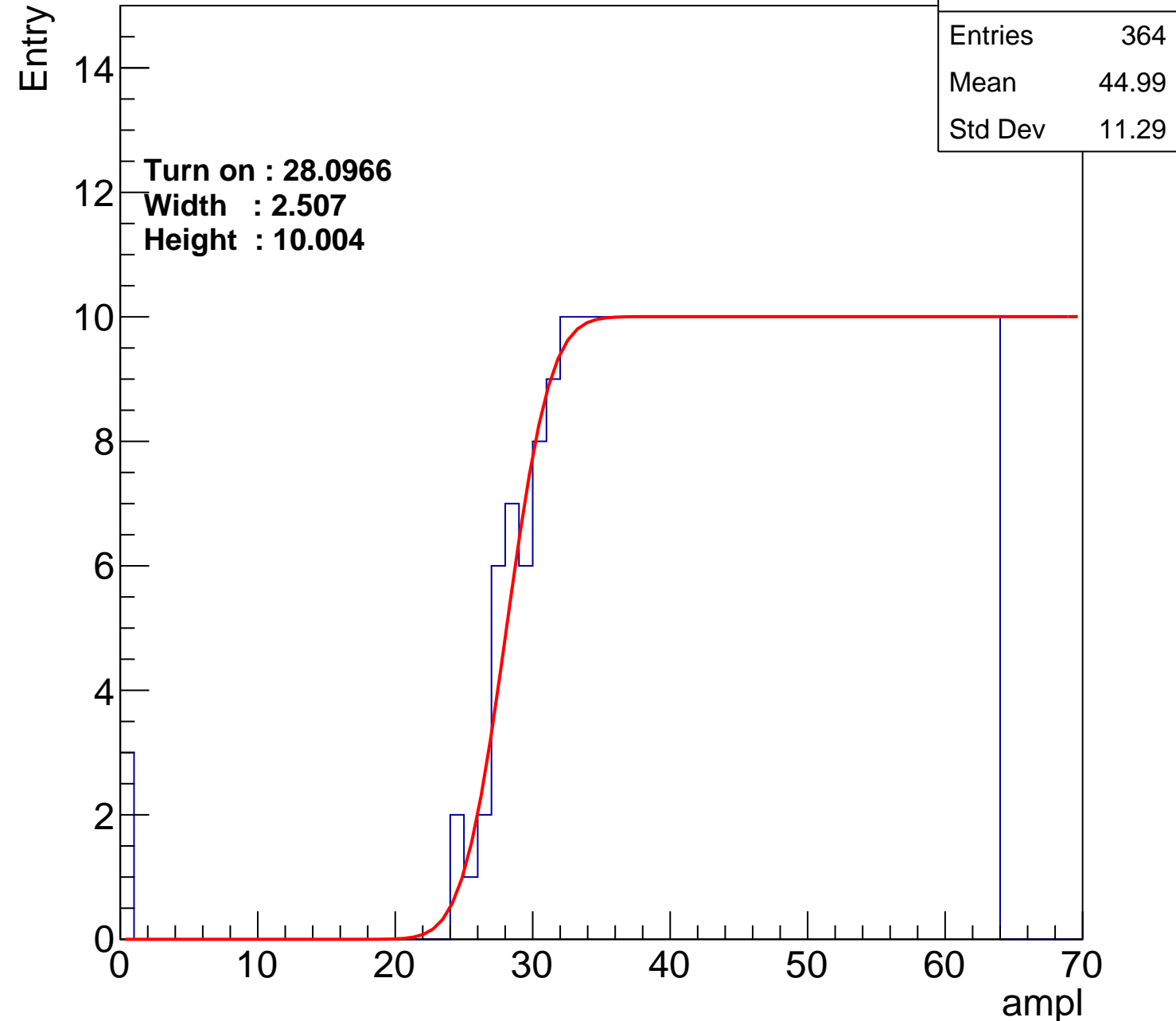
Width : 2.507

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.5635

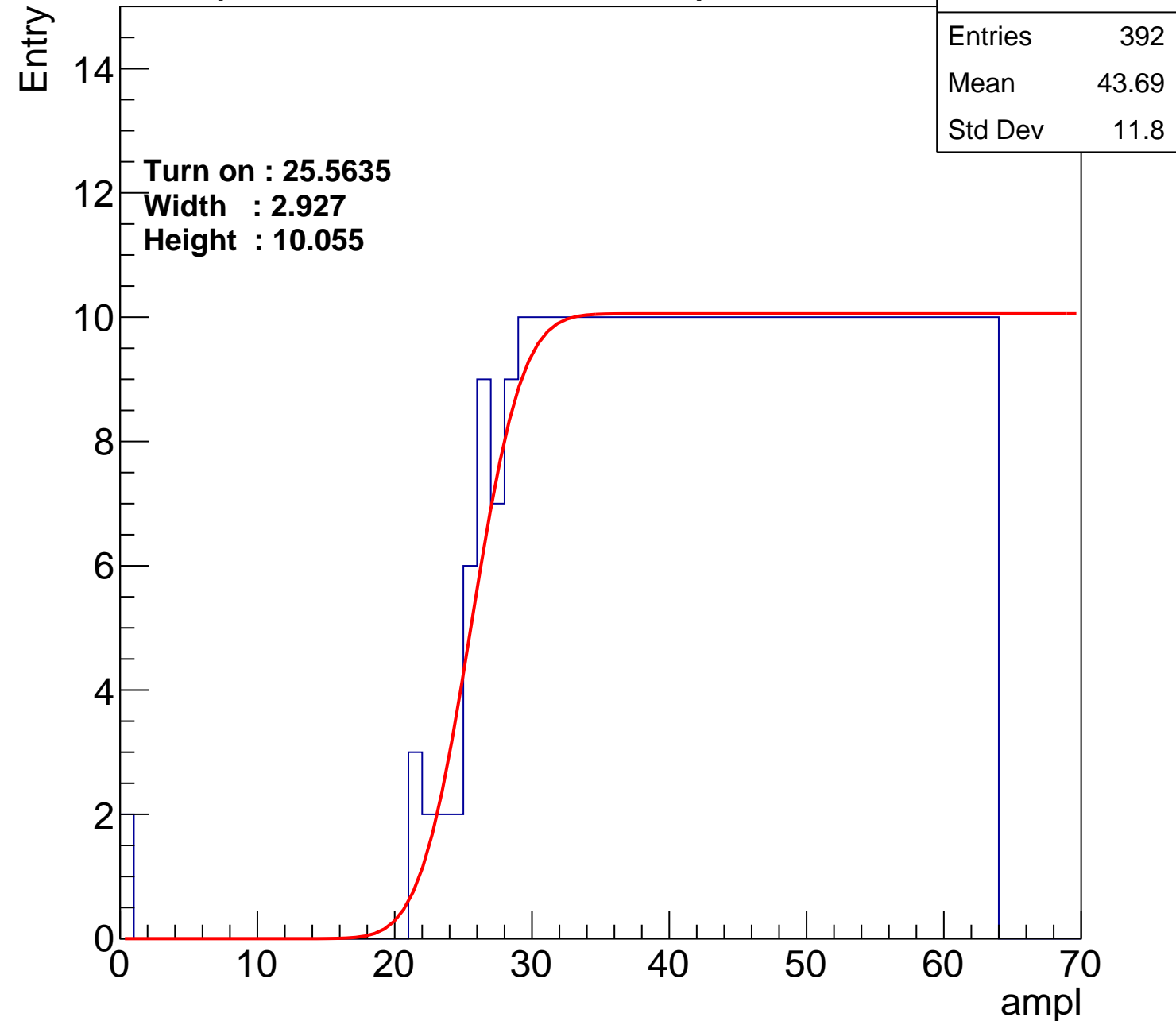
Width : 2.927

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.13
Std Dev	11.39

Turn on : 25.6767

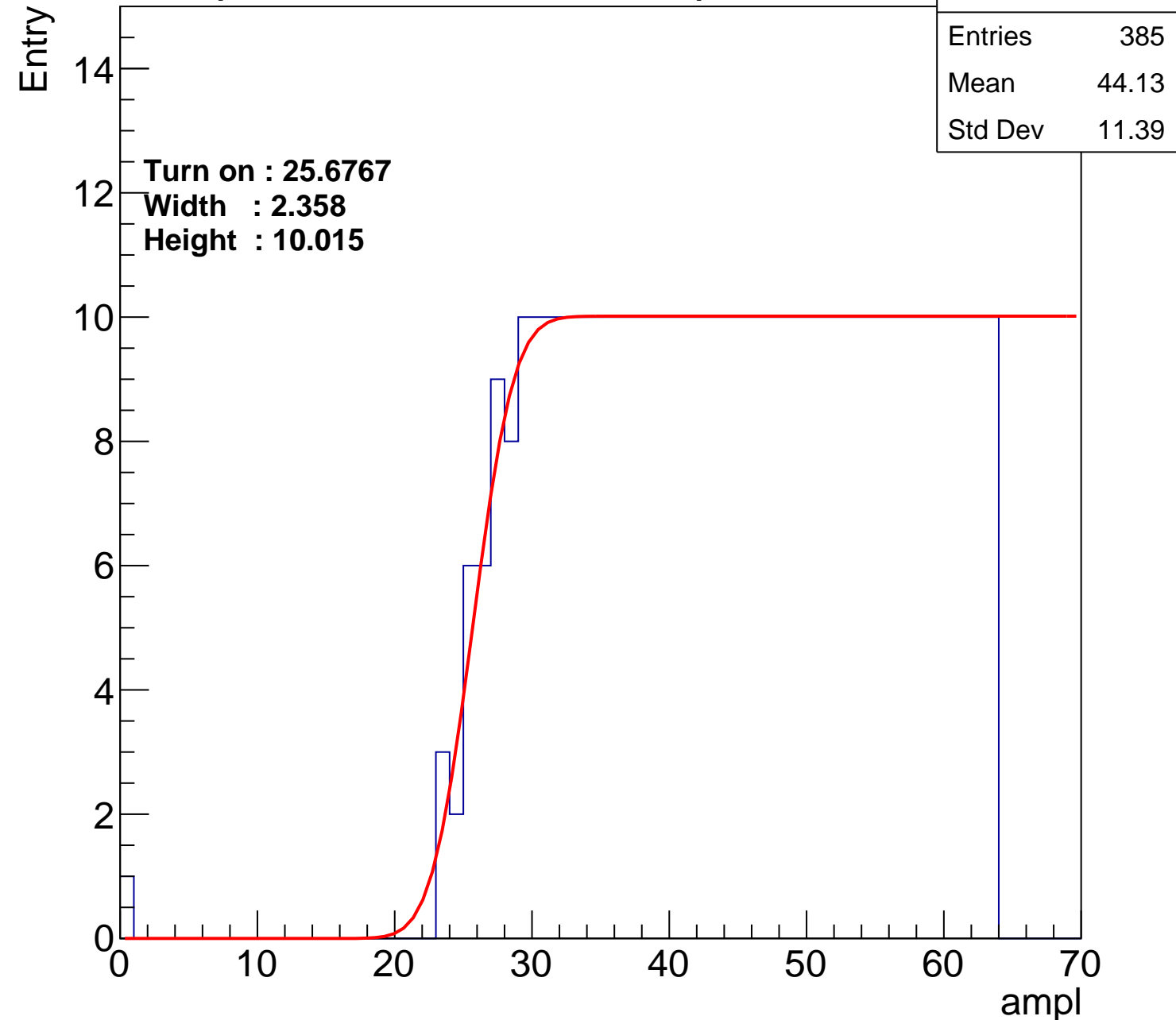
Width : 2.358

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.11
Std Dev	12.15

Turn on : 27.0194

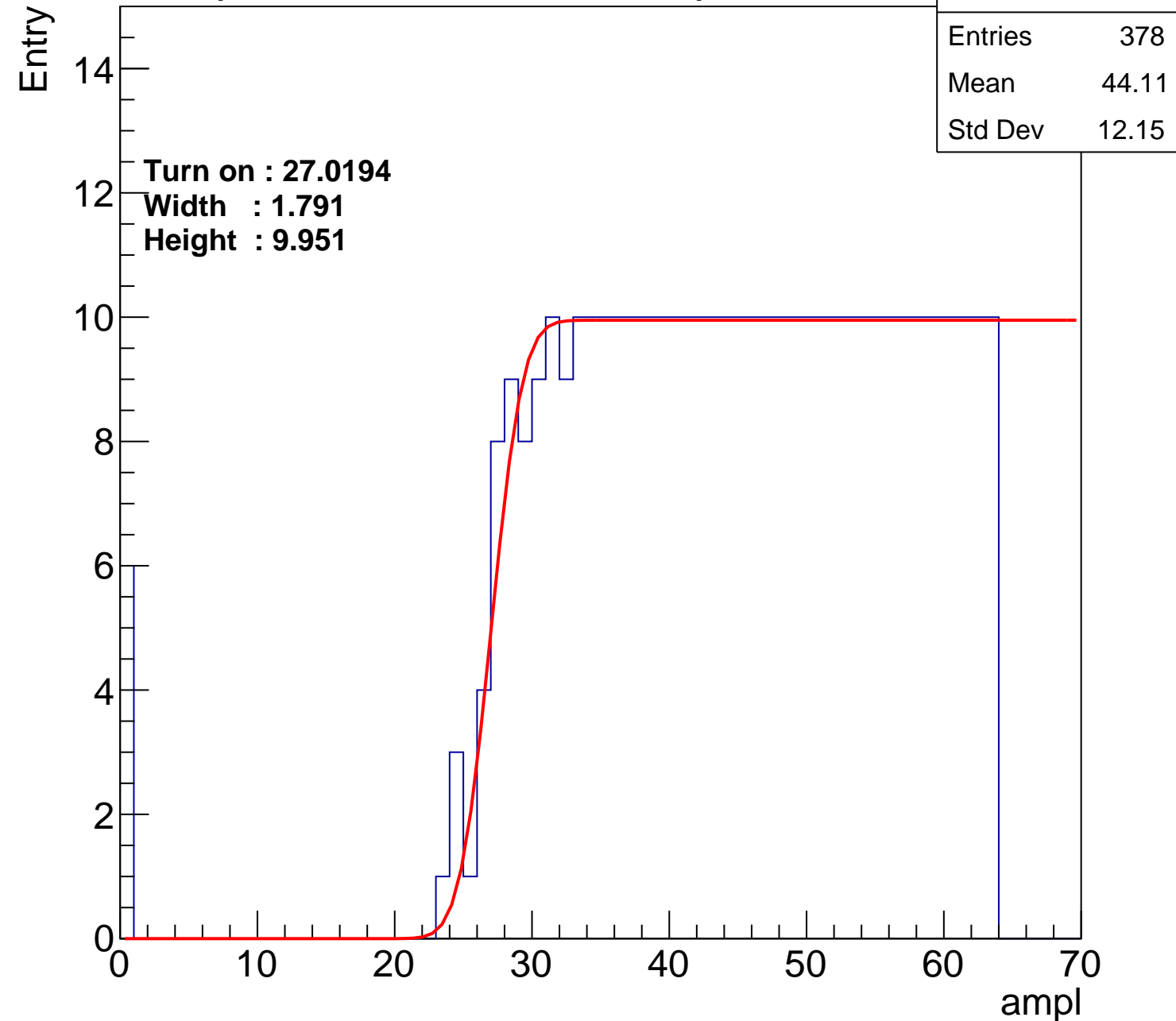
Width : 1.791

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	405
Mean	42.92
Std Dev	12.44

Turn on : 23.8993

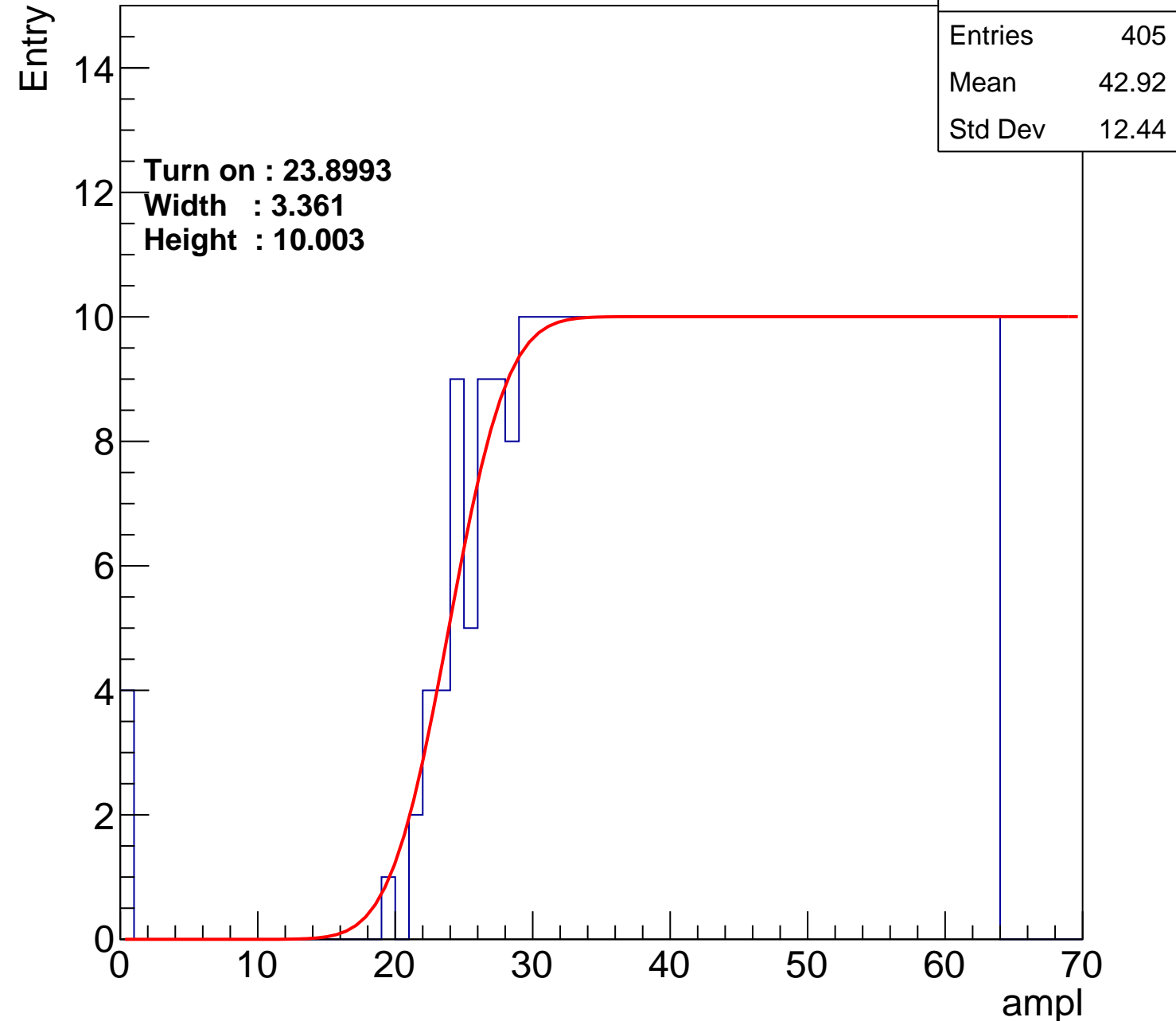
Width : 3.361

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch108

calib_packv5_042523_0143.root, FC#3, port B3

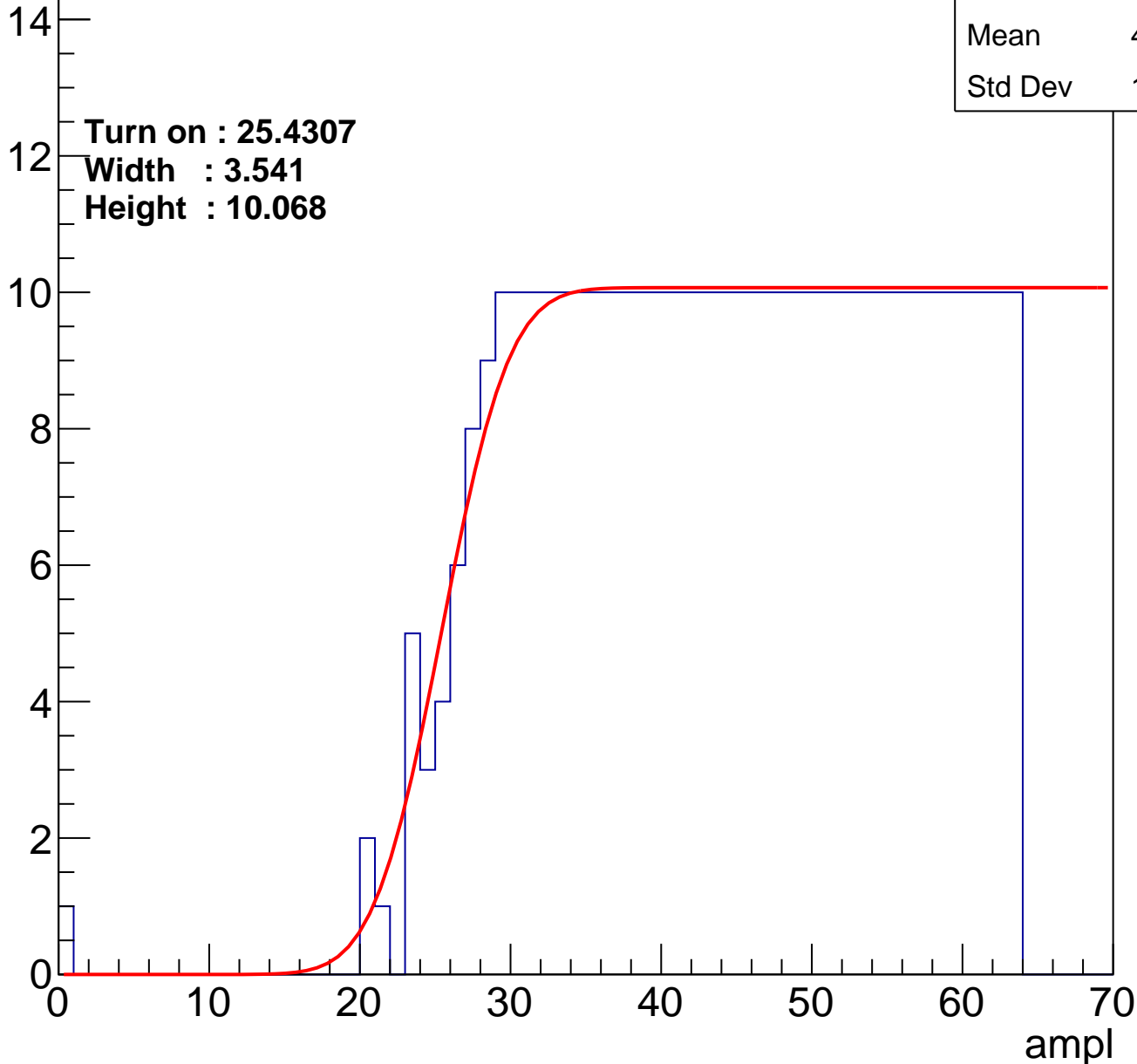
Entries	389
Mean	43.89
Std Dev	11.58

Turn on : 25.4307

Width : 3.541

Height : 10.068

Entry



B1L000S, U8-ch109

calib_packv5_042523_0143.root, FC#3, port B3

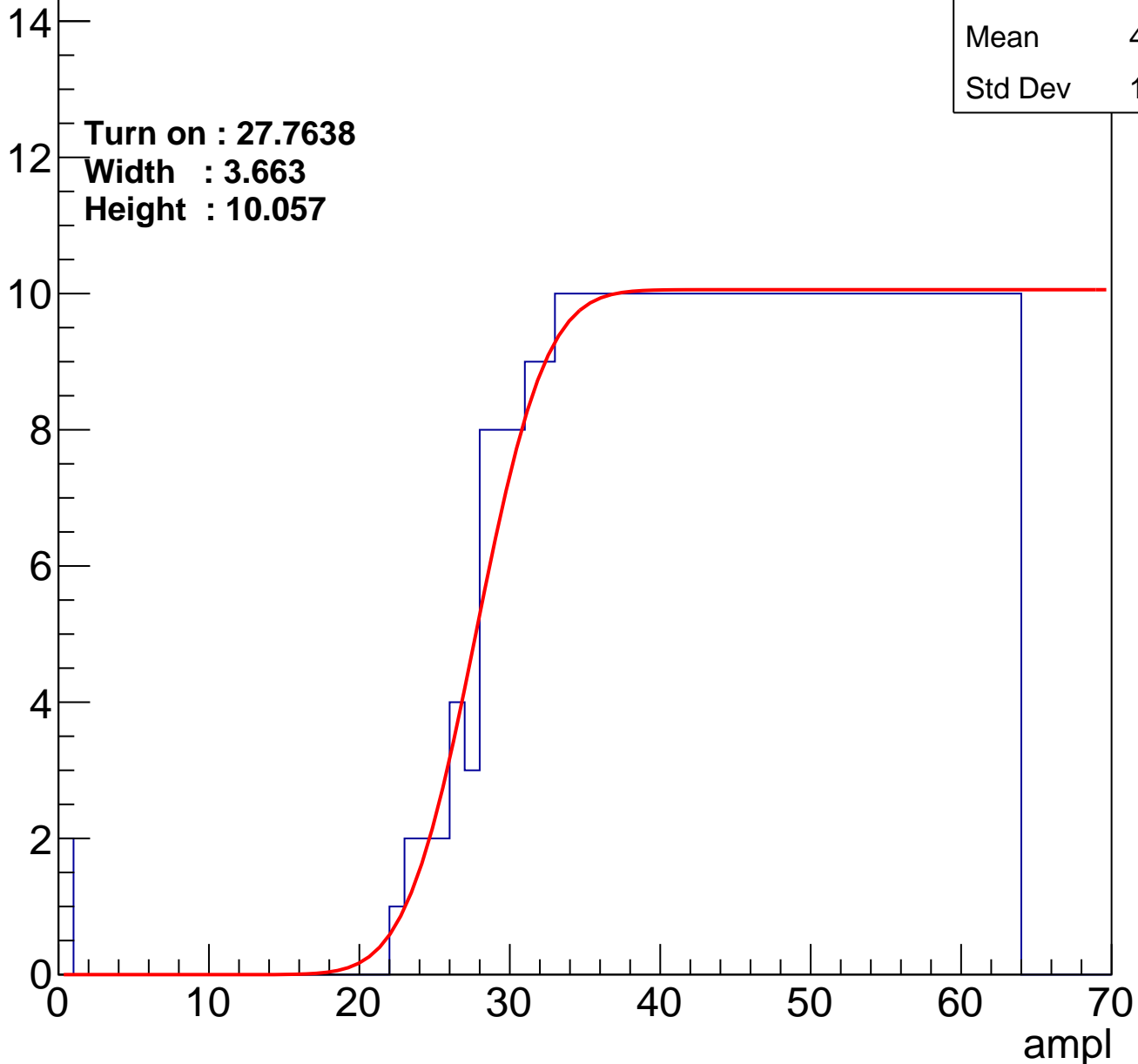
Entries	368
Mean	44.82
Std Dev	11.26

Turn on : 27.7638

Width : 3.663

Height : 10.057

Entry



B1L000S, U8-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.01
Std Dev	11.65

Turn on : 26.1795

Width : 2.923

Height : 10.033

Entry

14

12

10

8

6

4

2

0

0

10

20

30

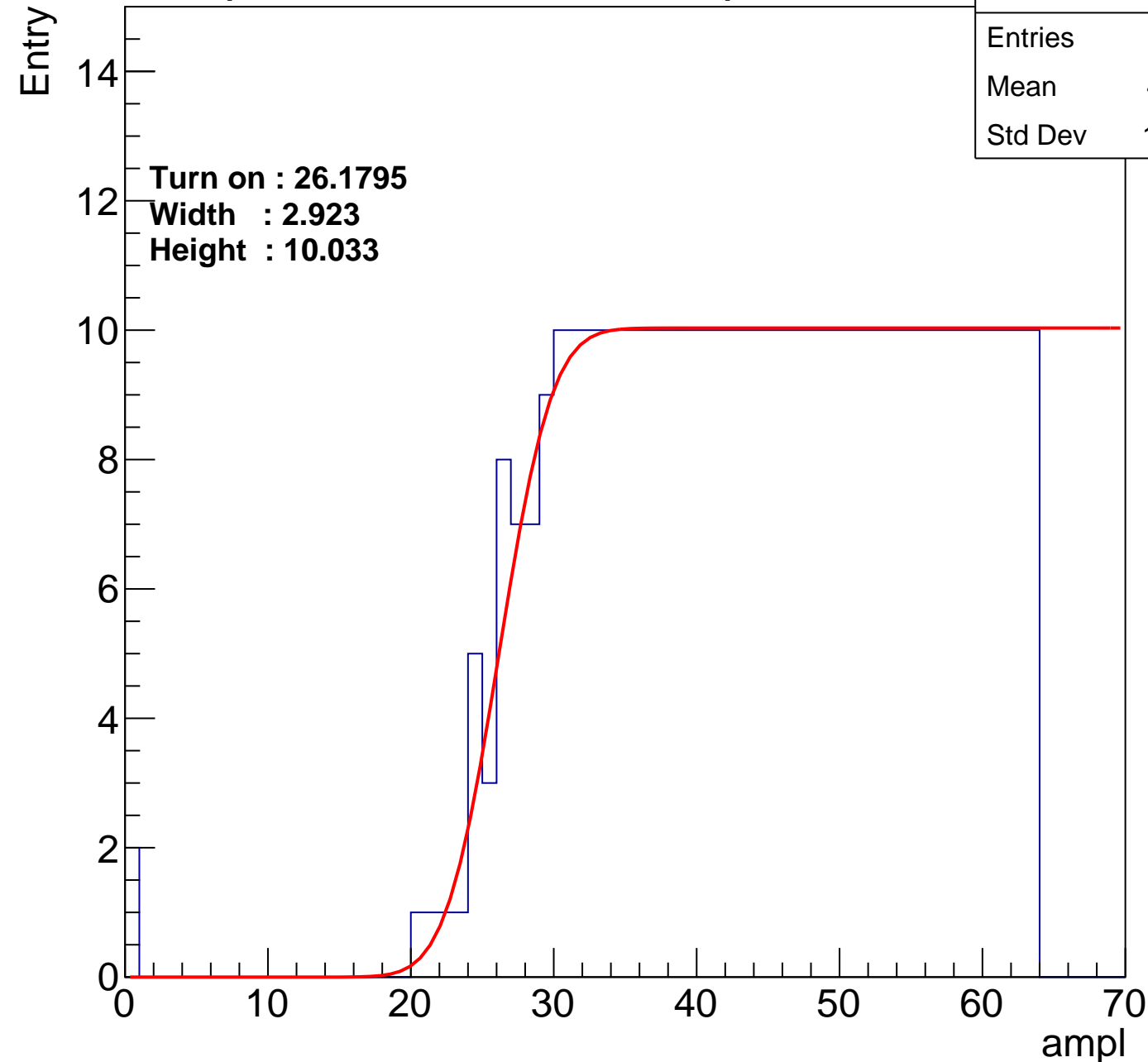
40

50

60

70

ampl



B1L000S, U8-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.36
Std Dev	11.73

Turn on : 26.6779

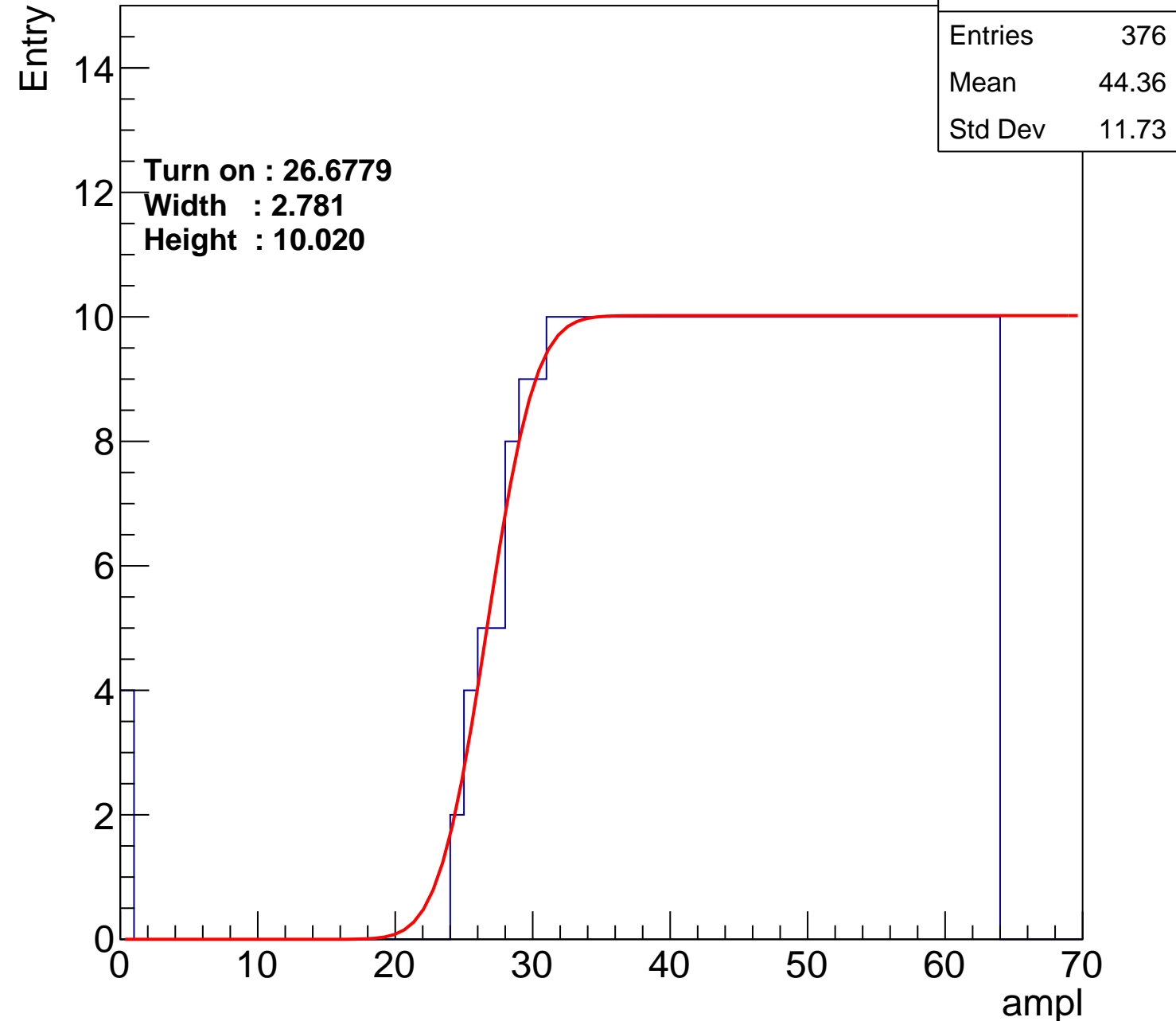
Width : 2.781

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	44.98
Std Dev	11.36

Turn on : 28.4492

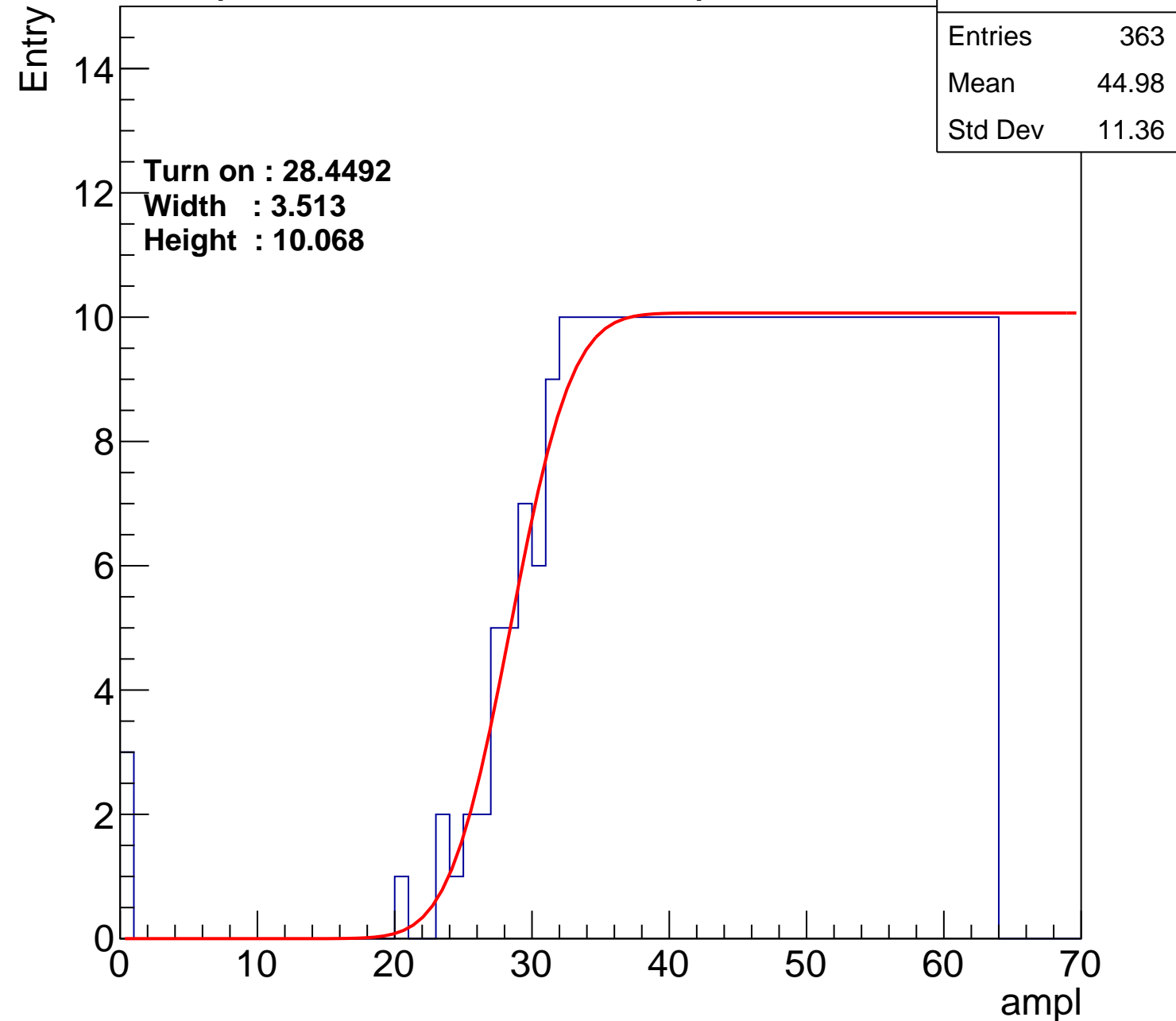
Width : 3.513

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.94
Std Dev	11.72

Turn on : 25.9057

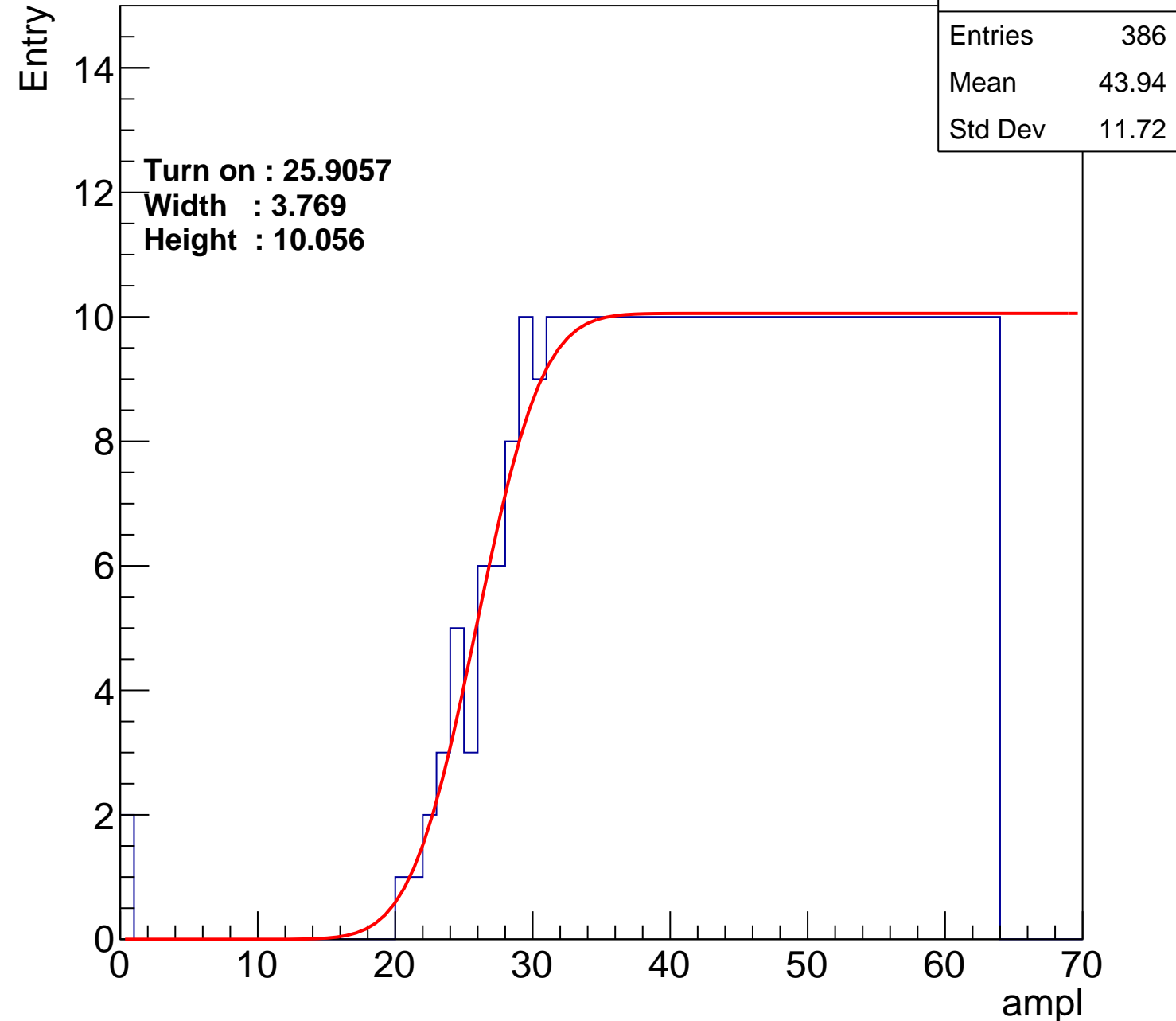
Width : 3.769

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	45.07
Std Dev	10.88

Turn on : 27.3888

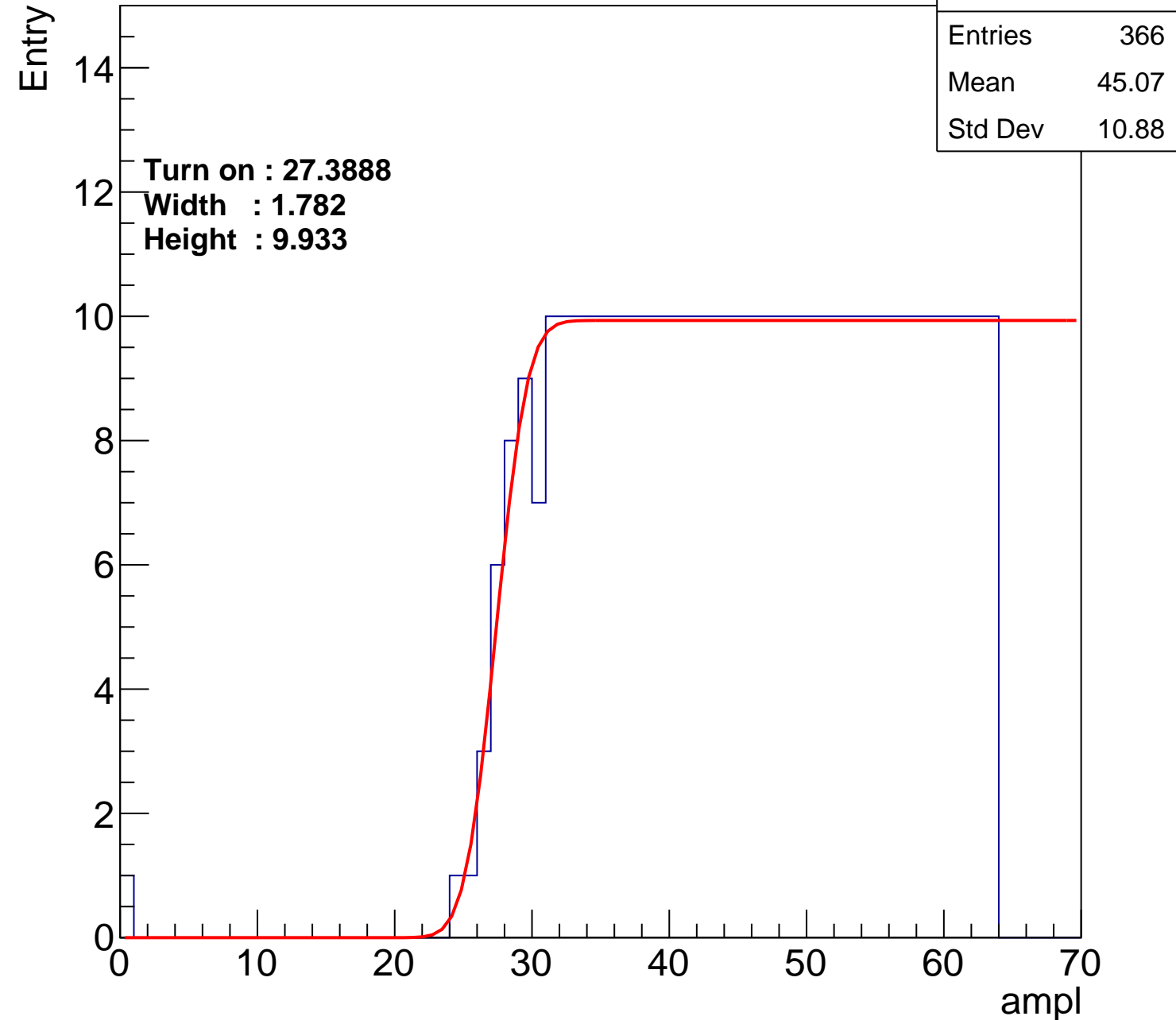
Width : 1.782

Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.04
Std Dev	11.27

Turn on : 28.0228

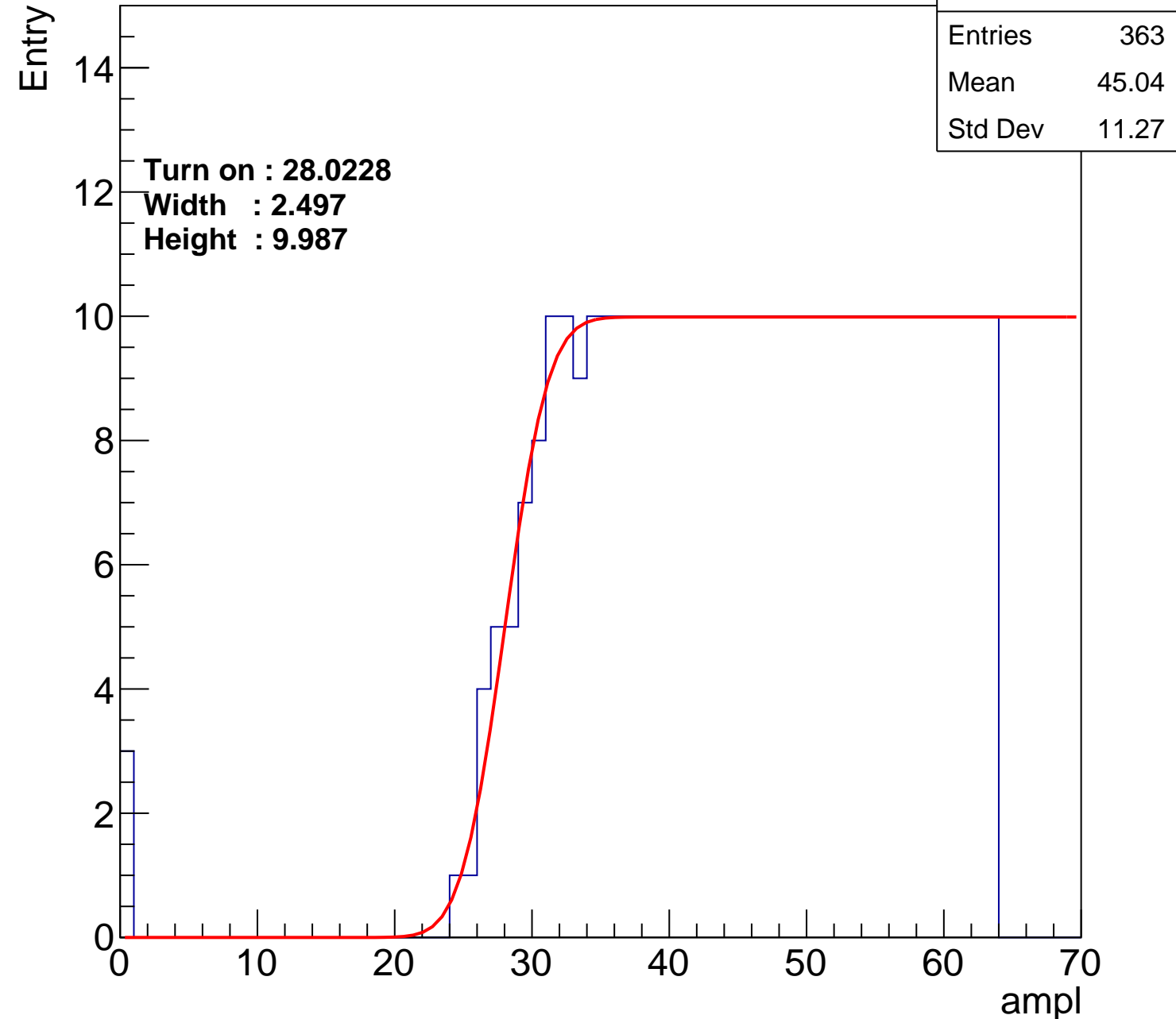
Width : 2.497

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.63
Std Dev	11.83

Turn on : 25.4335

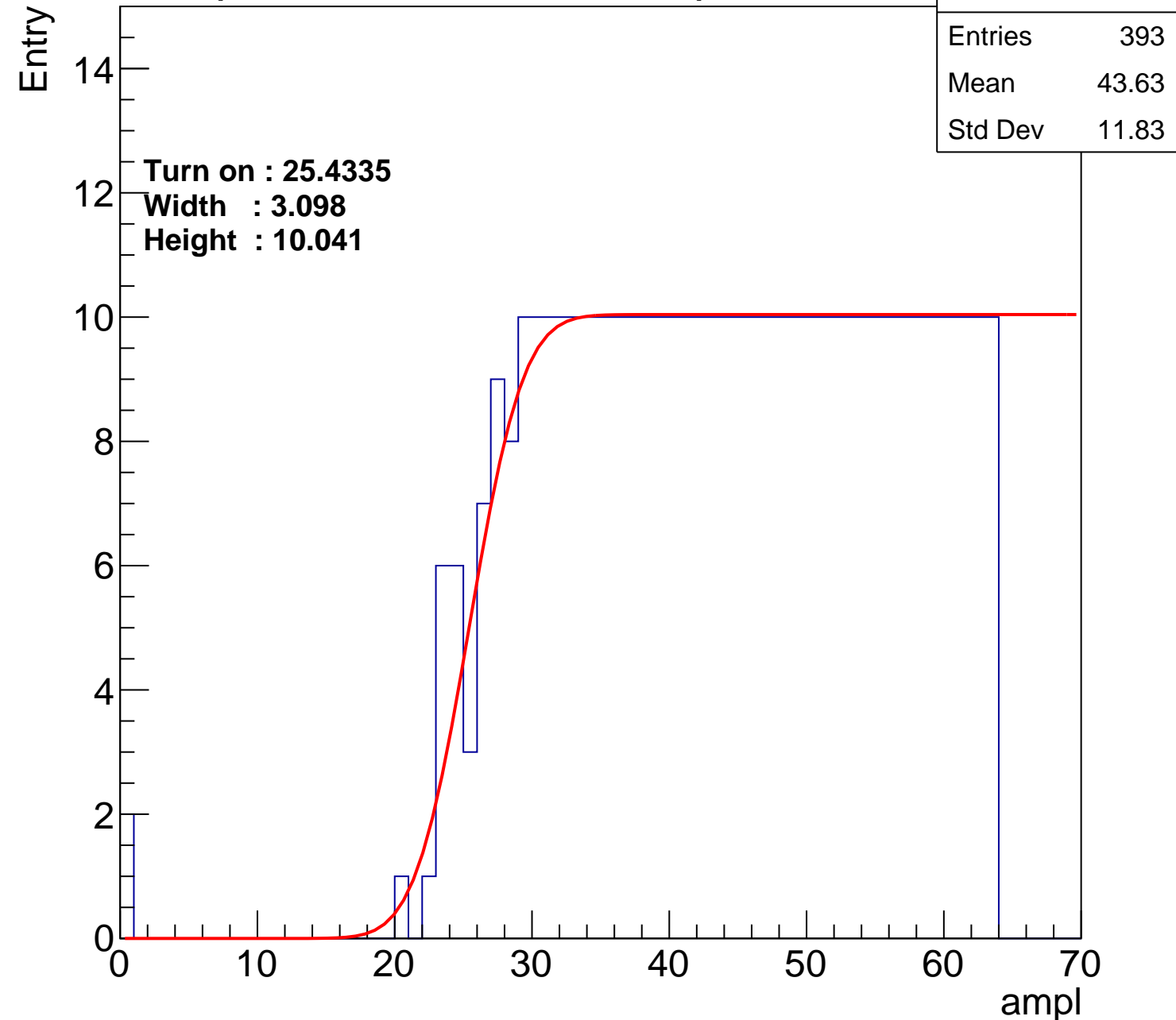
Width : 3.098

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.98
Std Dev	11.77

Turn on : 25.9382

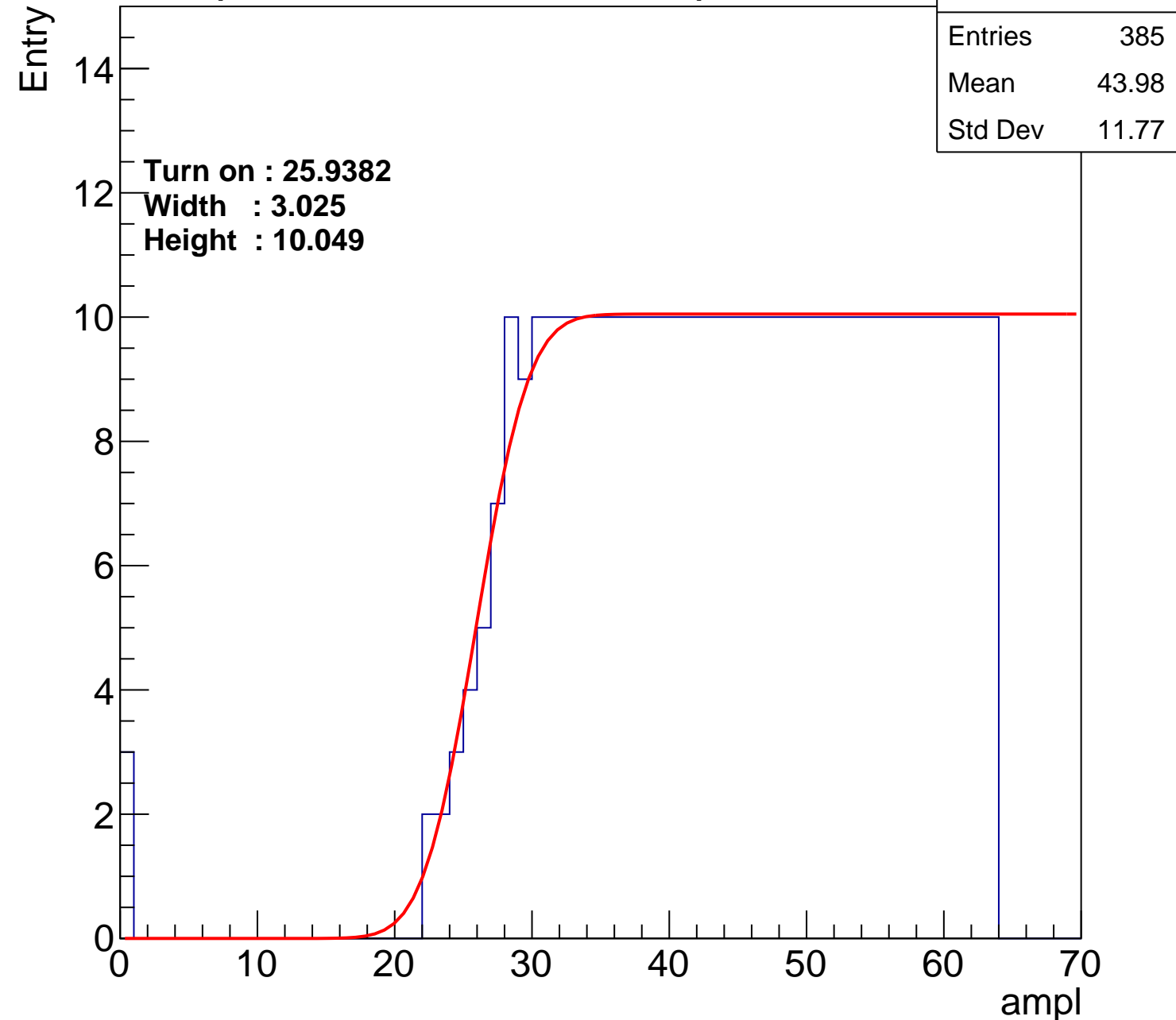
Width : 3.025

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.4877

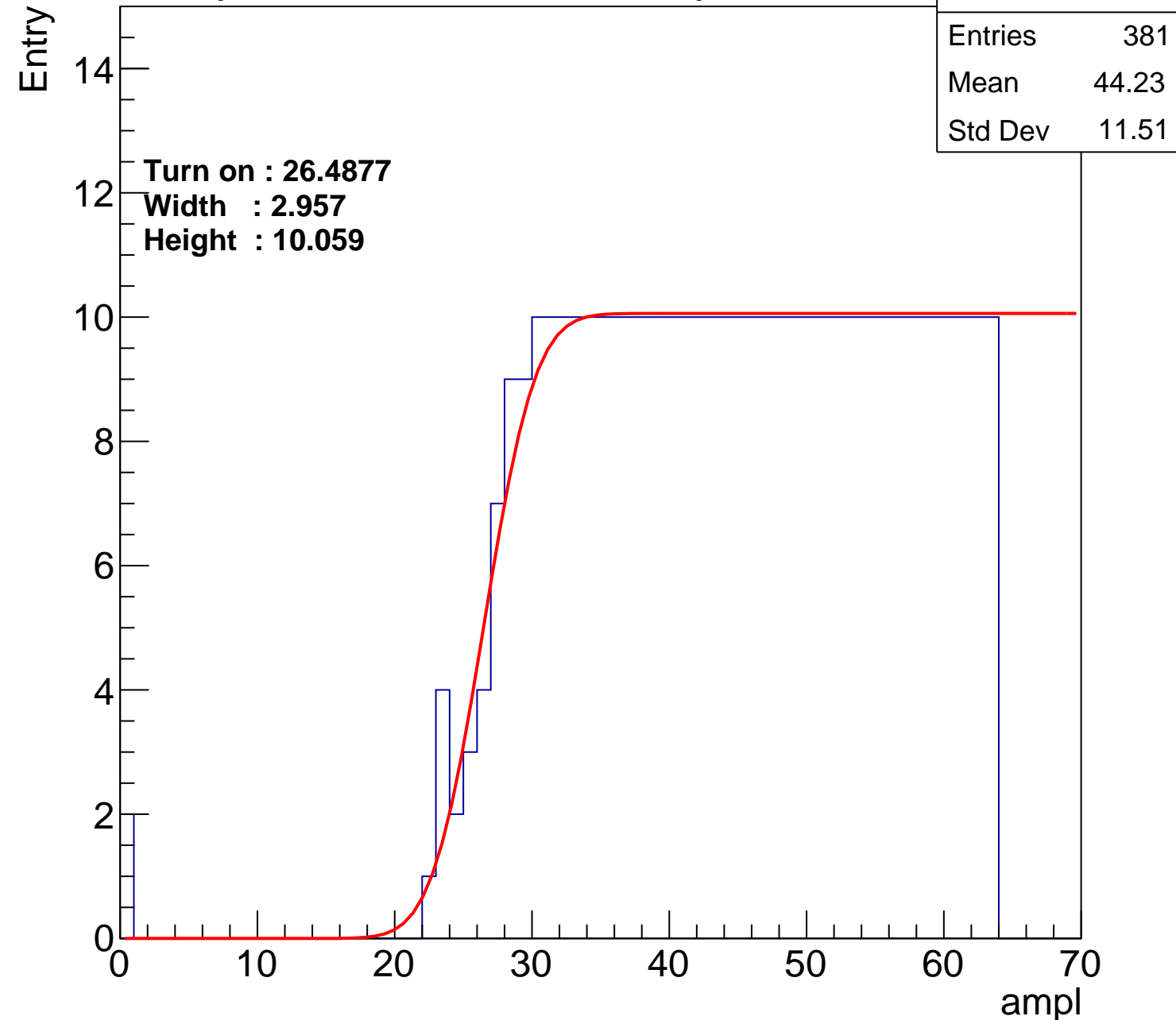
Width : 2.957

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch119

calib_packv5_042523_0143.root, FC#3, port B3

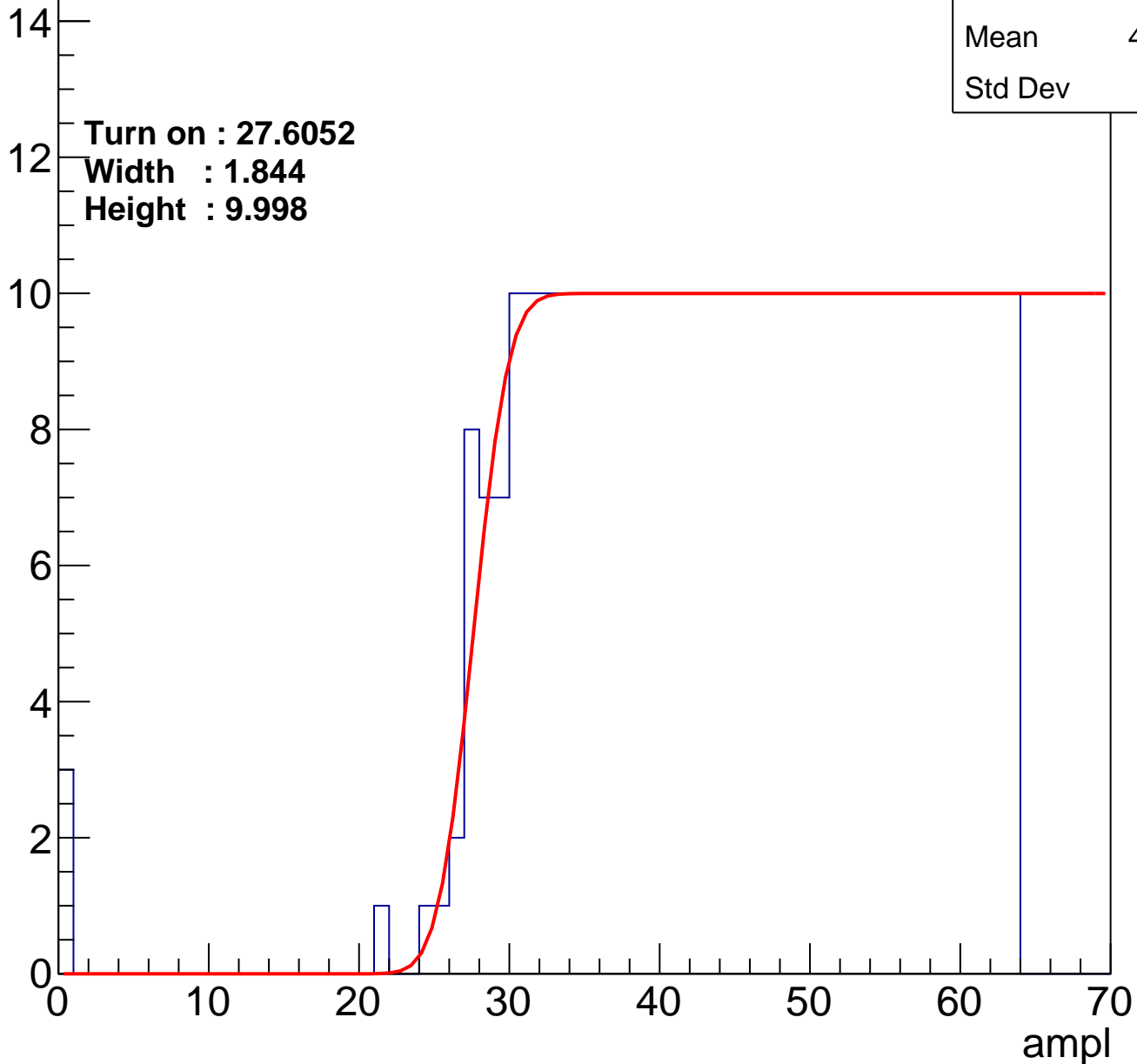
Entries	370
Mean	44.72
Std Dev	11.4

Turn on : 27.6052

Width : 1.844

Height : 9.998

Entry



B1L000S, U8-ch120

calib_packv5_042523_0143.root, FC#3, port B3

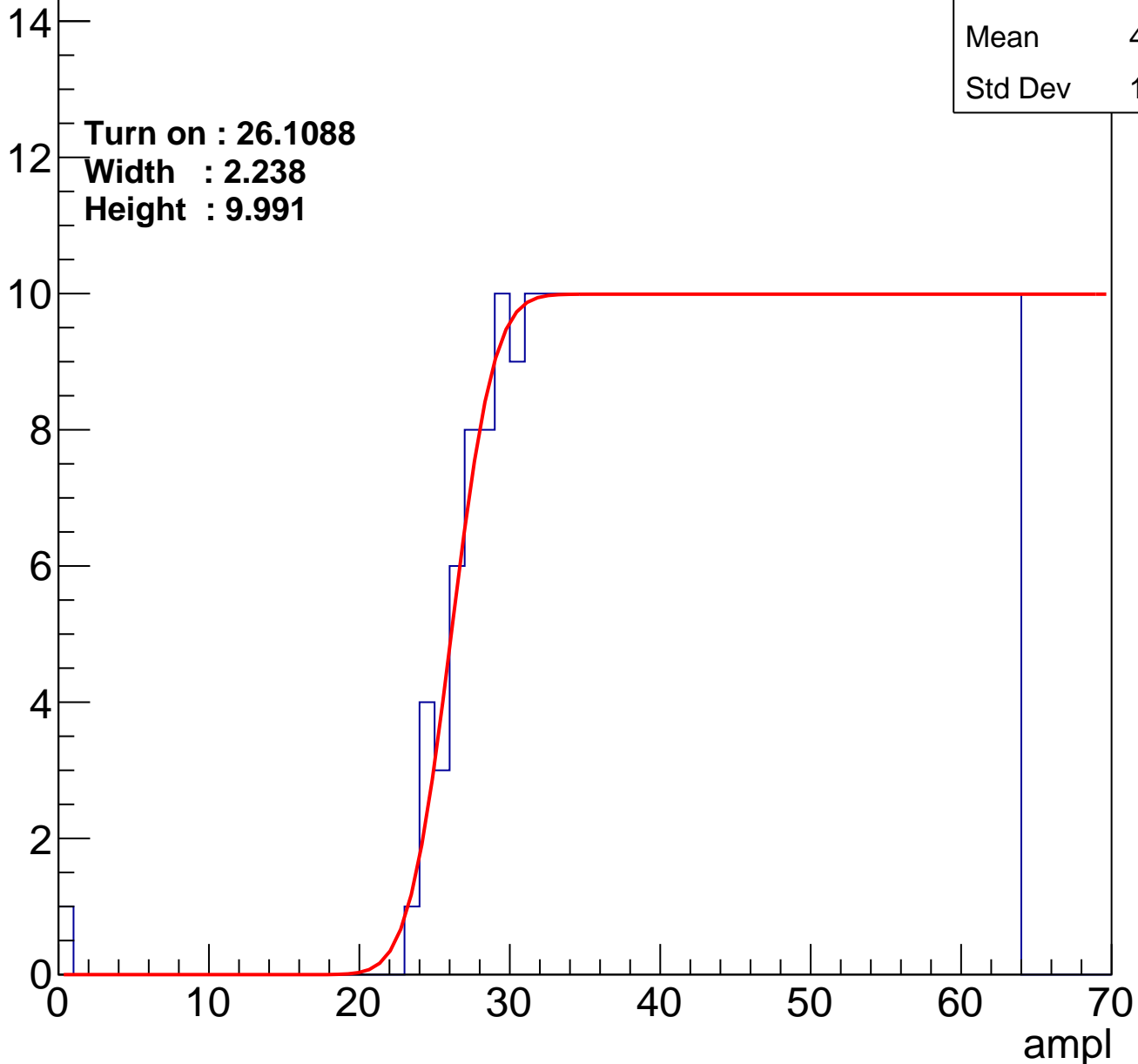
Entries	380
Mean	44.37
Std Dev	11.26

Turn on : 26.1088

Width : 2.238

Height : 9.991

Entry



B1L000S, U8-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.4
Std Dev	12.07

Turn on : 24.8990

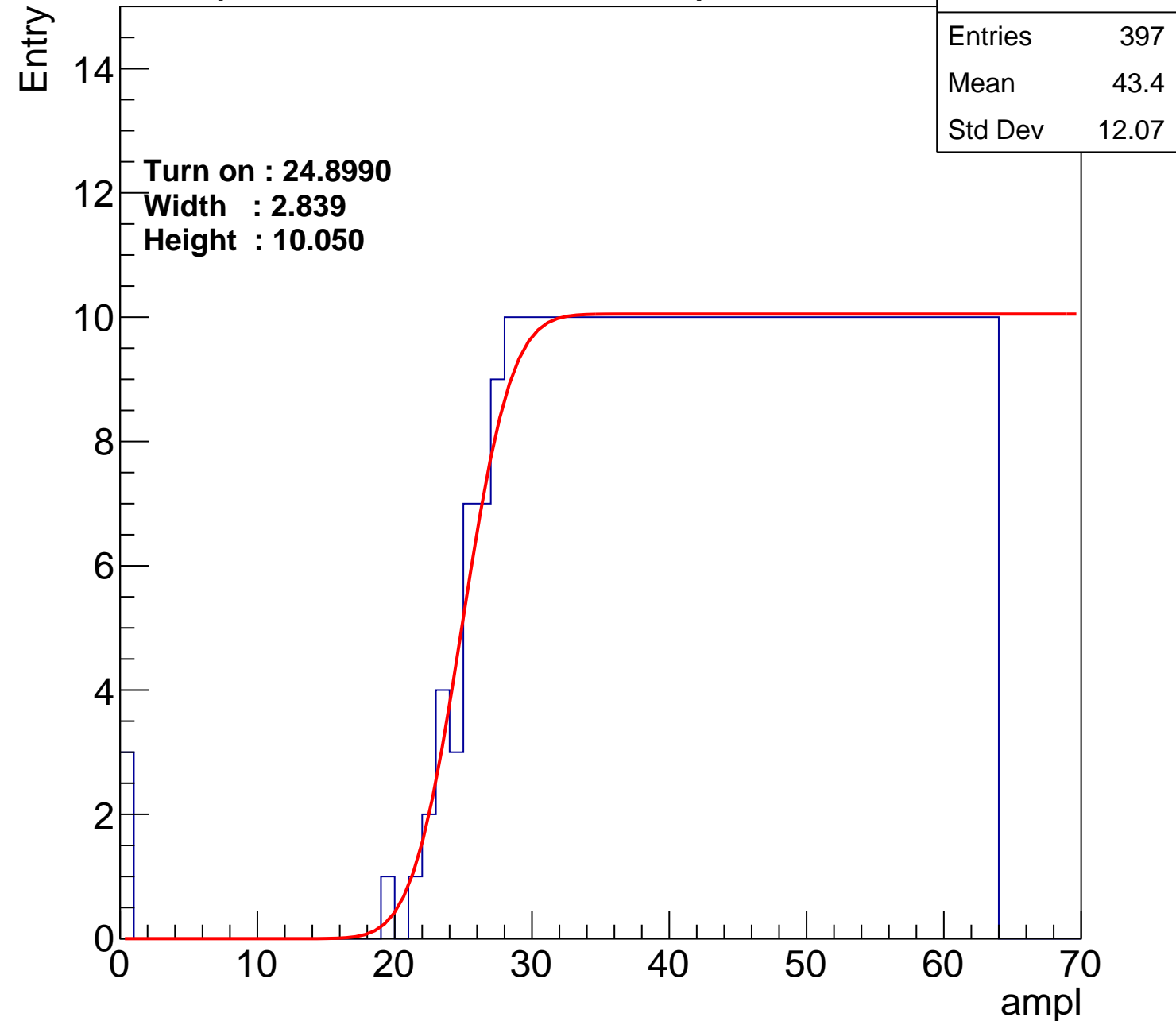
Width : 2.839

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch122

calib_packv5_042523_0143.root, FC#3, port B3

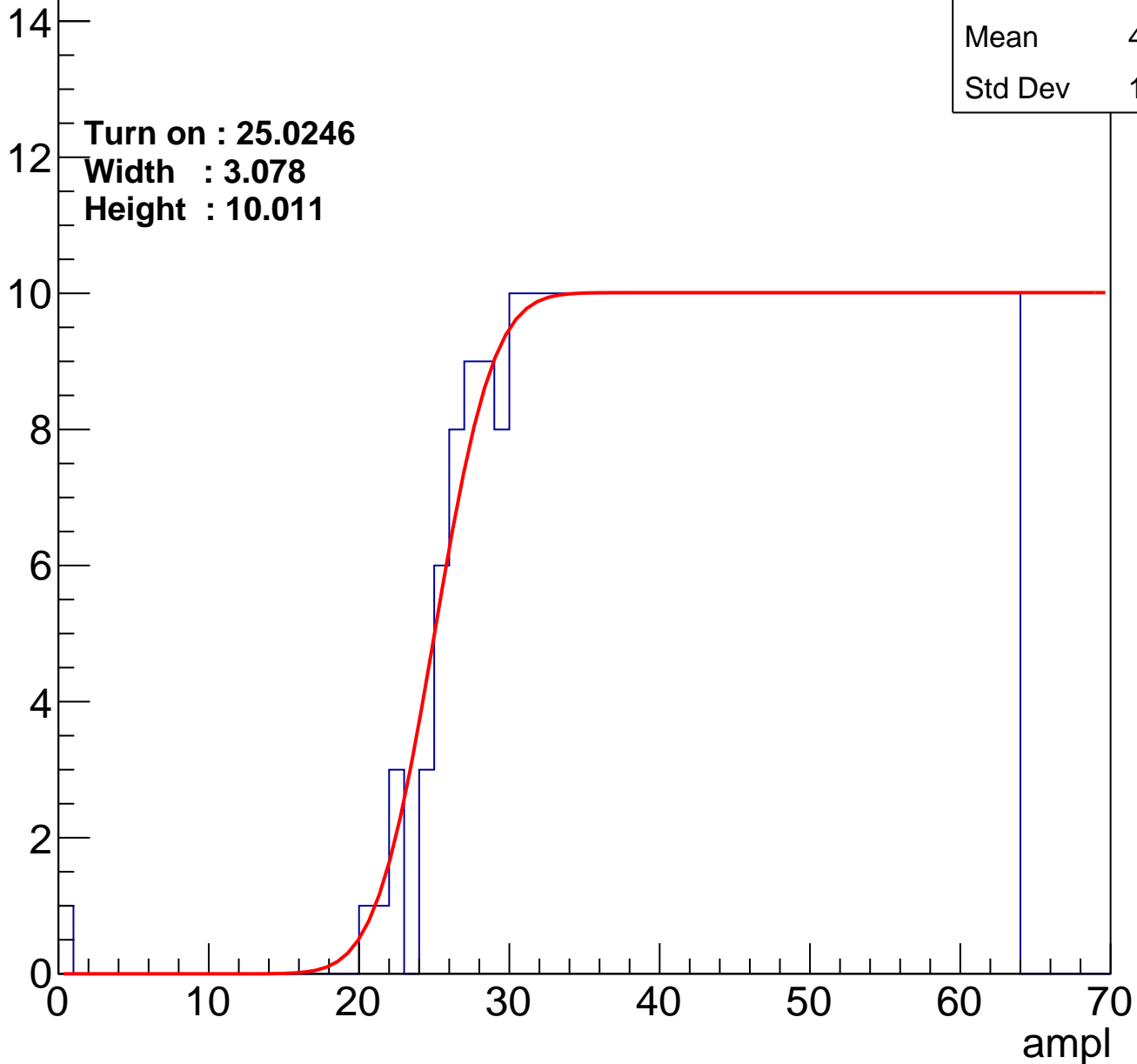
Entries	389
Mean	43.89
Std Dev	11.56

Turn on : 25.0246

Width : 3.078

Height : 10.011

Entry



B1L000S, U8-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.79
Std Dev	11.25

Turn on : 28.1685

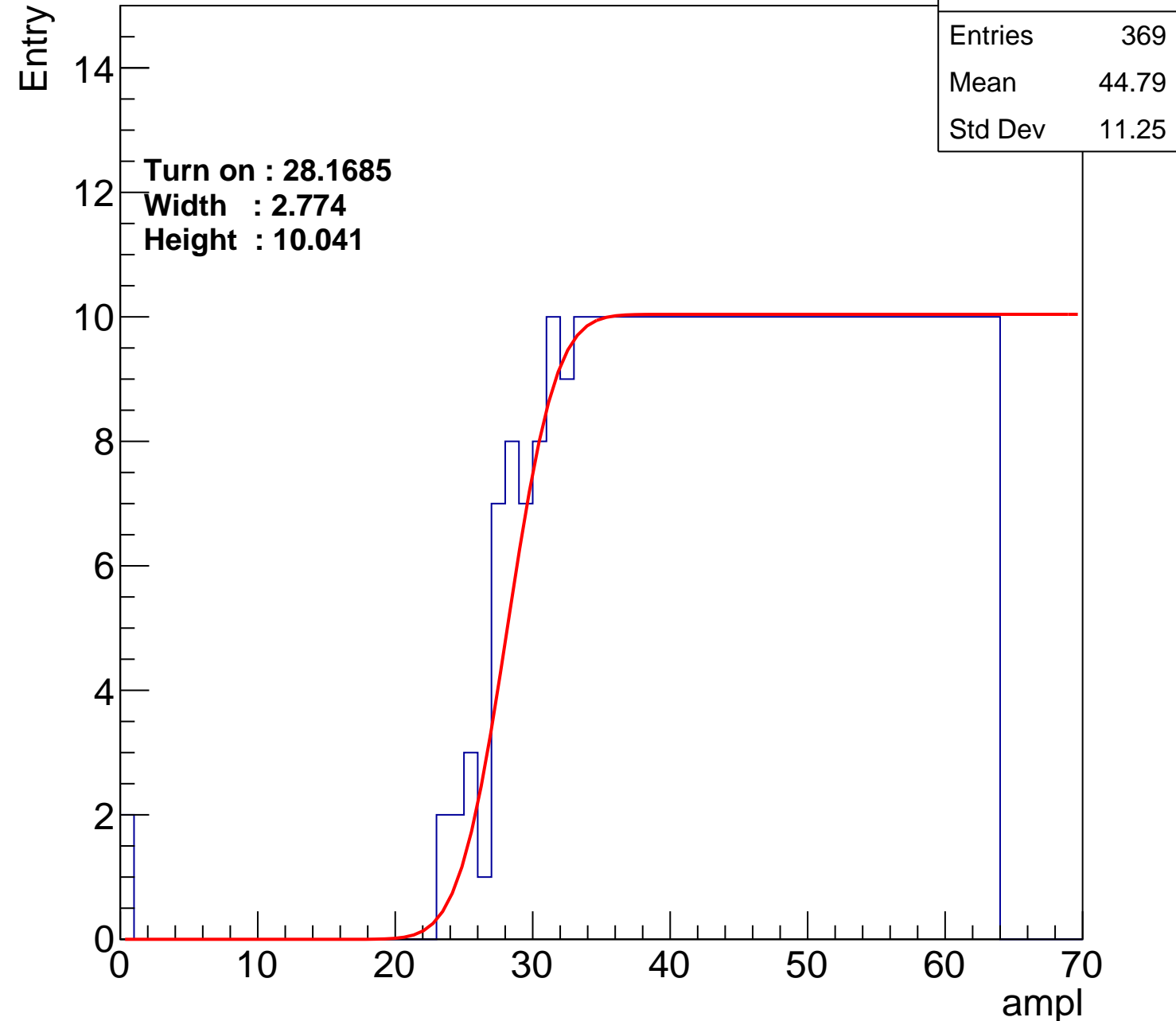
Width : 2.774

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.55
Std Dev	11.19

Turn on : 26.8644

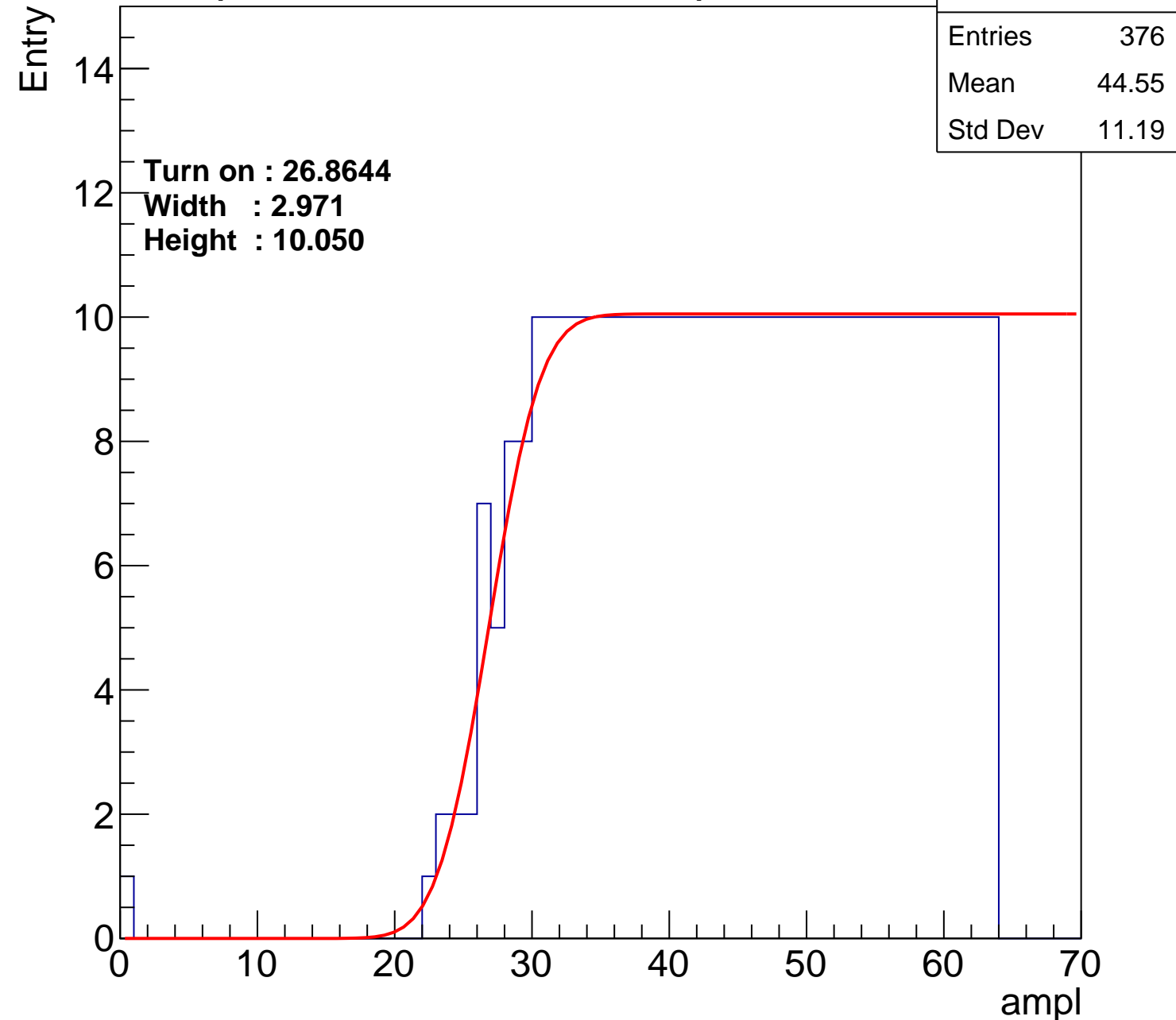
Width : 2.971

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch125

calib_packv5_042523_0143.root, FC#3, port B3

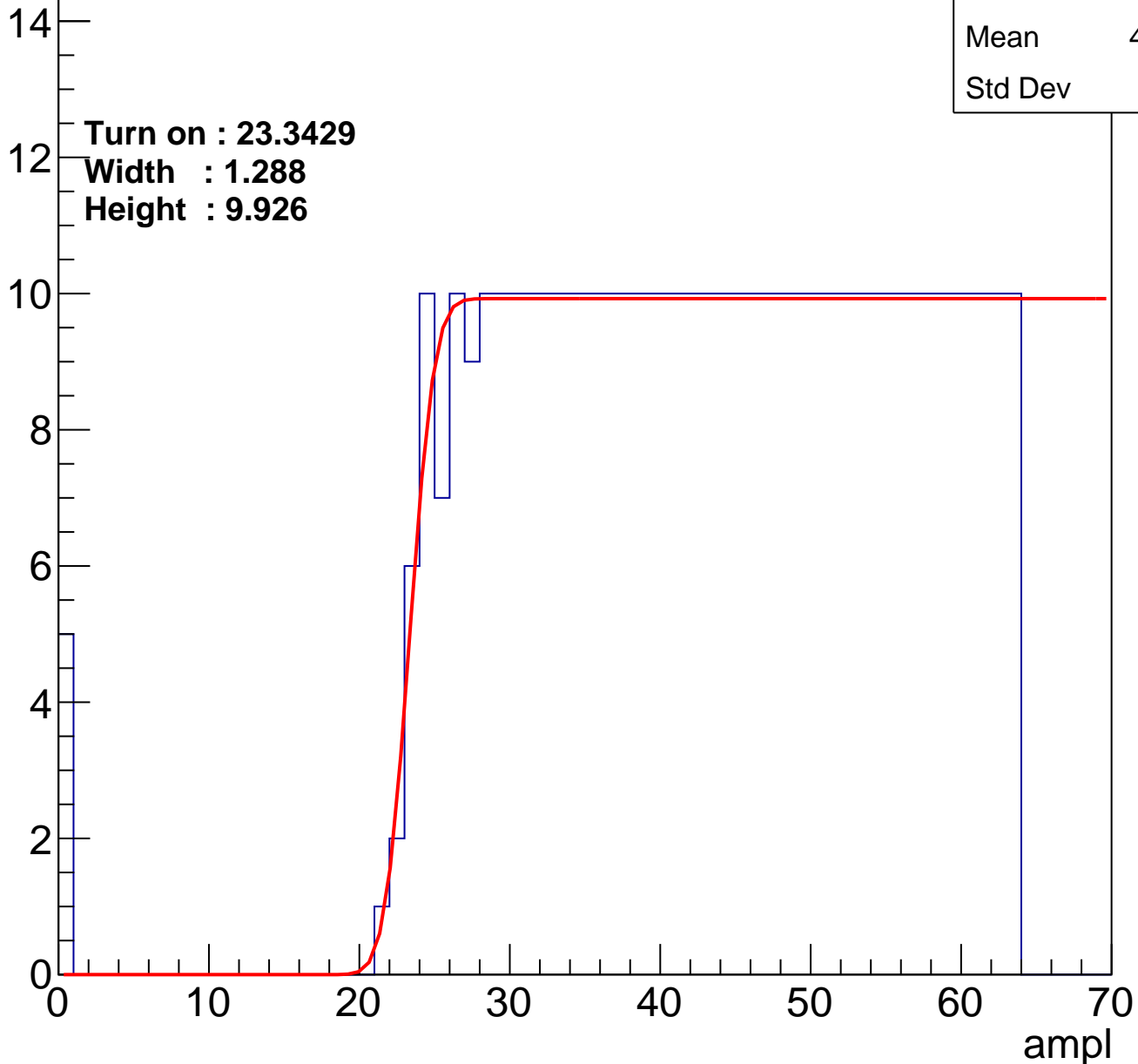
Entries	410
Mean	42.69
Std Dev	12.6

Turn on : 23.3429

Width : 1.288

Height : 9.926

Entry



B1L000S, U8-ch126

calib_packv5_042523_0143.root, FC#3, port B3

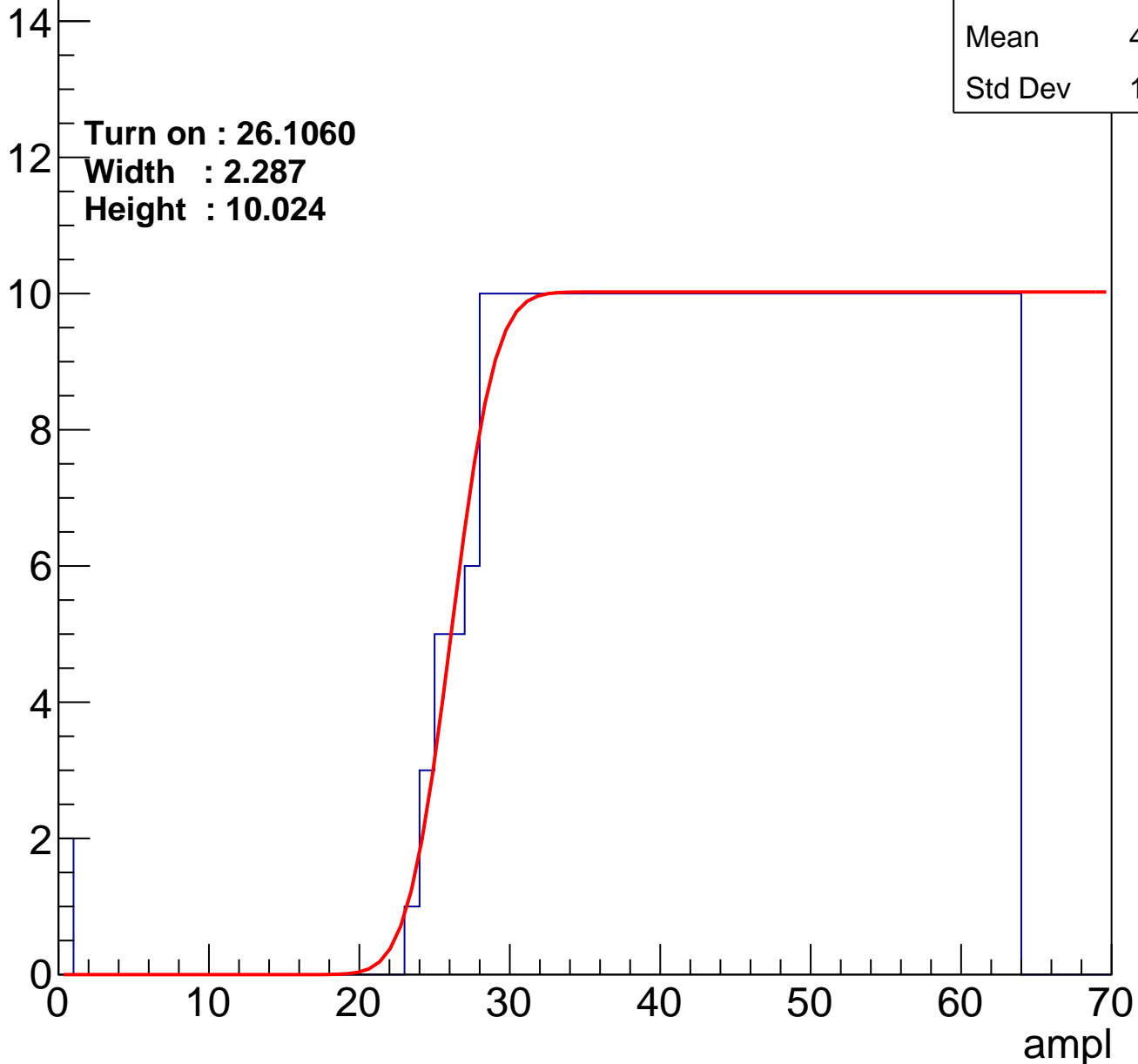
Entries	382
Mean	44.22
Std Dev	11.48

Turn on : 26.1060

Width : 2.287

Height : 10.024

Entry



B1L000S, U8-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.46
Std Dev	11.9

Turn on : 24.6796

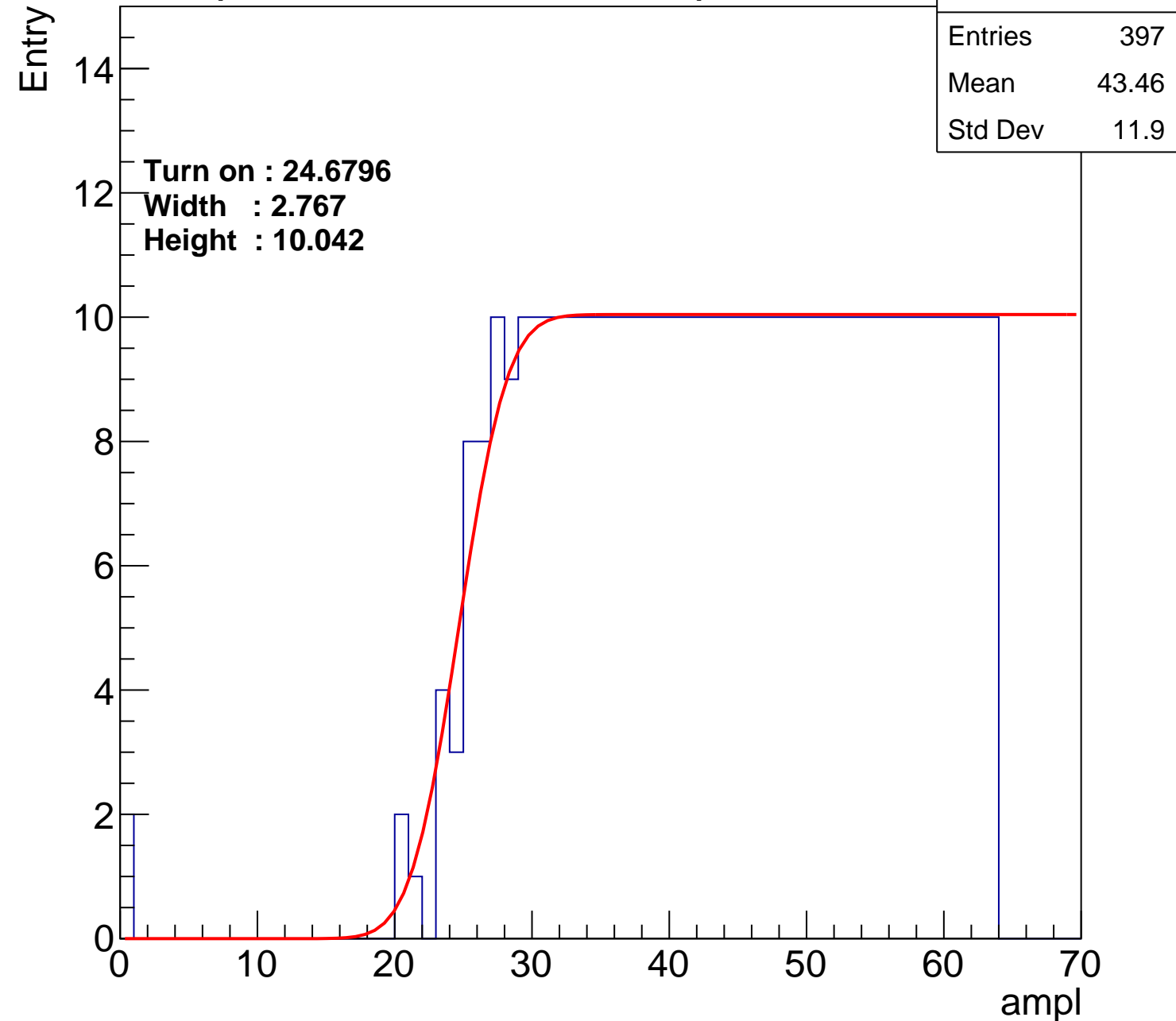
Width : 2.767

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U8-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.46
Std Dev	11.9

Turn on : 24.6796

Width : 2.767

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl

