

B1L103S, U18-ch0, adc0

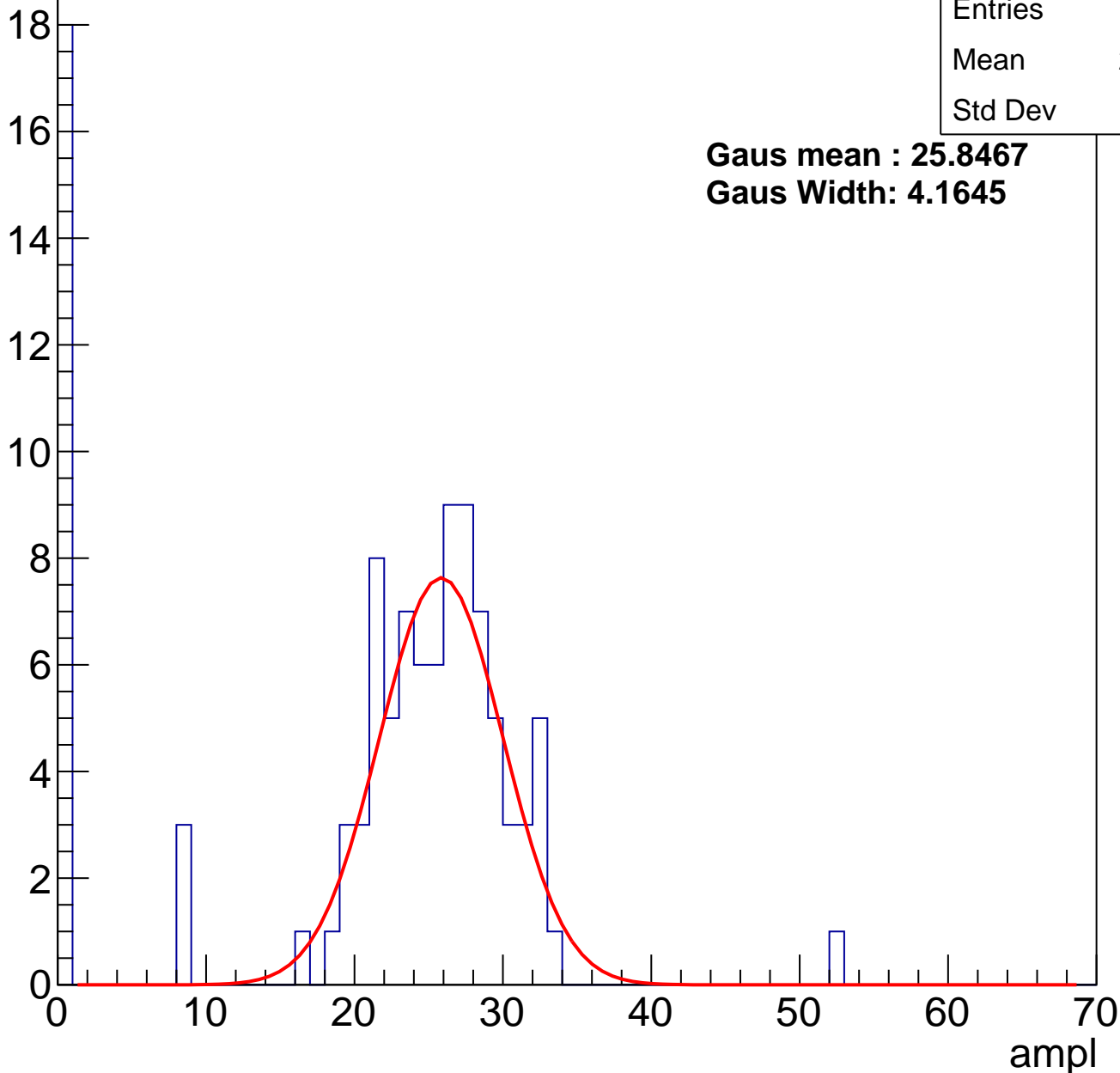
calib_packv5_041523_1651.root, FC#0, port C2

Entries	104
Mean	20.71
Std Dev	10.8

Gaus mean : 25.8467

Gaus Width: 4.1645

Entry



B1L103S, U18-ch0, adc1

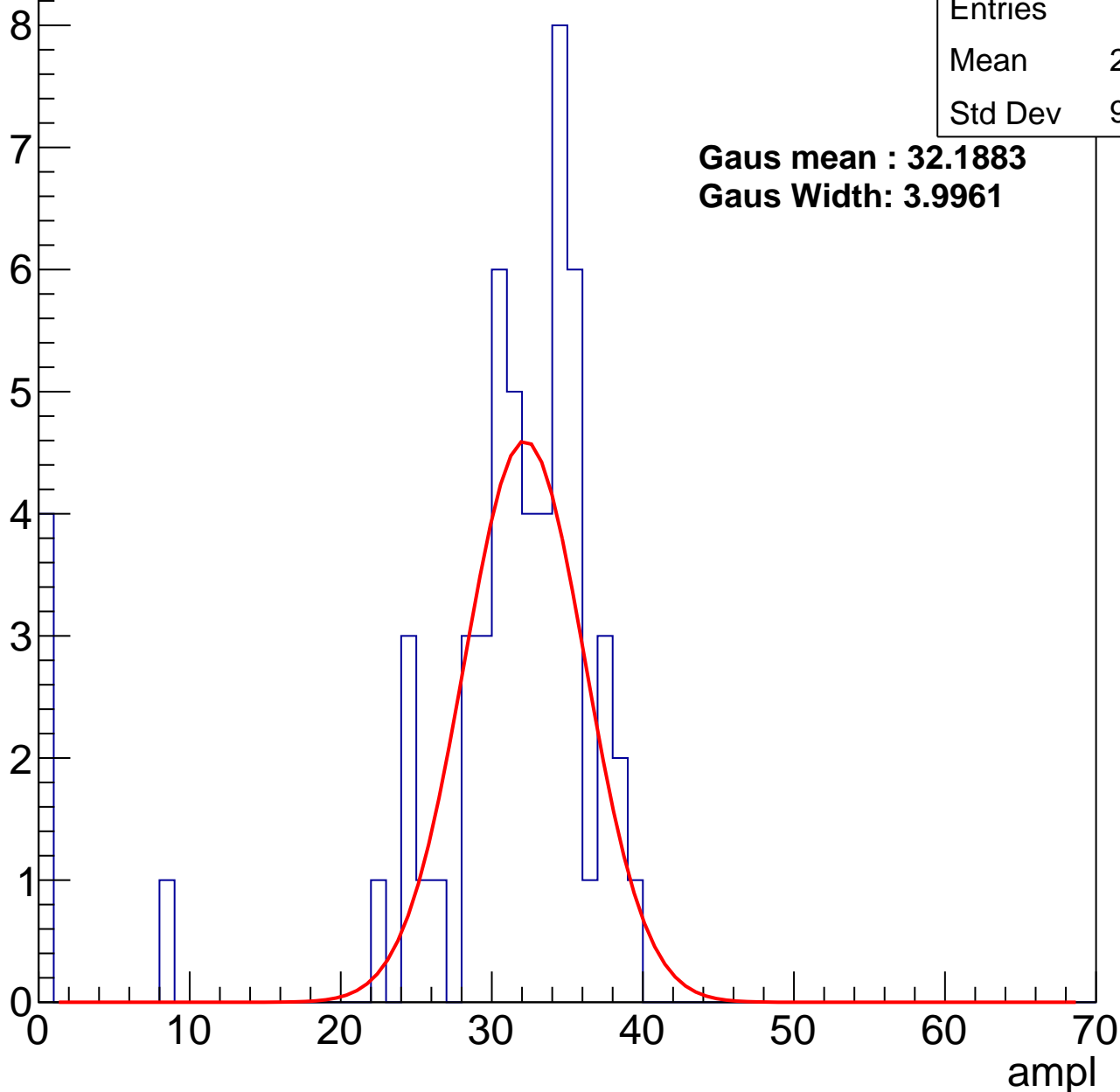
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	29.18
Std Dev	9.366

Gaus mean : 32.1883

Gaus Width: 3.9961



B1L103S, U18-ch0, adc2

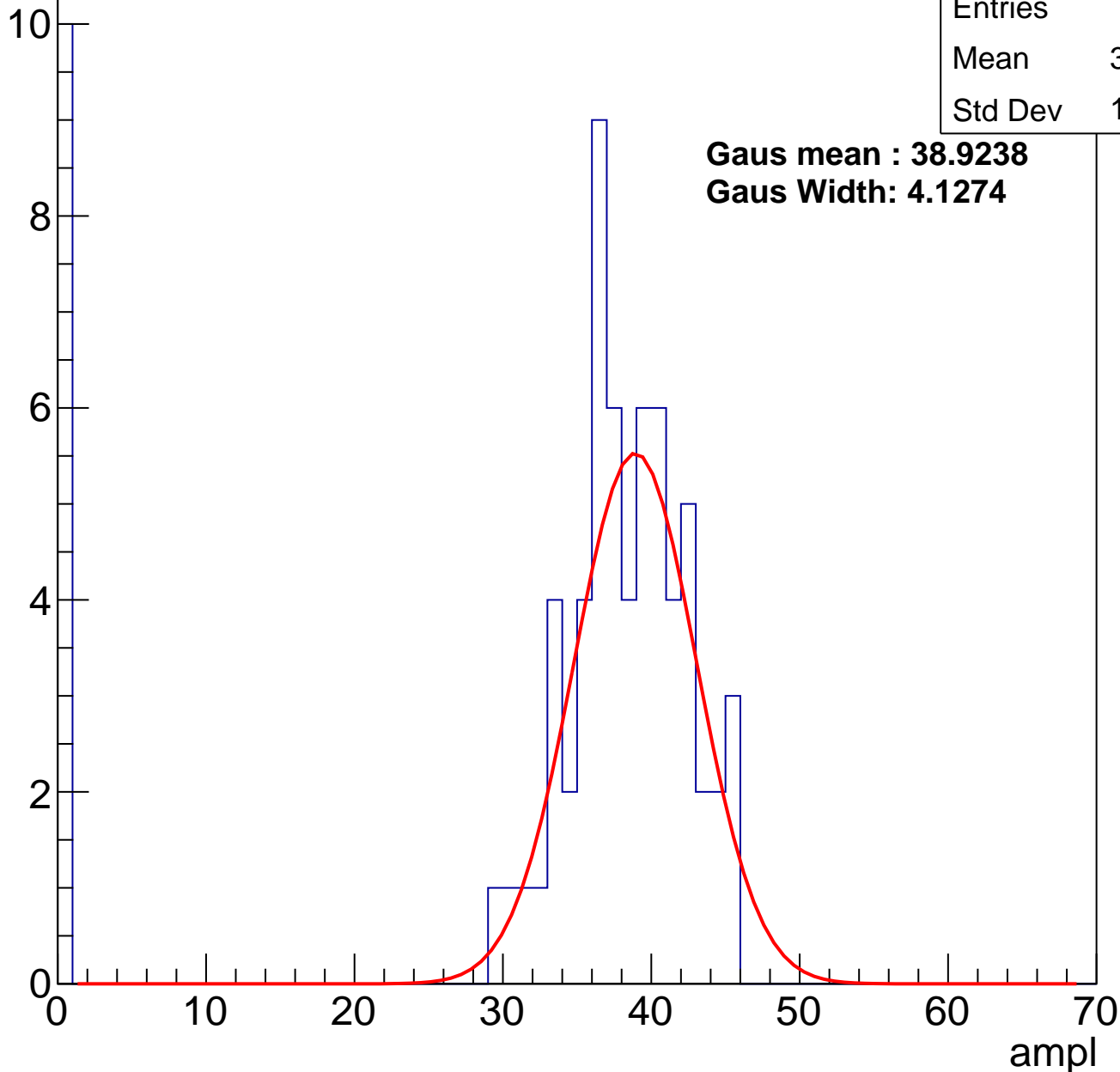
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	32.63
Std Dev	13.66

Gaus mean : 38.9238

Gaus Width: 4.1274

Entry

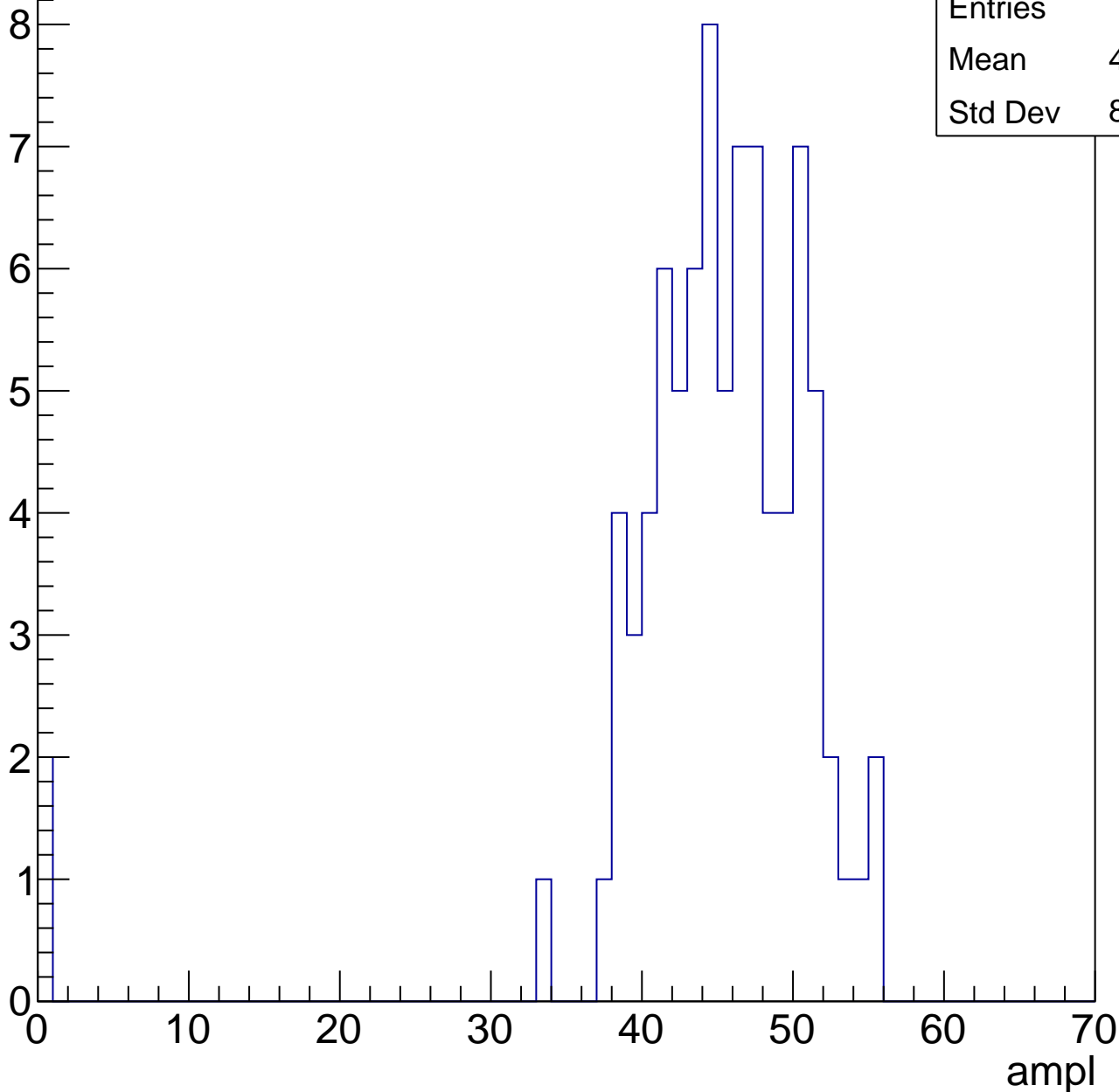


B1L103S, U18-ch0, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	44.18
Std Dev	8.196

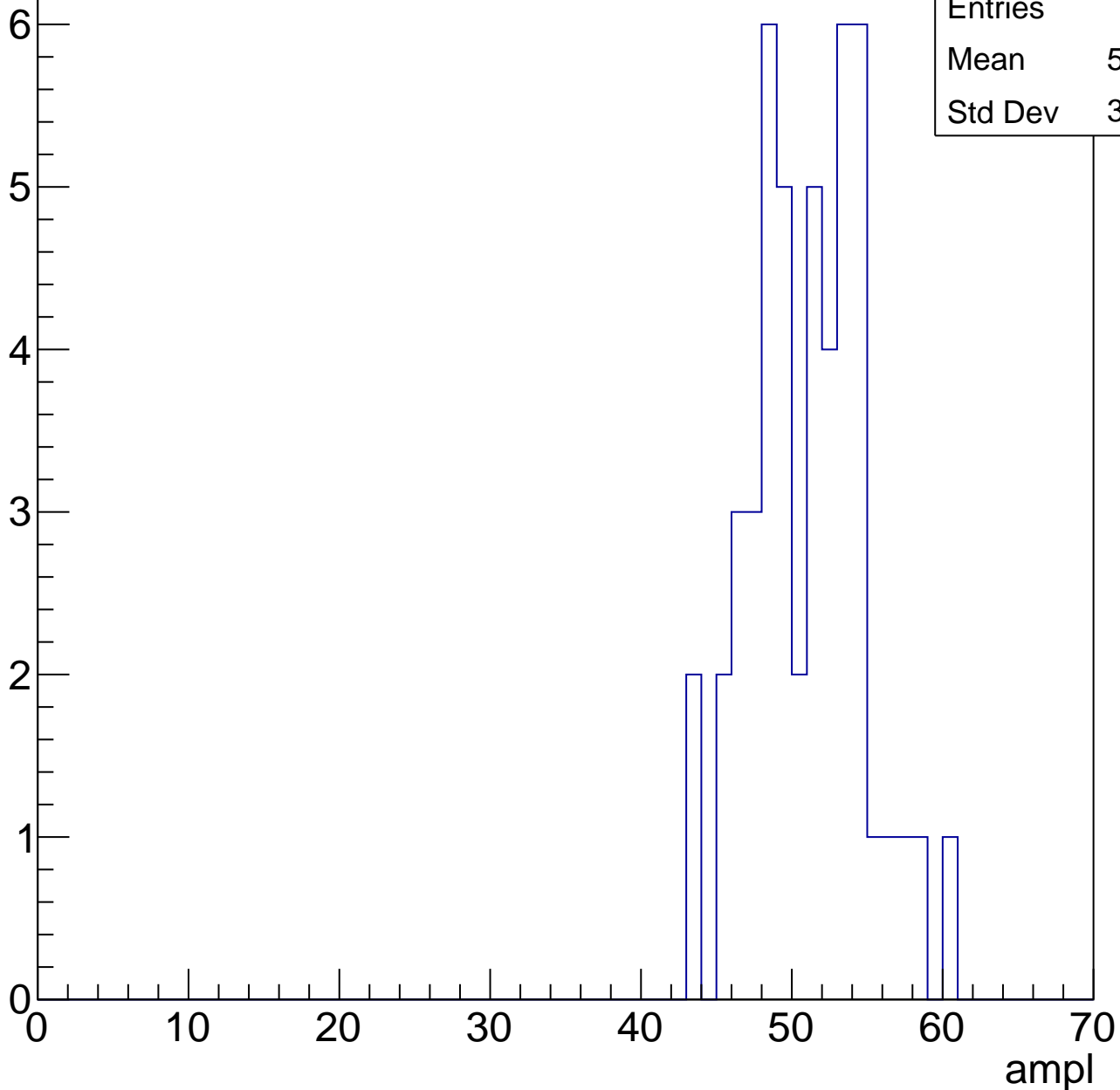


B1L103S, U18-ch0, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

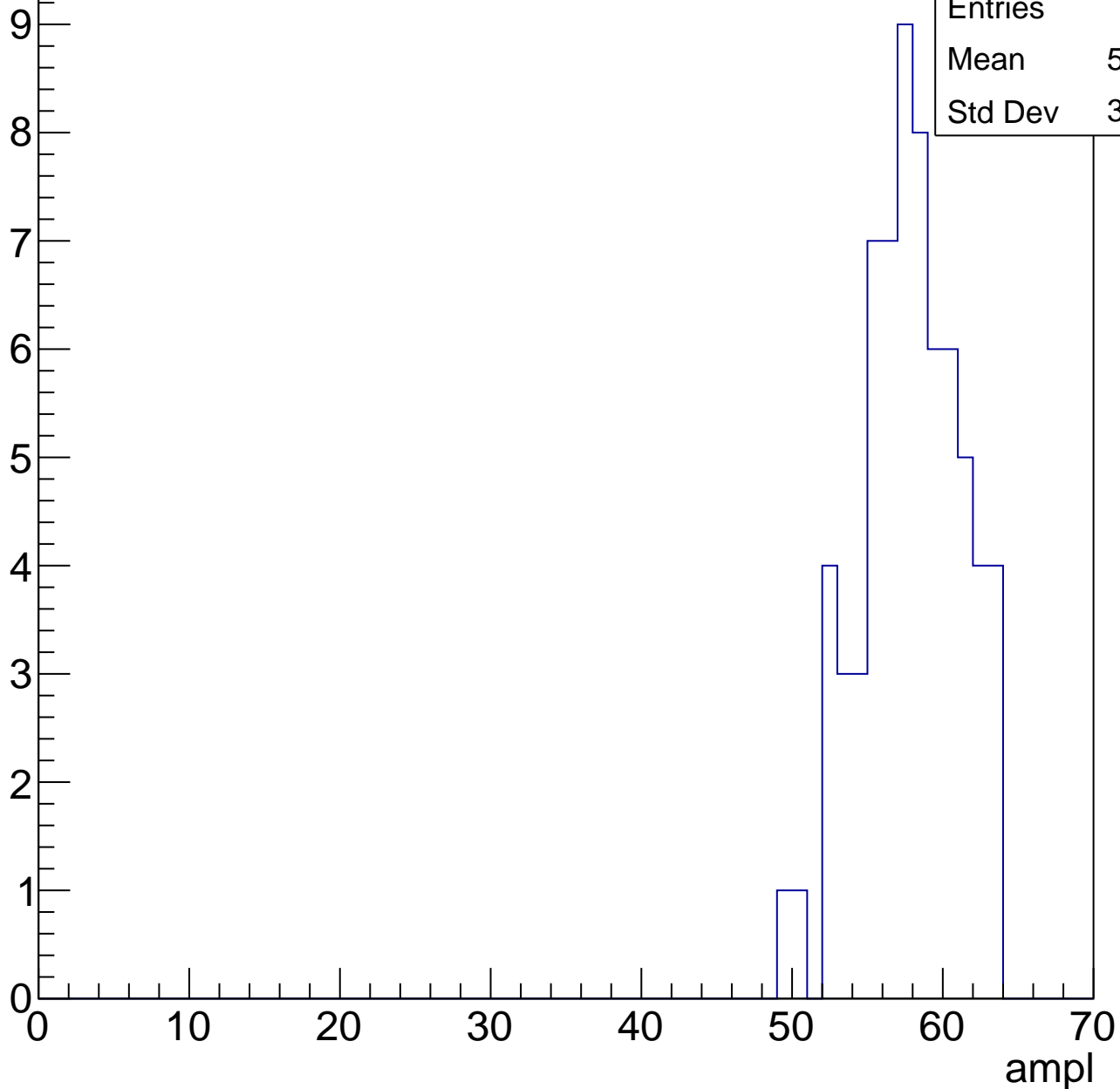
Entries	49
Mean	50.59
Std Dev	3.725



B1L103S, U18-ch0, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



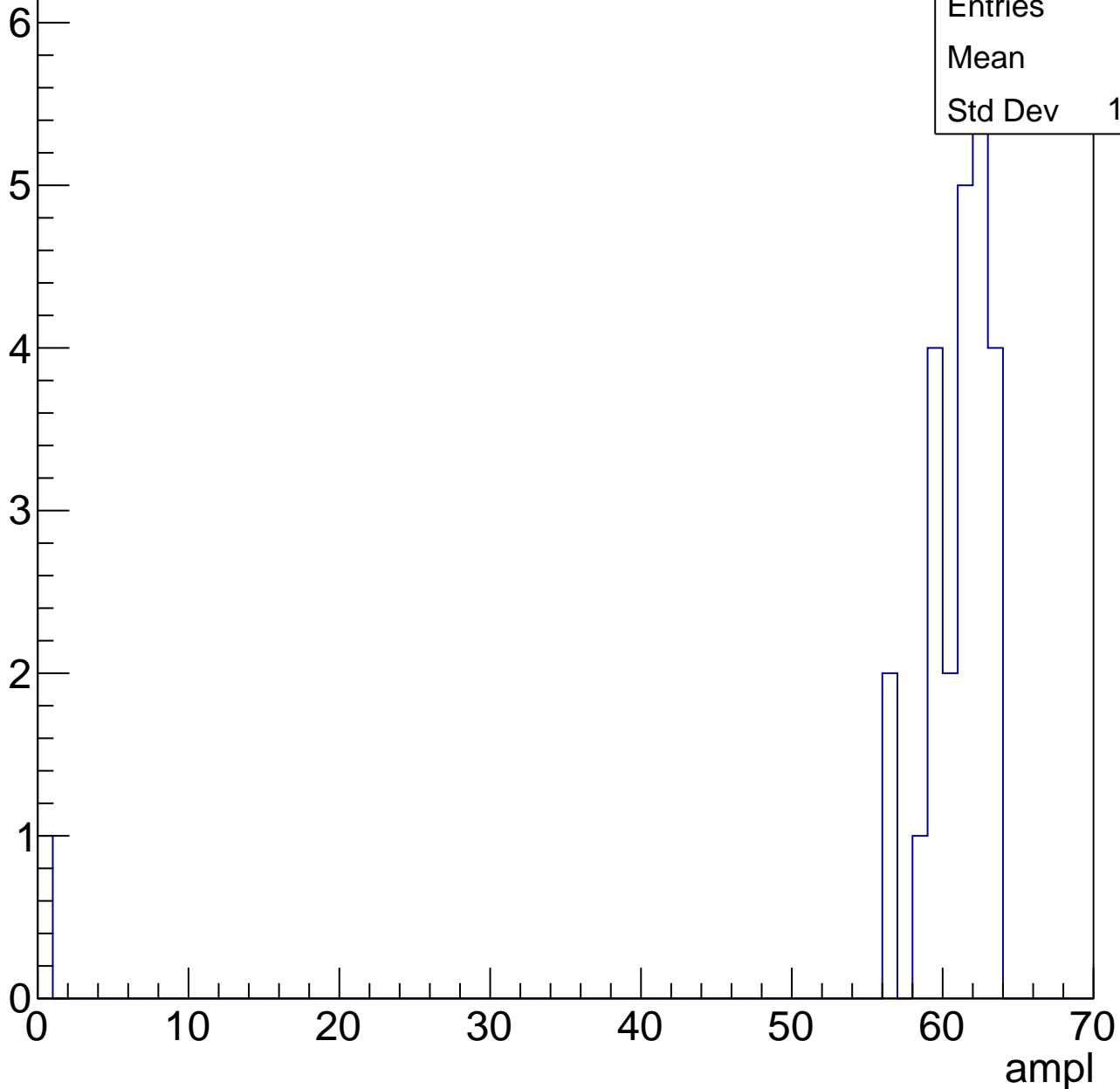
Entries	68
Mean	57.37
Std Dev	3.263

B1L103S, U18-ch0, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

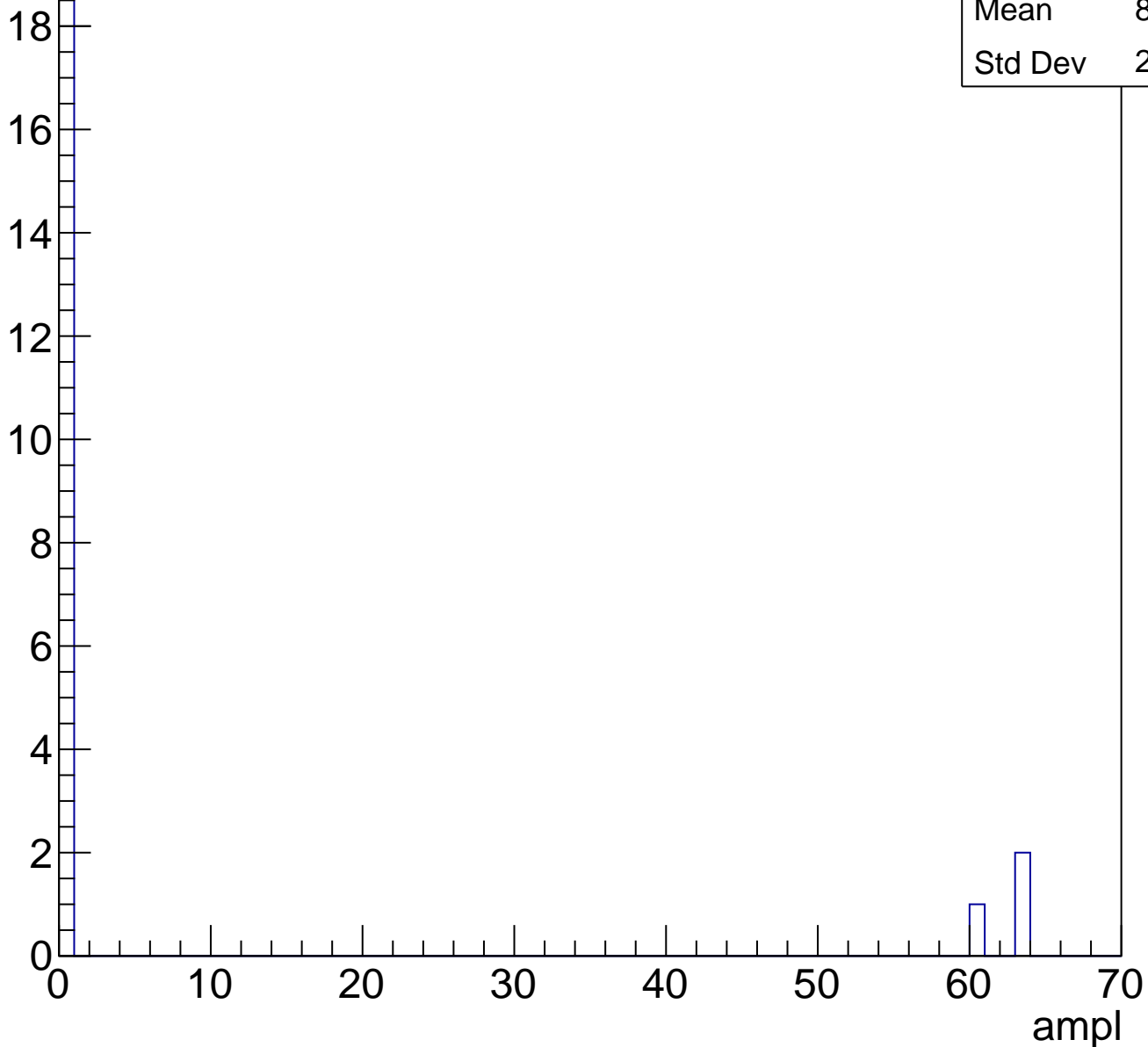
Entries	25
Mean	58.2
Std Dev	12.04



B1L103S, U18-ch0, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch1, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	25.99
Std Dev	9.435

Gaus mean : 29.9563

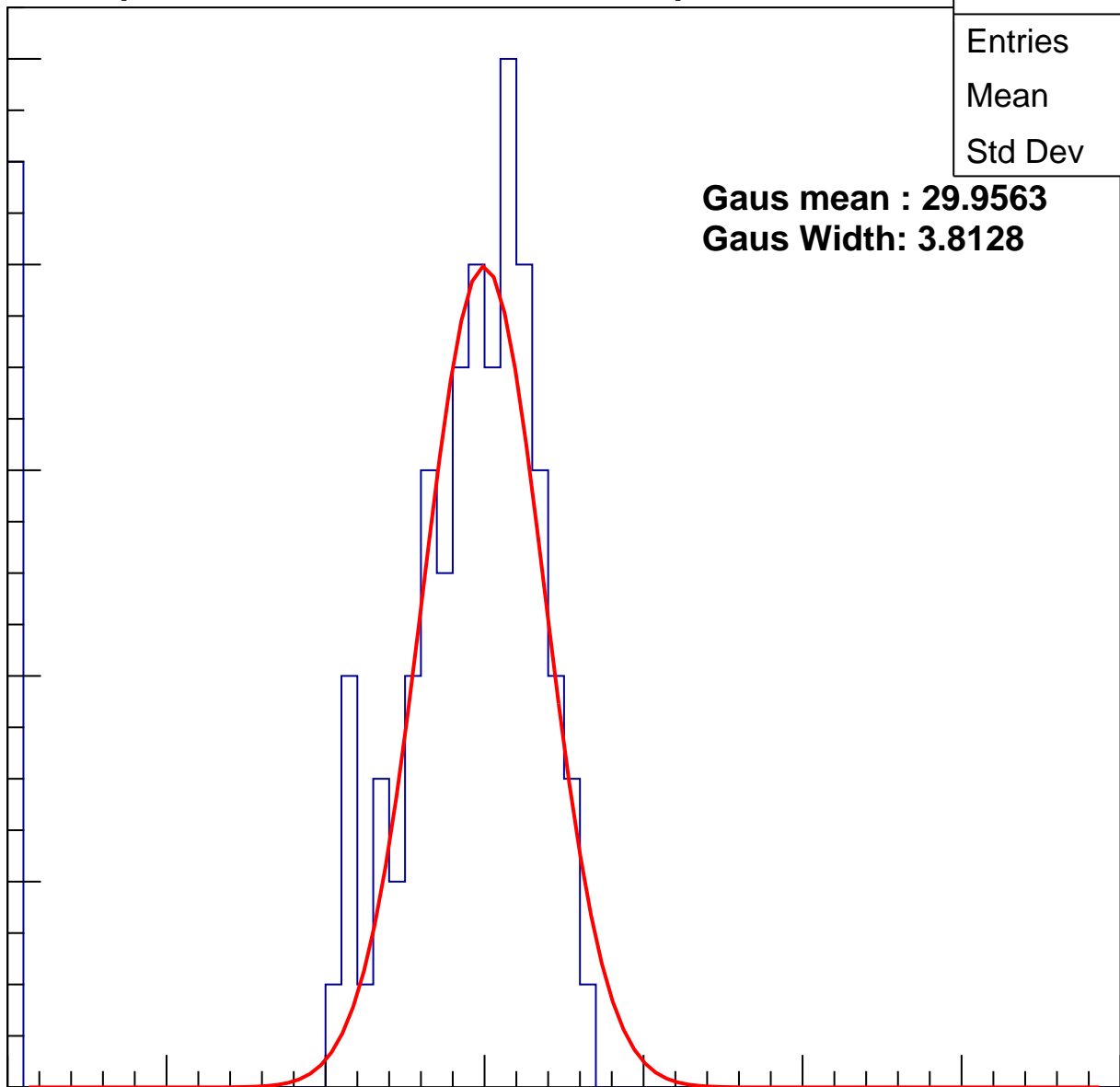
Gaus Width: 3.8128

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch1, adc1

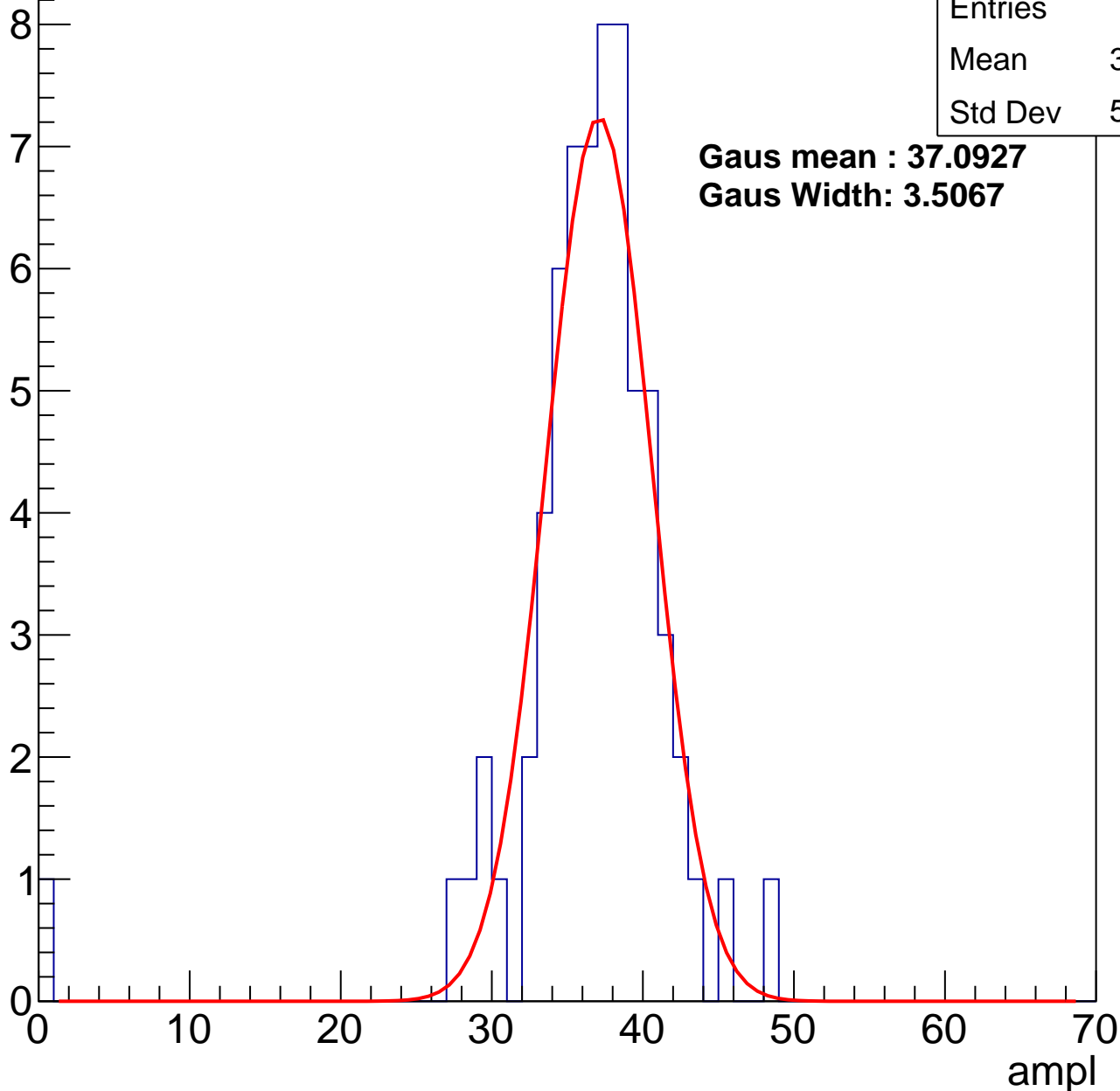
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.03
Std Dev	5.852

Gaus mean : 37.0927

Gaus Width: 3.5067



B1L103S, U18-ch1, adc2

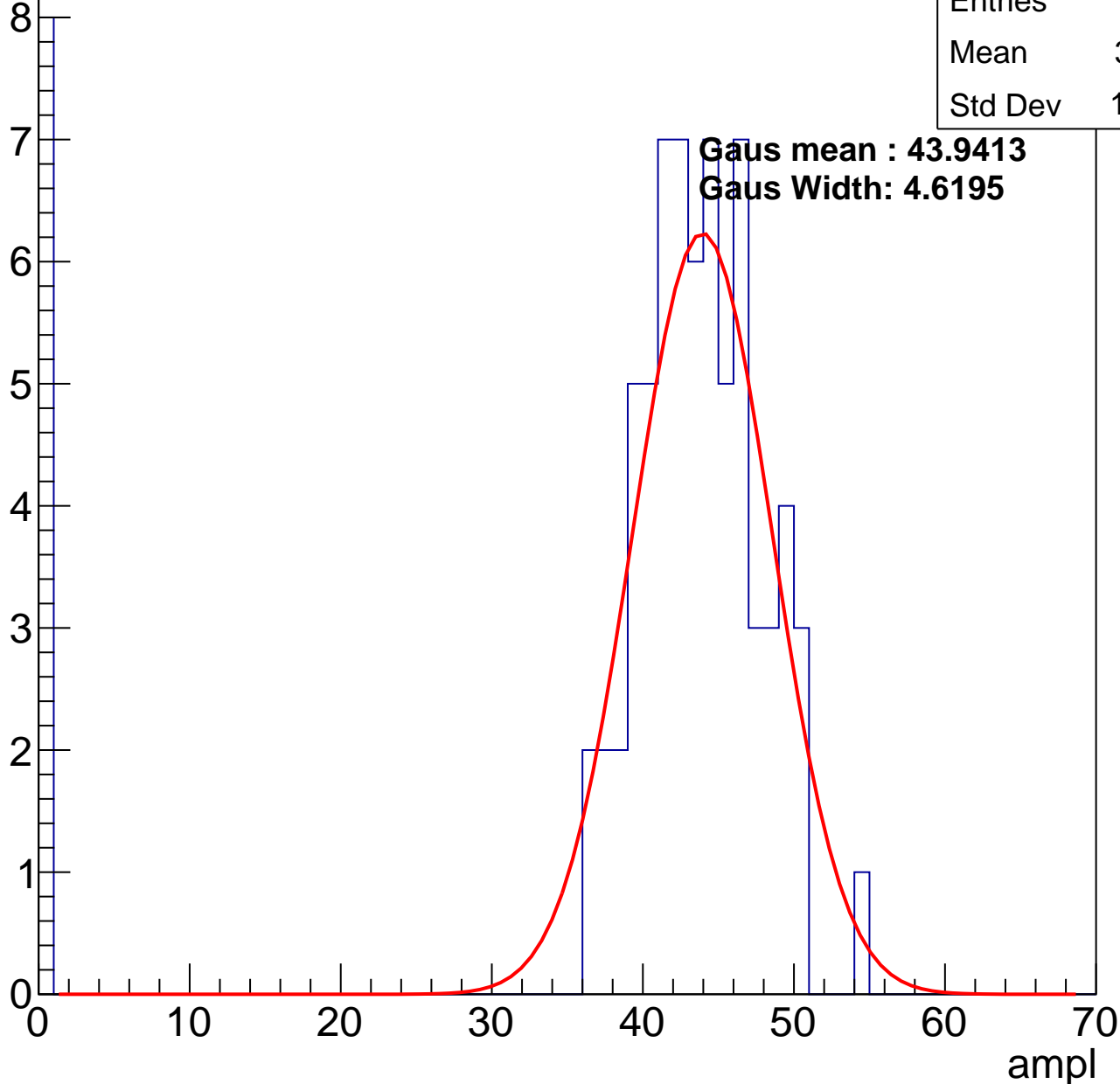
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	38.91
Std Dev	13.73

Gaus mean : 43.9413

Gaus Width: 4.6195

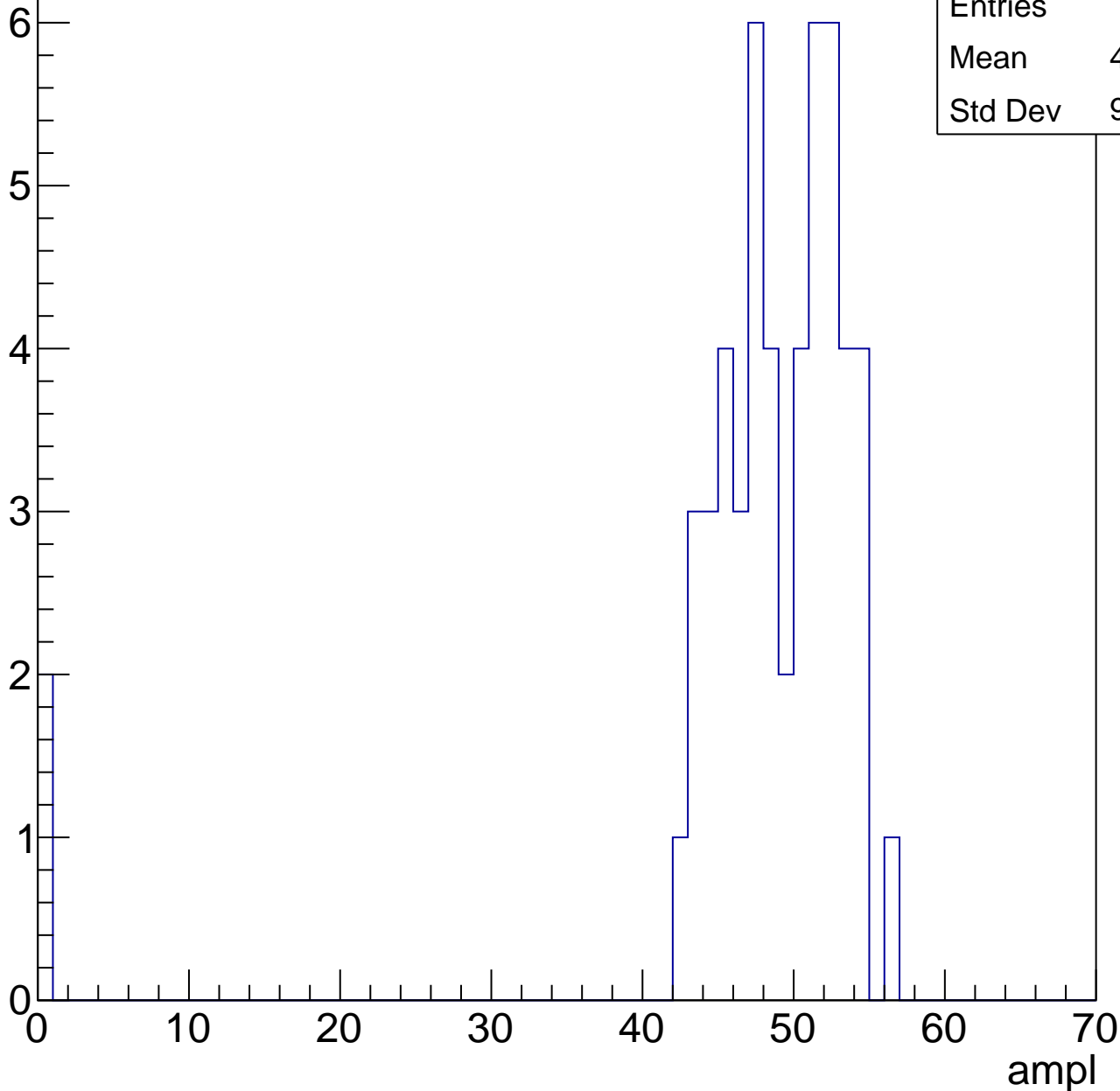


B1L103S, U18-ch1, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	47.08
Std Dev	9.954

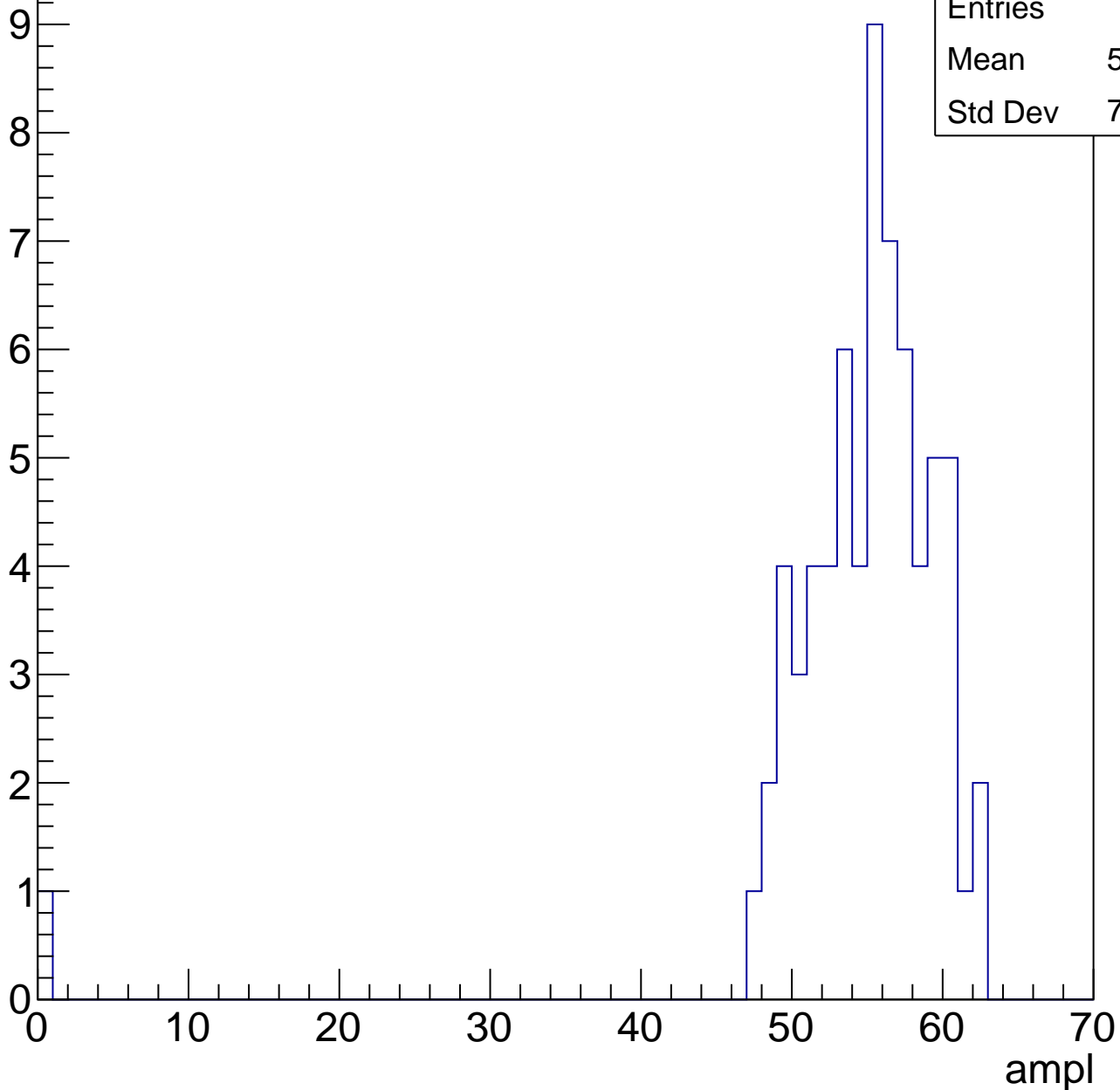


B1L103S, U18-ch1, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	54.06
Std Dev	7.554

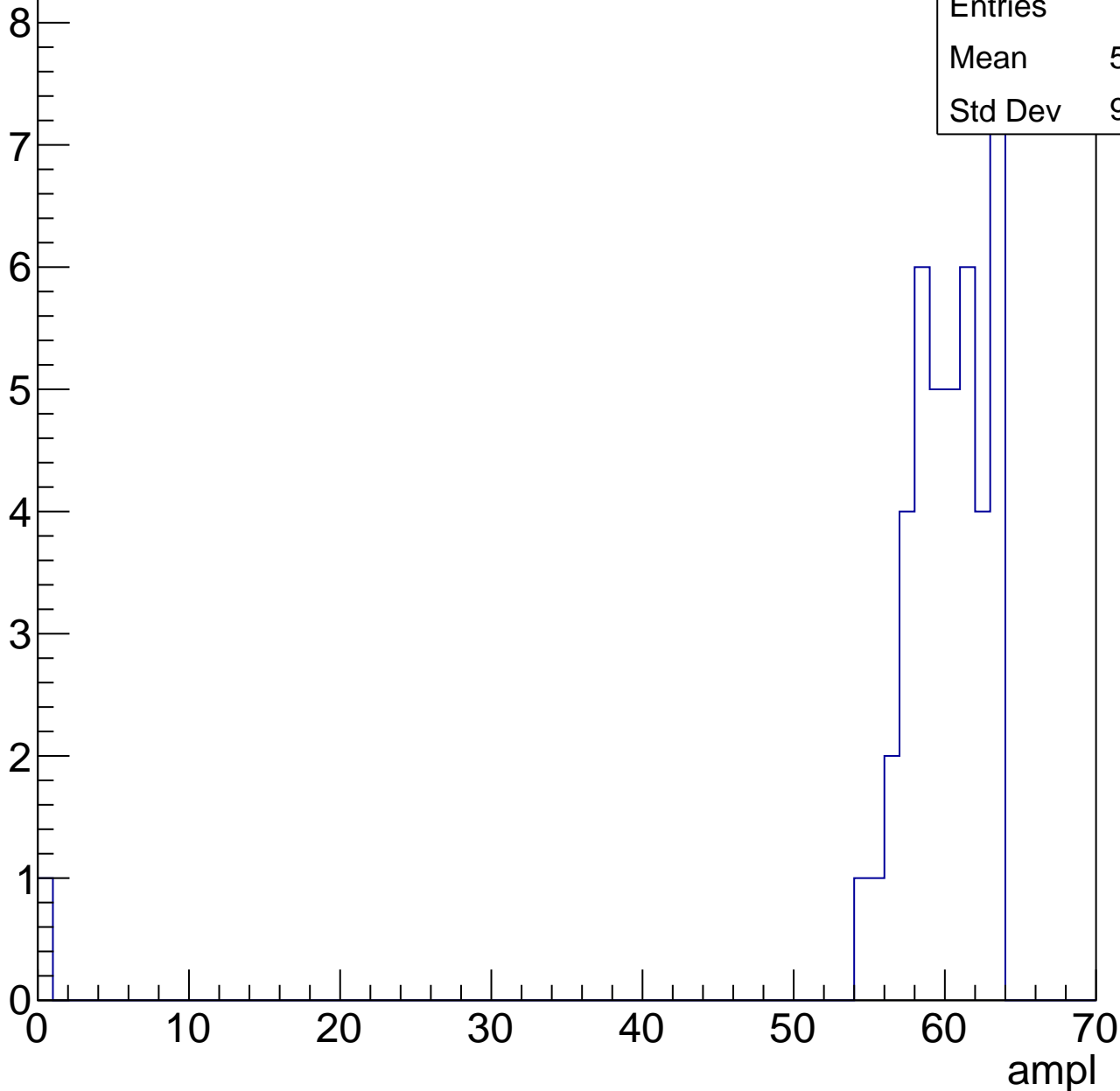


B1L103S, U18-ch1, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	58.37
Std Dev	9.324



B1L103S, U18-ch1, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.88

Std Dev

0.7806

B1L103S, U18-ch1, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U18-ch2, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	19.47
Std Dev	12.96

Gaus mean : 28.5741

Gaus Width: 4.3104

Entry

25

20

15

10

5

0

0

10

20

30

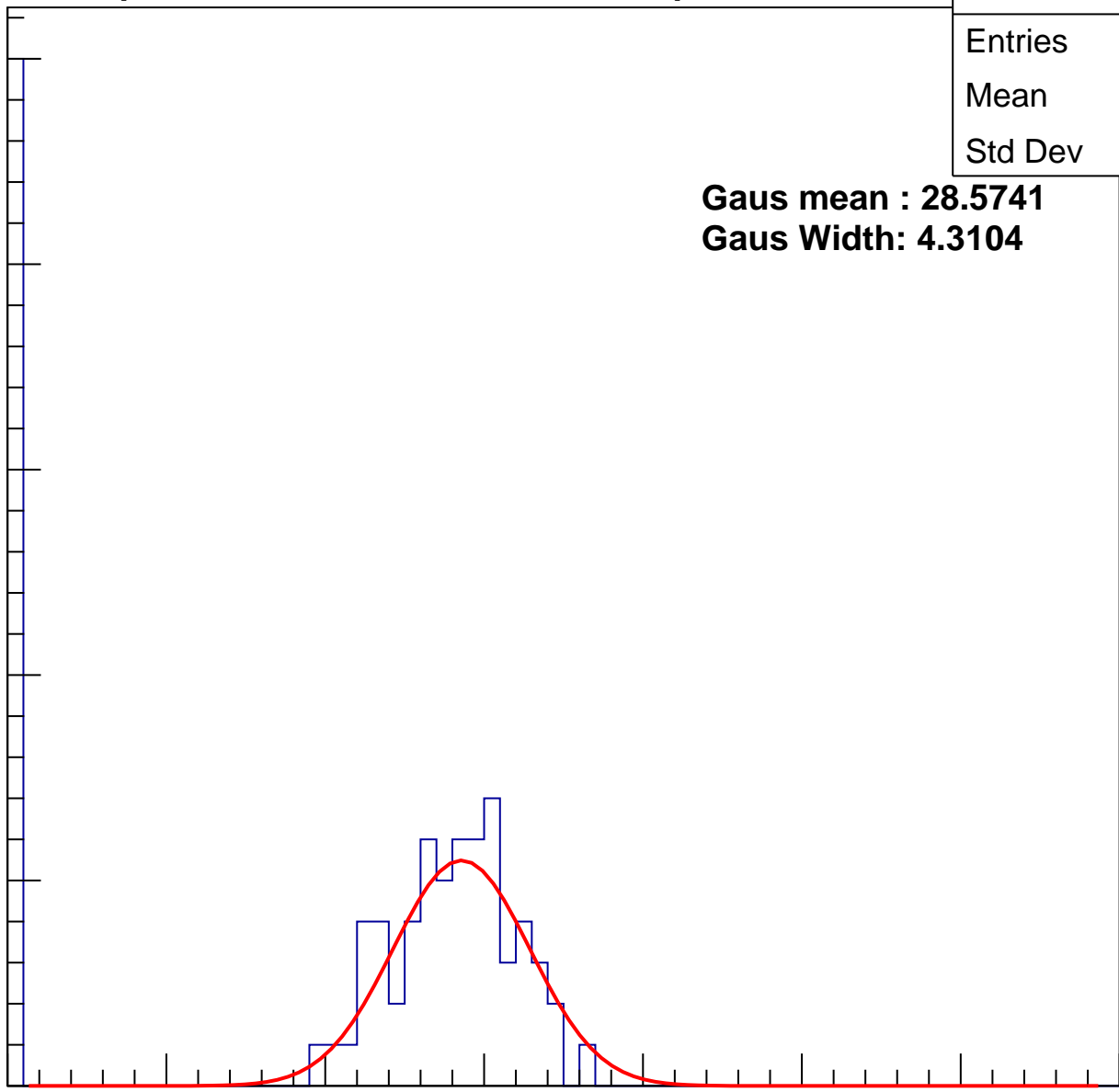
40

50

60

70

ampl



B1L103S, U18-ch2, adc1

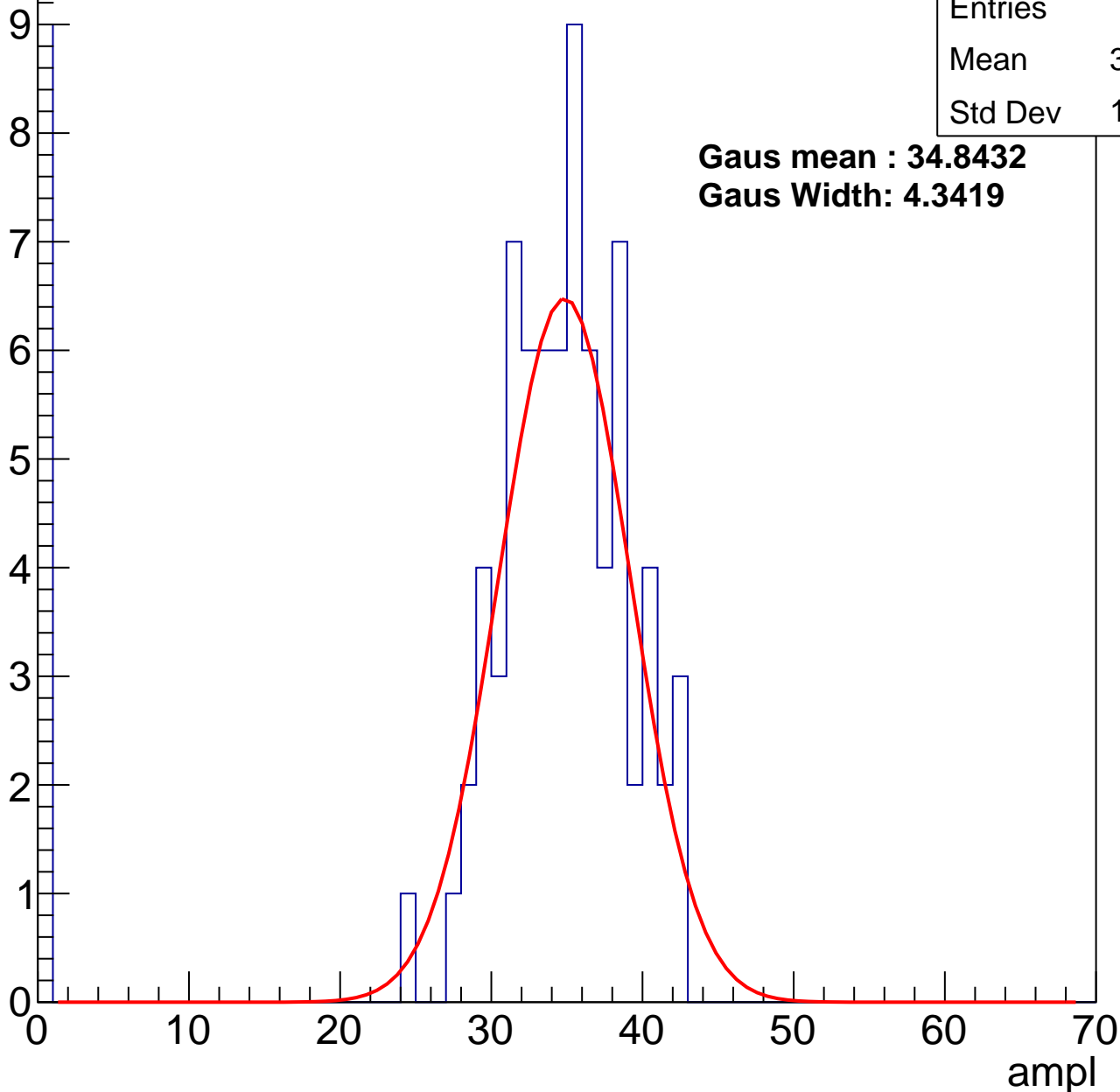
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	30.67
Std Dev	11.38

Gaus mean : 34.8432

Gaus Width: 4.3419



B1L103S, U18-ch2, adc2

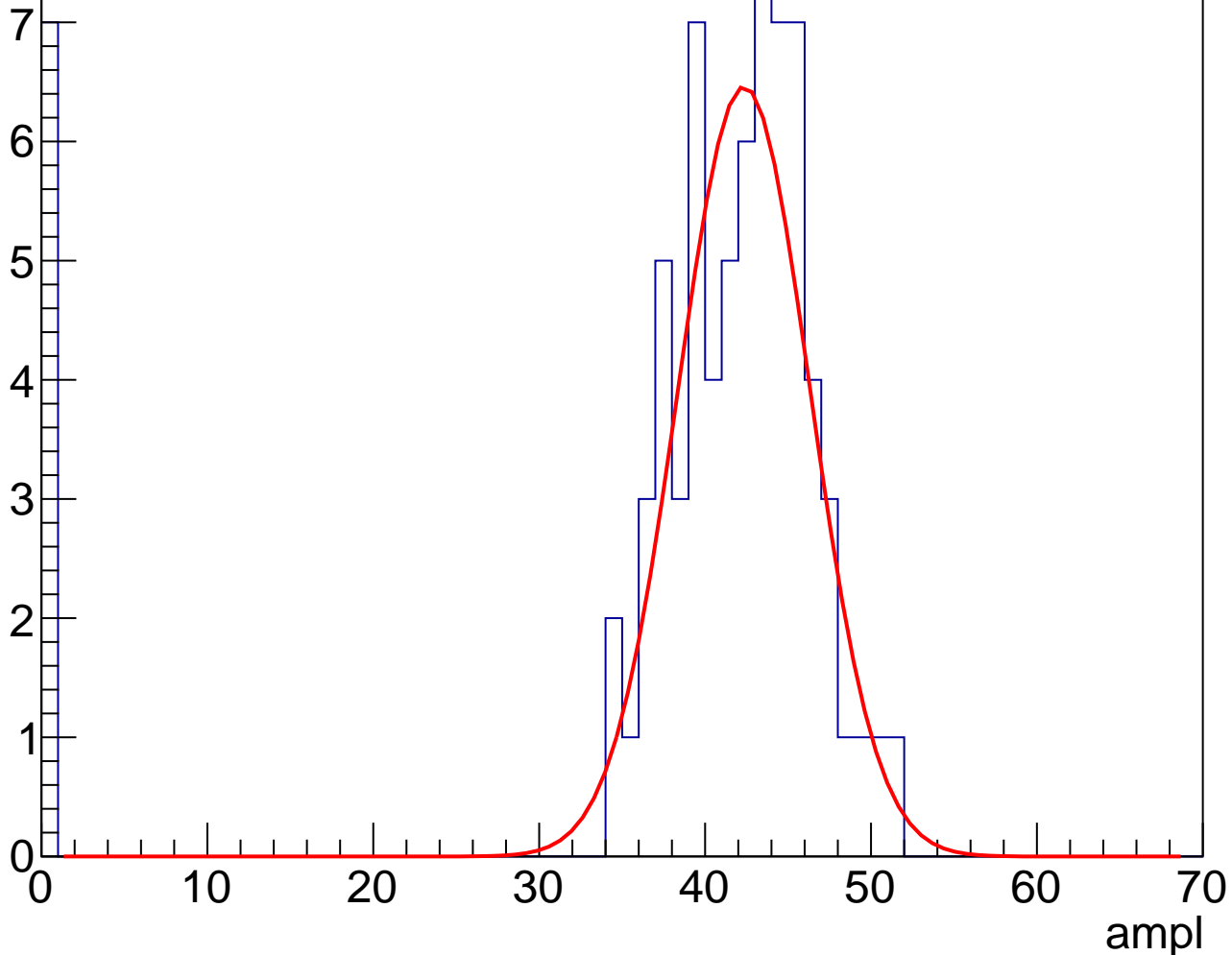
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	38.09
Std Dev	12.58

Gaus mean : 42.3496

Gaus Width: 3.9820

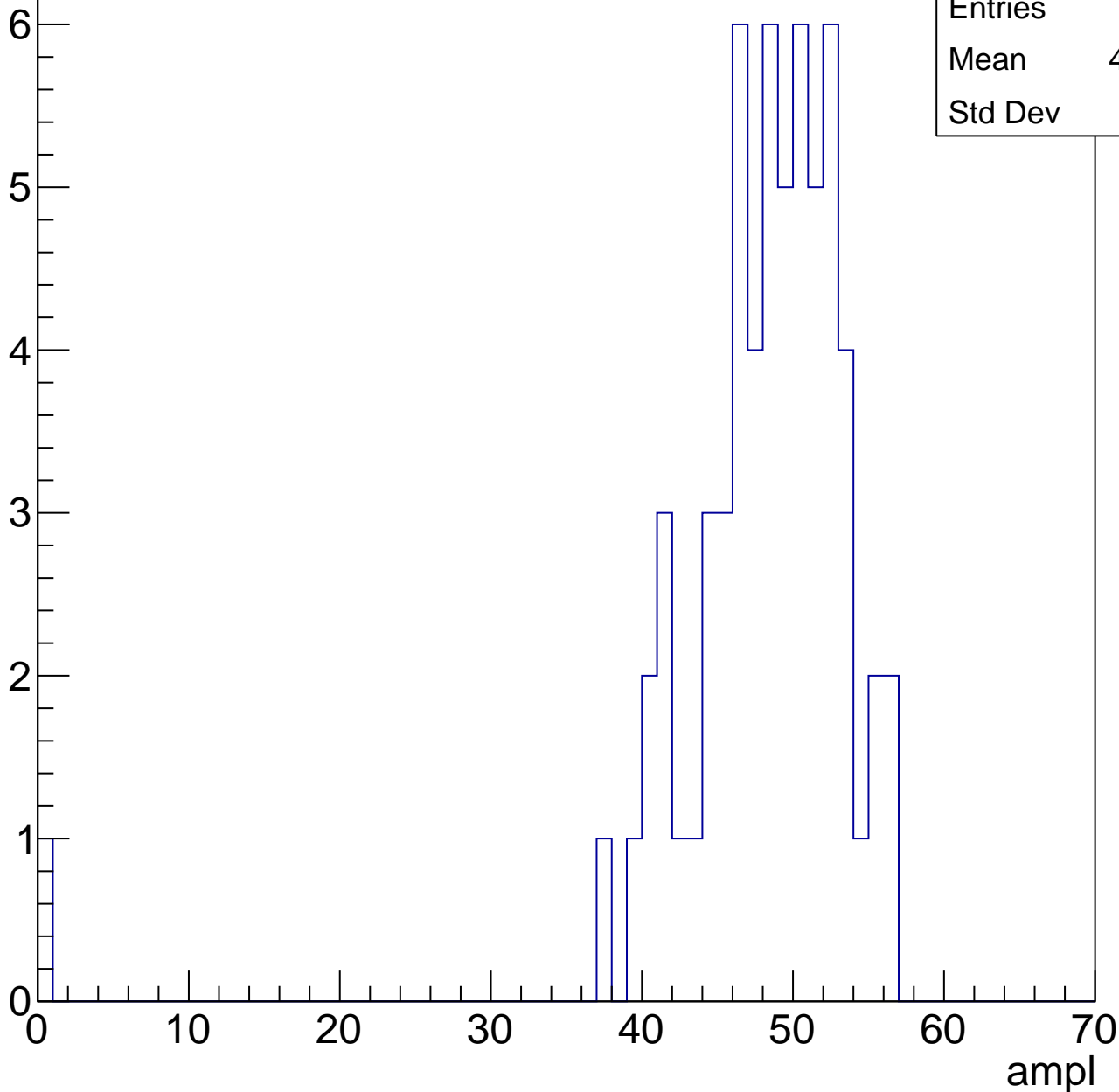


B1L103S, U18-ch2, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.35
Std Dev	7.4

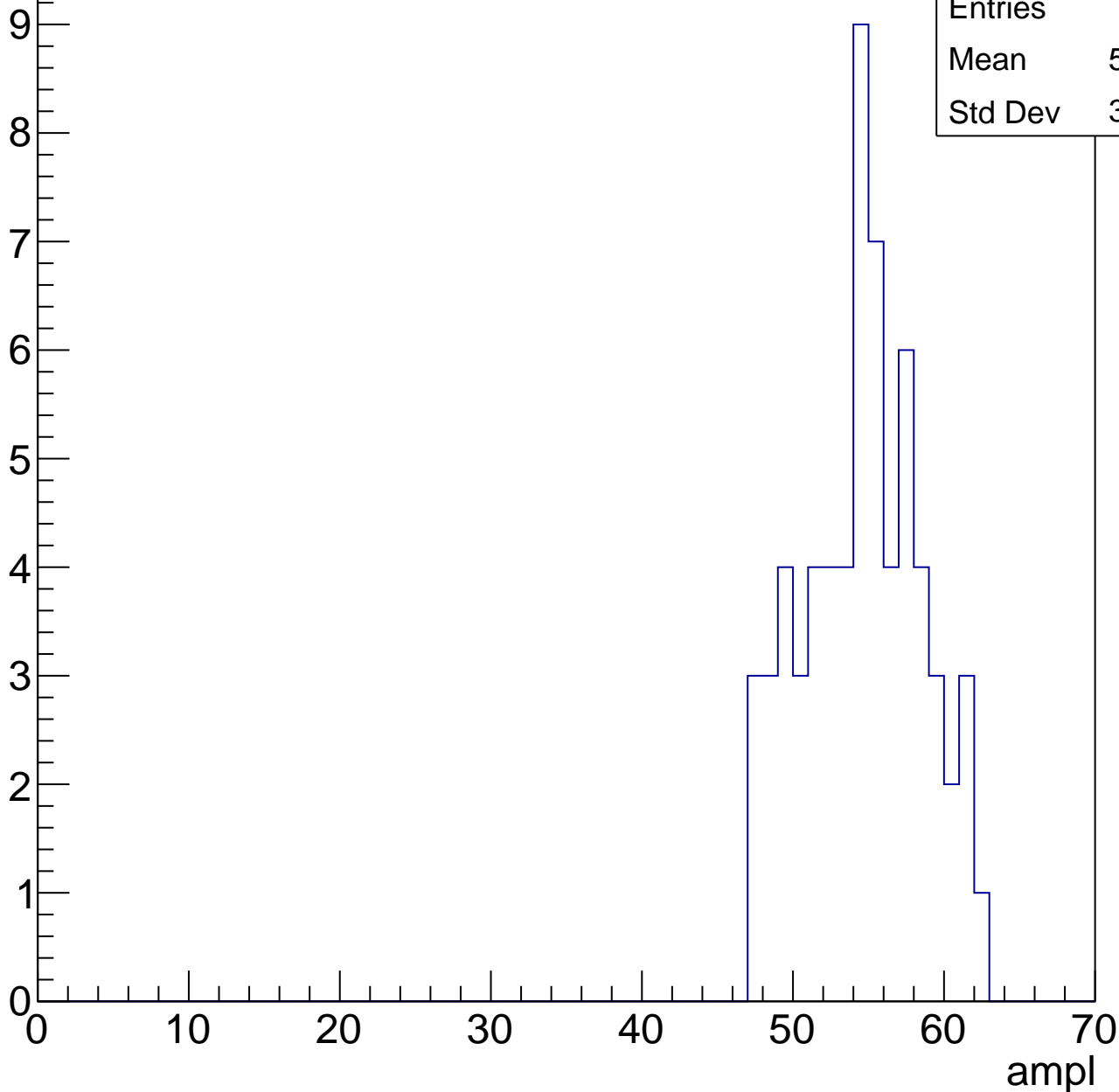


B1L103S, U18-ch2, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.16
Std Dev	3.866

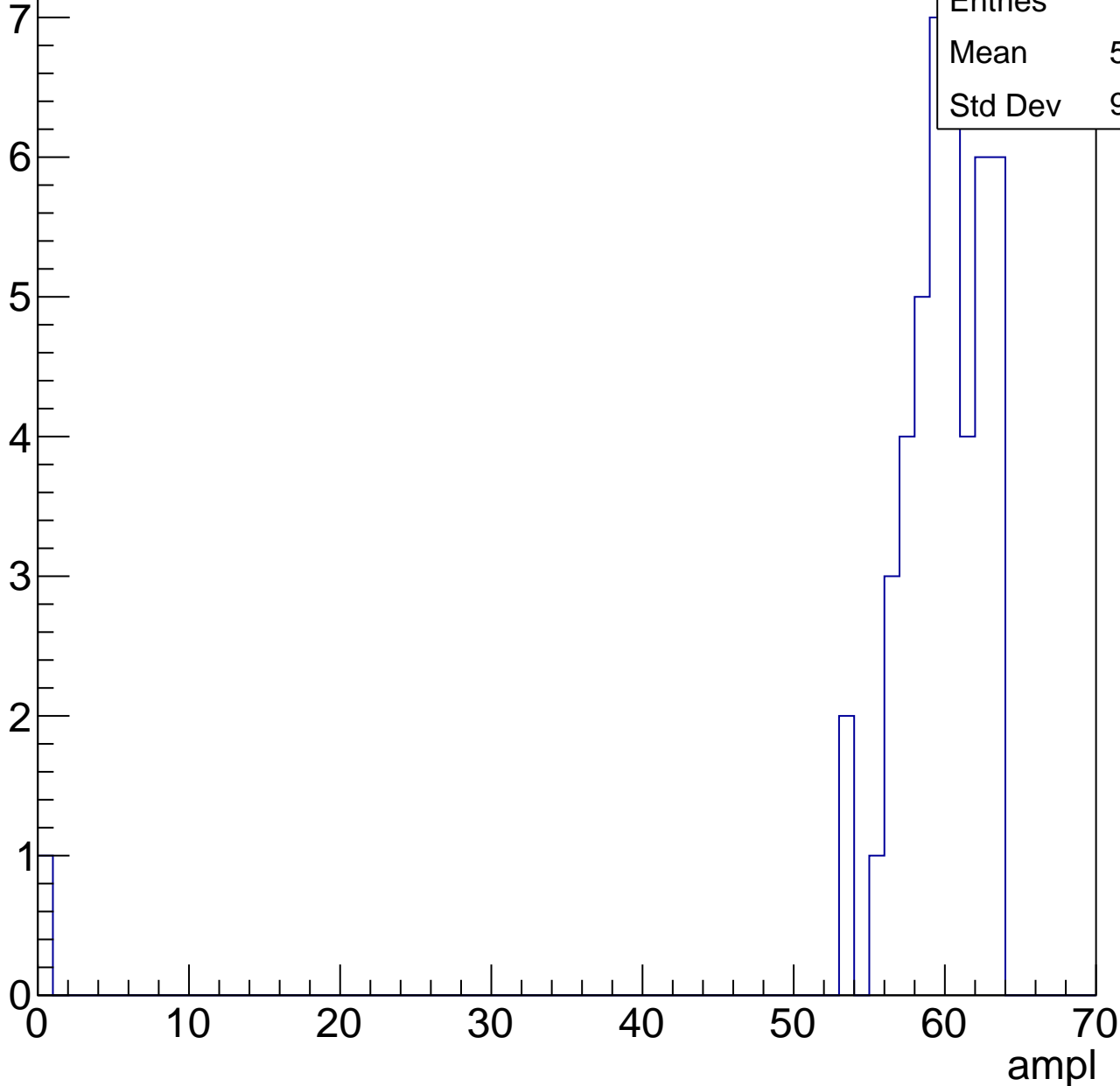


B1L103S, U18-ch2, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

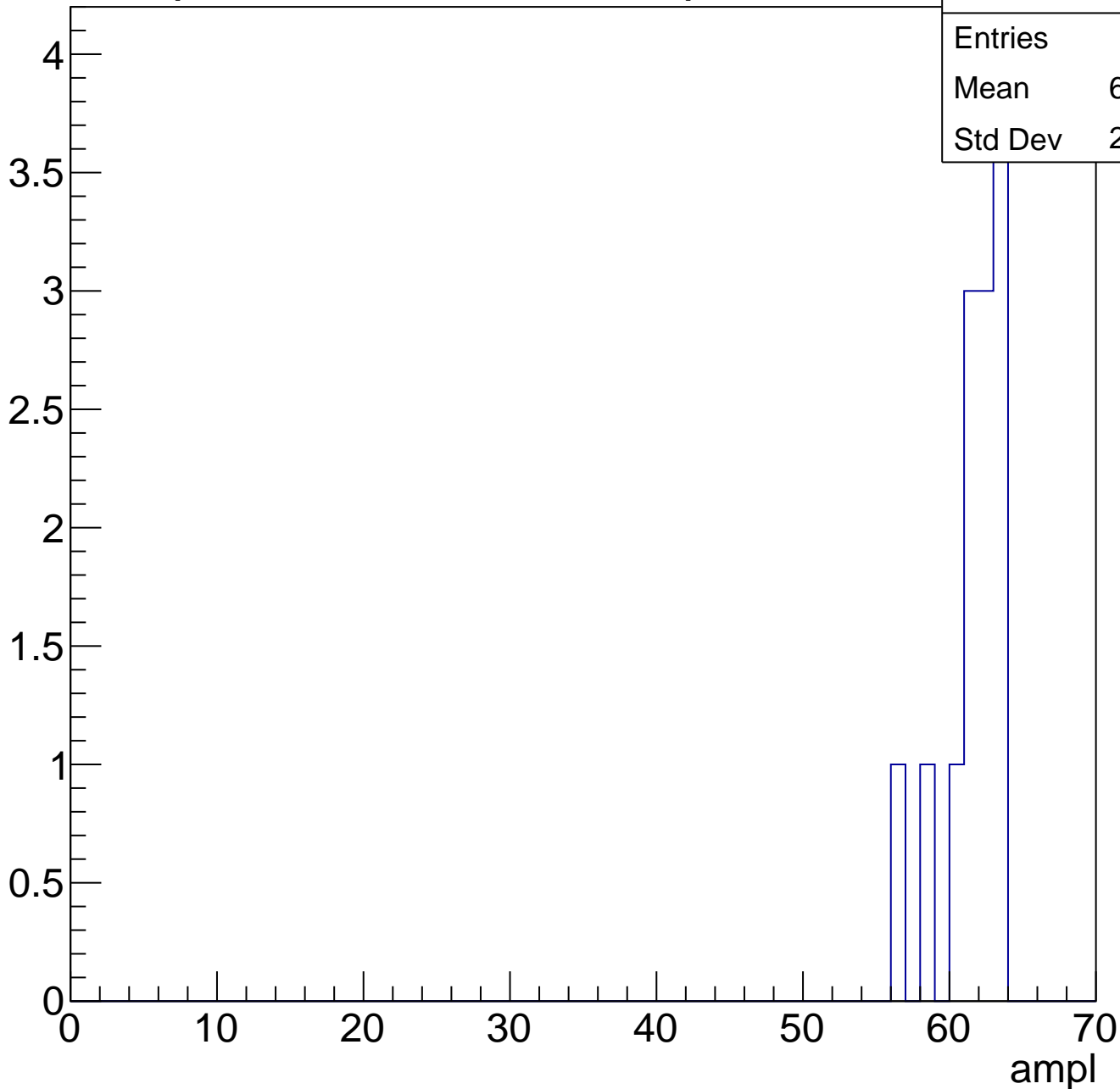
Entries	46
Mean	58.13
Std Dev	9.035



B1L103S, U18-ch2, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch2, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch3, adc0

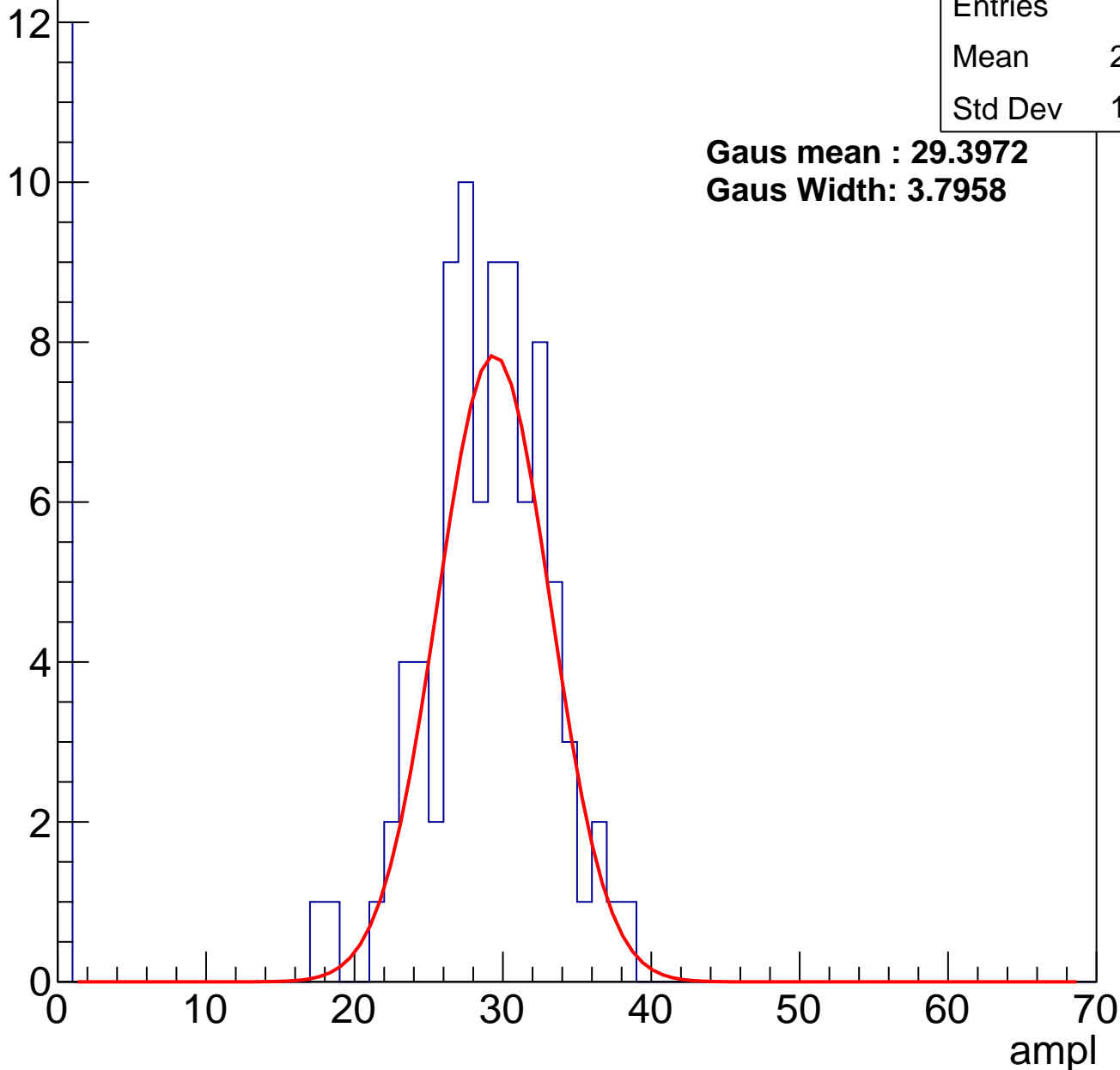
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	25.07
Std Dev	10.14

Gaus mean : 29.3972

Gaus Width: 3.7958

Entry



B1L103S, U18-ch3, adc1

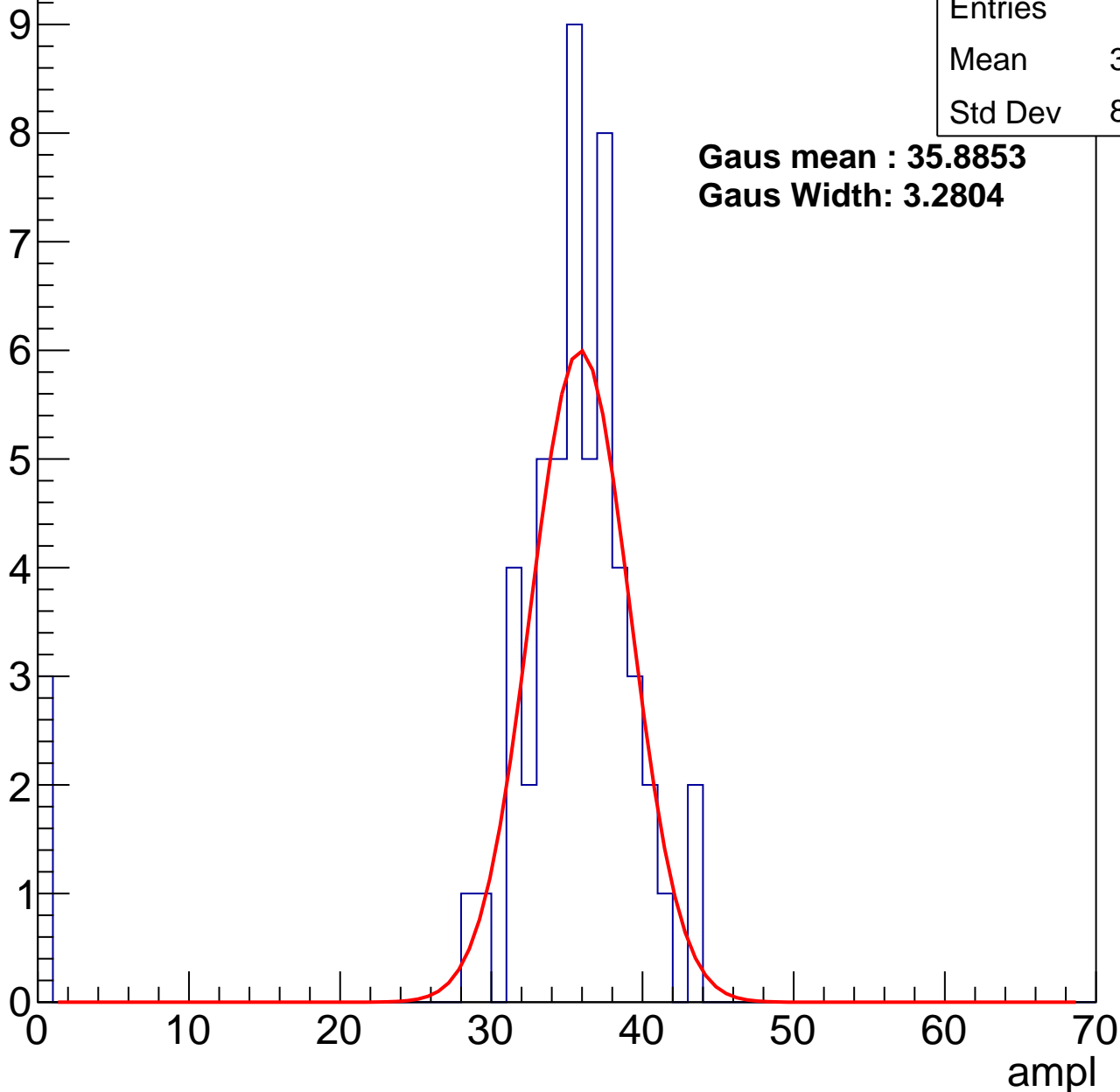
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	33.58
Std Dev	8.625

Gaus mean : 35.8853

Gaus Width: 3.2804



B1L103S, U18-ch3, adc2

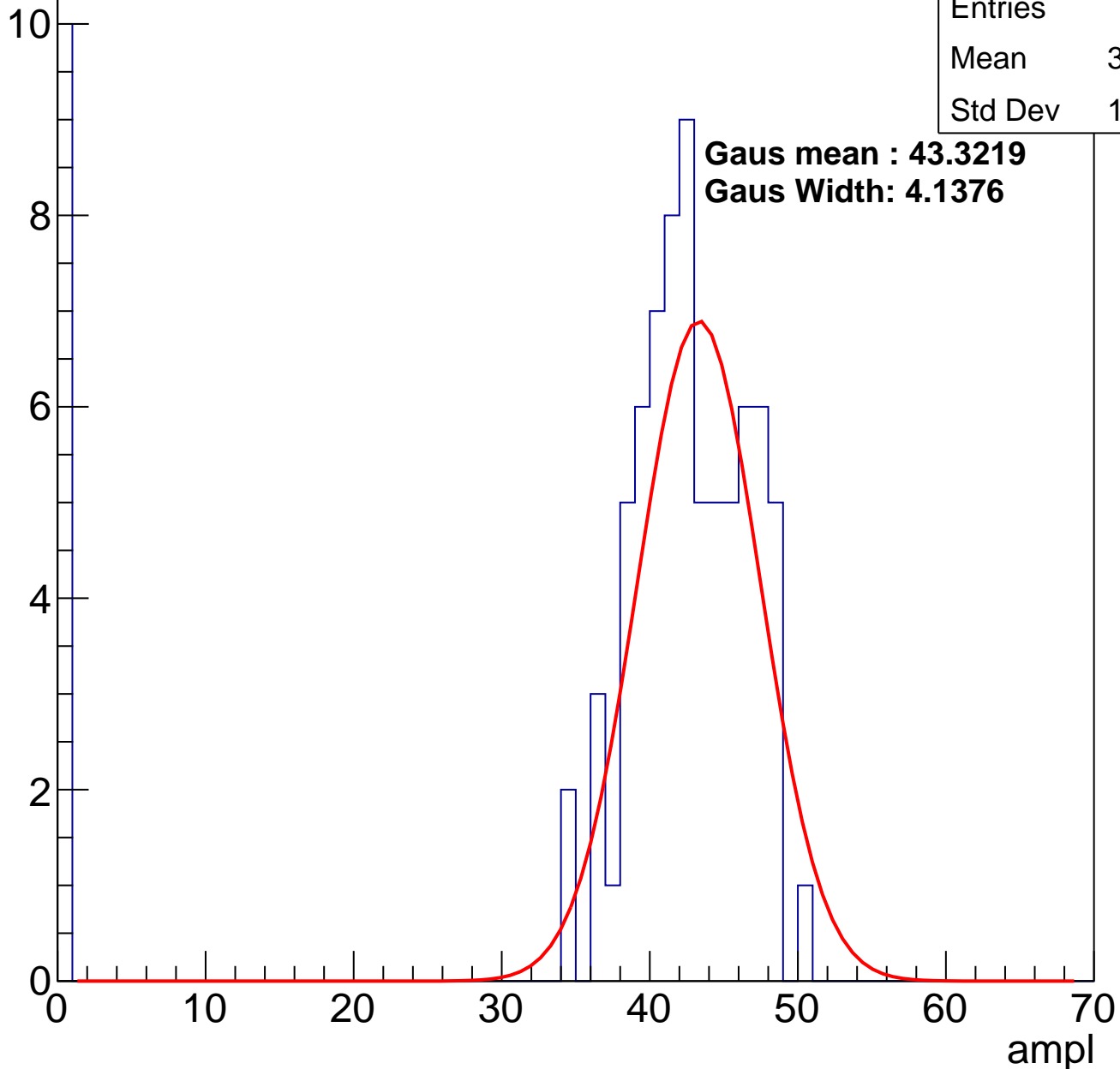
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	37.27
Std Dev	14.12

Gaus mean : 43.3219

Gaus Width: 4.1376

Entry

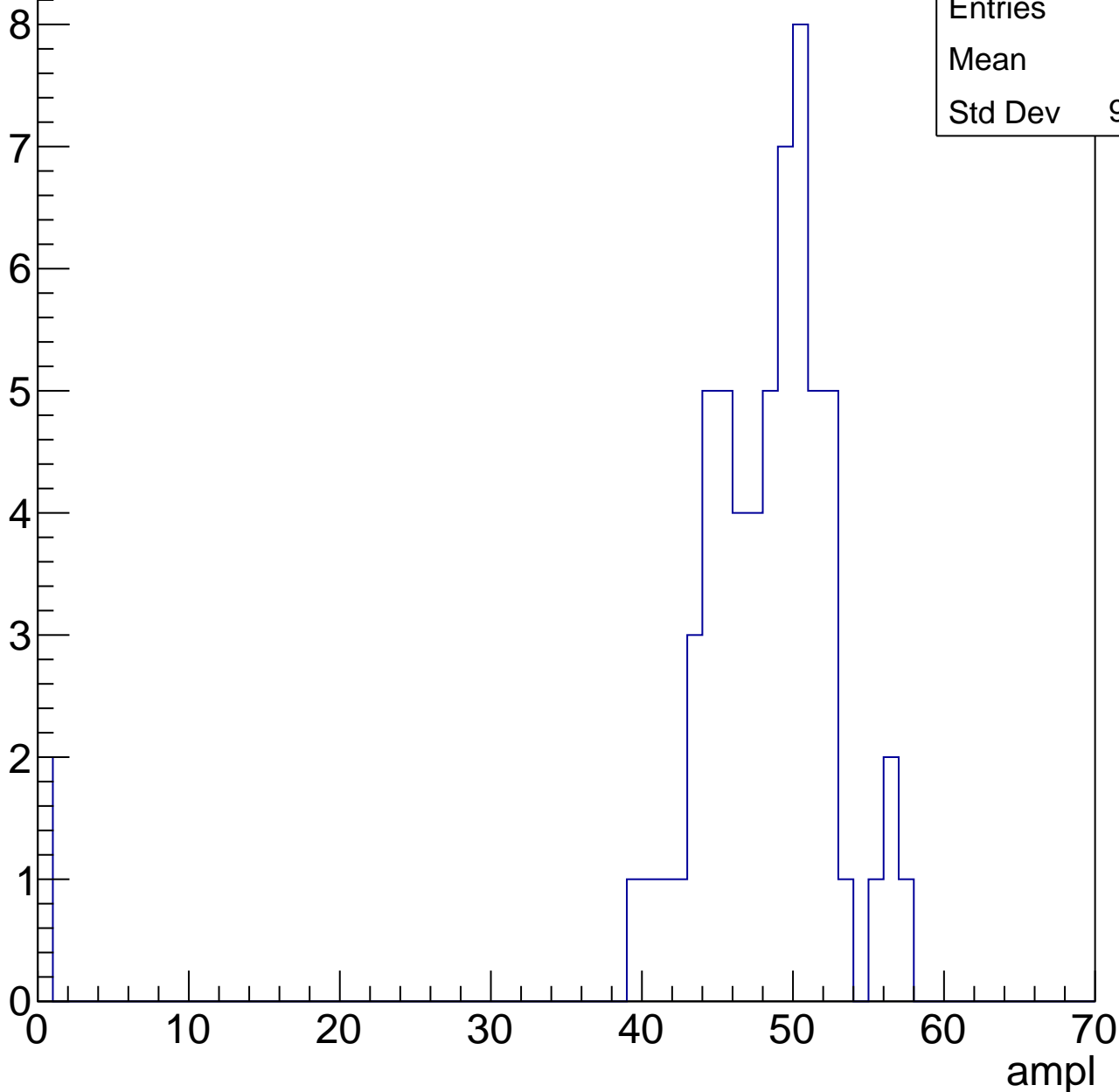


B1L103S, U18-ch3, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	46.5
Std Dev	9.303

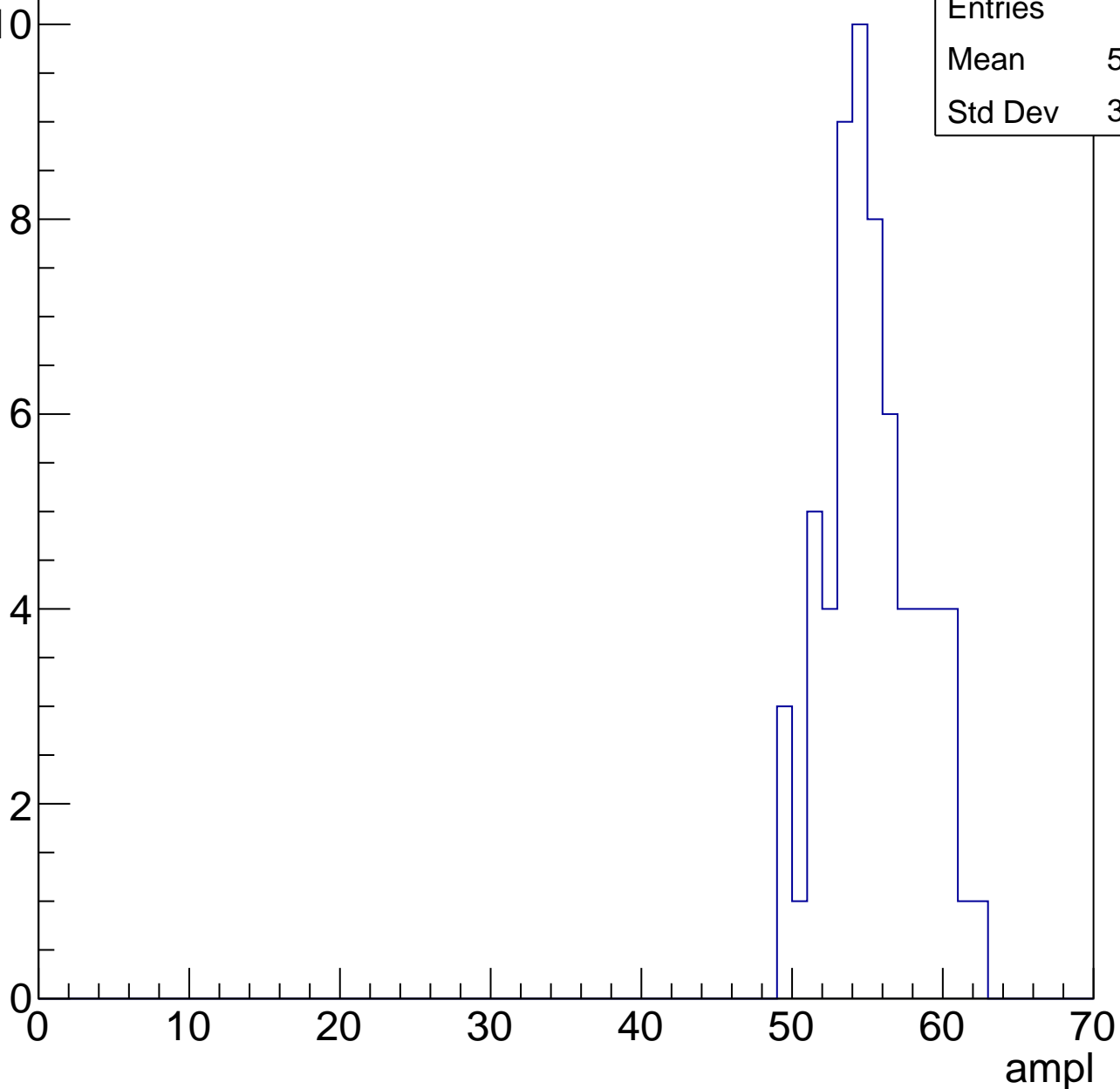


B1L103S, U18-ch3, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.88
Std Dev	3.064

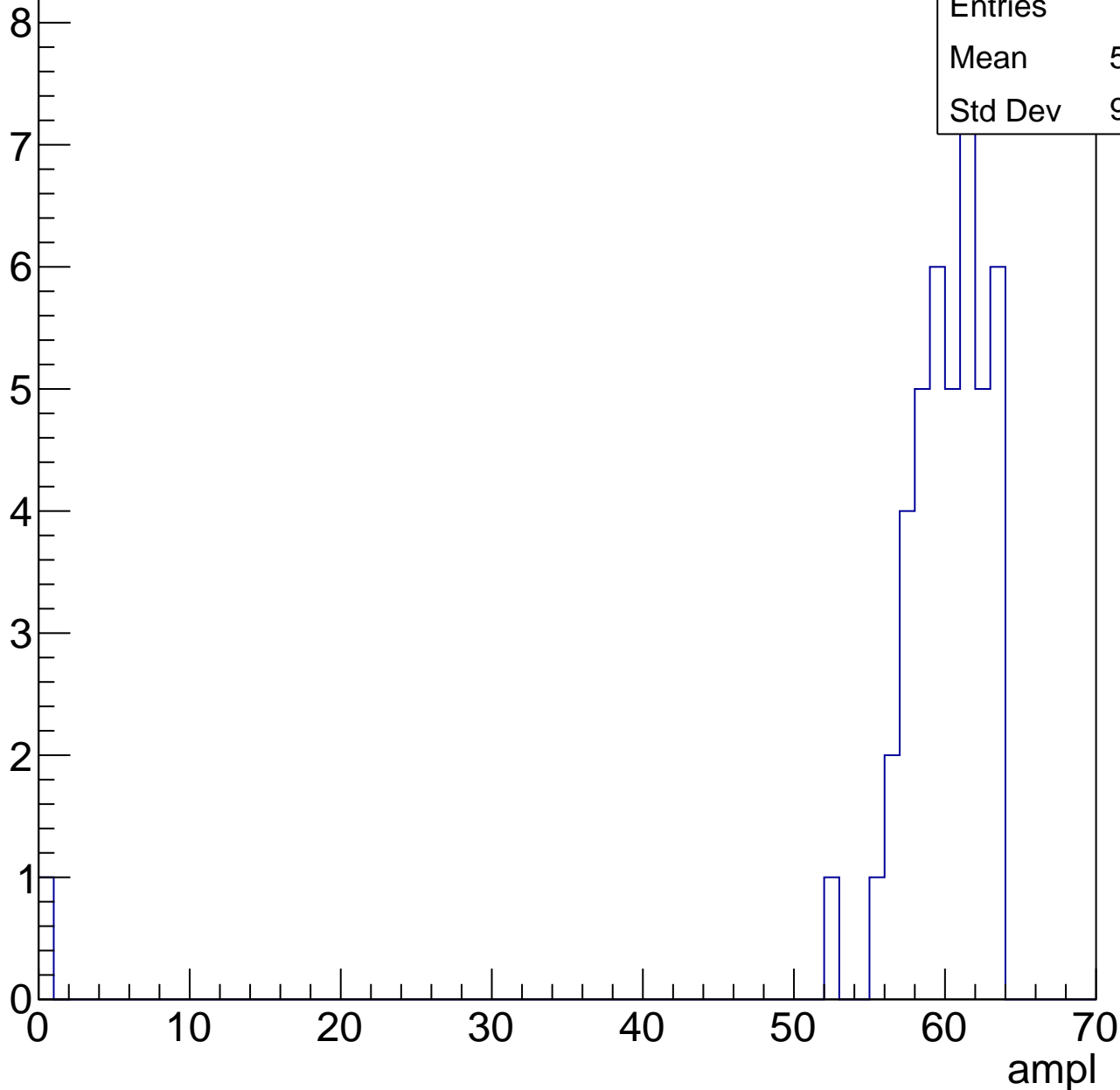


B1L103S, U18-ch3, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

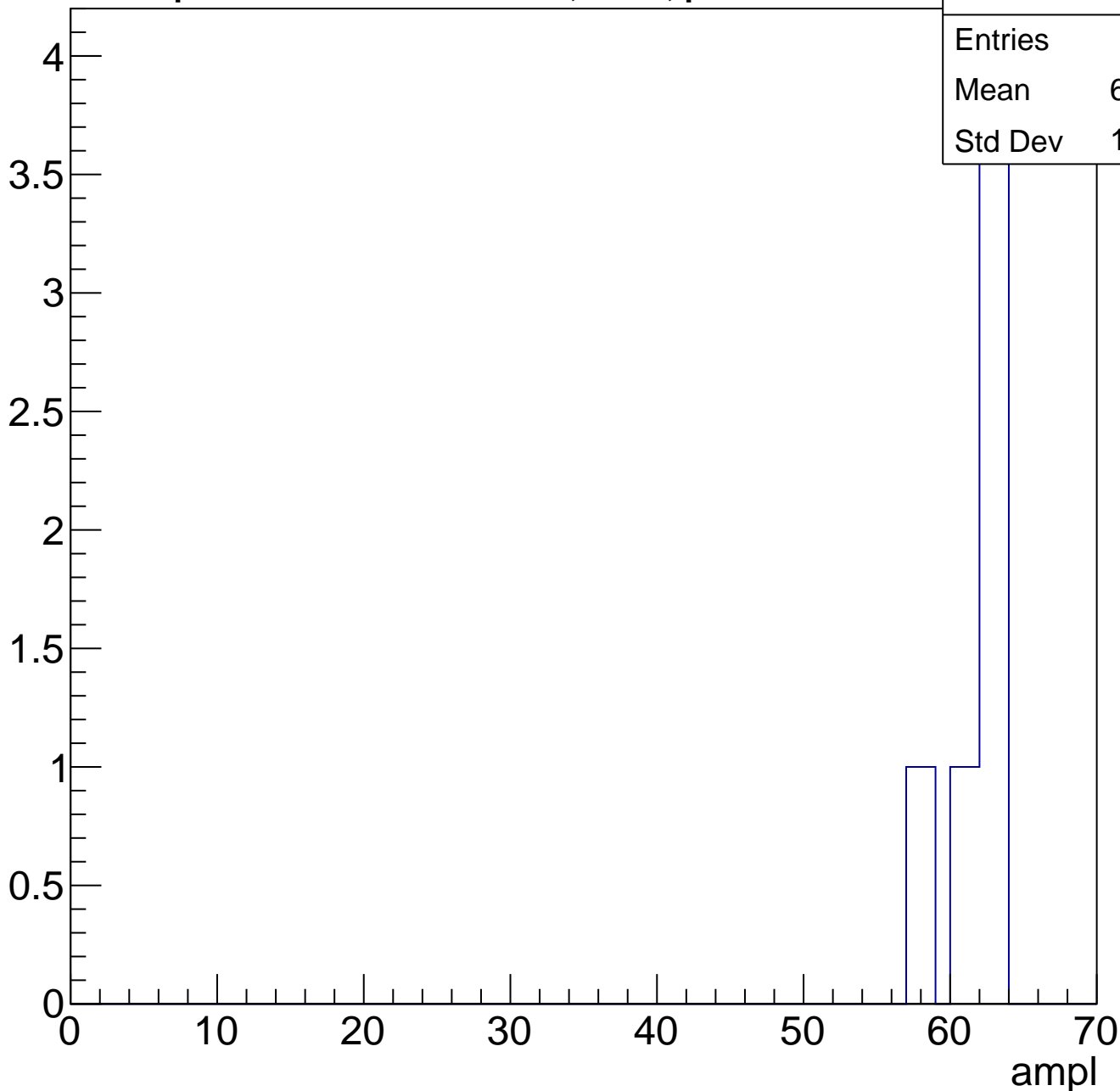
Entries	44
Mean	58.34
Std Dev	9.224



B1L103S, U18-ch3, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch3, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U18-ch4, adc0

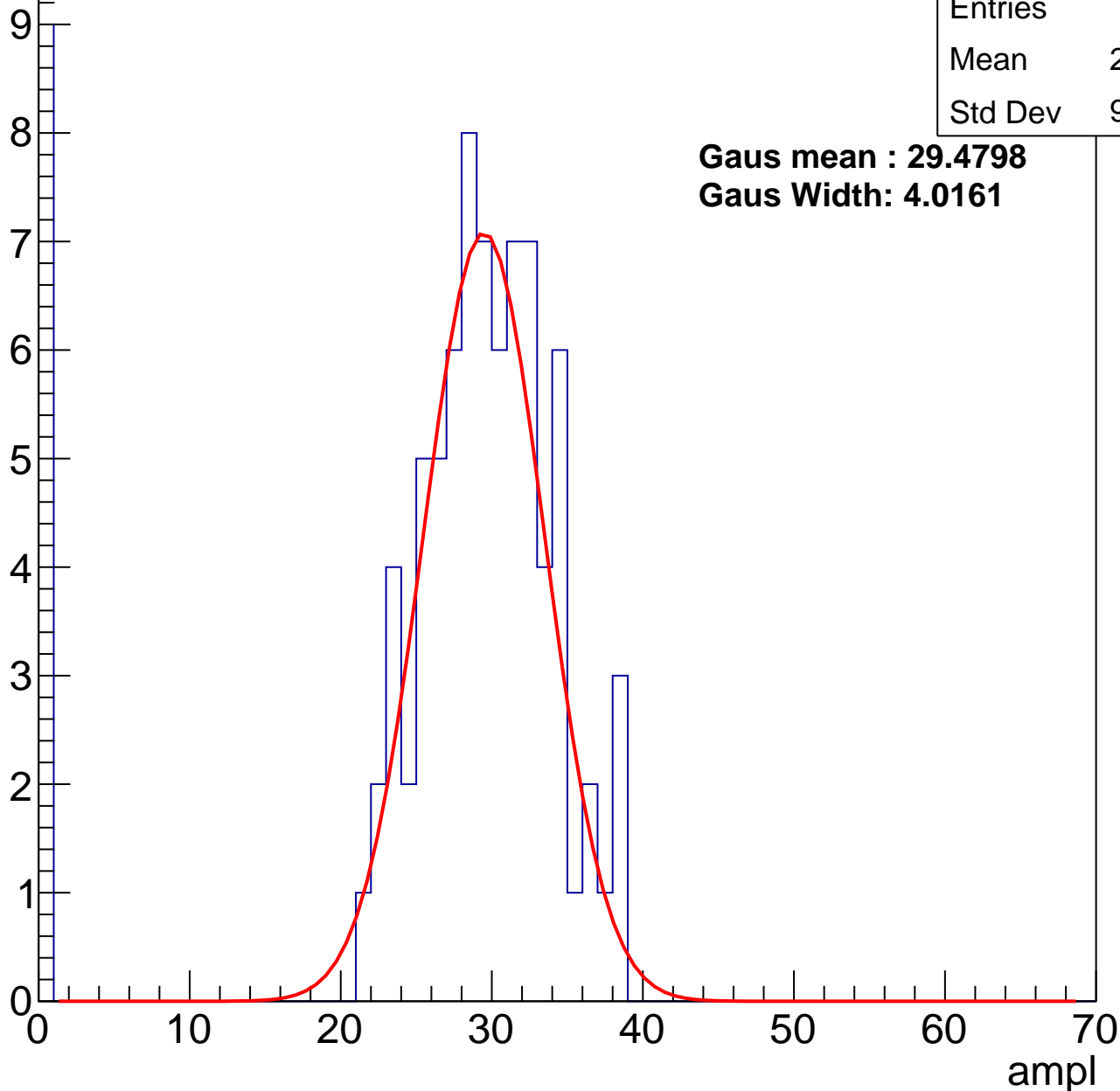
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	26.33
Std Dev	9.776

Gaus mean : 29.4798

Gaus Width: 4.0161



B1L103S, U18-ch4, adc1

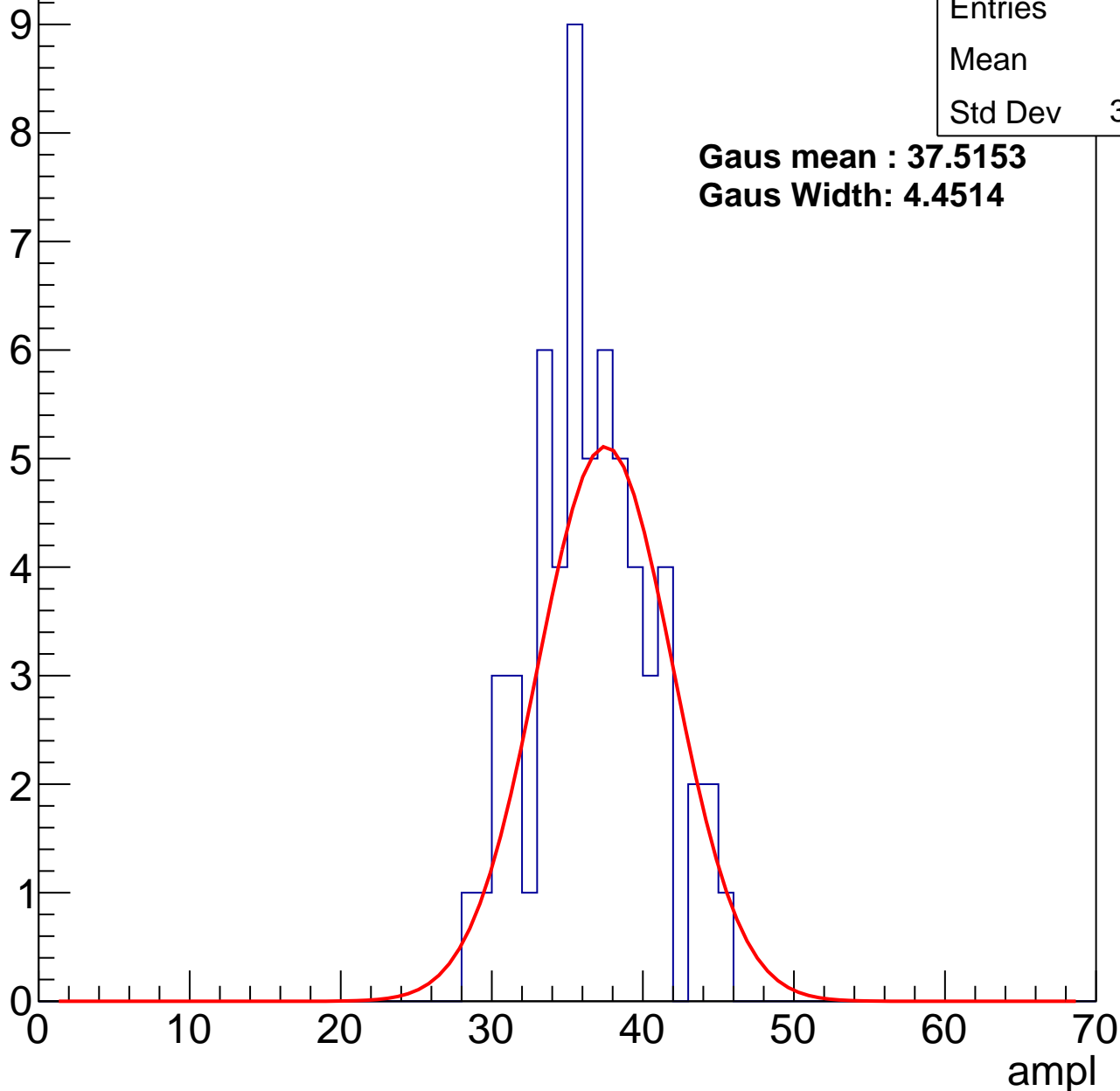
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	36.2
Std Dev	3.885

Gaus mean : 37.5153

Gaus Width: 4.4514



B1L103S, U18-ch4, adc2

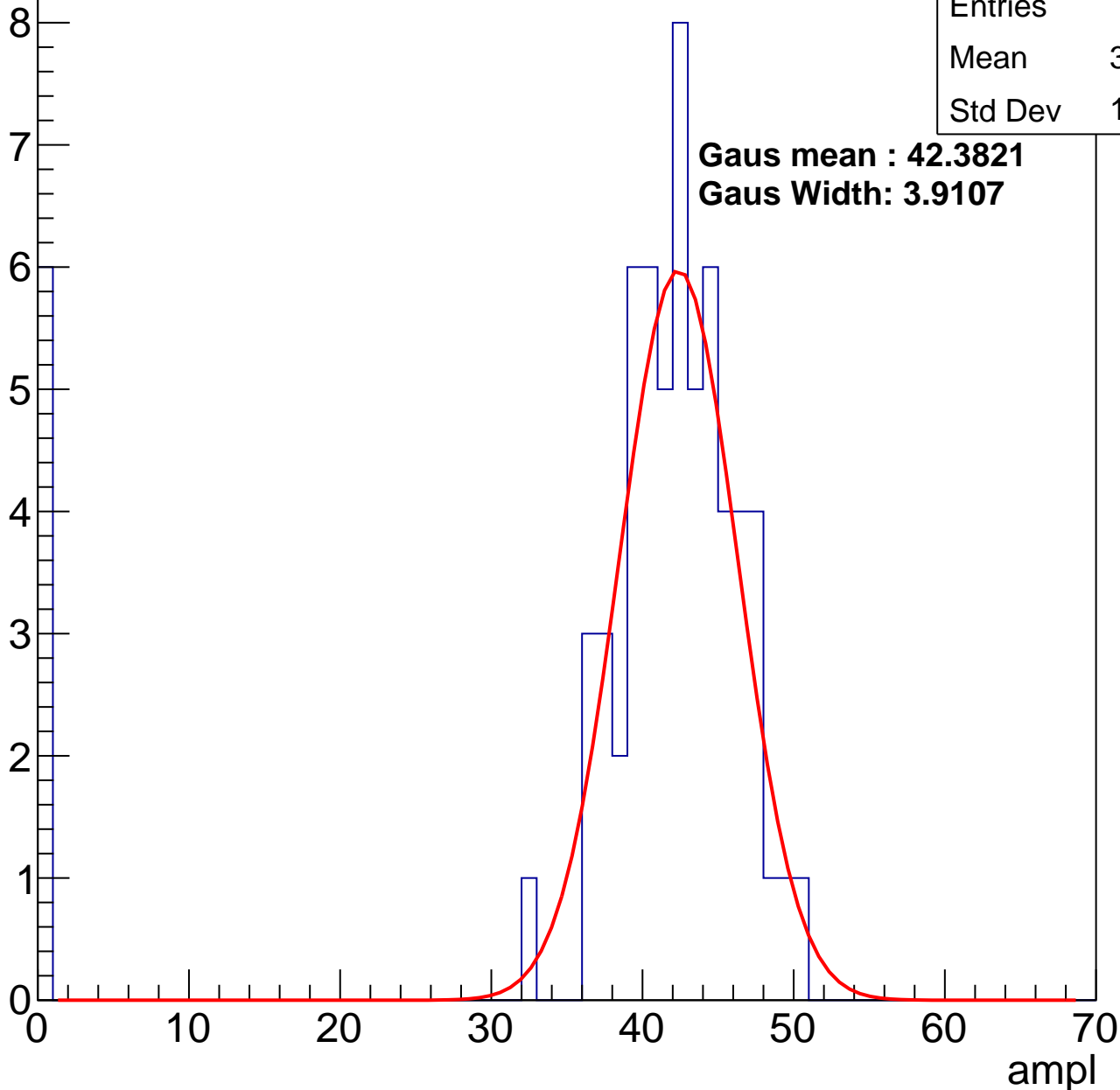
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	38.18
Std Dev	12.55

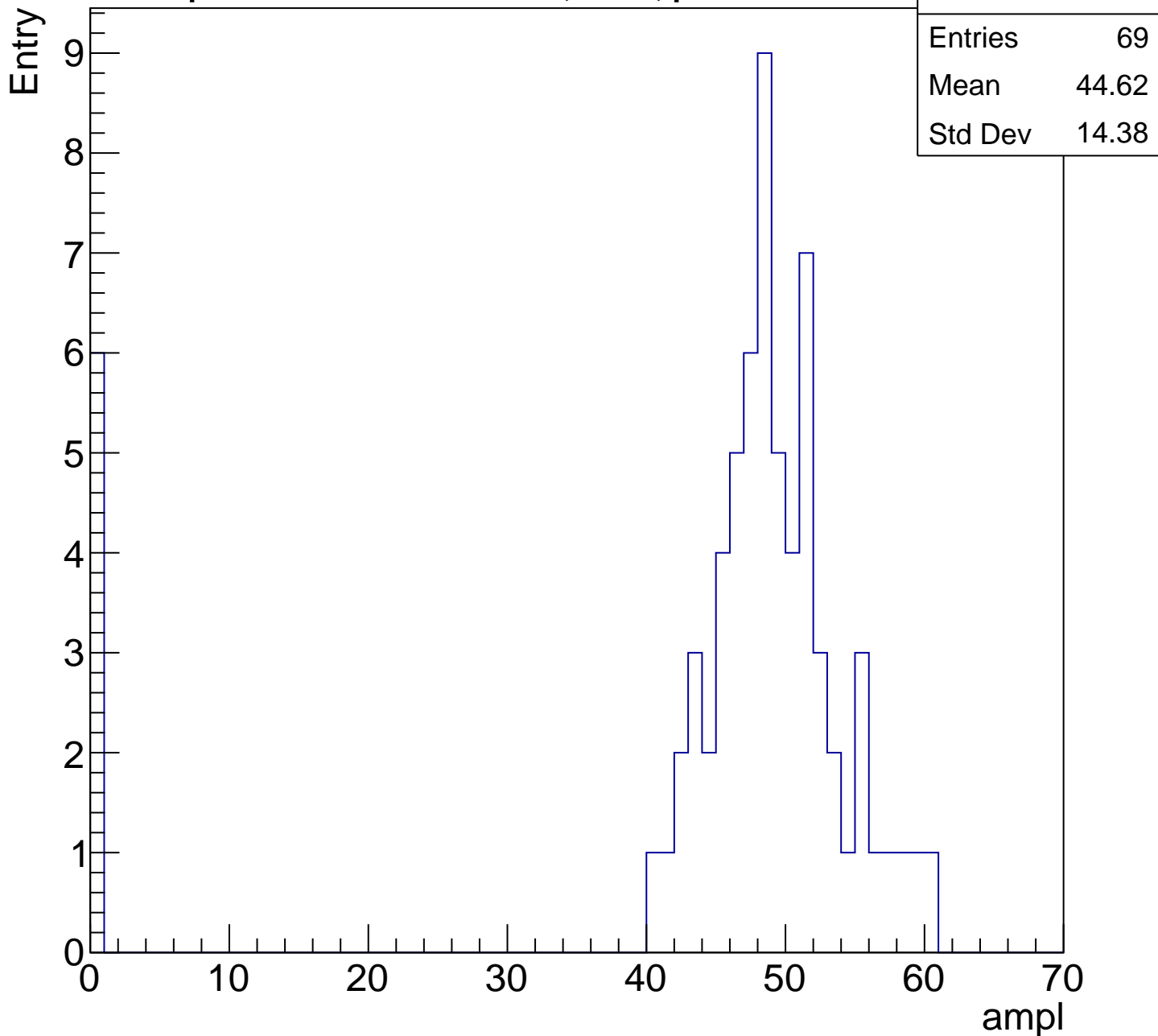
Gaus mean : 42.3821

Gaus Width: 3.9107



B1L103S, U18-ch4, adc3

calib_packv5_041523_1651.root, FC#0, port C2

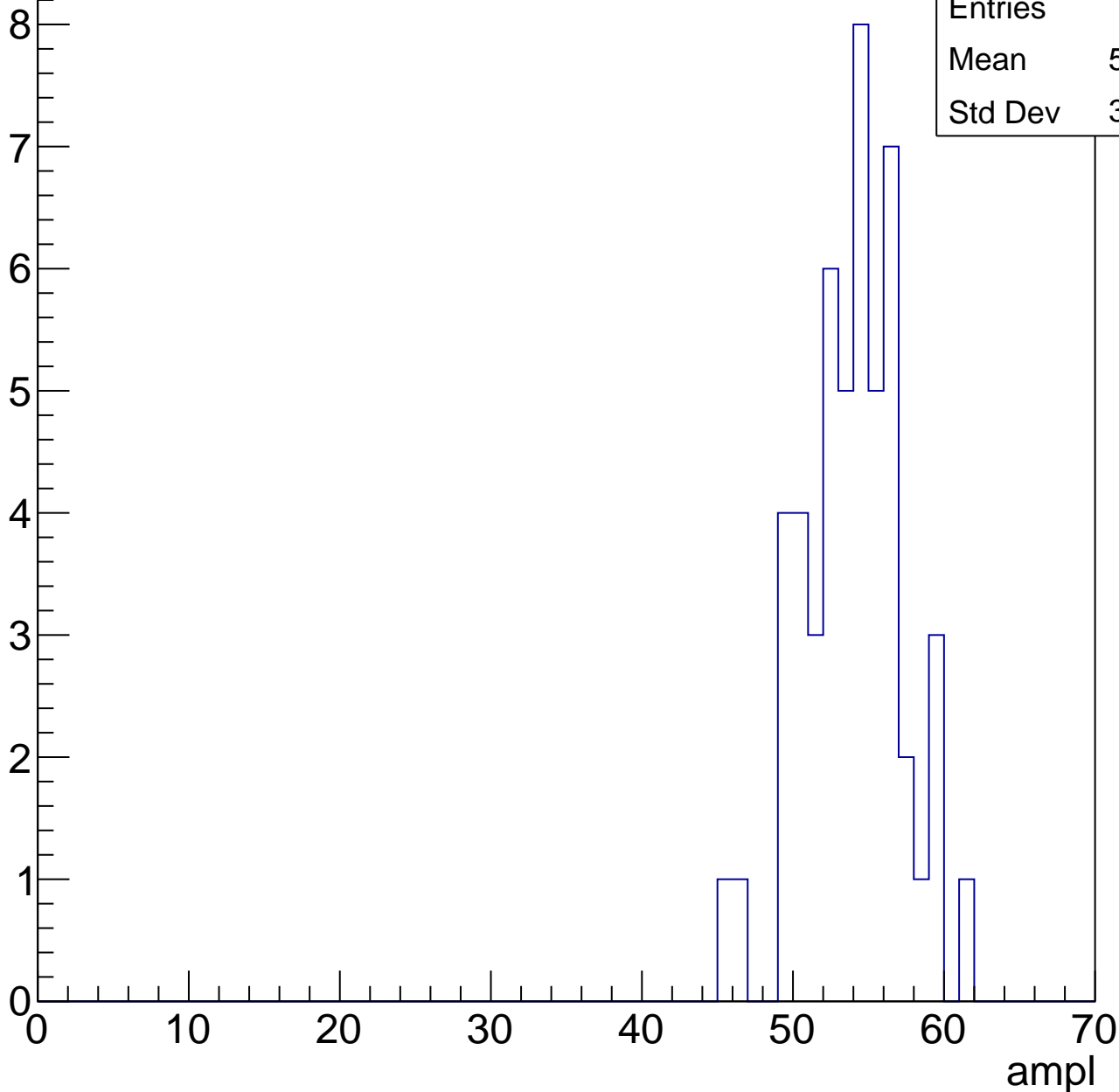


B1L103S, U18-ch4, adc4

calib_packv5_041523_1651.root, FC#0, port C2

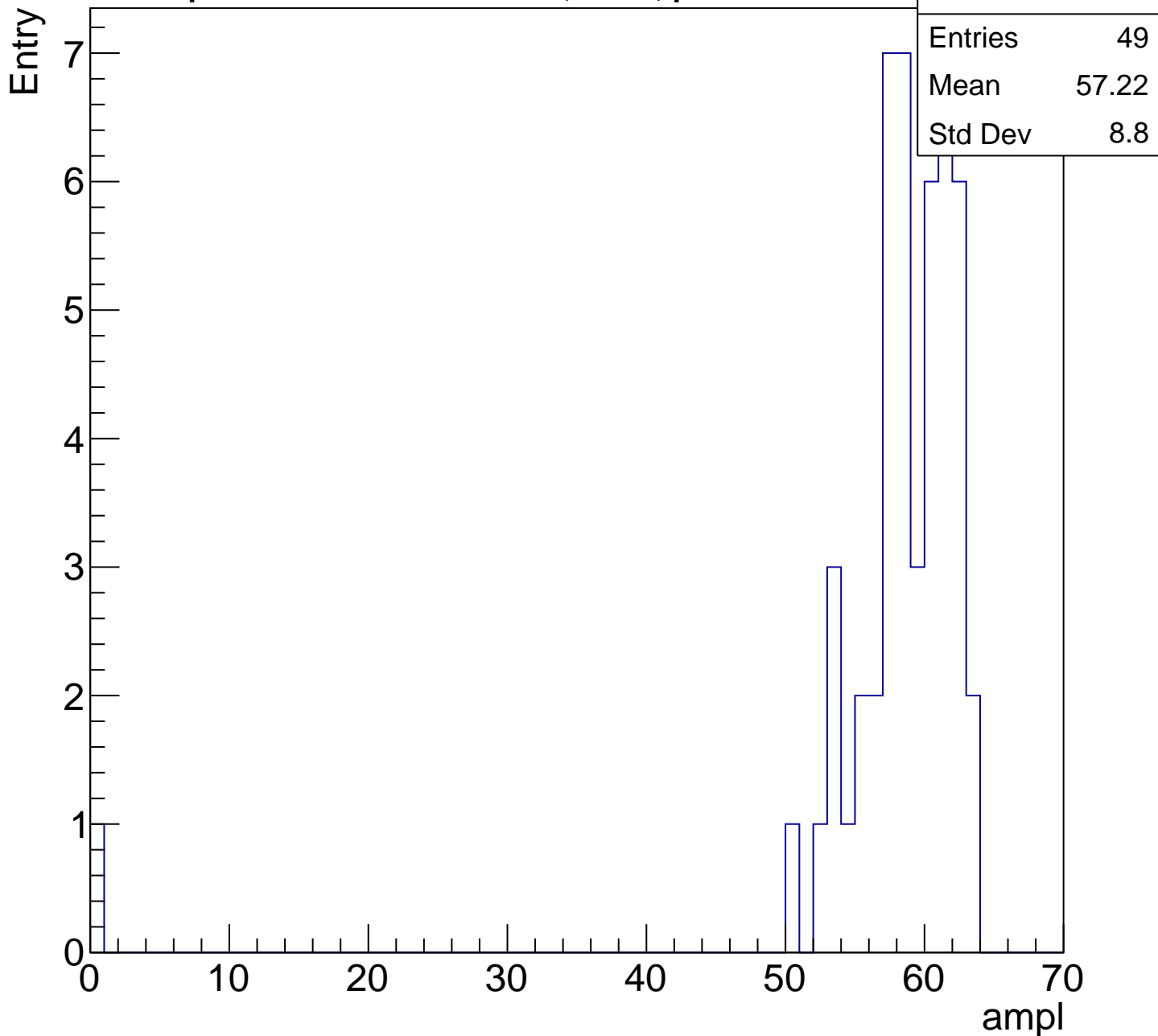
Entry

Entries	51
Mean	53.45
Std Dev	3.256



B1L103S, U18-ch4, adc5

calib_packv5_041523_1651.root, FC#0, port C2

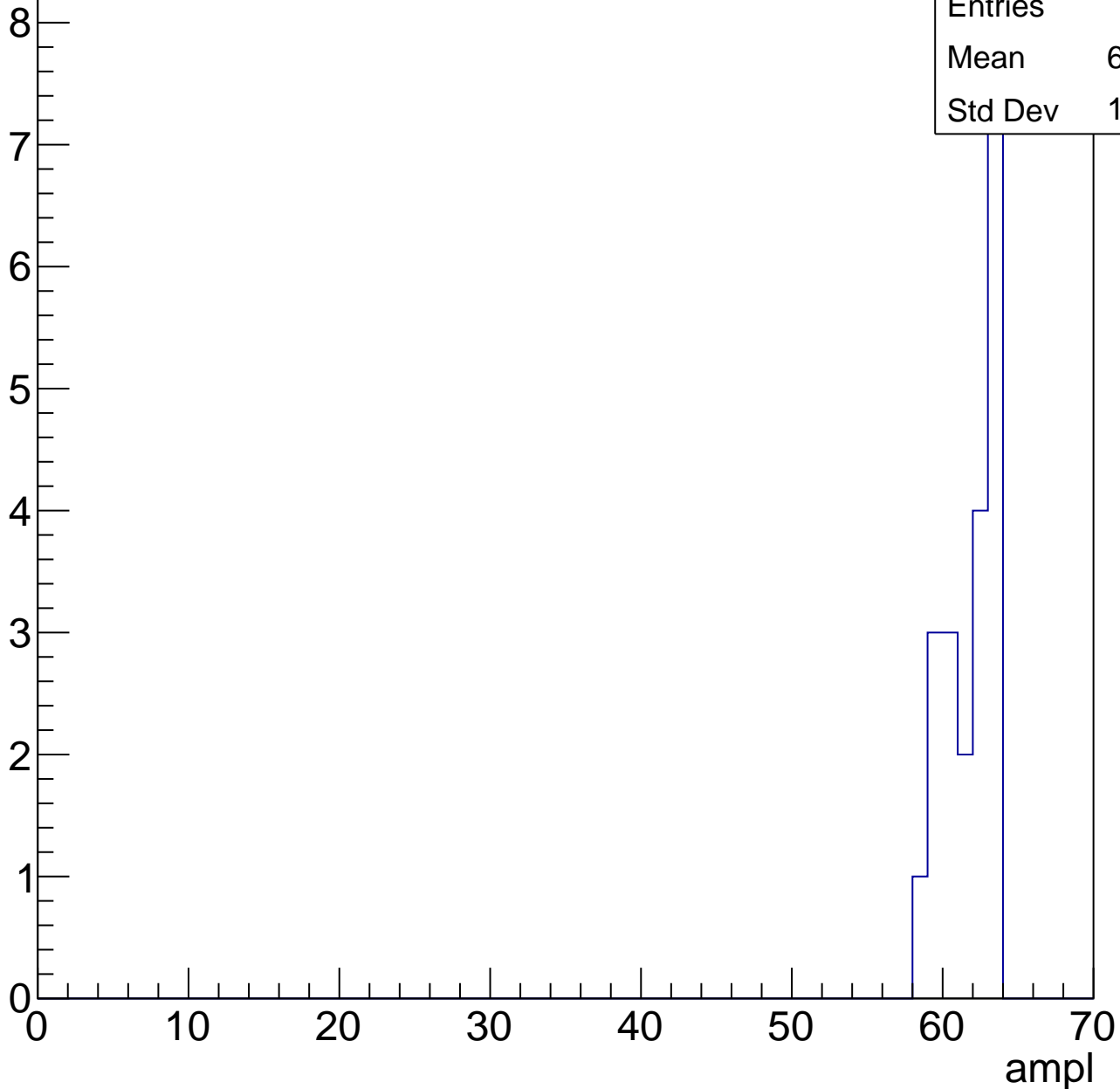


B1L103S, U18-ch4, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	61.38
Std Dev	1.647



B1L103S, U18-ch4, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch5, adc0

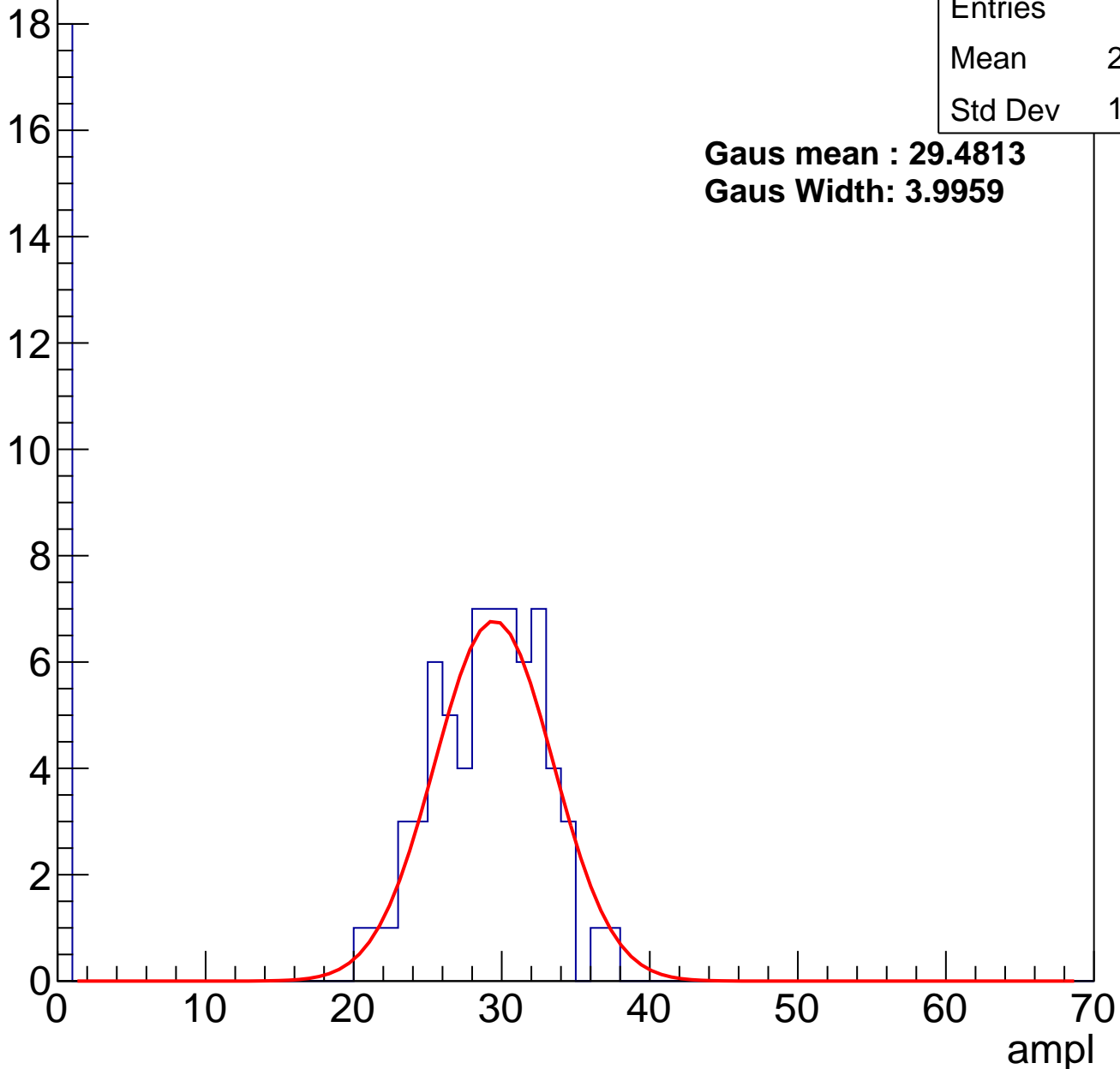
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	22.56
Std Dev	12.13

Gaus mean : 29.4813

Gaus Width: 3.9959

Entry



B1L103S, U18-ch5, adc1

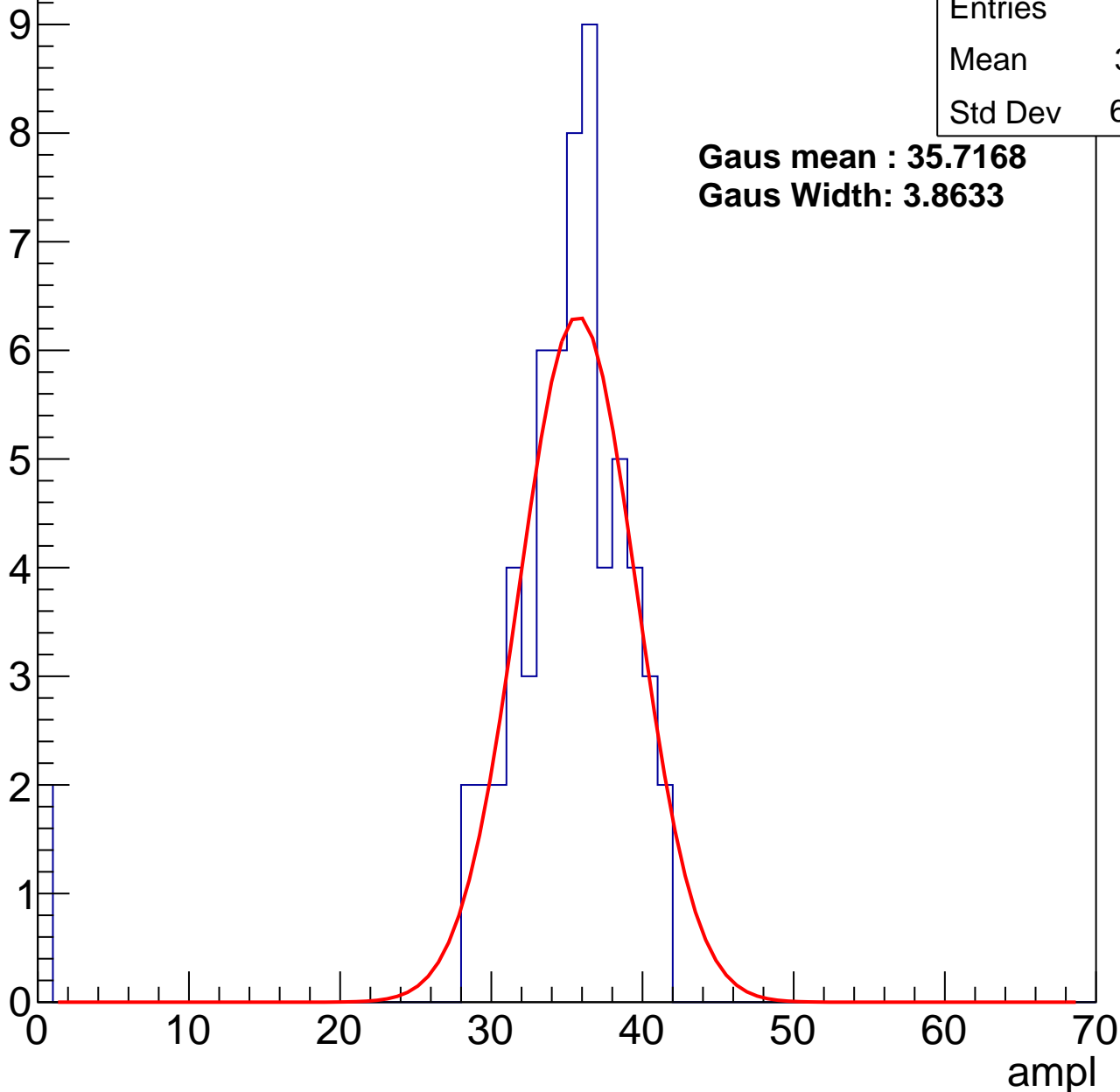
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	33.81
Std Dev	6.937

Gaus mean : 35.7168

Gaus Width: 3.8633



B1L103S, U18-ch5, adc2

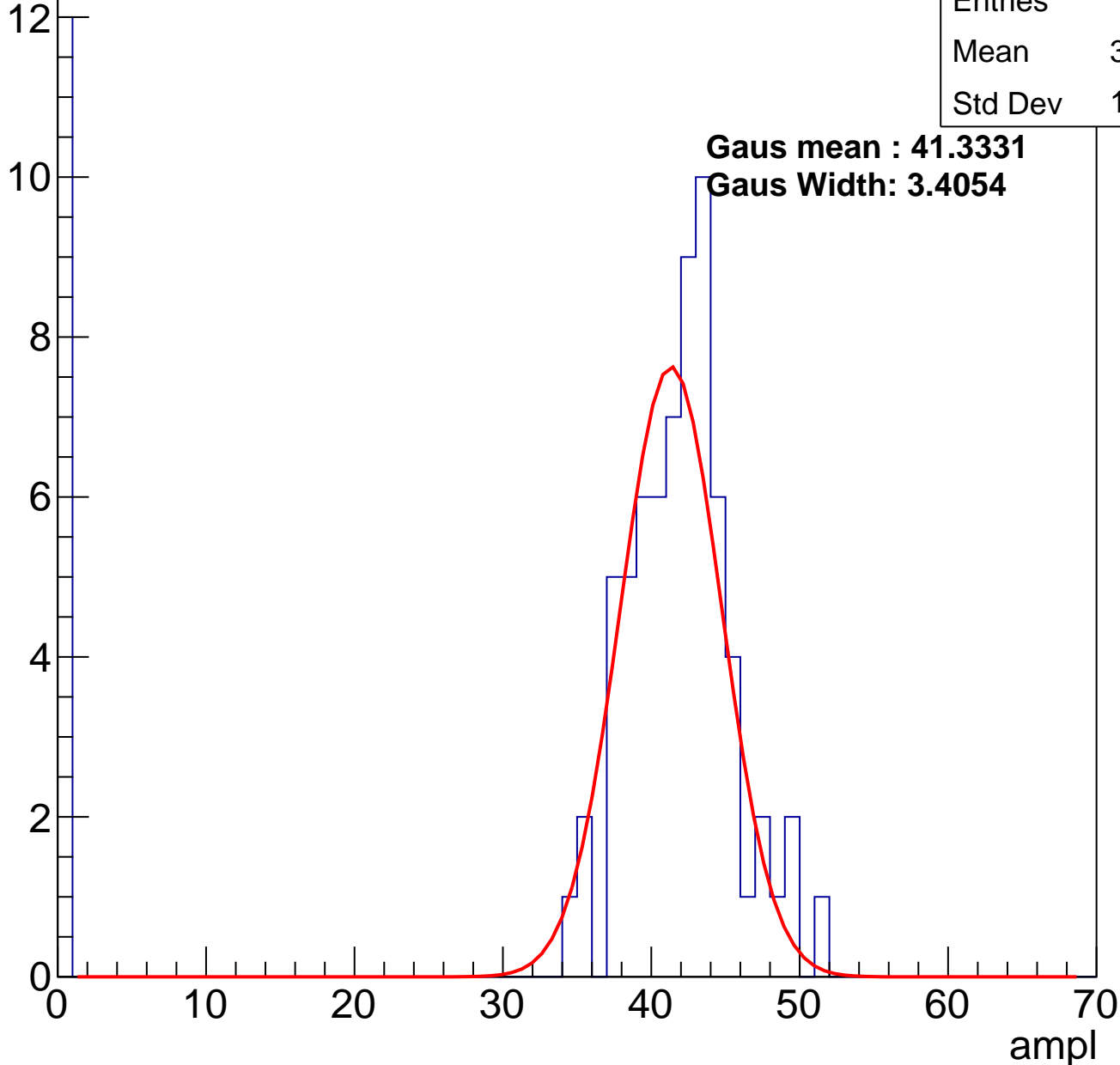
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	35.36
Std Dev	15.18

Gaus mean : 41.3331

Gaus Width: 3.4054

Entry

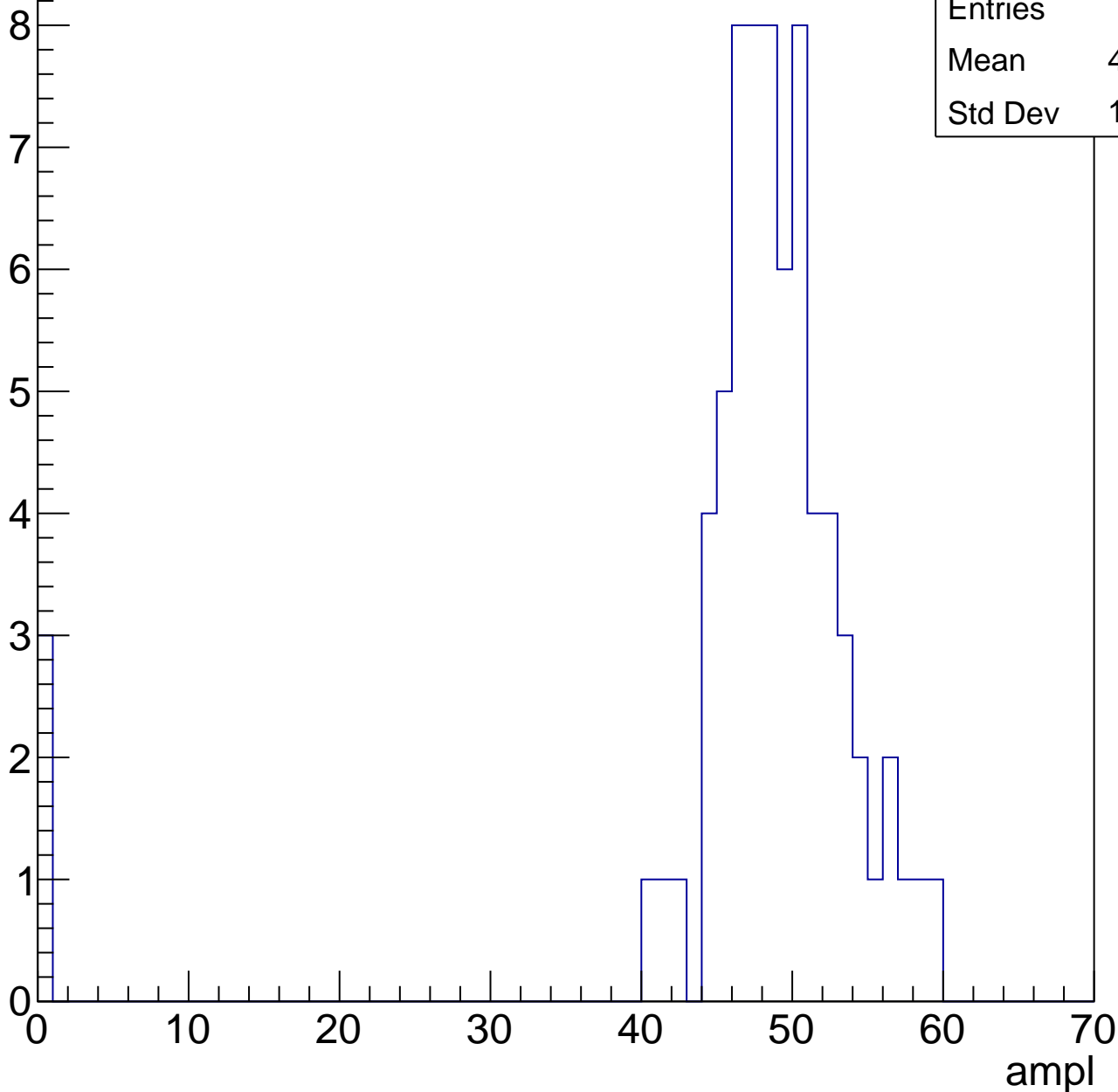


B1L103S, U18-ch5, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	46.75
Std Dev	10.45

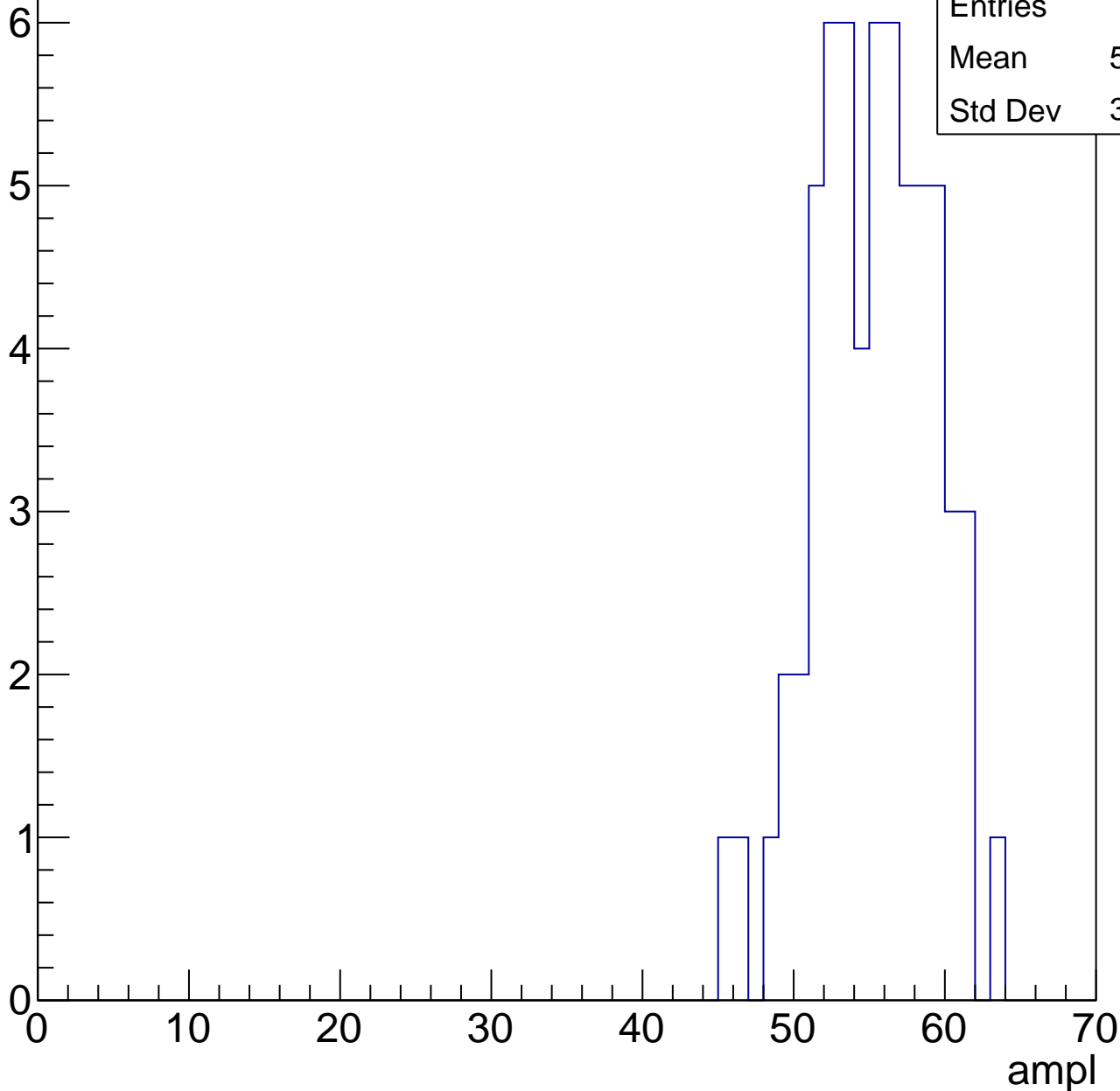


B1L103S, U18-ch5, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	54.84
Std Dev	3.832

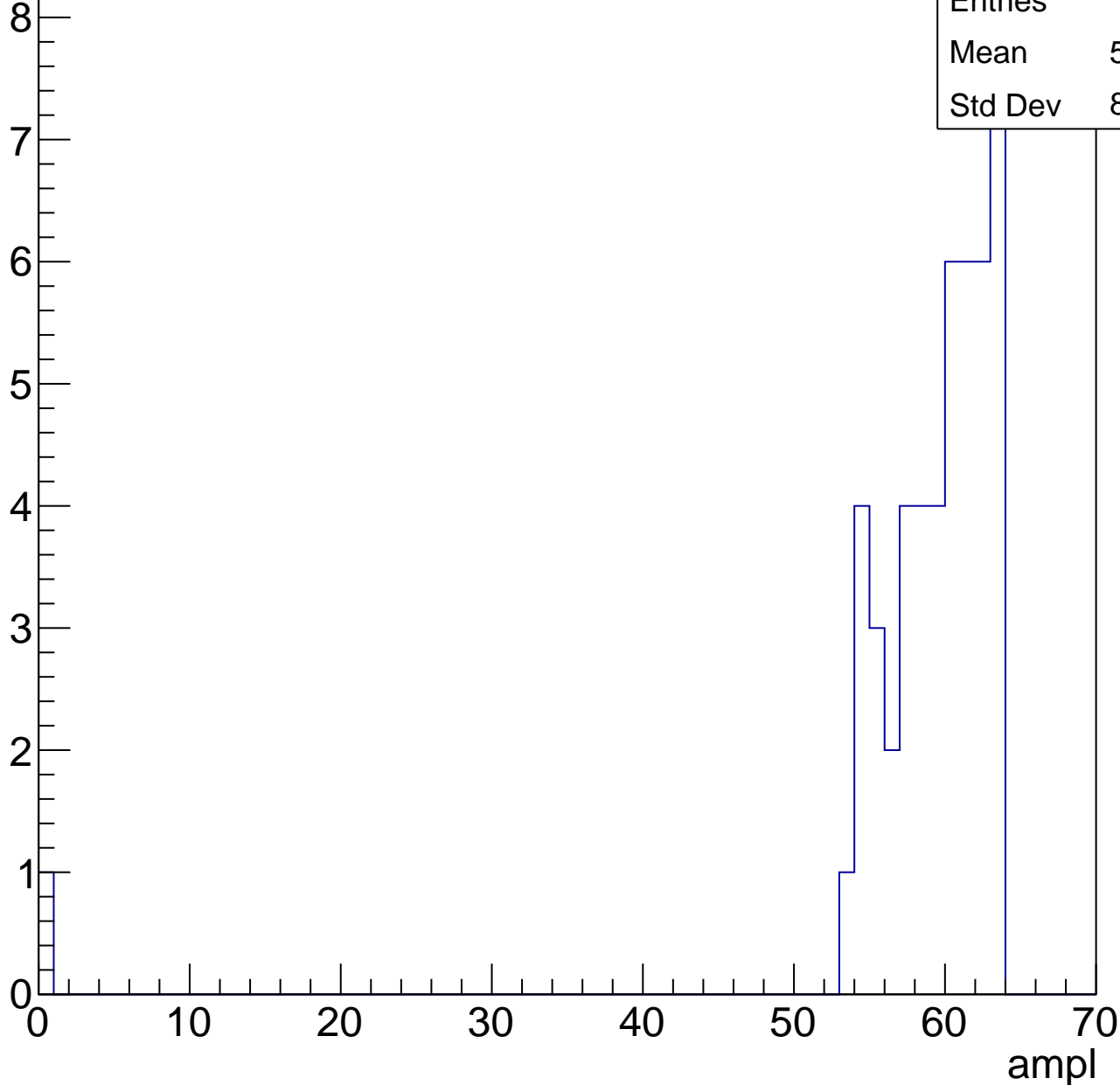


B1L103S, U18-ch5, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

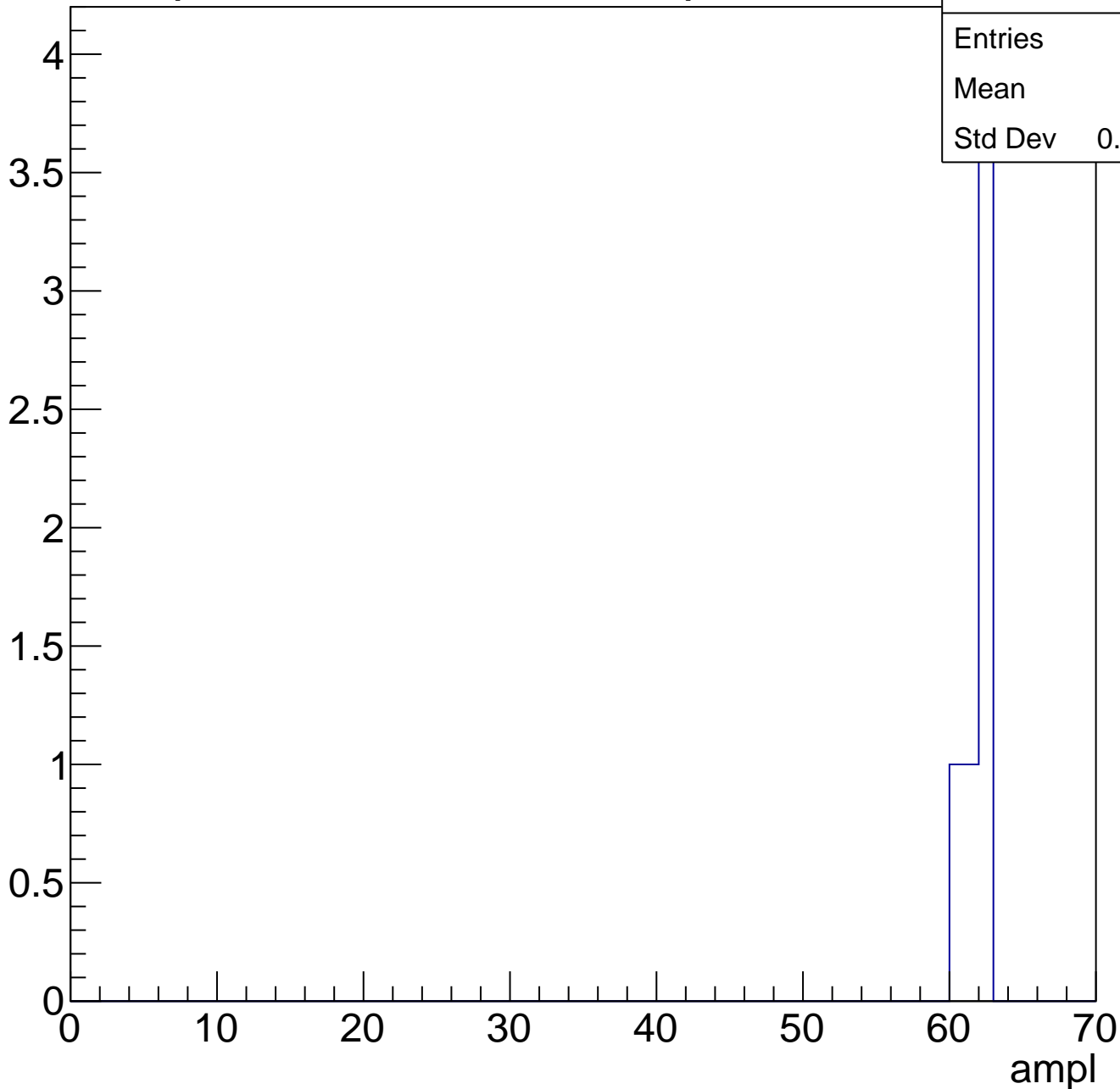
Entries	49
Mean	58.04
Std Dev	8.887



B1L103S, U18-ch5, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch5, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	20
Mean	3.15
Std Dev	13.73

B1L103S, U18-ch6, adc0

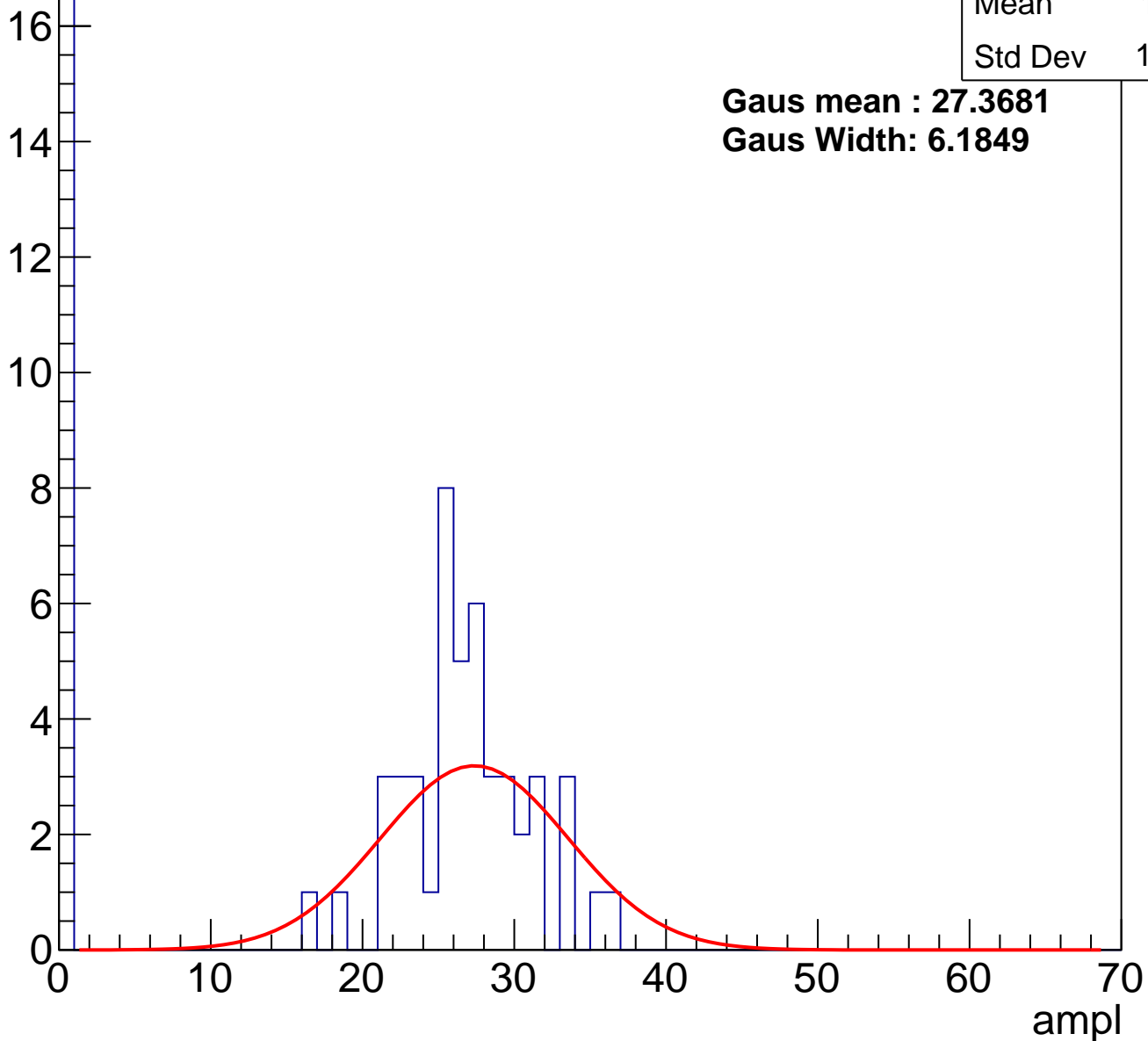
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	19.41
Std Dev	12.19

Gaus mean : 27.3681

Gaus Width: 6.1849

Entry



B1L103S, U18-ch6, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	31.99
Std Dev	7.078

Gaus mean : 33.5965

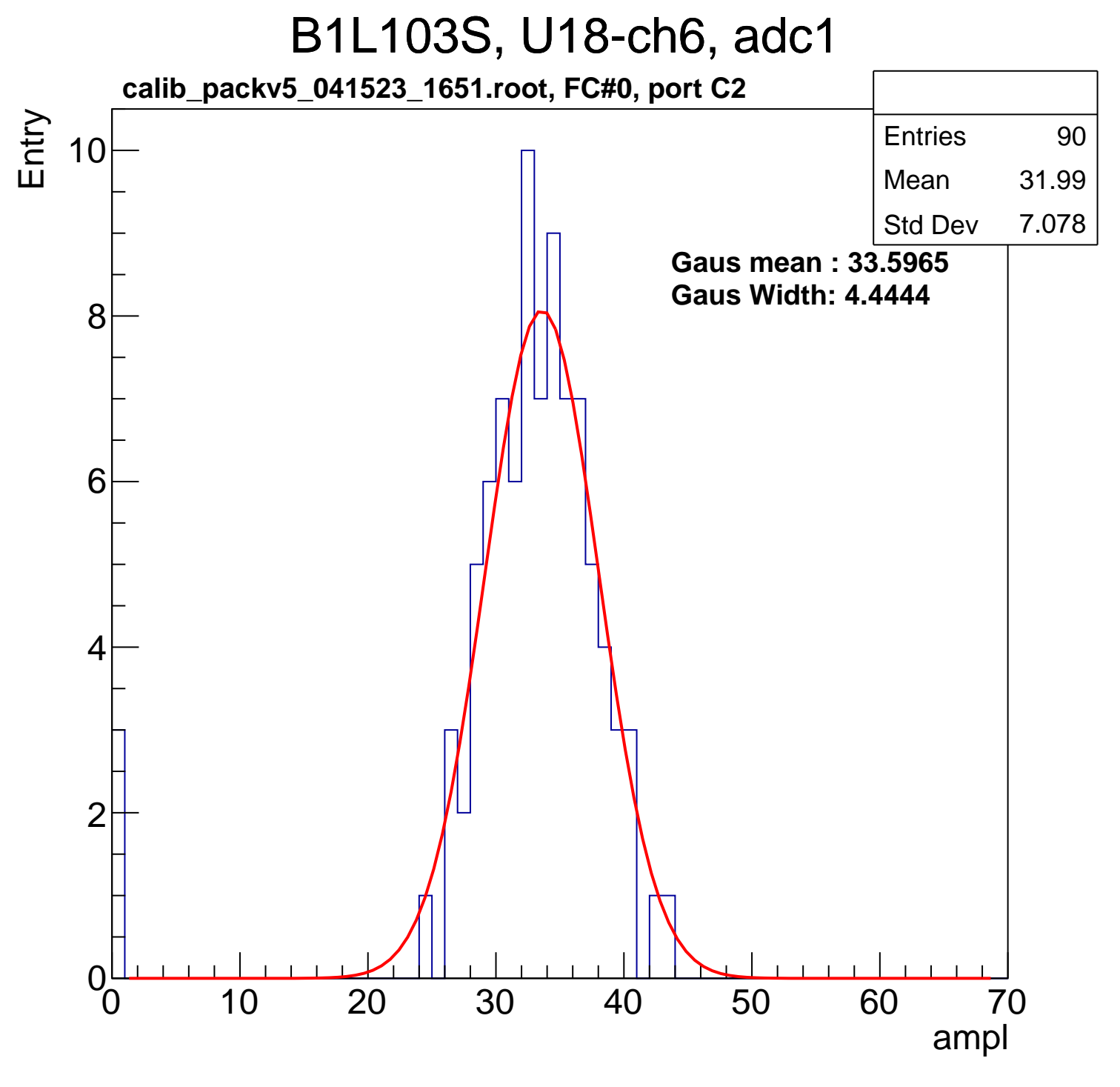
Gaus Width: 4.4444

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch6, adc2

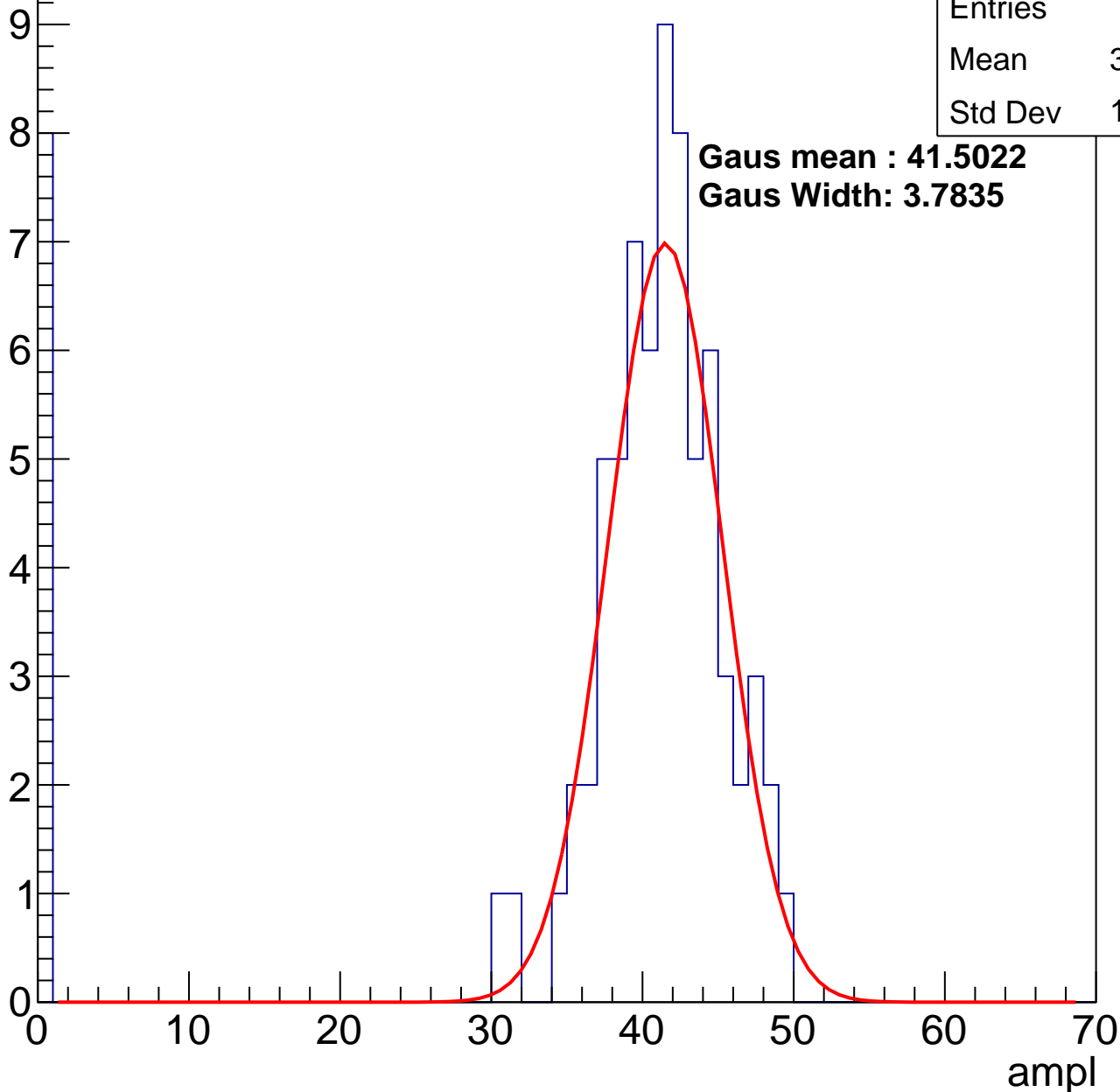
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	36.65
Std Dev	12.99

Gaus mean : 41.5022

Gaus Width: 3.7835

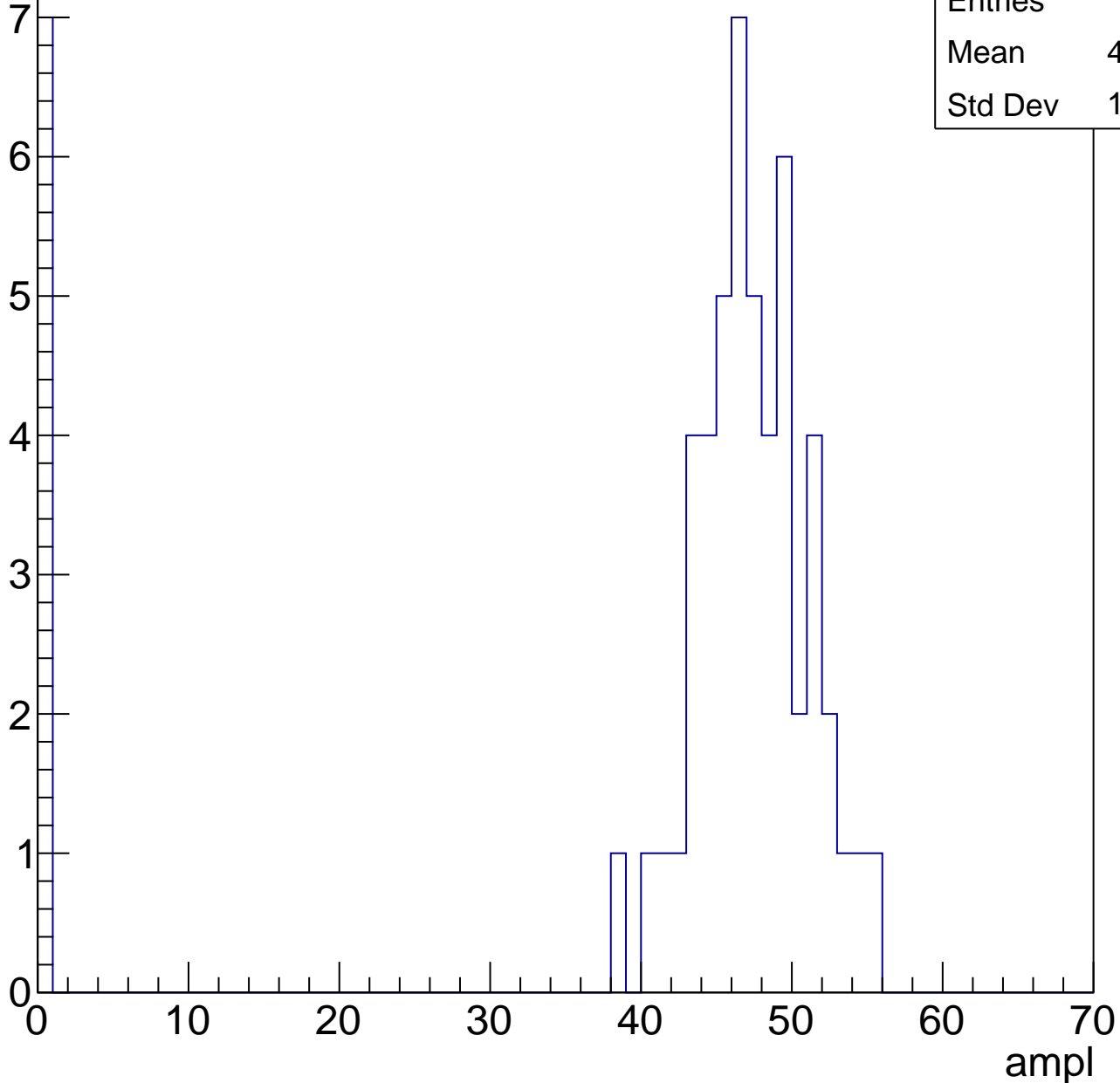


B1L103S, U18-ch6, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	41.18
Std Dev	15.76

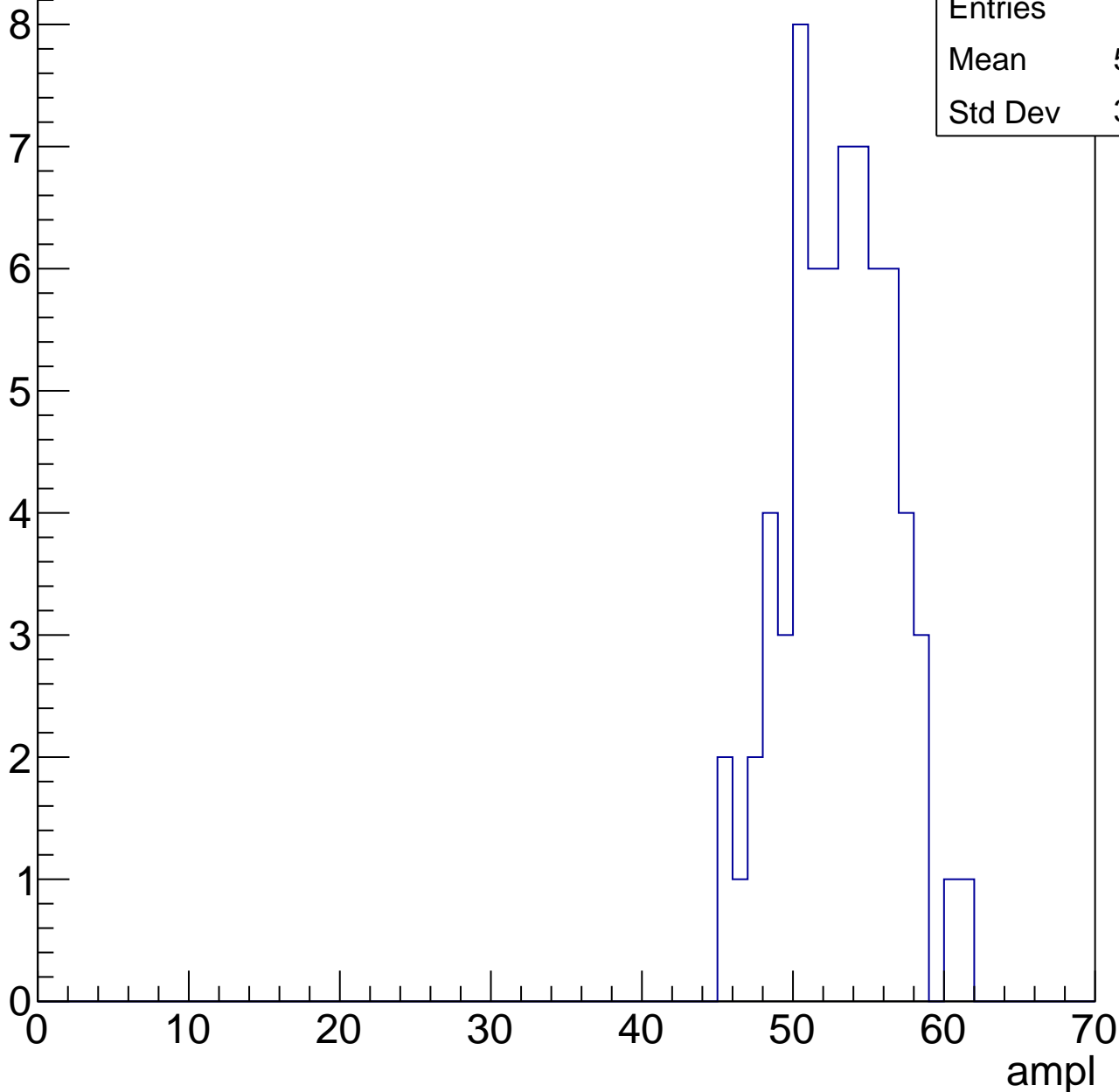


B1L103S, U18-ch6, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	52.61
Std Dev	3.511

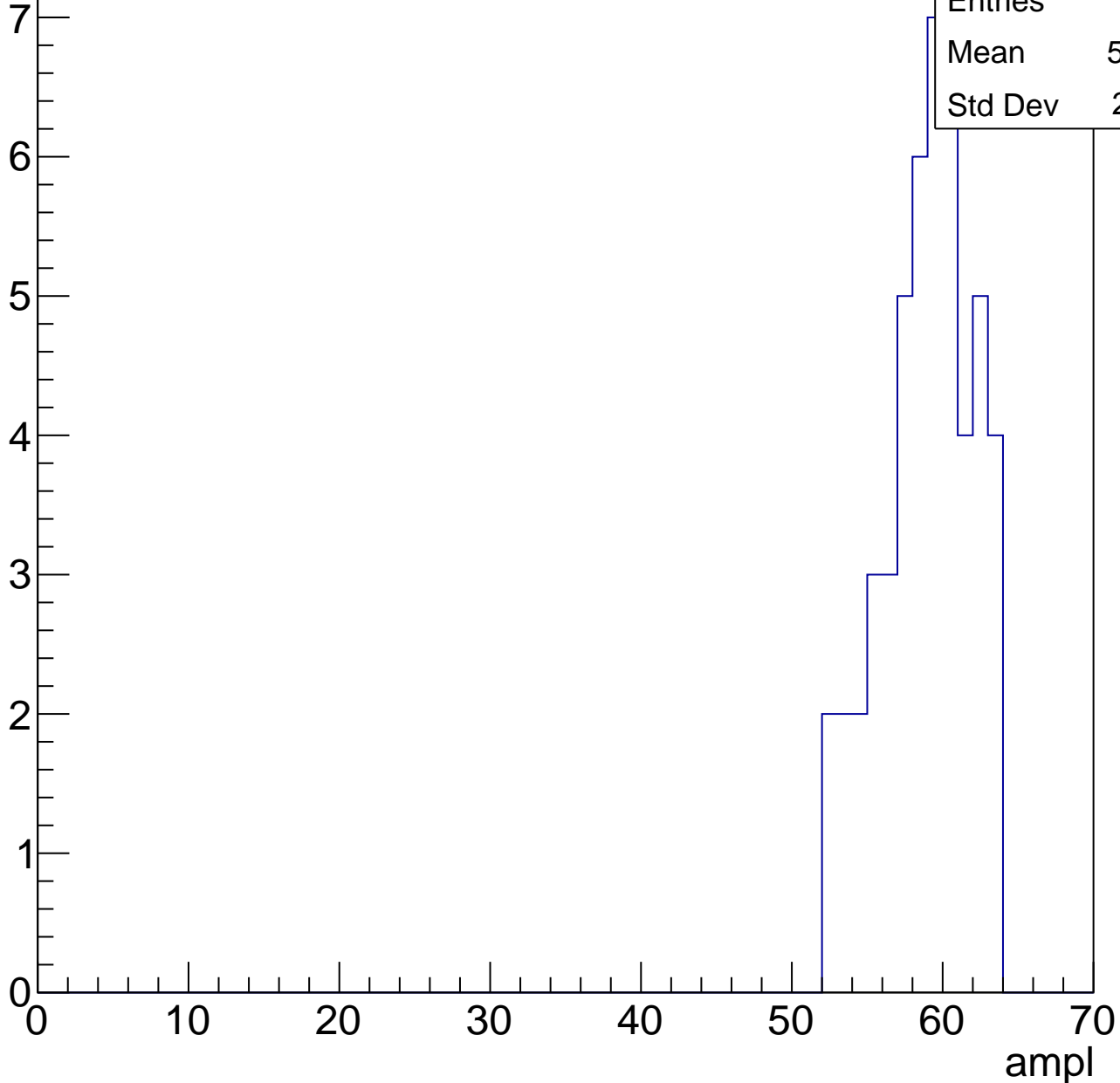


B1L103S, U18-ch6, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.46
Std Dev	2.961

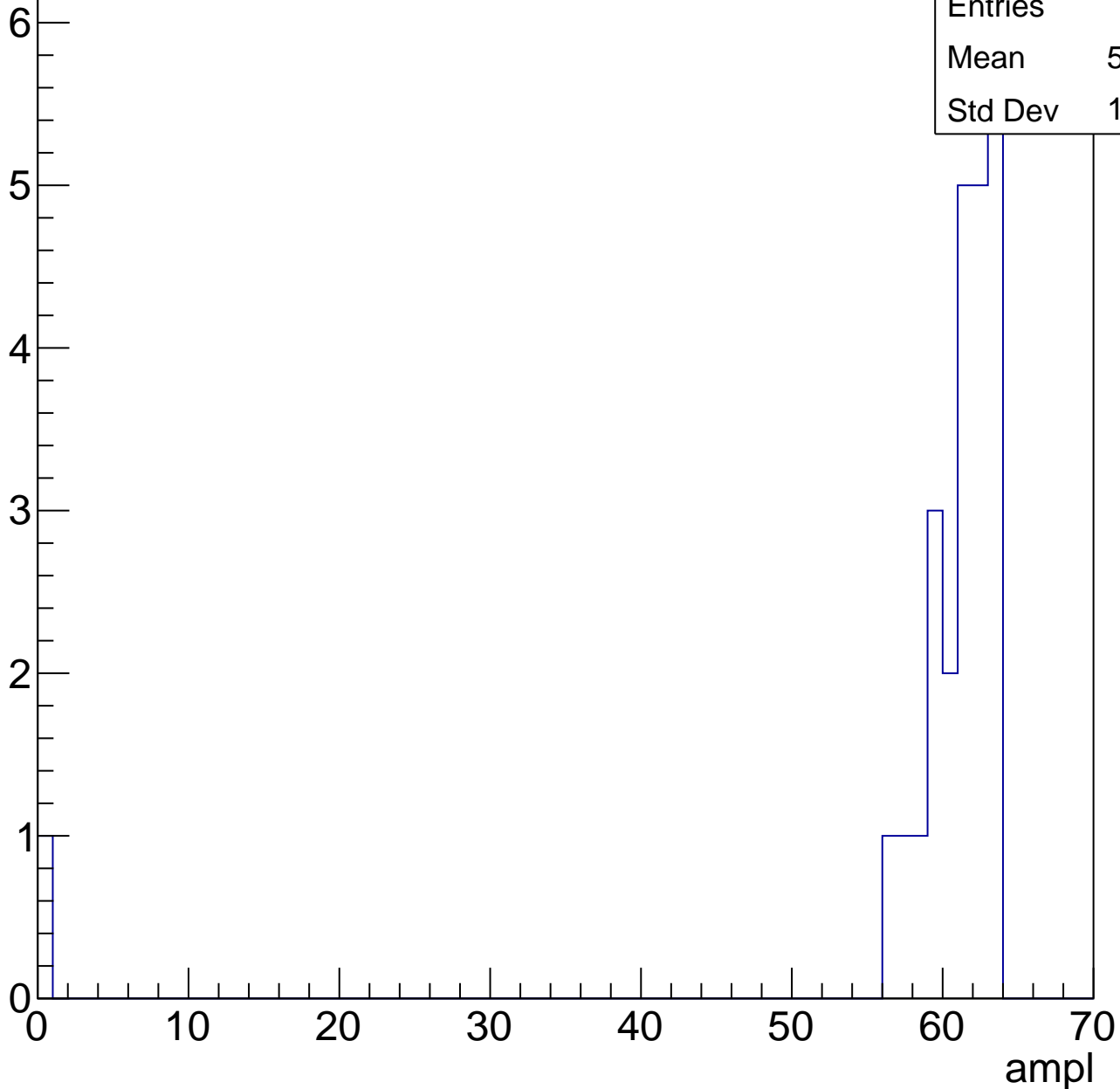


B1L103S, U18-ch6, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	25
Mean	58.44
Std Dev	12.08



B1L103S, U18-ch6, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U18-ch7, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

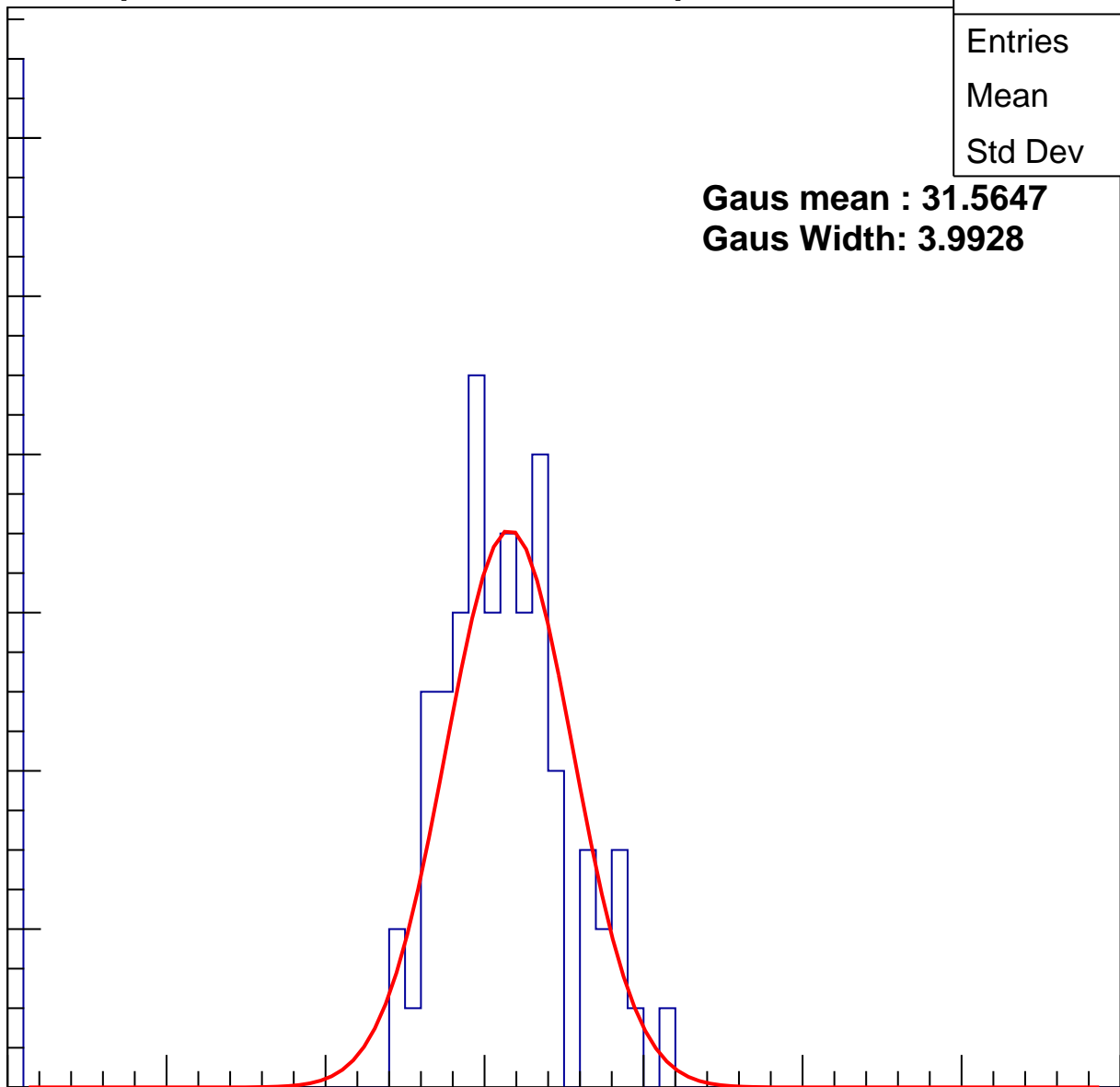
70

ampl

Entries	82
Mean	26
Std Dev	11.8

Gaus mean : 31.5647

Gaus Width: 3.9928



B1L103S, U18-ch7, adc1

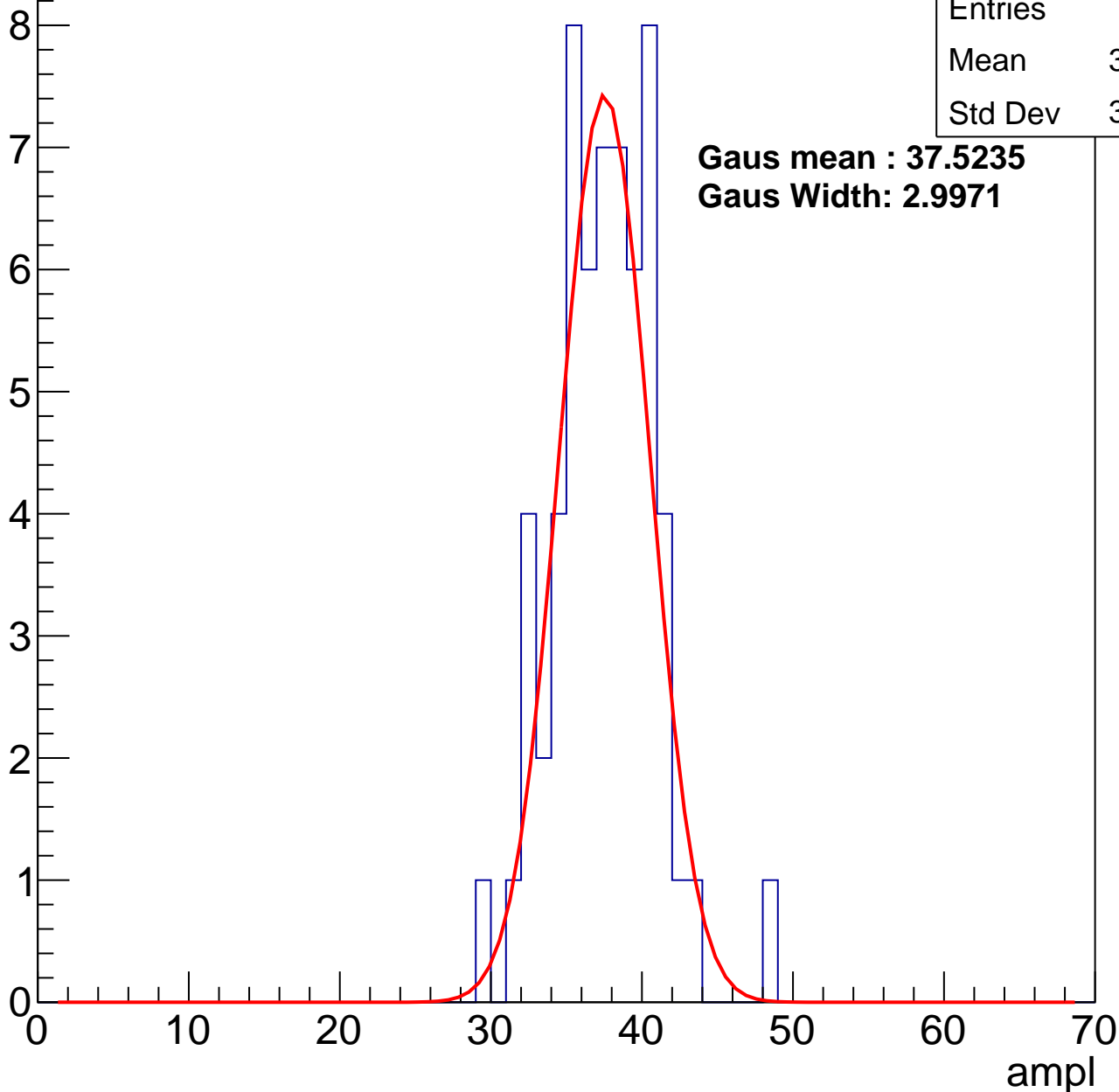
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	37.08
Std Dev	3.276

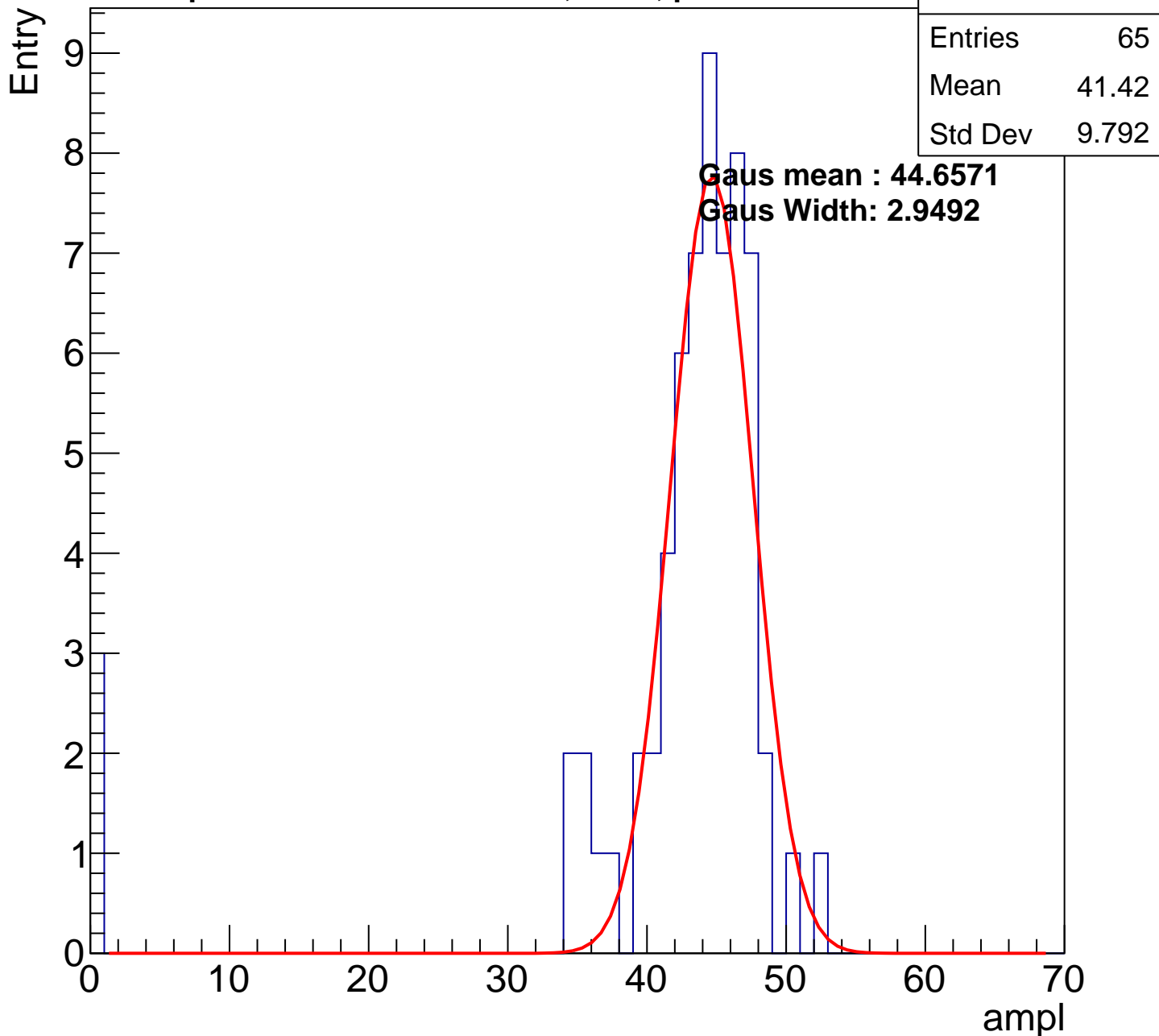
Gaus mean : 37.5235

Gaus Width: 2.9971



B1L103S, U18-ch7, adc2

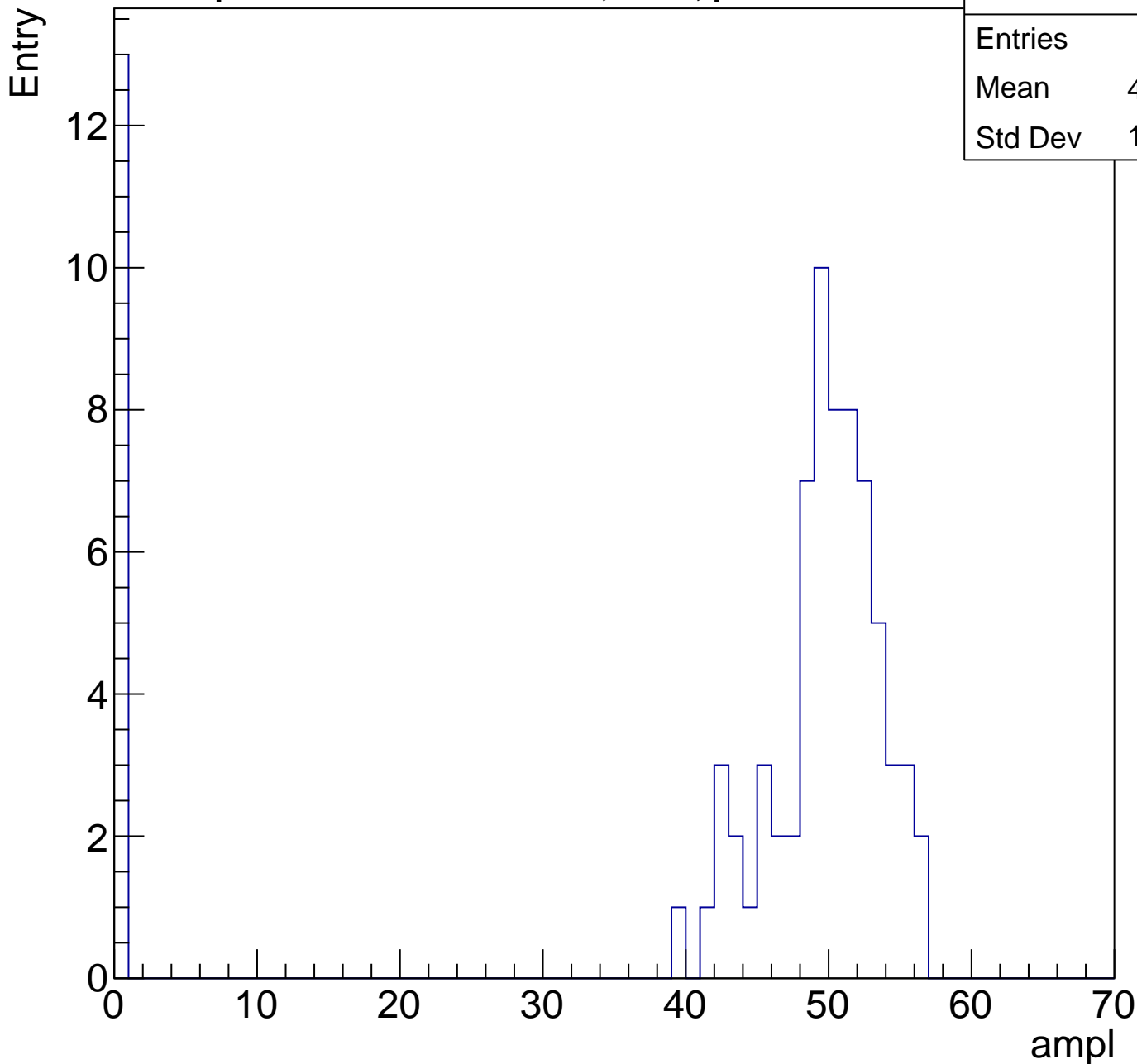
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U18-ch7, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	41.47
Std Dev	18.45

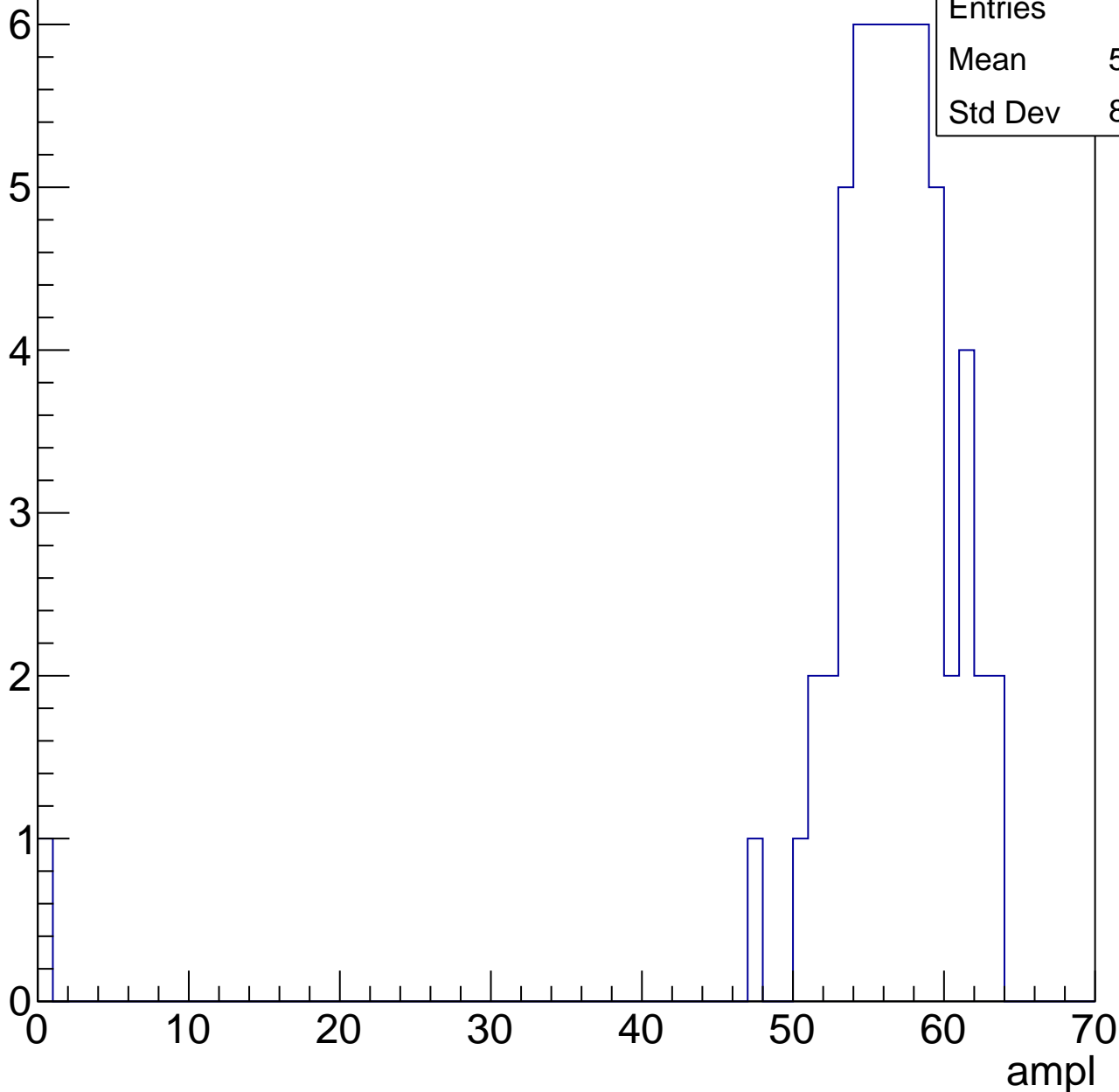


B1L103S, U18-ch7, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

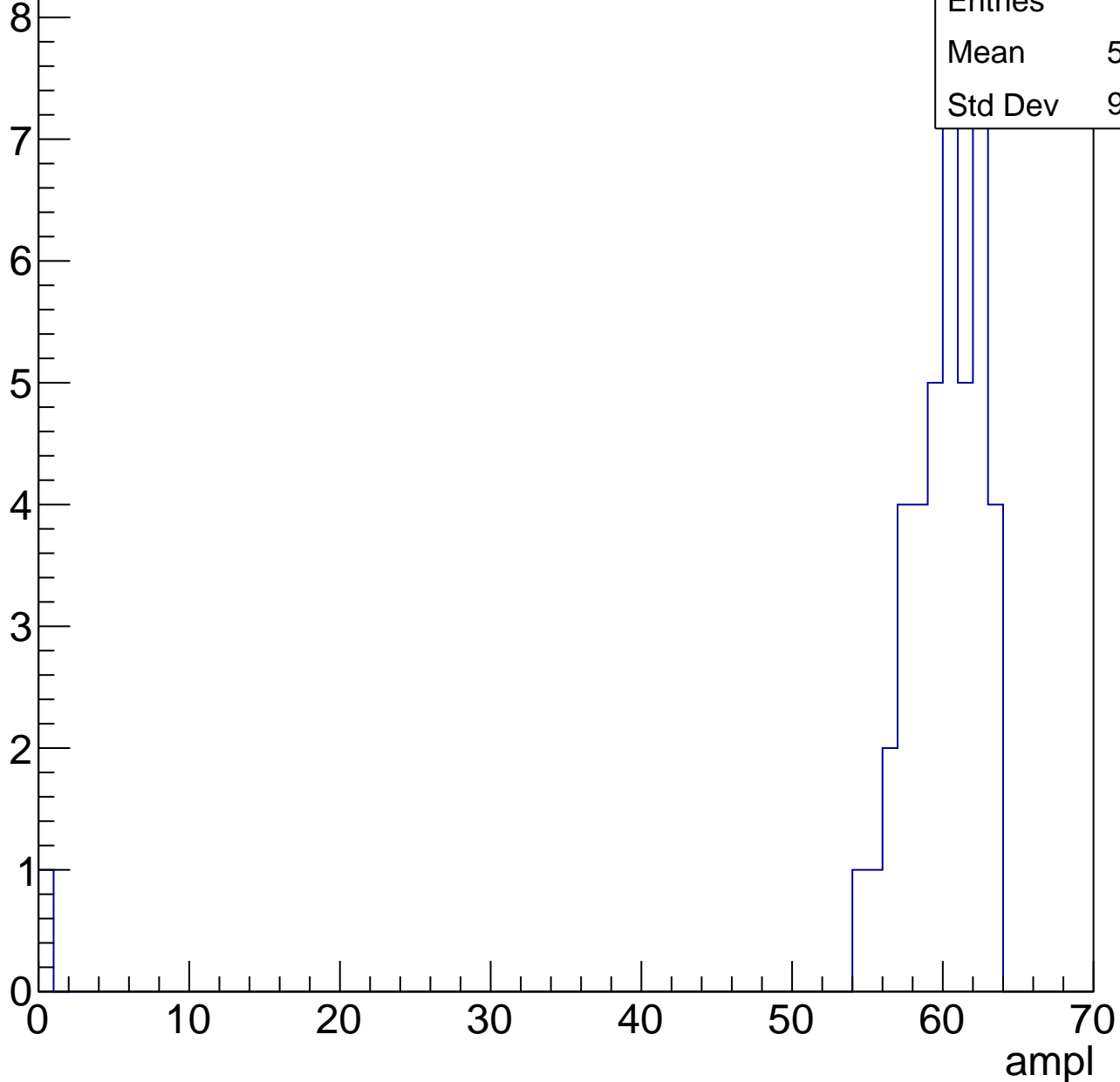
Entries	57
Mean	55.39
Std Dev	8.128



B1L103S, U18-ch7, adc5

calib_packv5_041523_1651.root, FC#0, port C2

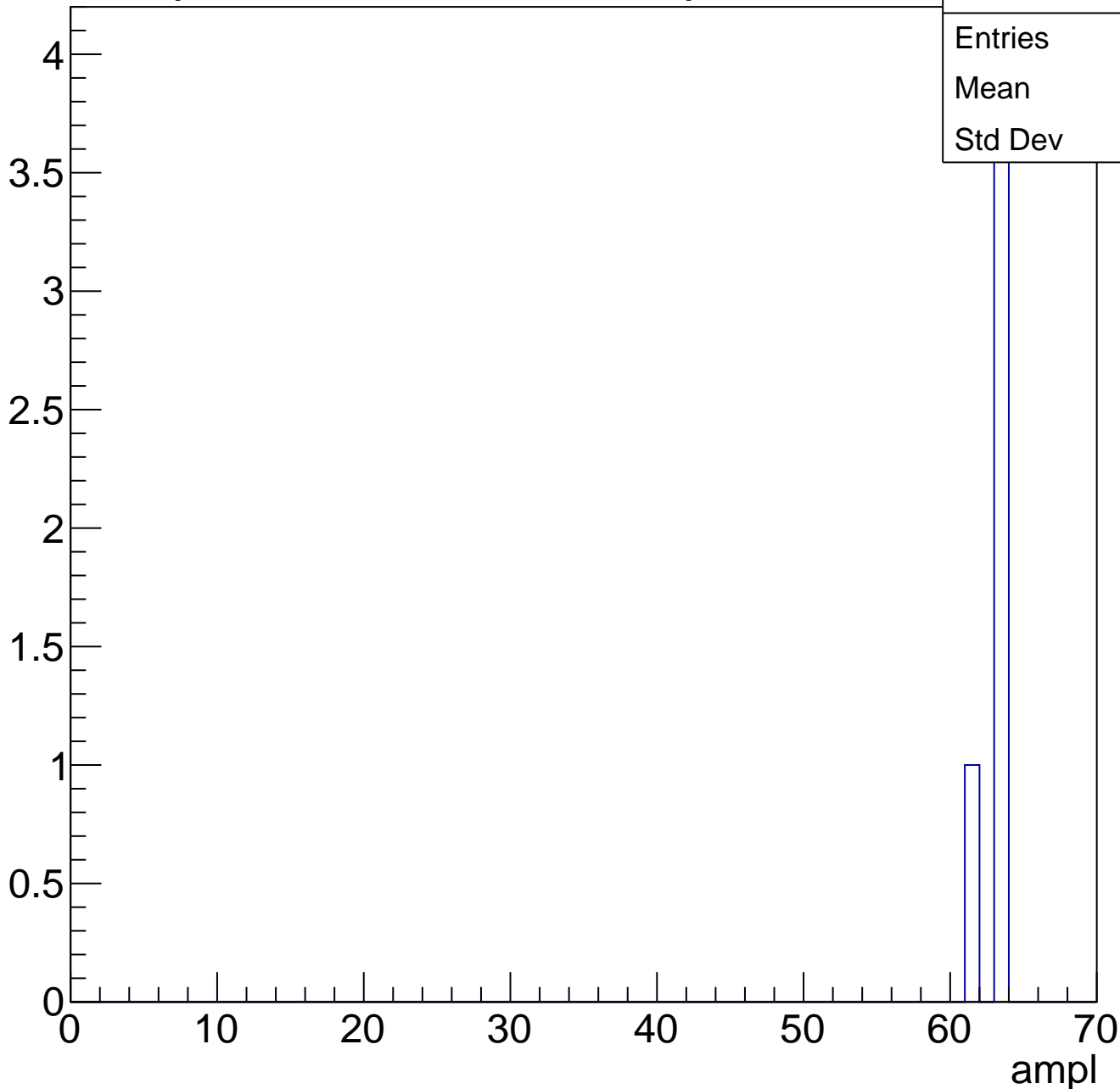
Entry



B1L103S, U18-ch7, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

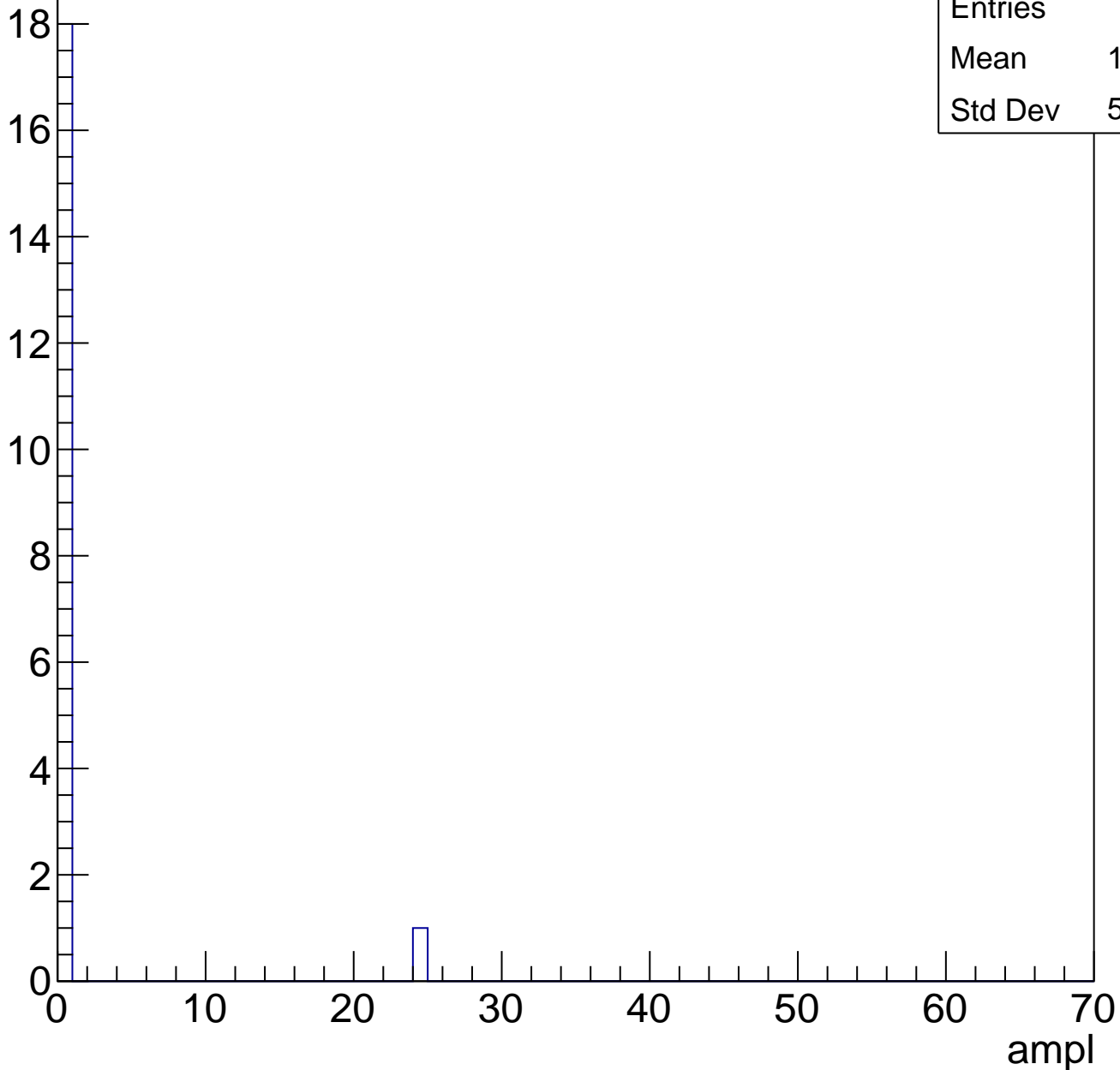


B1L103S, U18-ch7, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.263
Std Dev	5.359

Entry



B1L103S, U18-ch8, adc0

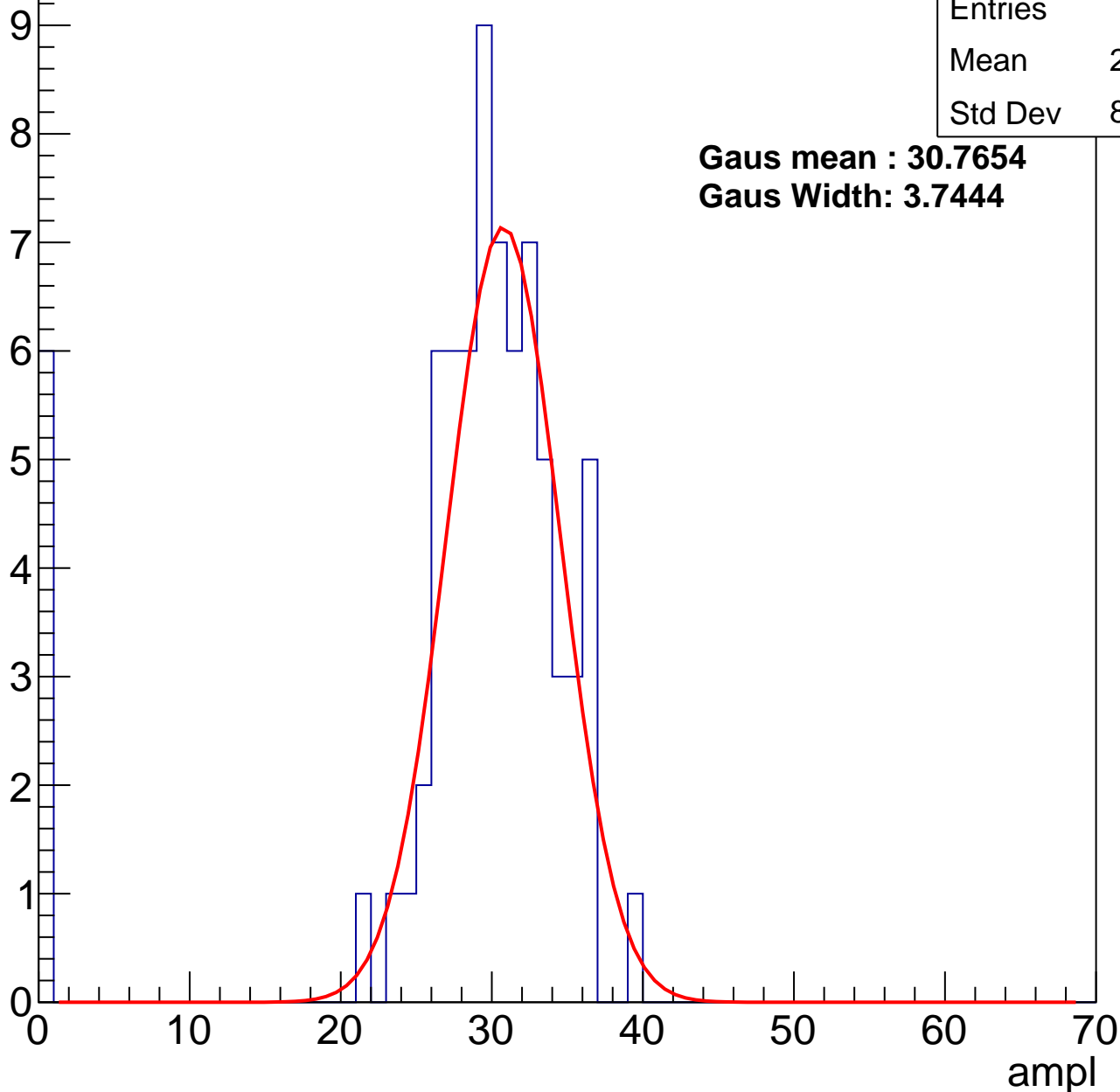
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	27.68
Std Dev	8.838

Gaus mean : 30.7654

Gaus Width: 3.7444



B1L103S, U18-ch8, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	36.84
Std Dev	3.282

Gaus mean : 37.6039

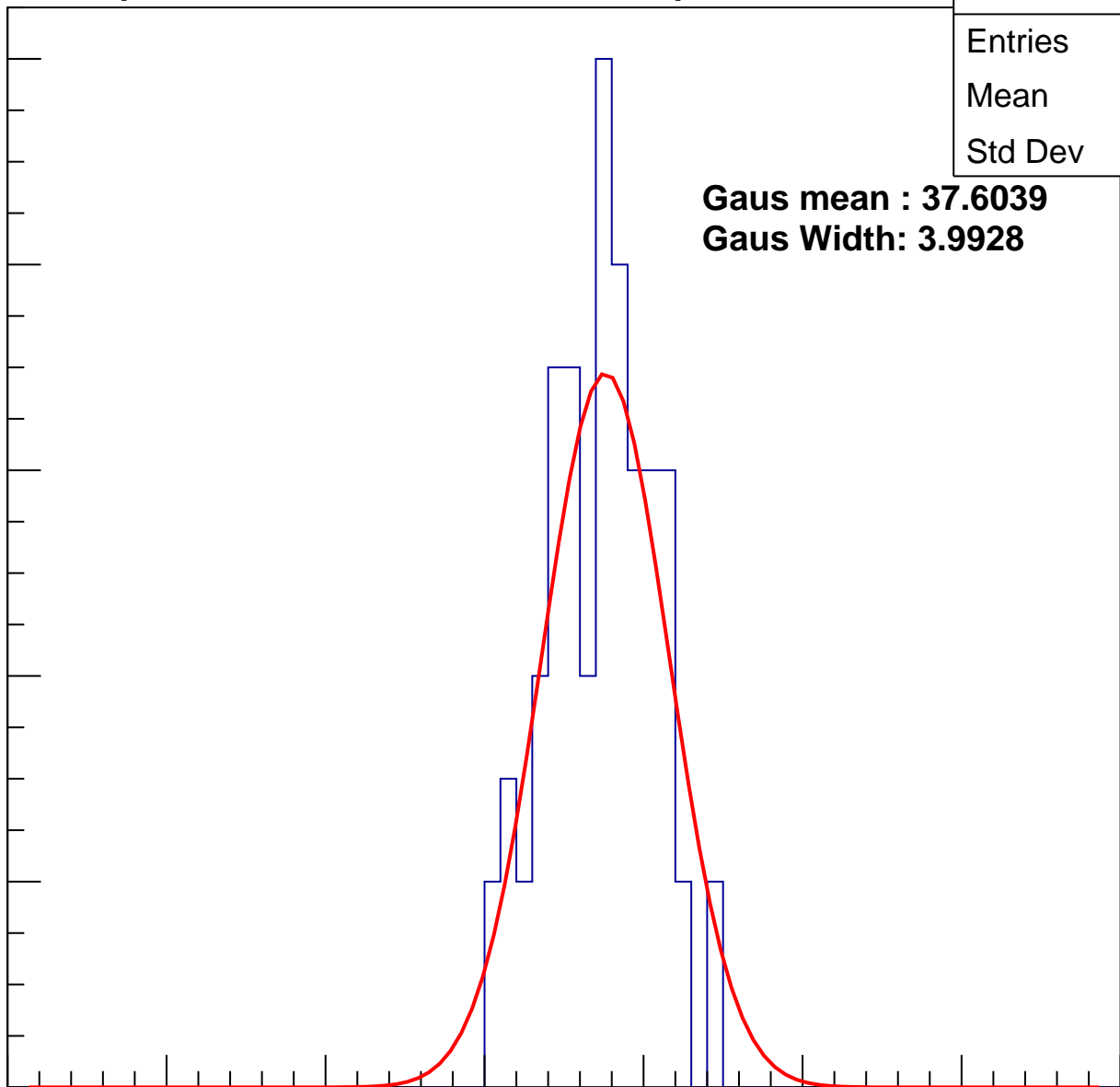
Gaus Width: 3.9928

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch8, adc2

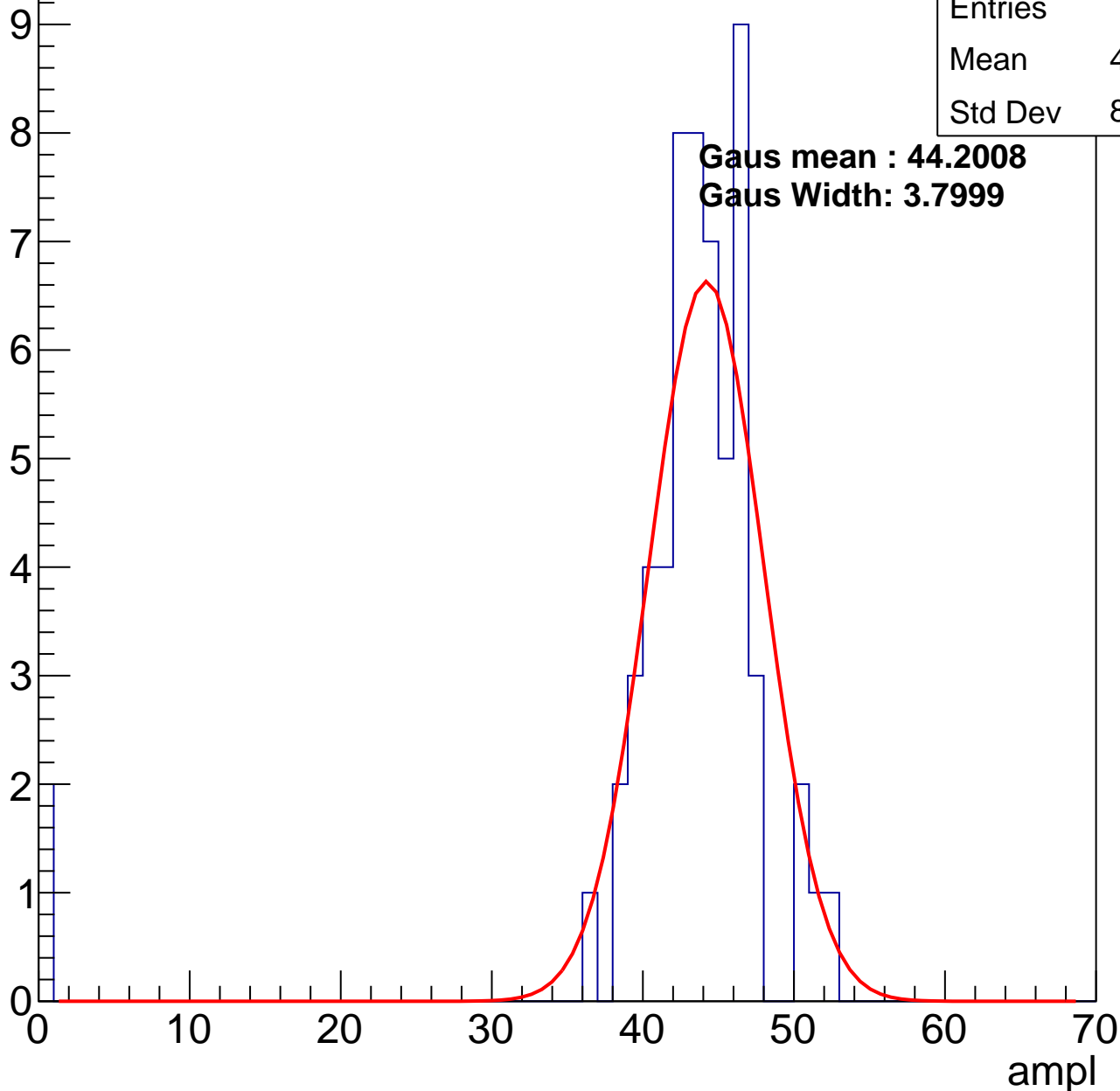
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	42.07
Std Dev	8.418

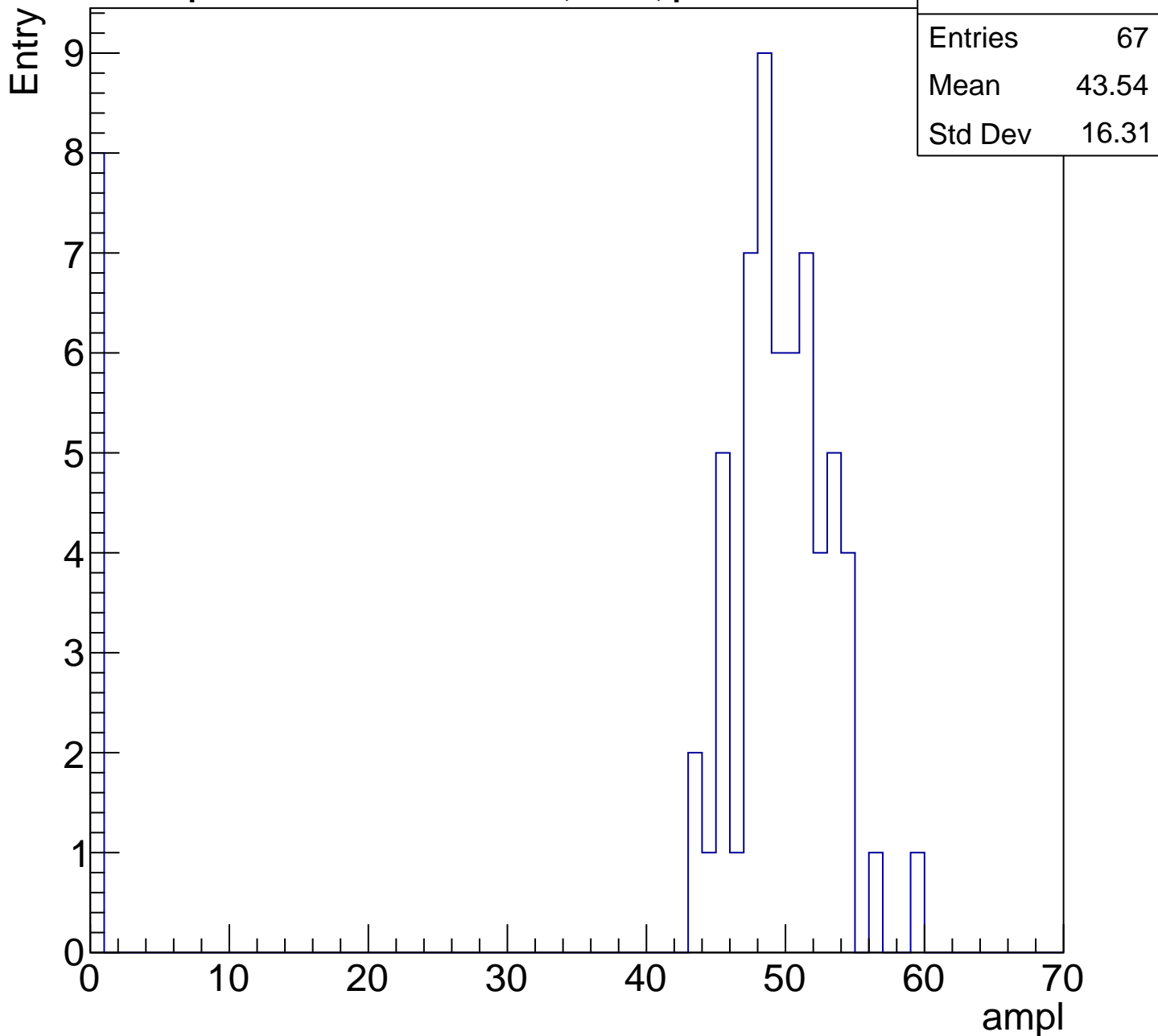
Gaus mean : 44.2008

Gaus Width: 3.7999



B1L103S, U18-ch8, adc3

calib_packv5_041523_1651.root, FC#0, port C2

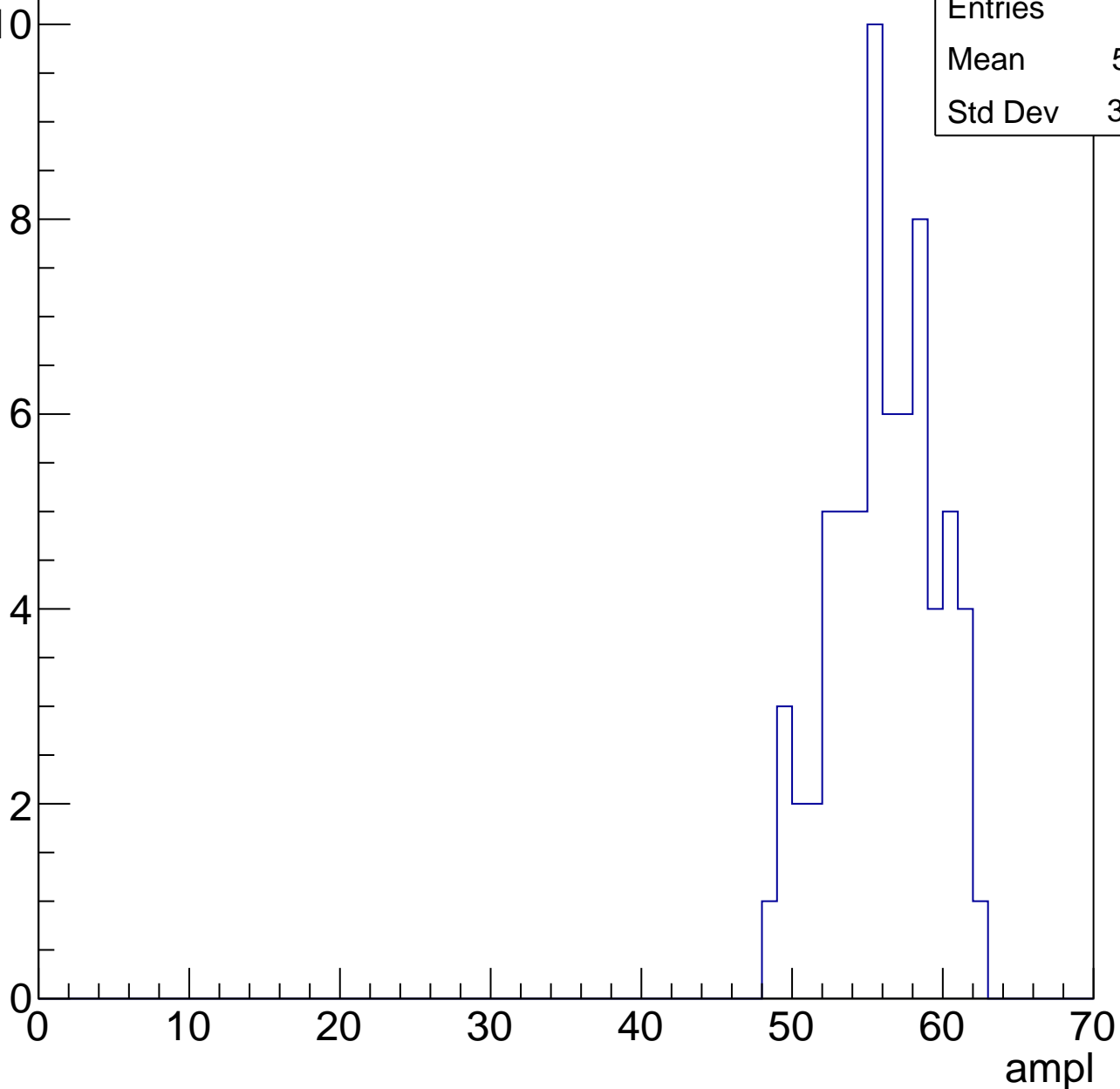


B1L103S, U18-ch8, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	55.61
Std Dev	3.385

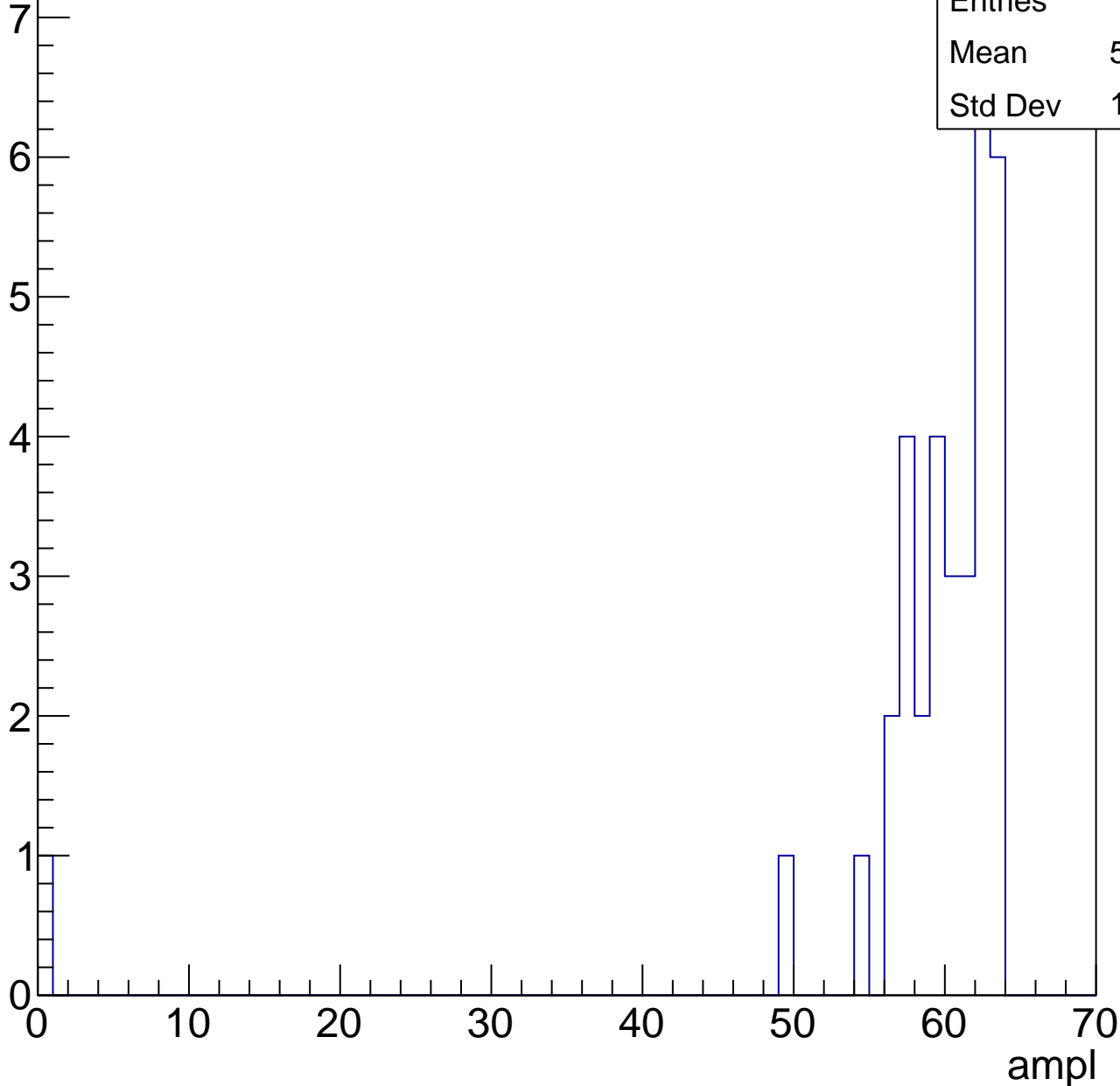


B1L103S, U18-ch8, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

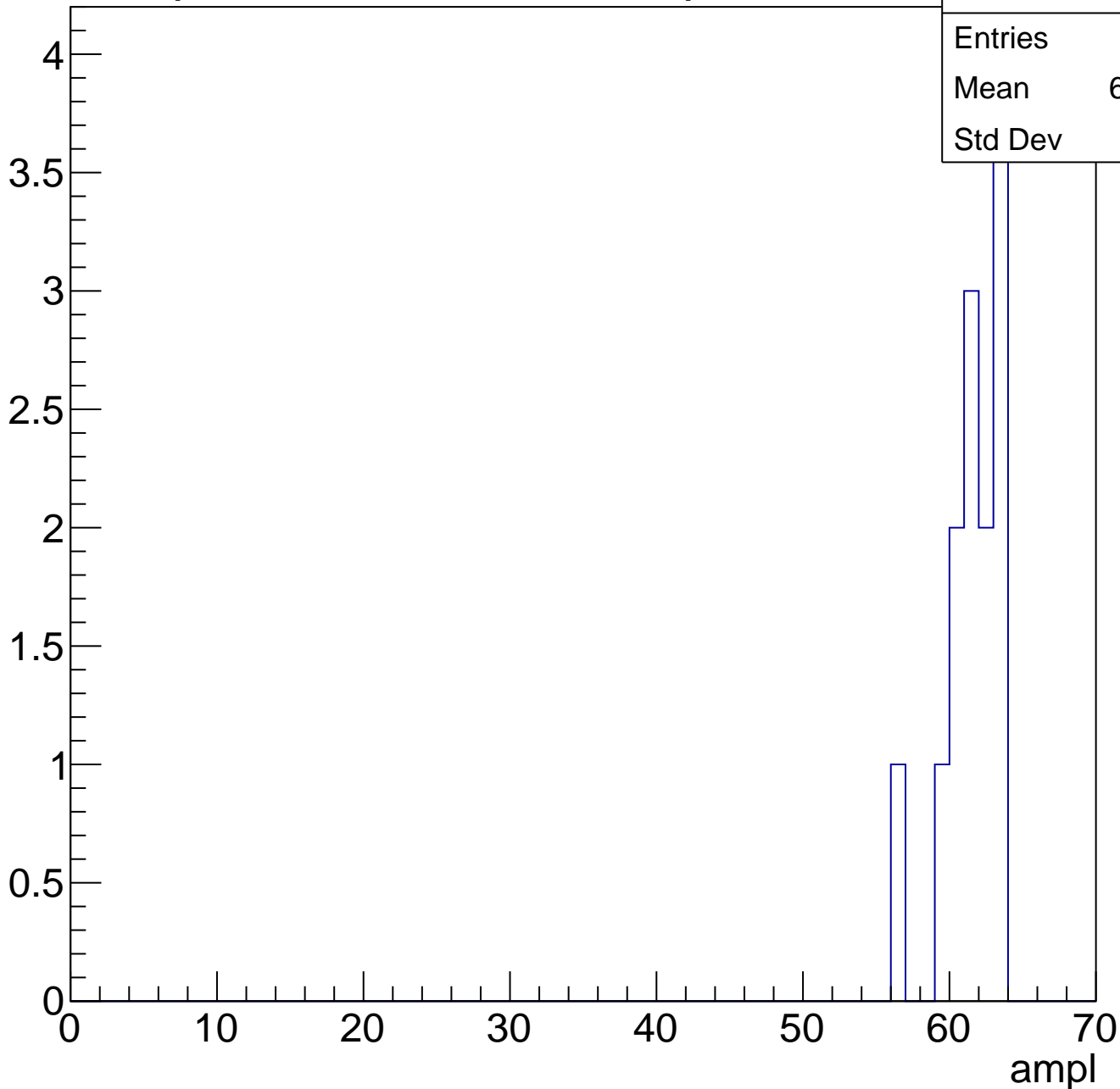
Entries	34
Mean	57.94
Std Dev	10.54



B1L103S, U18-ch8, adc6

calib_packv5_041523_1651.root, FC#0, port C2

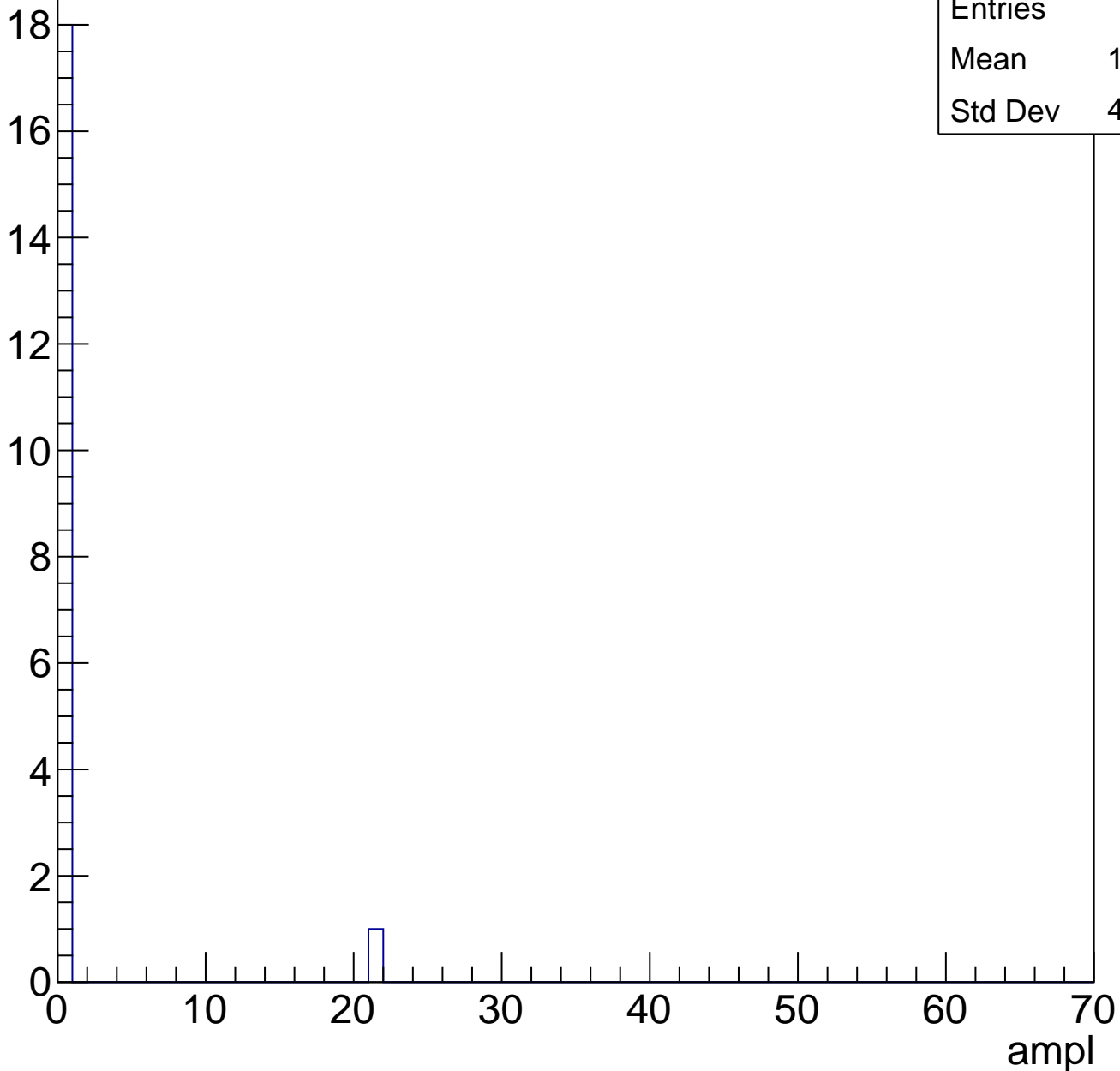
Entry



B1L103S, U18-ch8, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch9, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	24.38
Std Dev	11.76

Gaus mean : 29.8320

Gaus Width: 3.3987

Entry

12

10

8

6

4

2

0

0

10

20

30

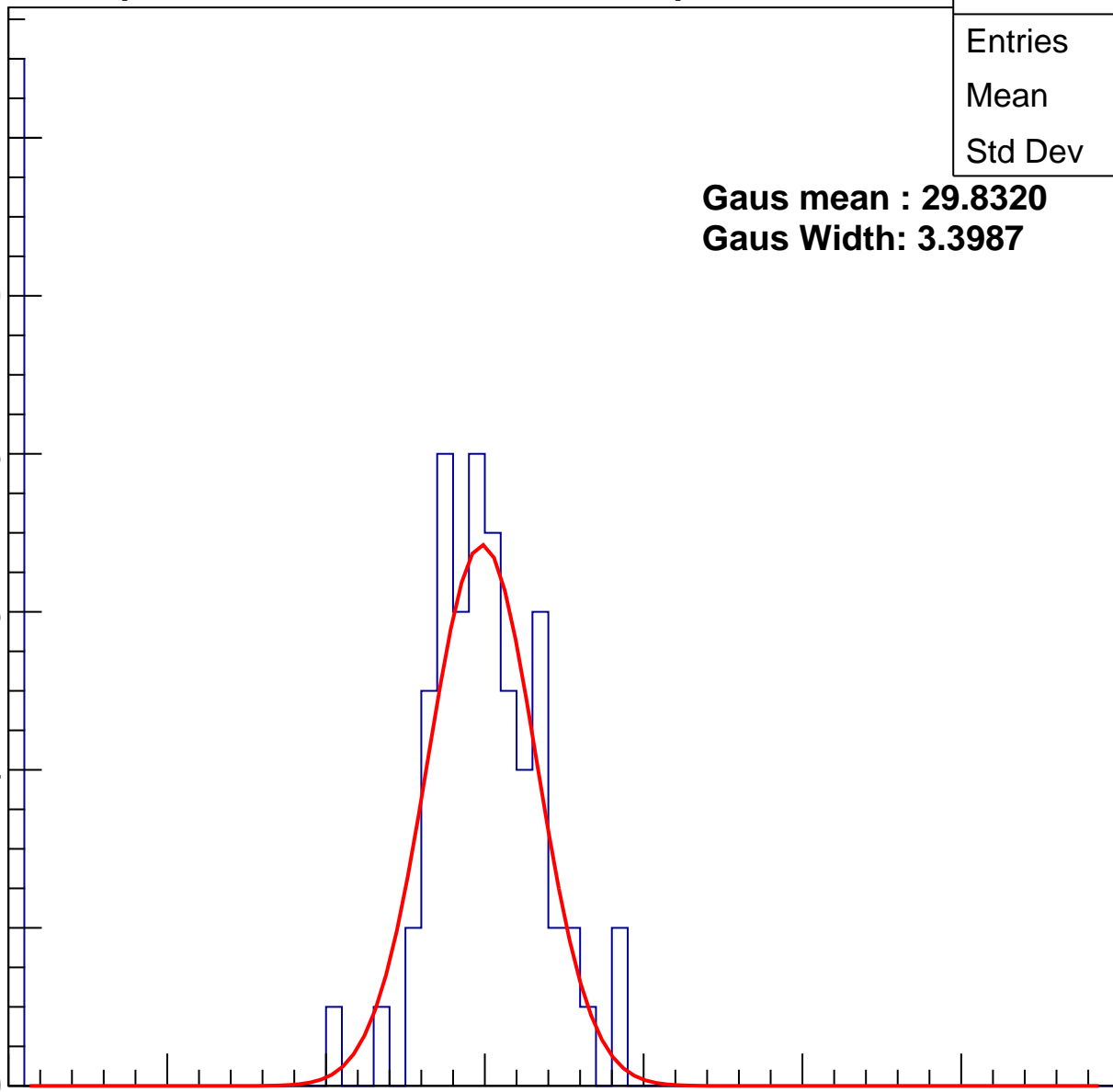
40

50

60

70

ampl



B1L103S, U18-ch9, adc1

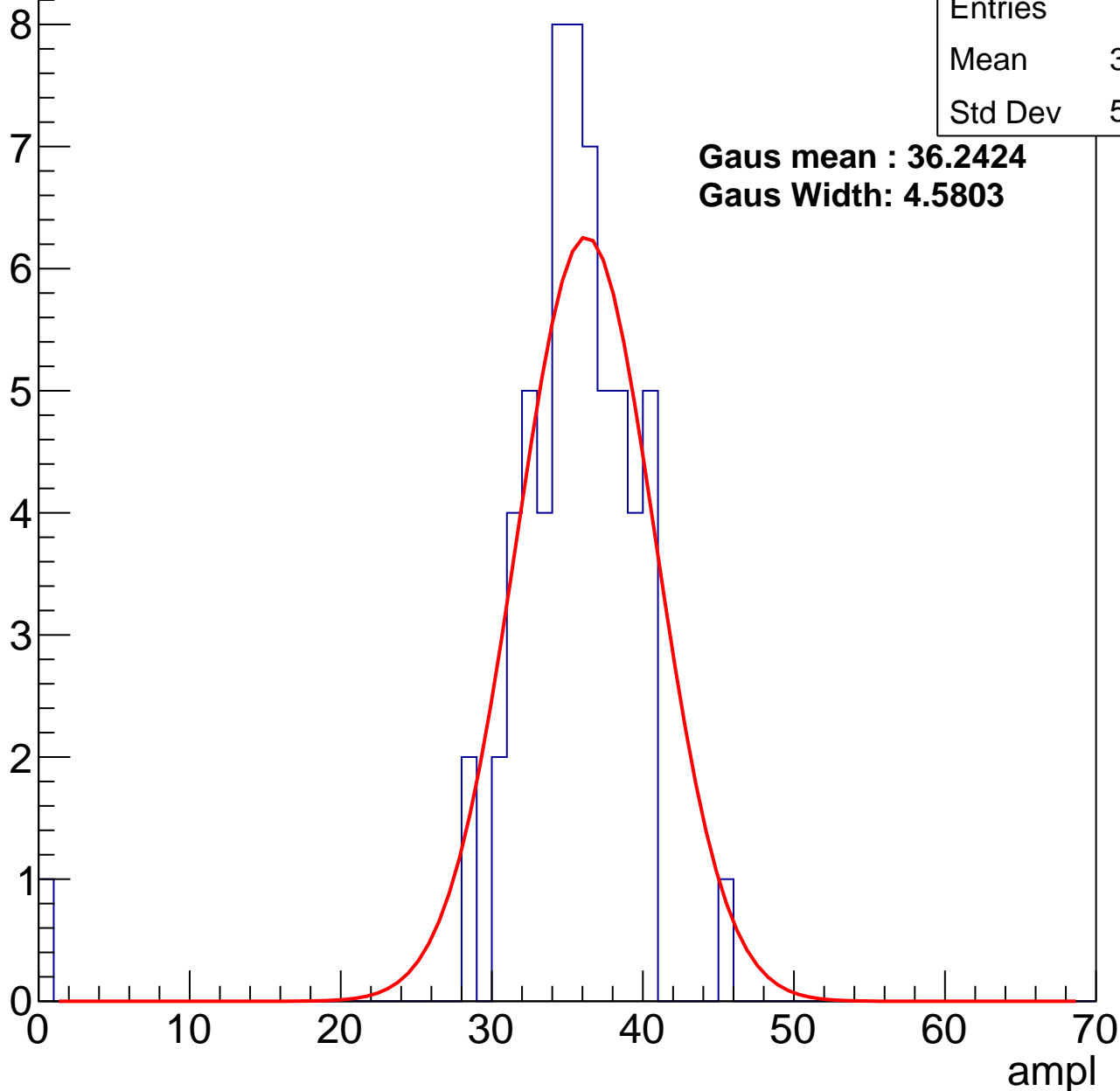
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.62
Std Dev	5.519

Gaus mean : 36.2424

Gaus Width: 4.5803



B1L103S, U18-ch9, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	36.68
Std Dev	14.29

Gaus mean : 42.1633

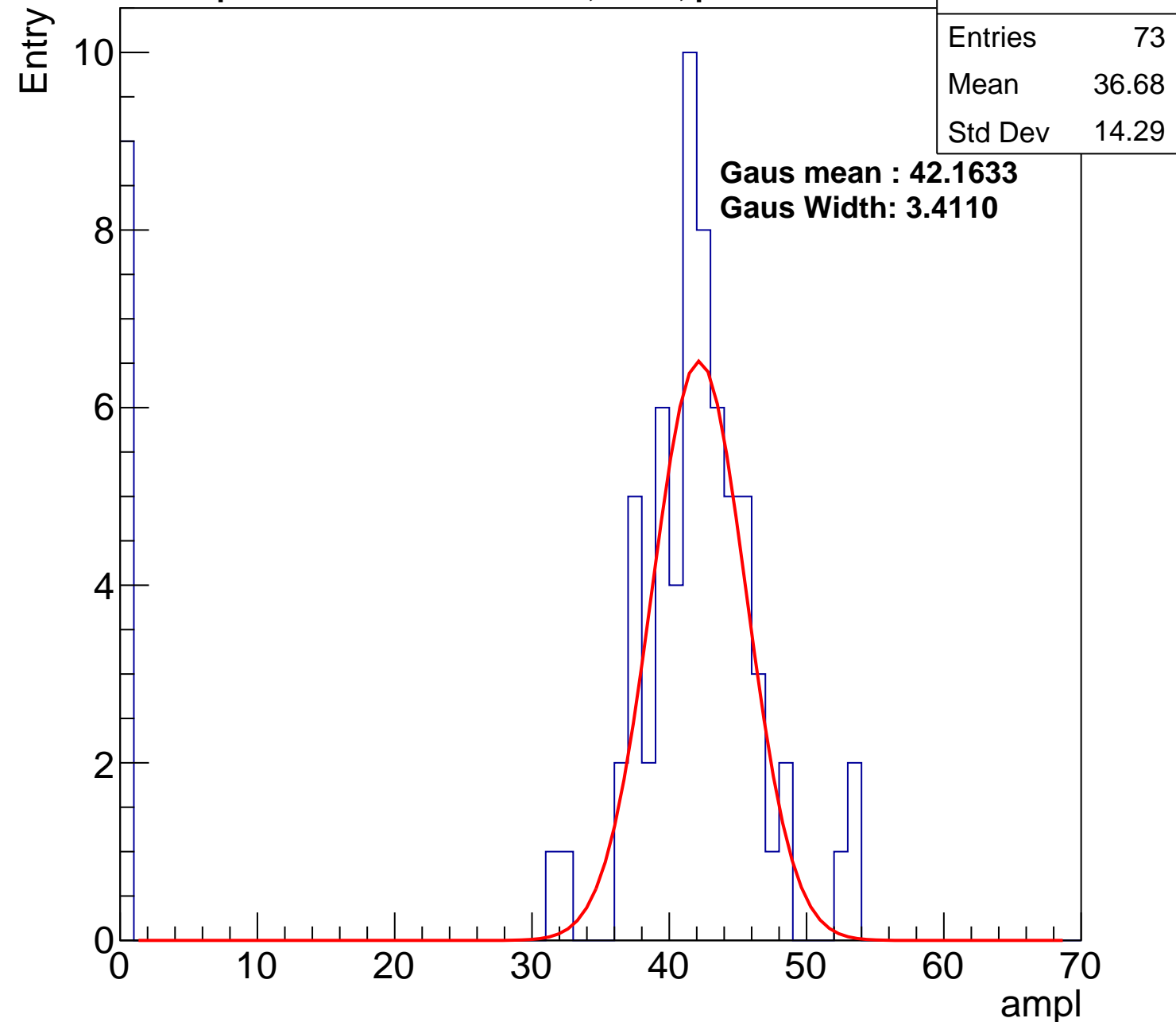
Gaus Width: 3.4110

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

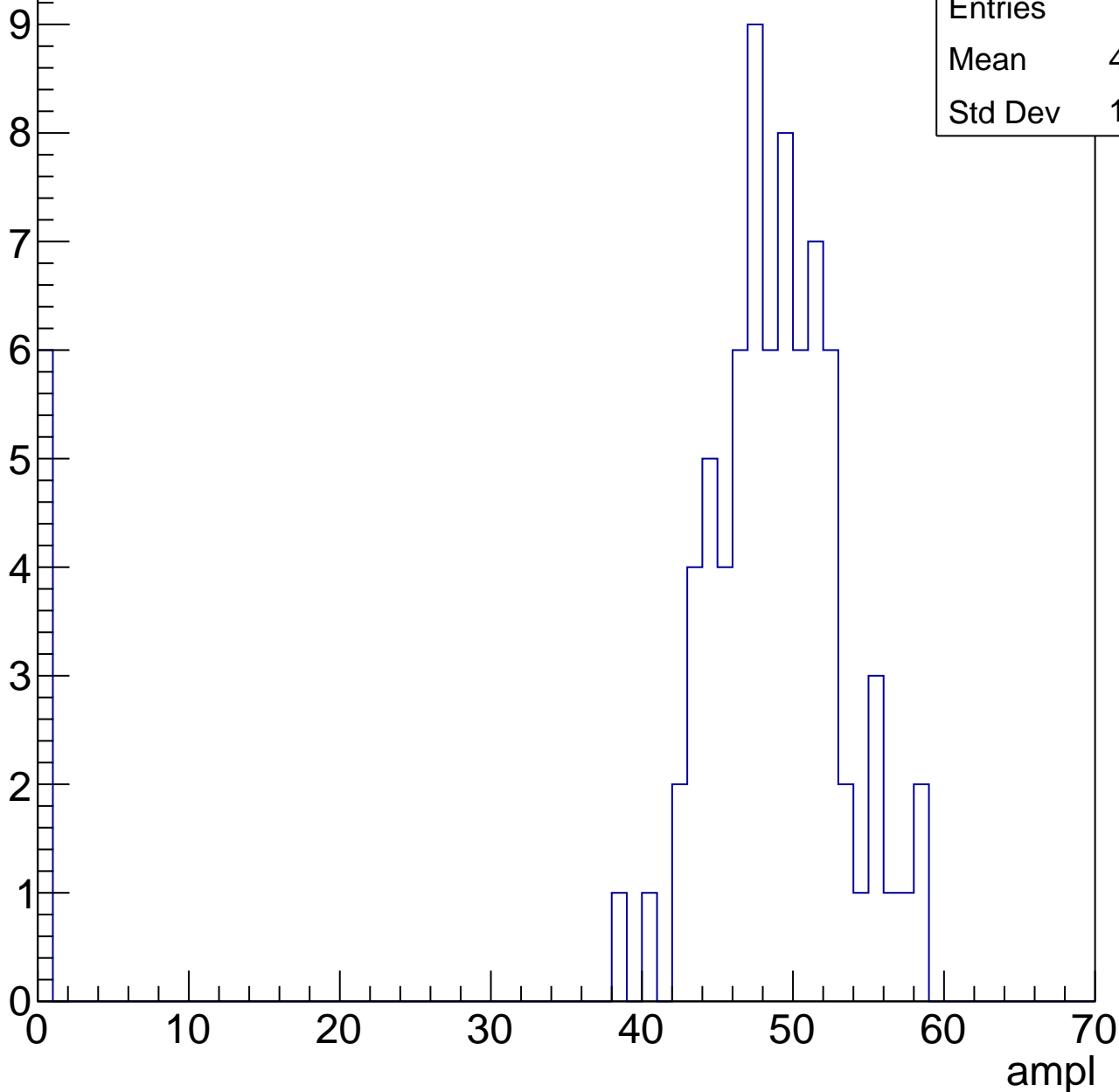


B1L103S, U18-ch9, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	44.89
Std Dev	13.28

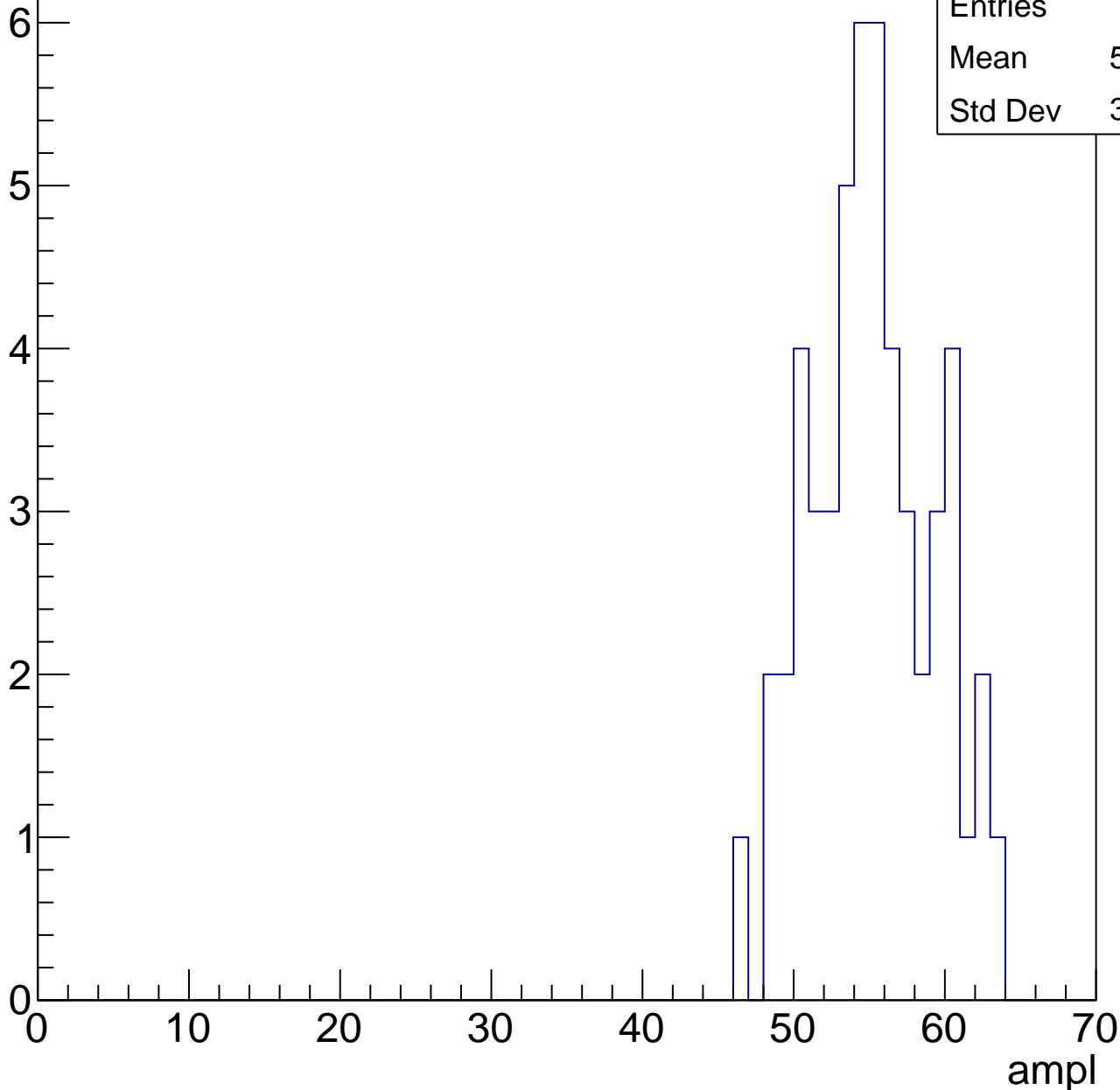


B1L103S, U18-ch9, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	54.69
Std Dev	3.988

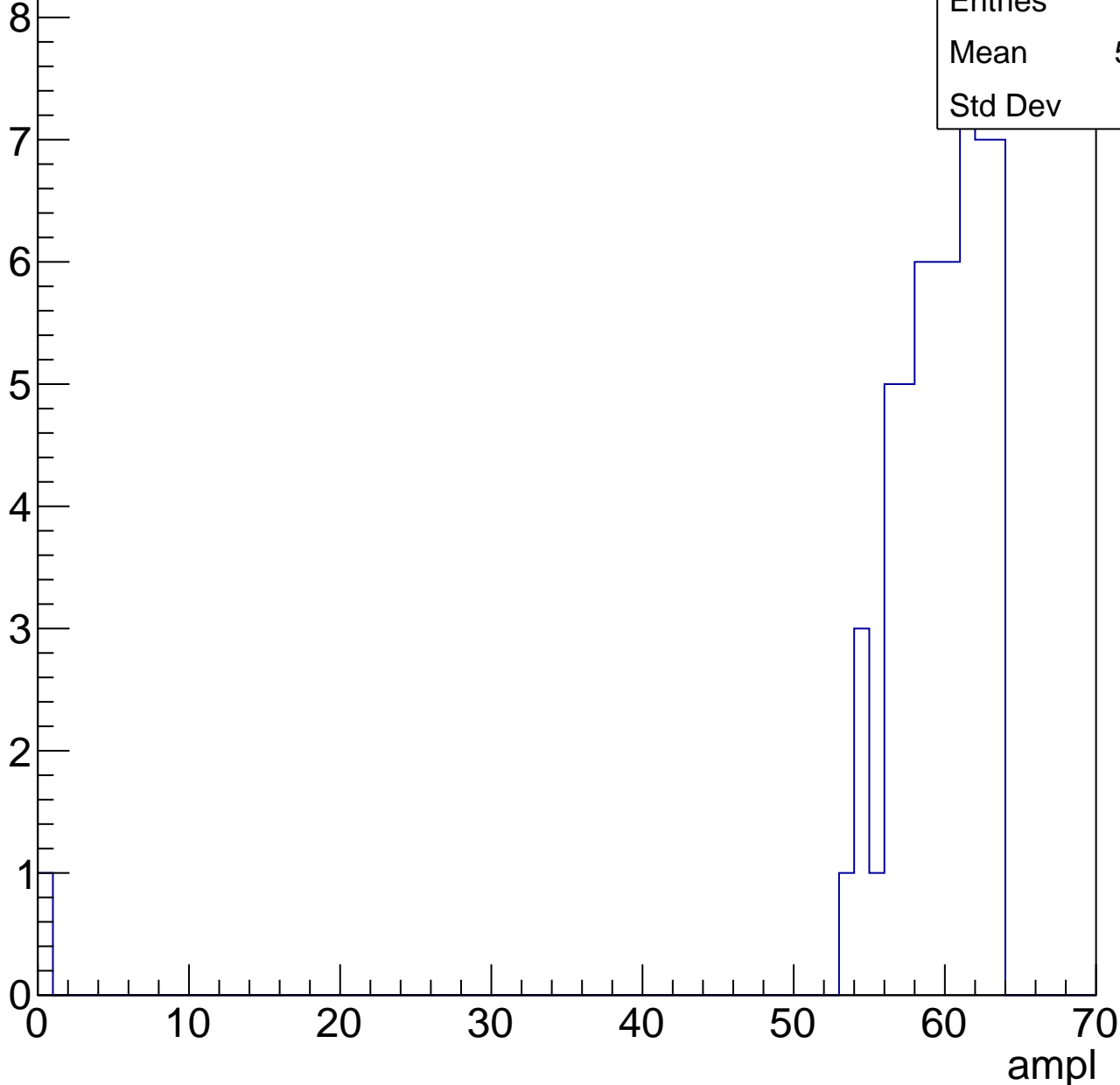


B1L103S, U18-ch9, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.21
Std Dev	8.3



B1L103S, U18-ch9, adc6

calib_packv5_041523_1651.root, FC#0, port C2

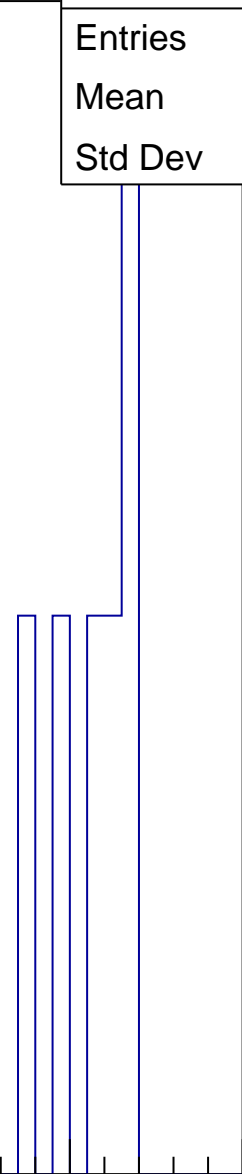
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.83
Std Dev	2.192

0 10 20 30 40 50 60 70

ampl



B1L103S, U18-ch9, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U18-ch10, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	22.01
Std Dev	12.09

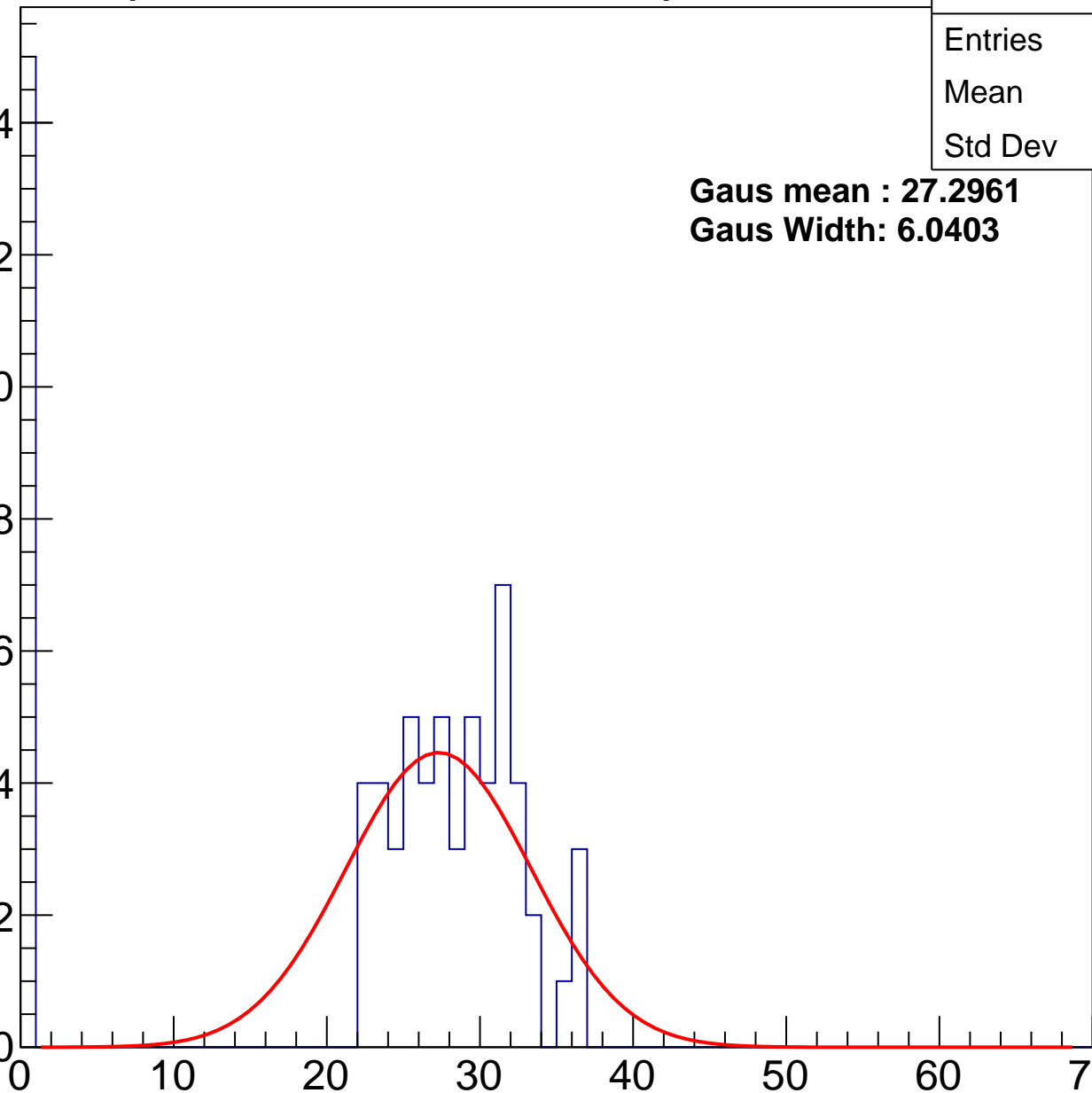
Gaus mean : 27.2961

Gaus Width: 6.0403

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U18-ch10, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	34.57
Std Dev	5.455

Gaus mean : 35.7587

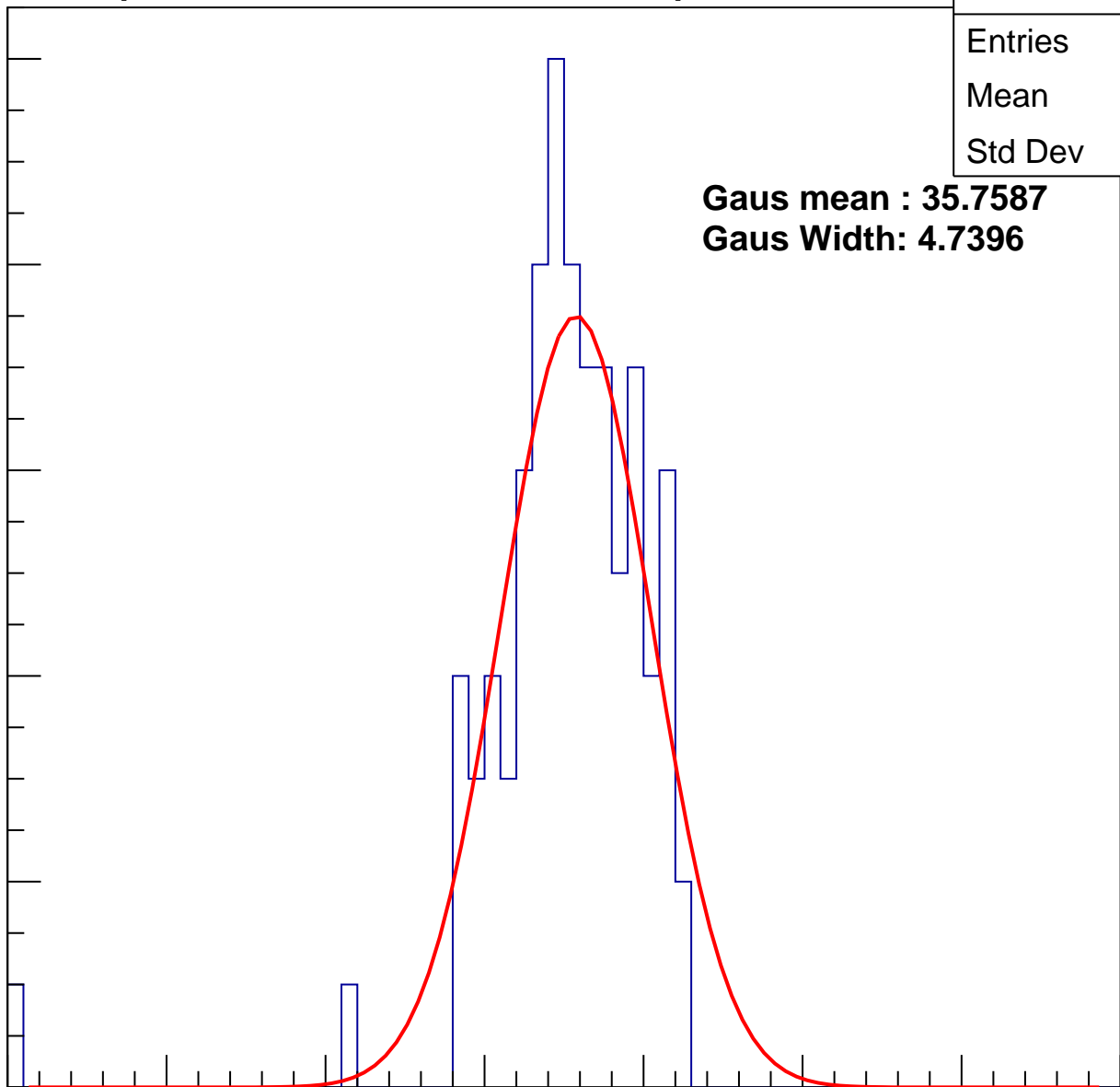
Gaus Width: 4.7396

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch10, adc2

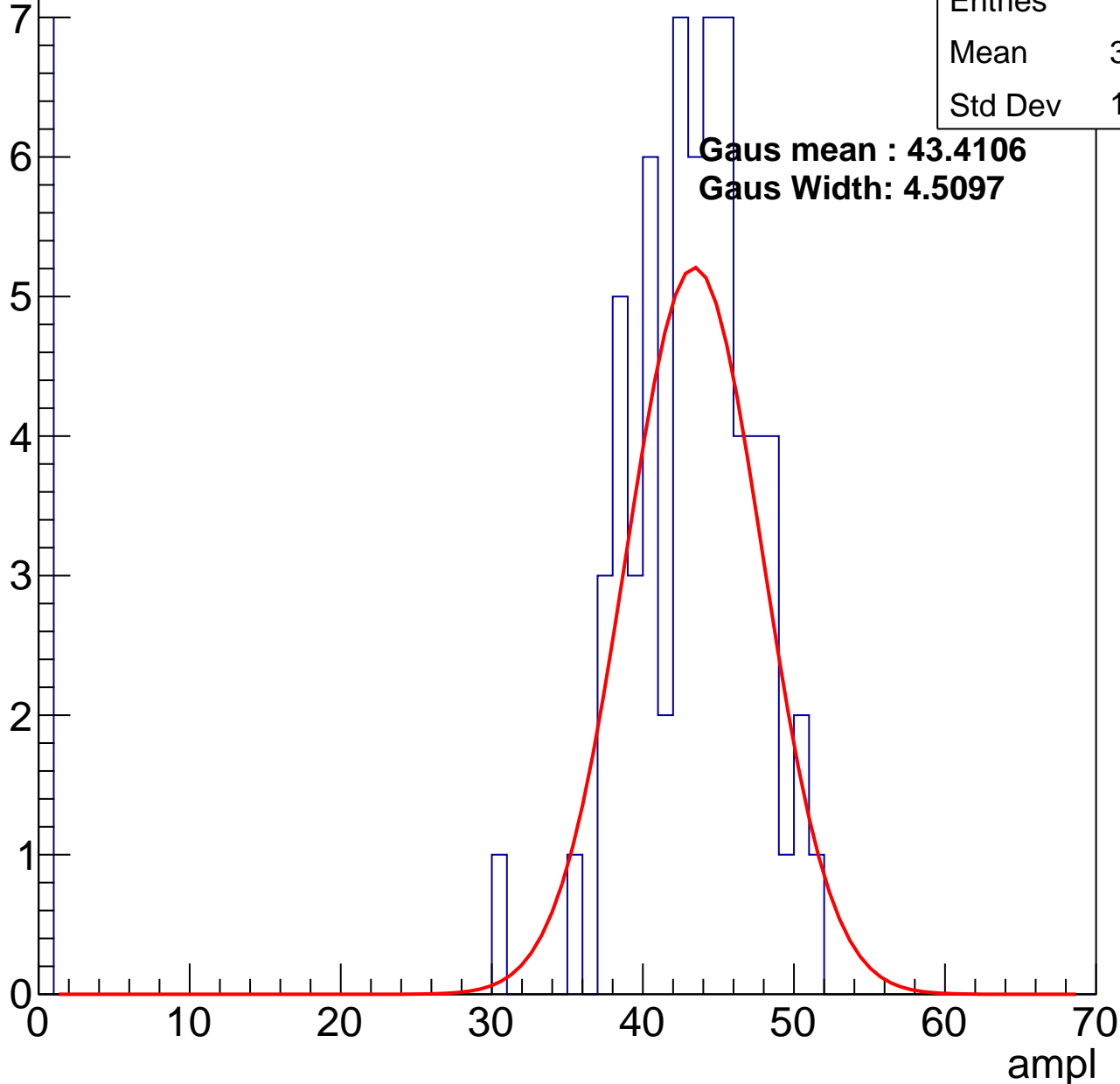
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	38.65
Std Dev	13.33

Gaus mean : 43.4106

Gaus Width: 4.5097

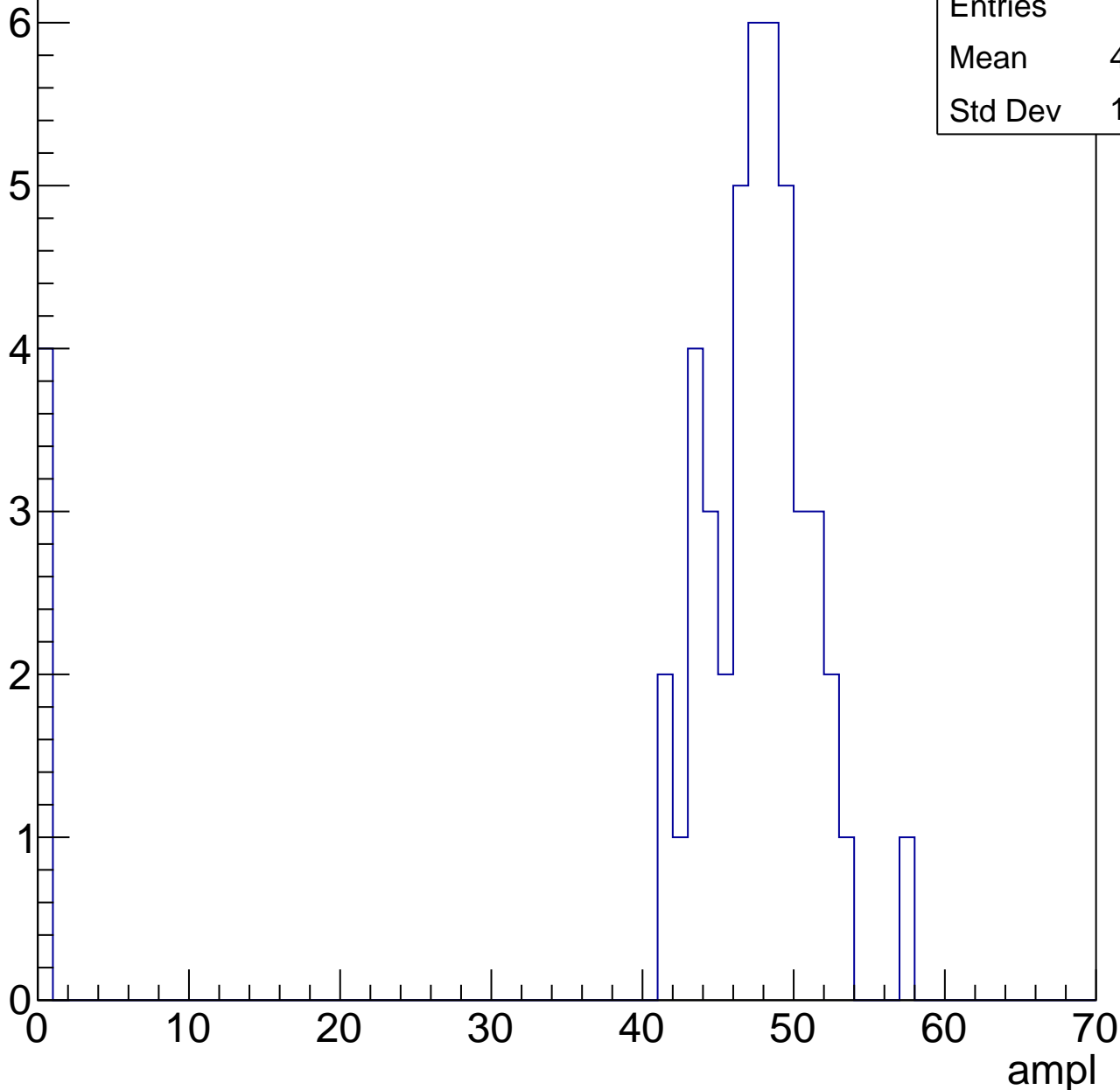


B1L103S, U18-ch10, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	43.33
Std Dev	13.45

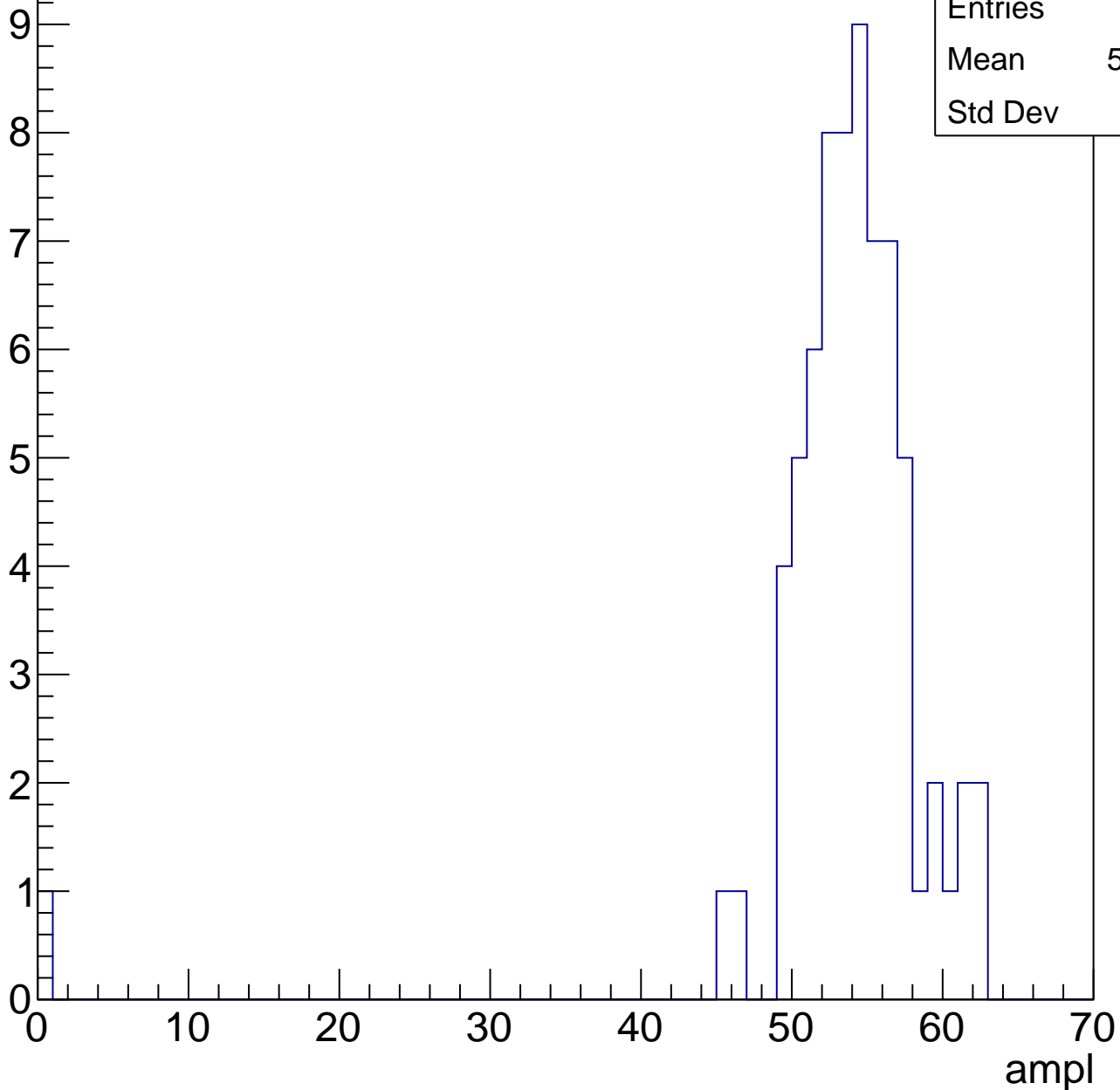


B1L103S, U18-ch10, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

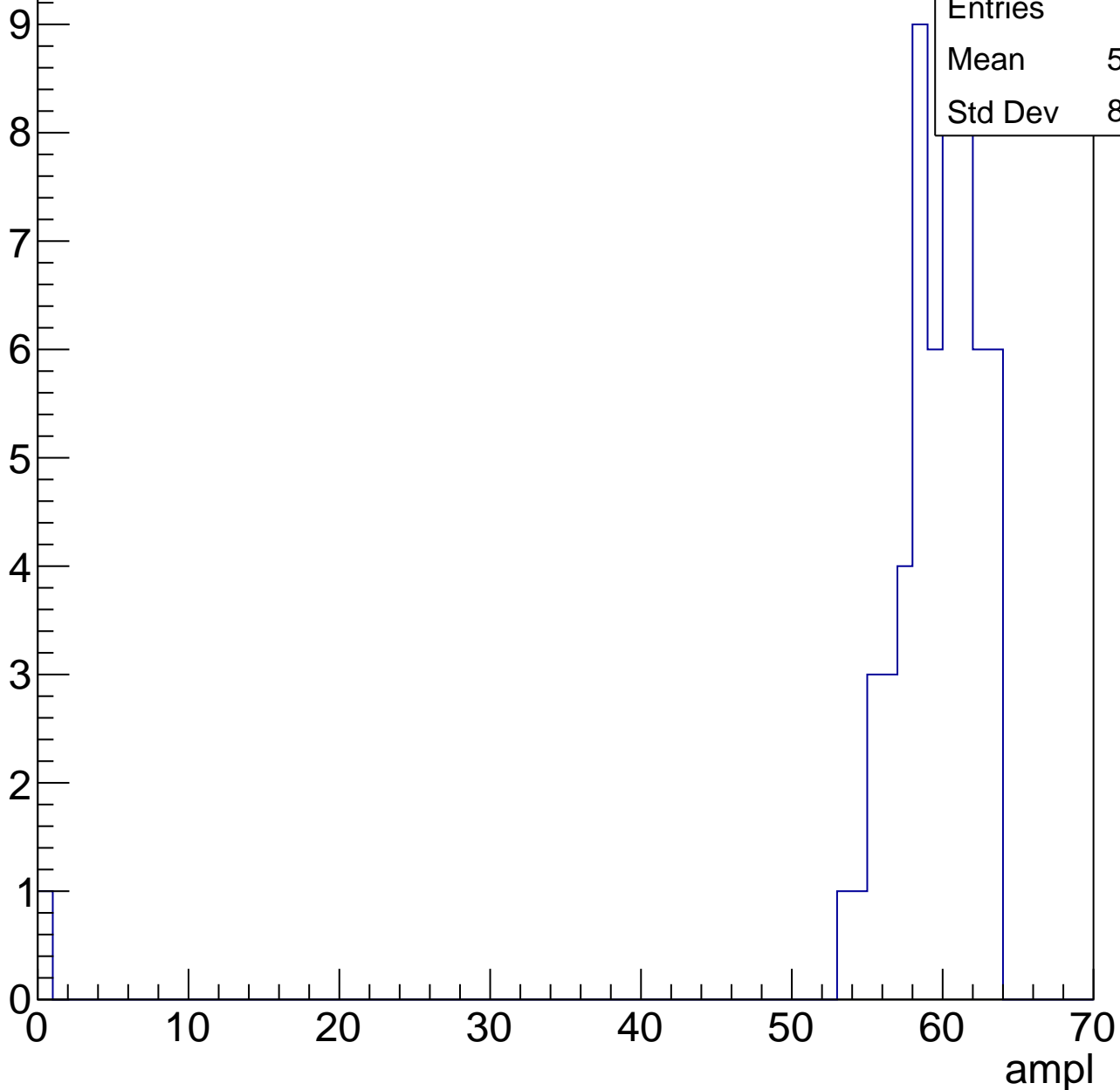
Entries	70
Mean	53.04
Std Dev	7.25



B1L103S, U18-ch10, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

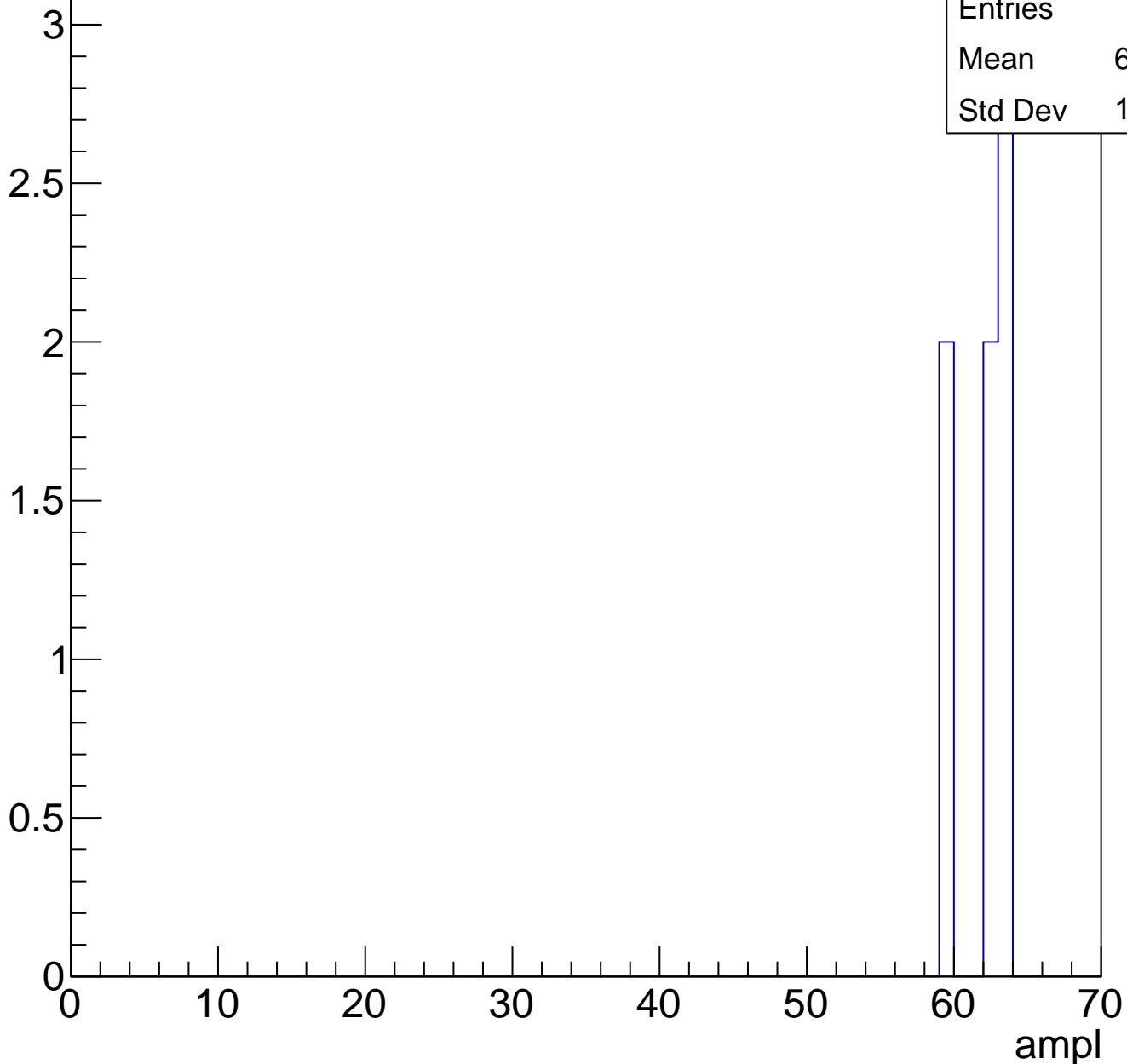


Entries	57
Mean	58.28
Std Dev	8.169

B1L103S, U18-ch10, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

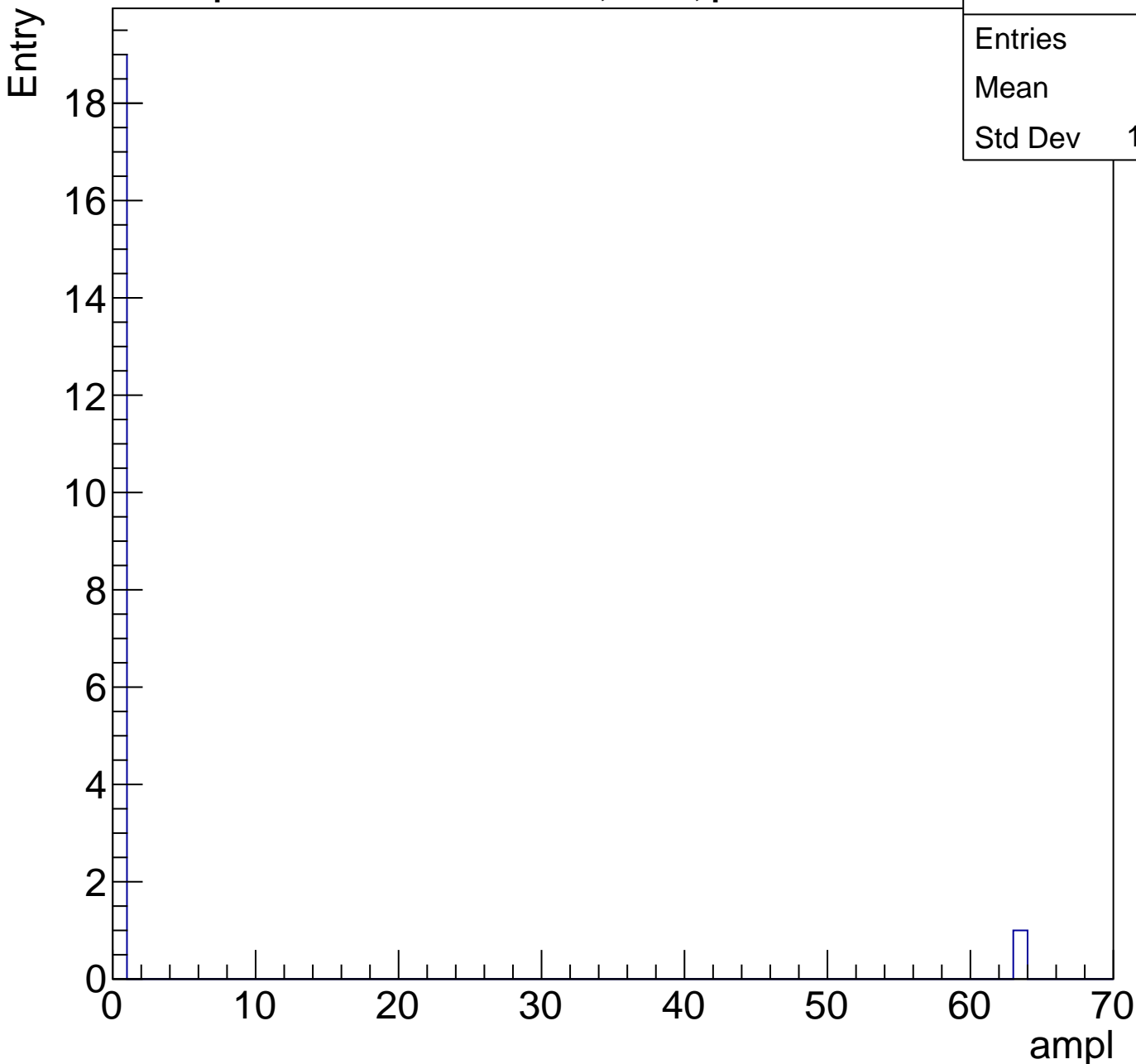


Entries	7
Mean	61.57
Std Dev	1.678

B1L103S, U18-ch10, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U18-ch11, adc0

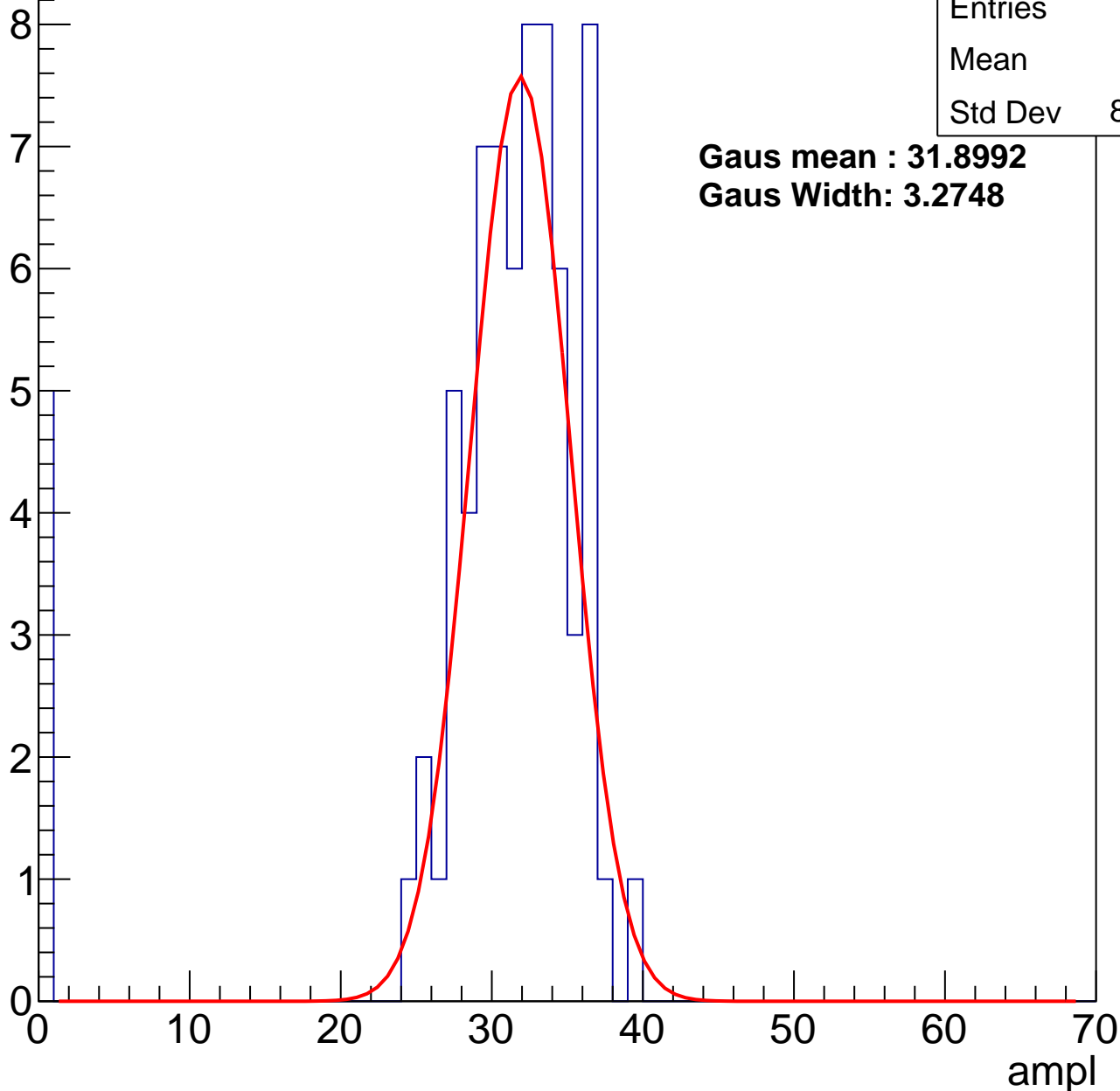
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	29.3
Std Dev	8.548

Gaus mean : 31.8992

Gaus Width: 3.2748



B1L103S, U18-ch11, adc1

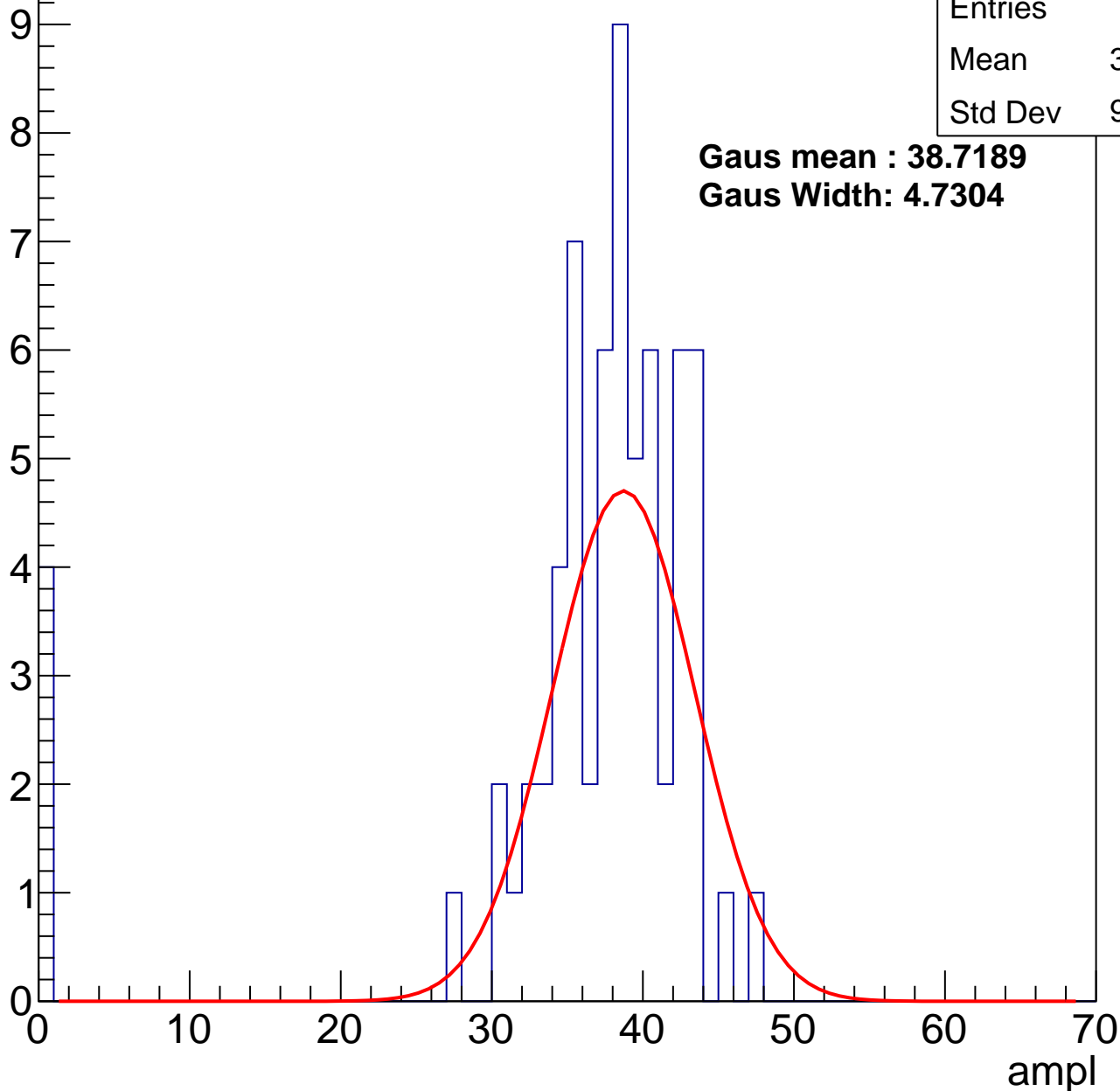
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.58
Std Dev	9.749

Gaus mean : 38.7189

Gaus Width: 4.7304



B1L103S, U18-ch11, adc2

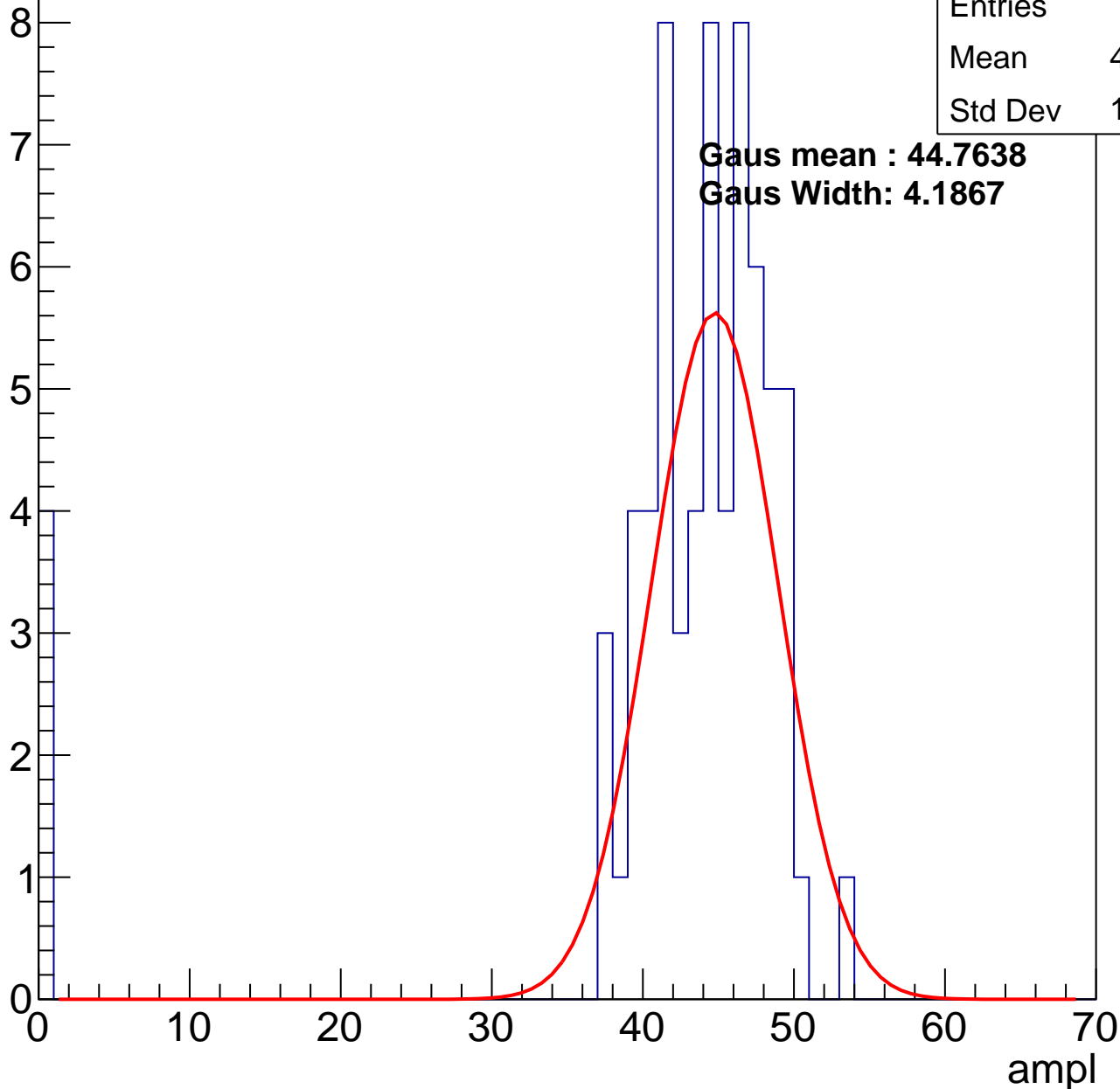
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	41.46
Std Dev	10.87

Gaus mean : 44.7638

Gaus Width: 4.1867

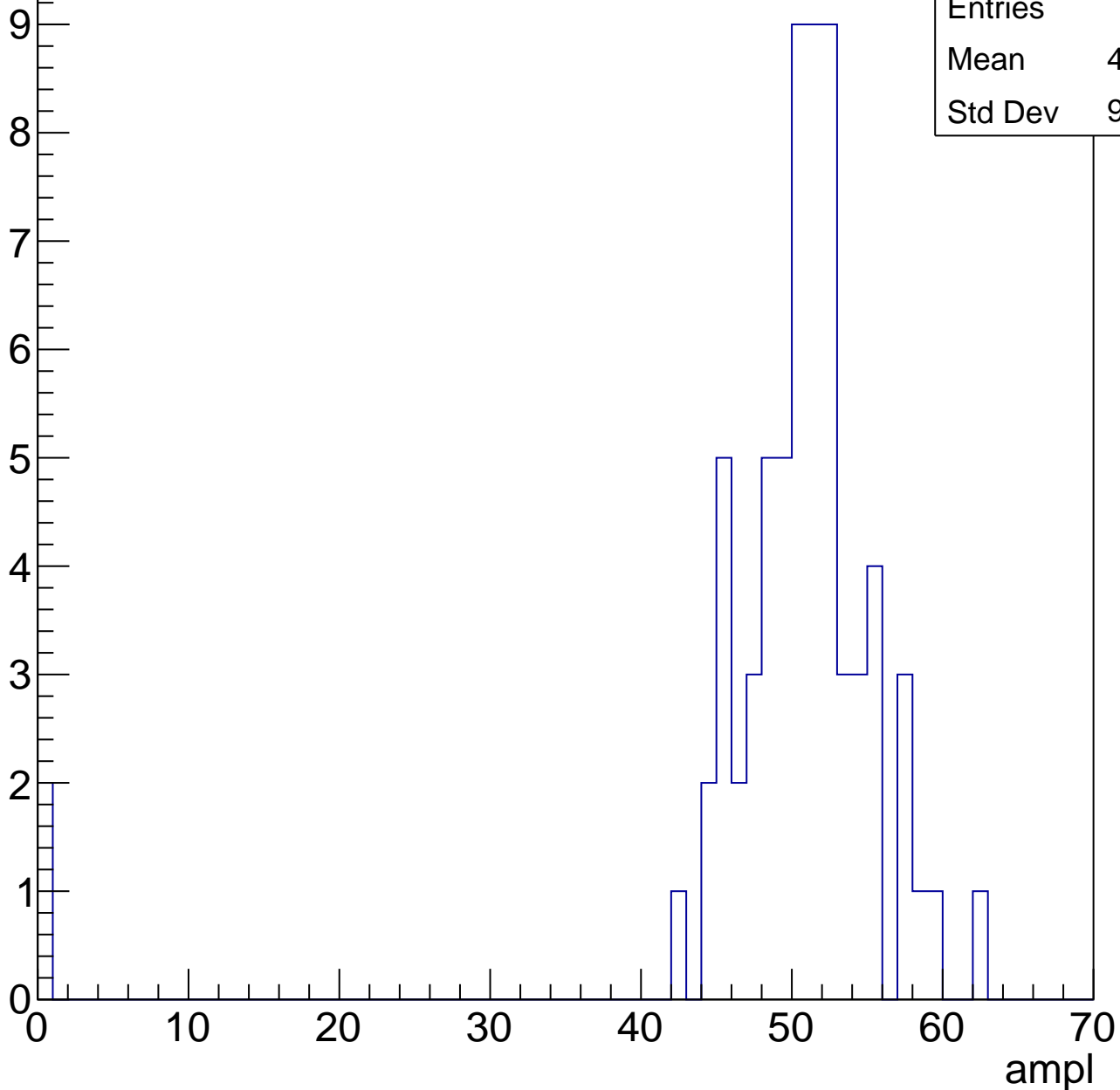


B1L103S, U18-ch11, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	49.13
Std Dev	9.354

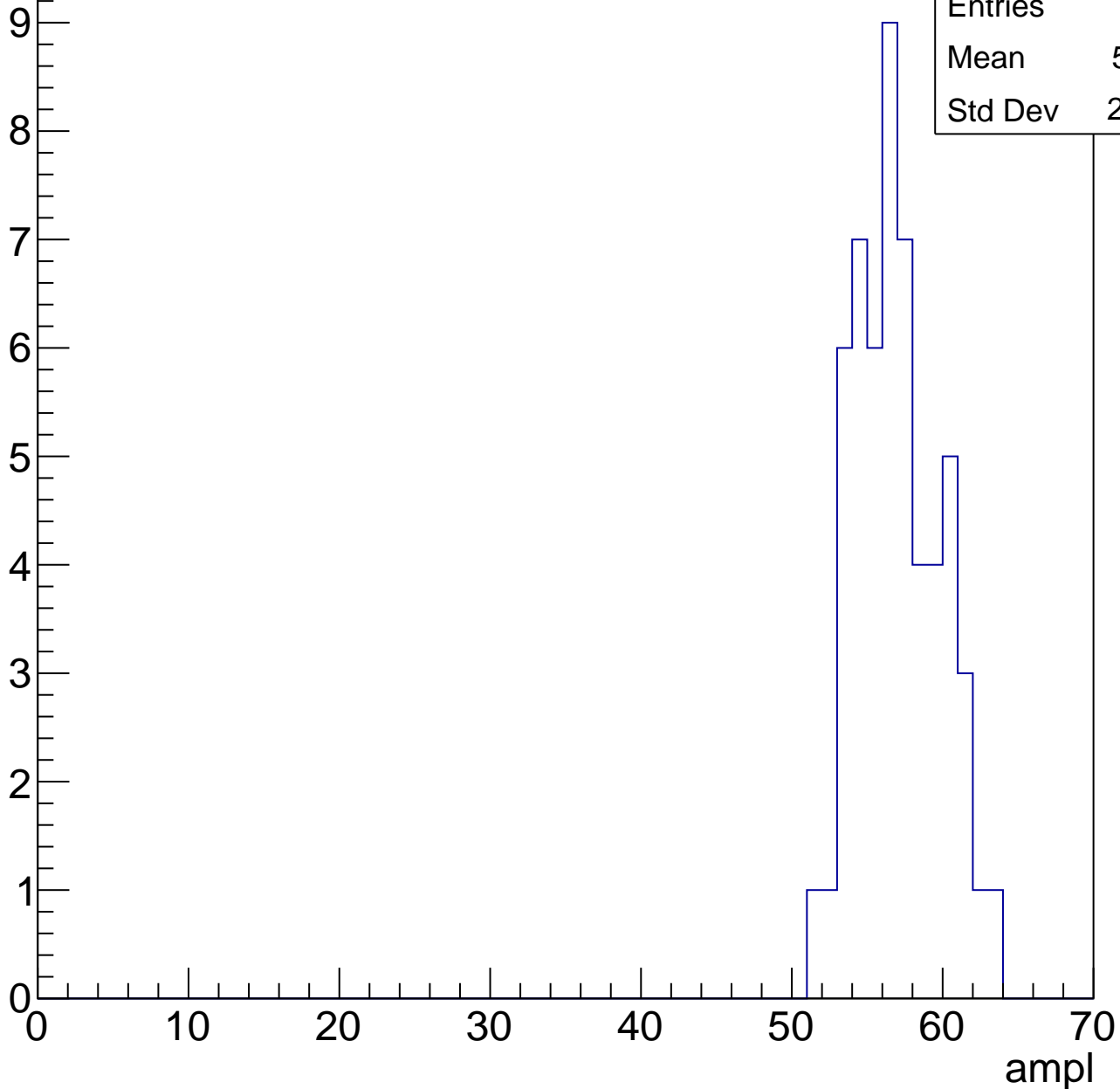


B1L103S, U18-ch11, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	56.51
Std Dev	2.743

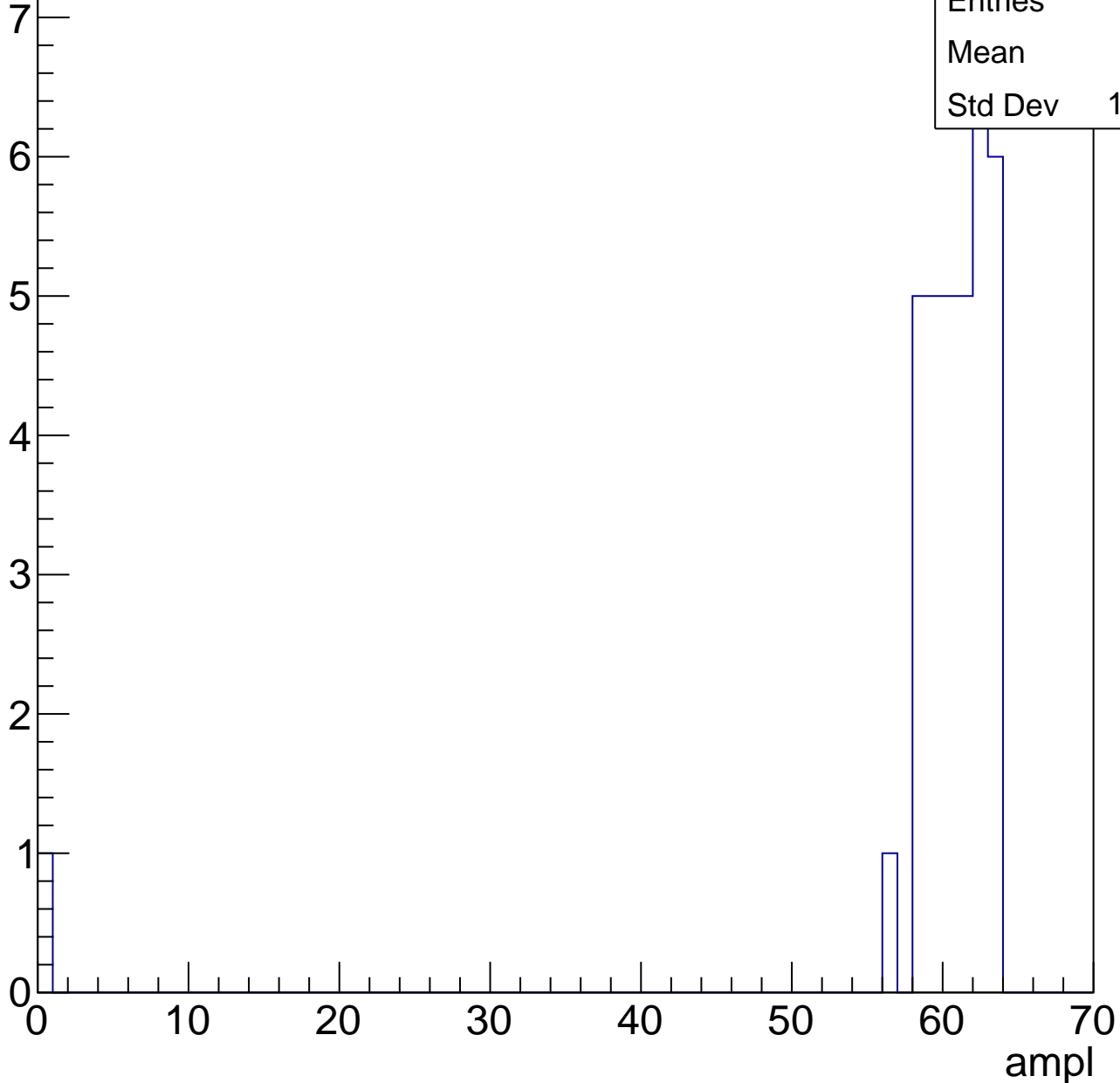


B1L103S, U18-ch11, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	58.8
Std Dev	10.25



B1L103S, U18-ch11, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch11, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U18-ch12, adc0

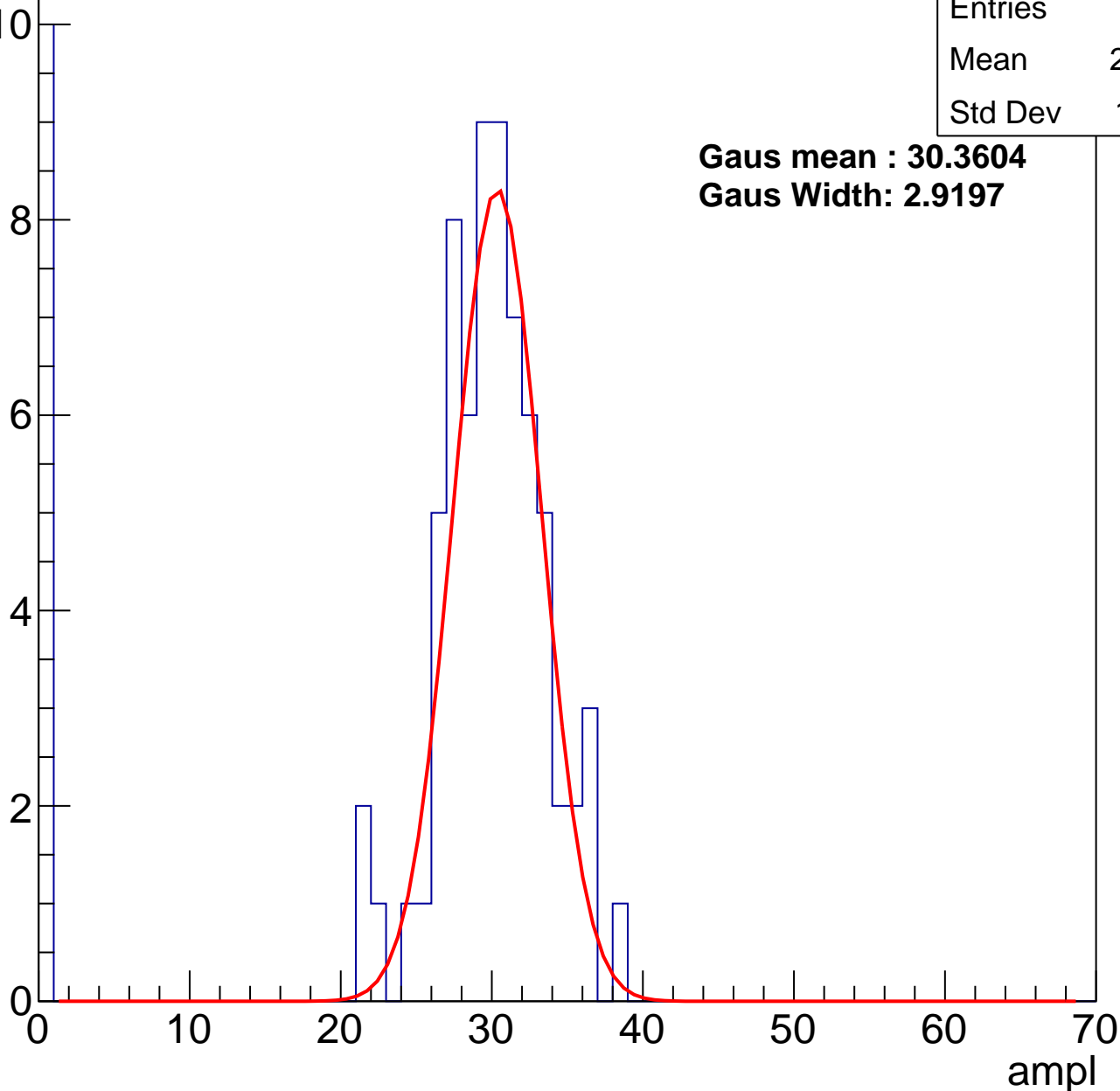
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	25.85
Std Dev	10.41

Gaus mean : 30.3604

Gaus Width: 2.9197



B1L103S, U18-ch12, adc1

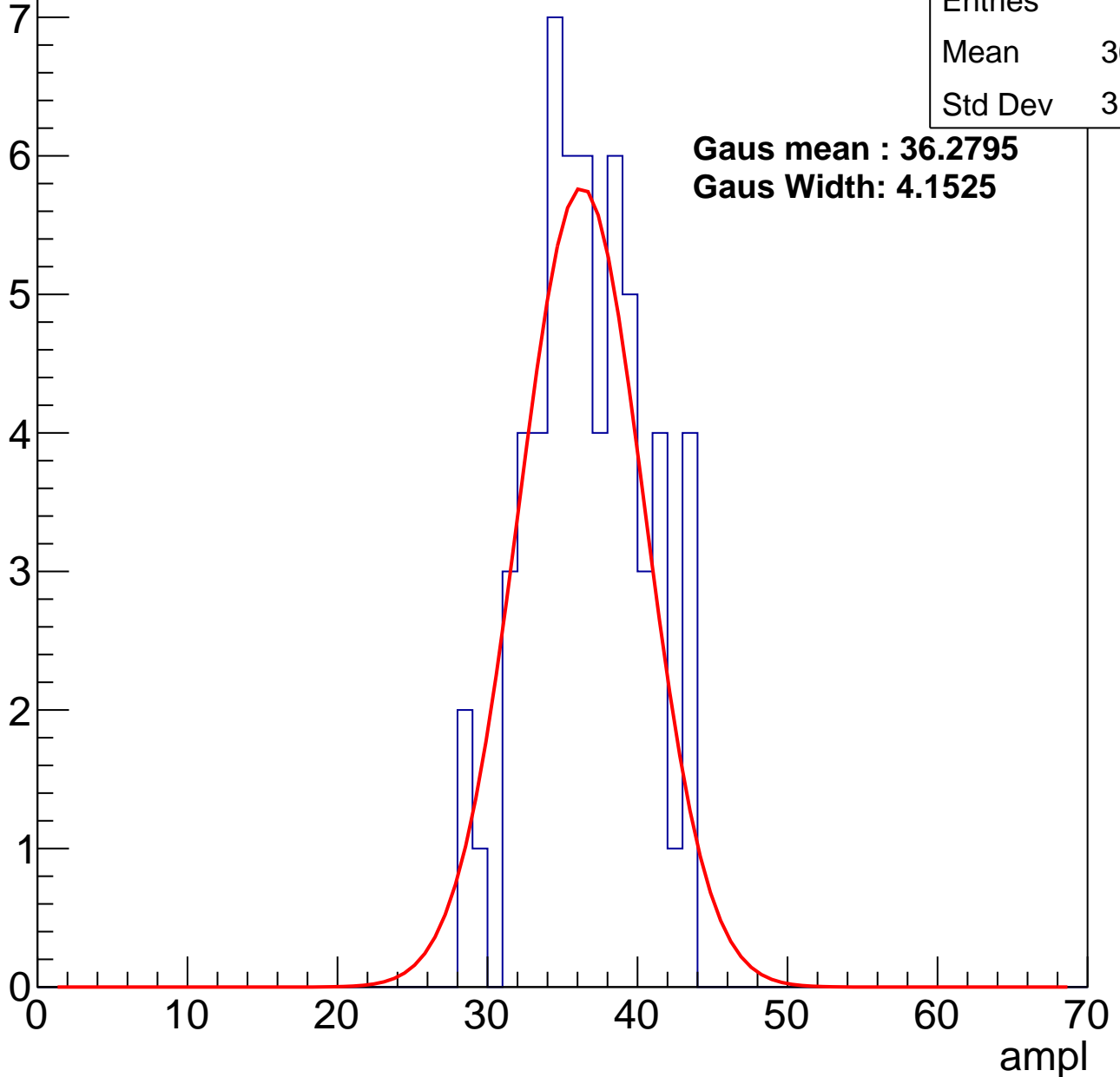
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	36.18
Std Dev	3.735

Gaus mean : 36.2795

Gaus Width: 4.1525



B1L103S, U18-ch12, adc2

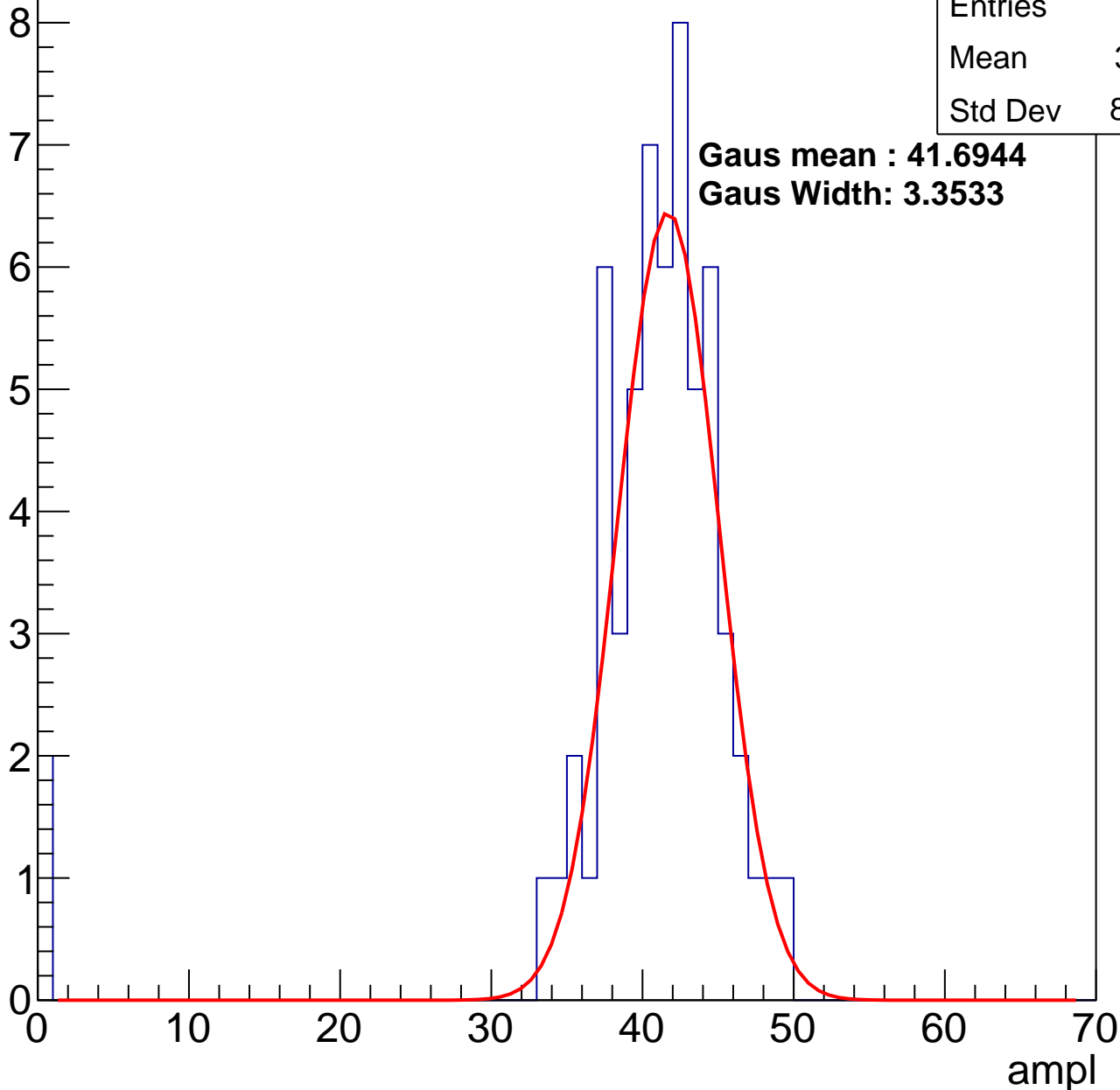
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	39.61
Std Dev	8.029

Gaus mean : 41.6944

Gaus Width: 3.3533

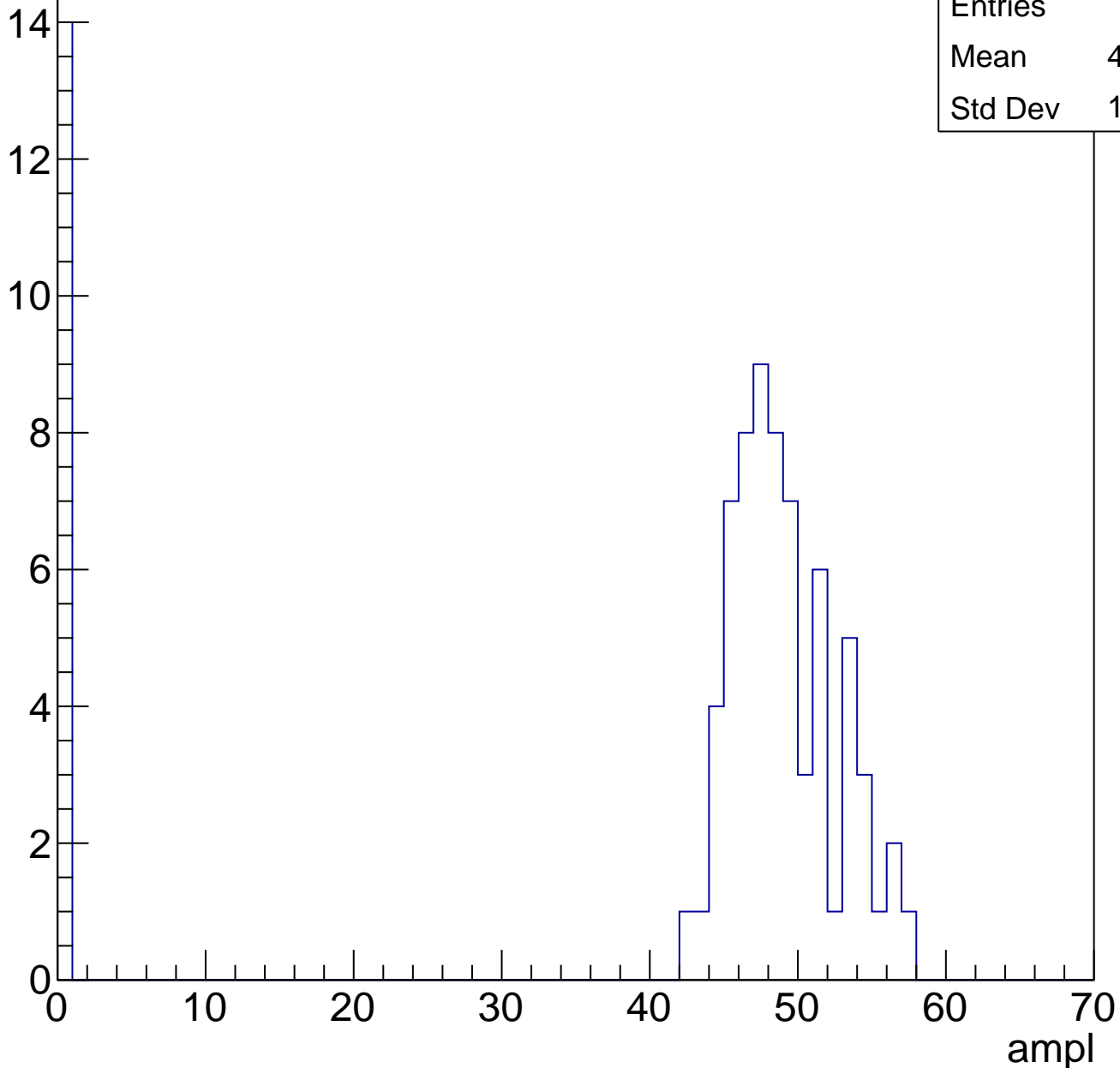


B1L103S, U18-ch12, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	40.16
Std Dev	18.62

Entry

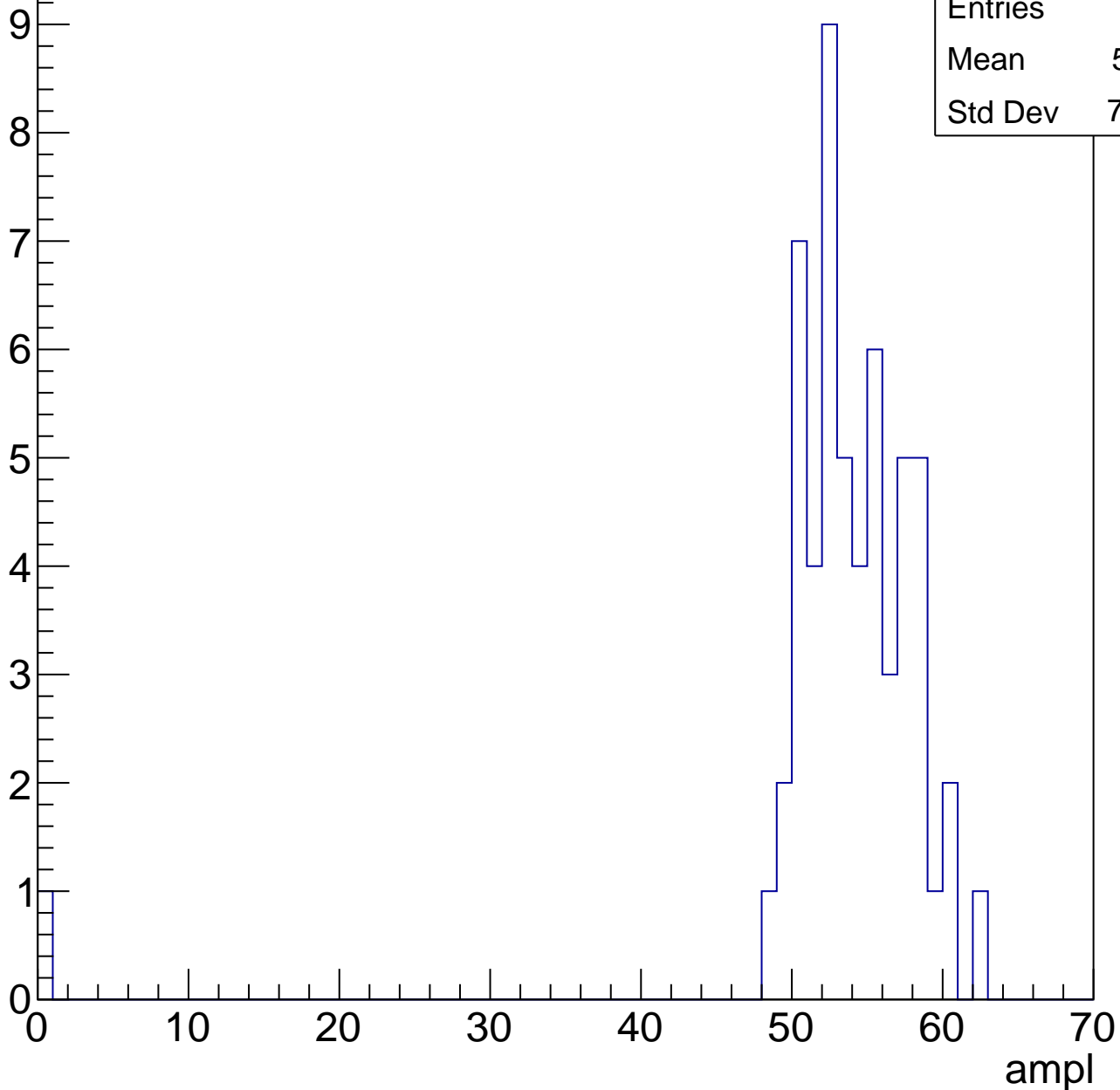


B1L103S, U18-ch12, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

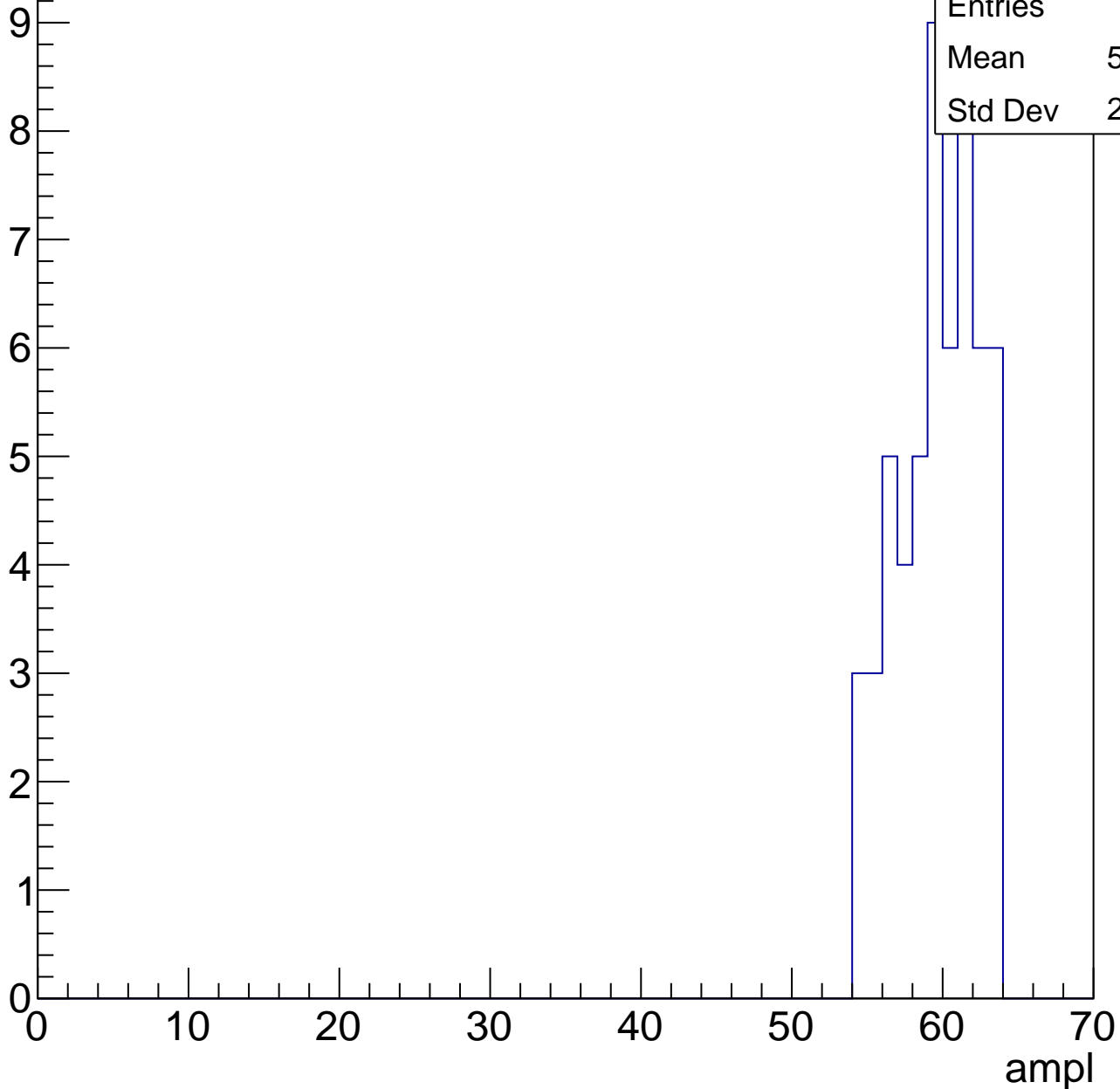
Entries	56
Mean	52.91
Std Dev	7.825



B1L103S, U18-ch12, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

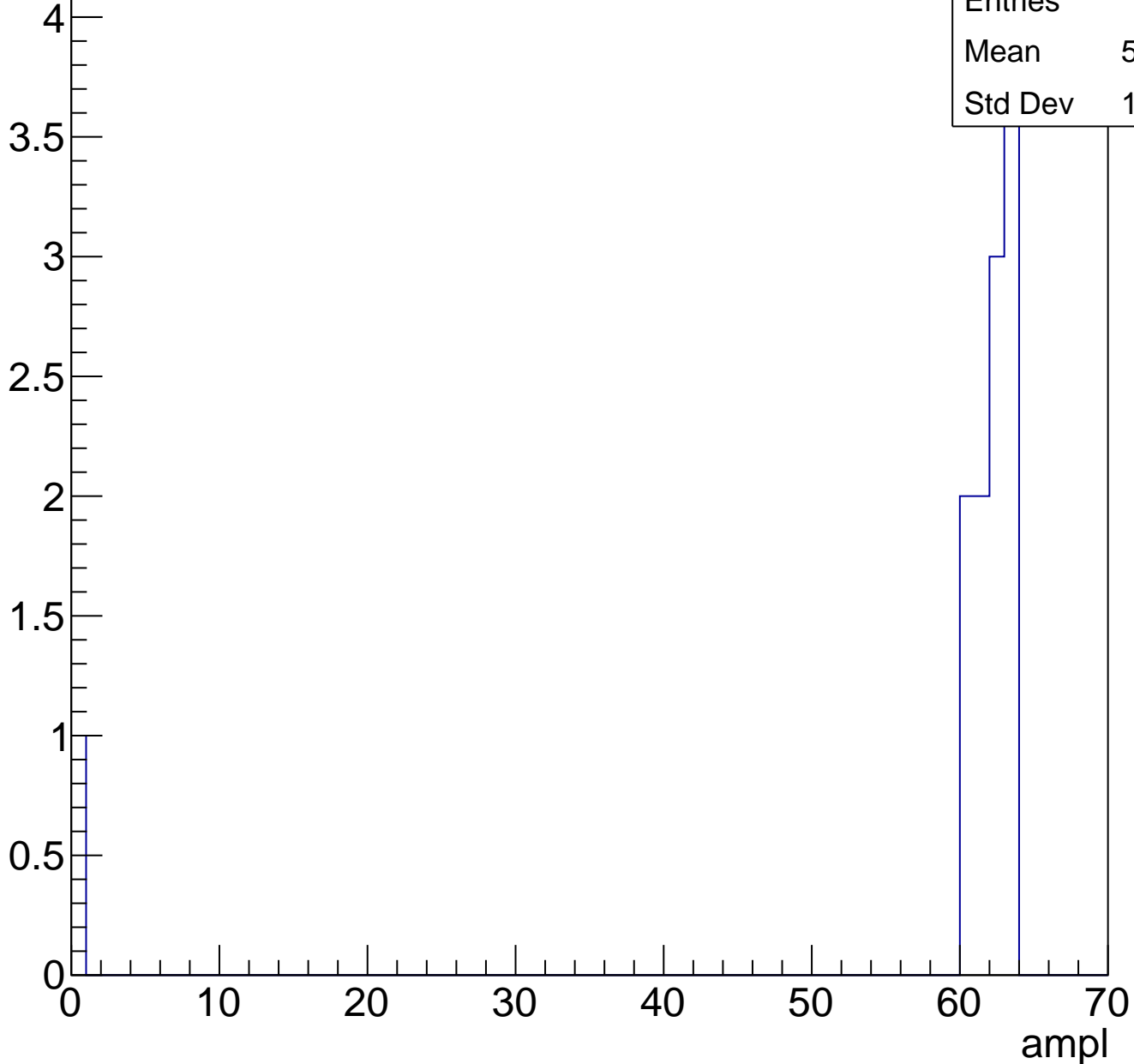


Entries	55
Mean	59.16
Std Dev	2.613

B1L103S, U18-ch12, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch12, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch13, adc0

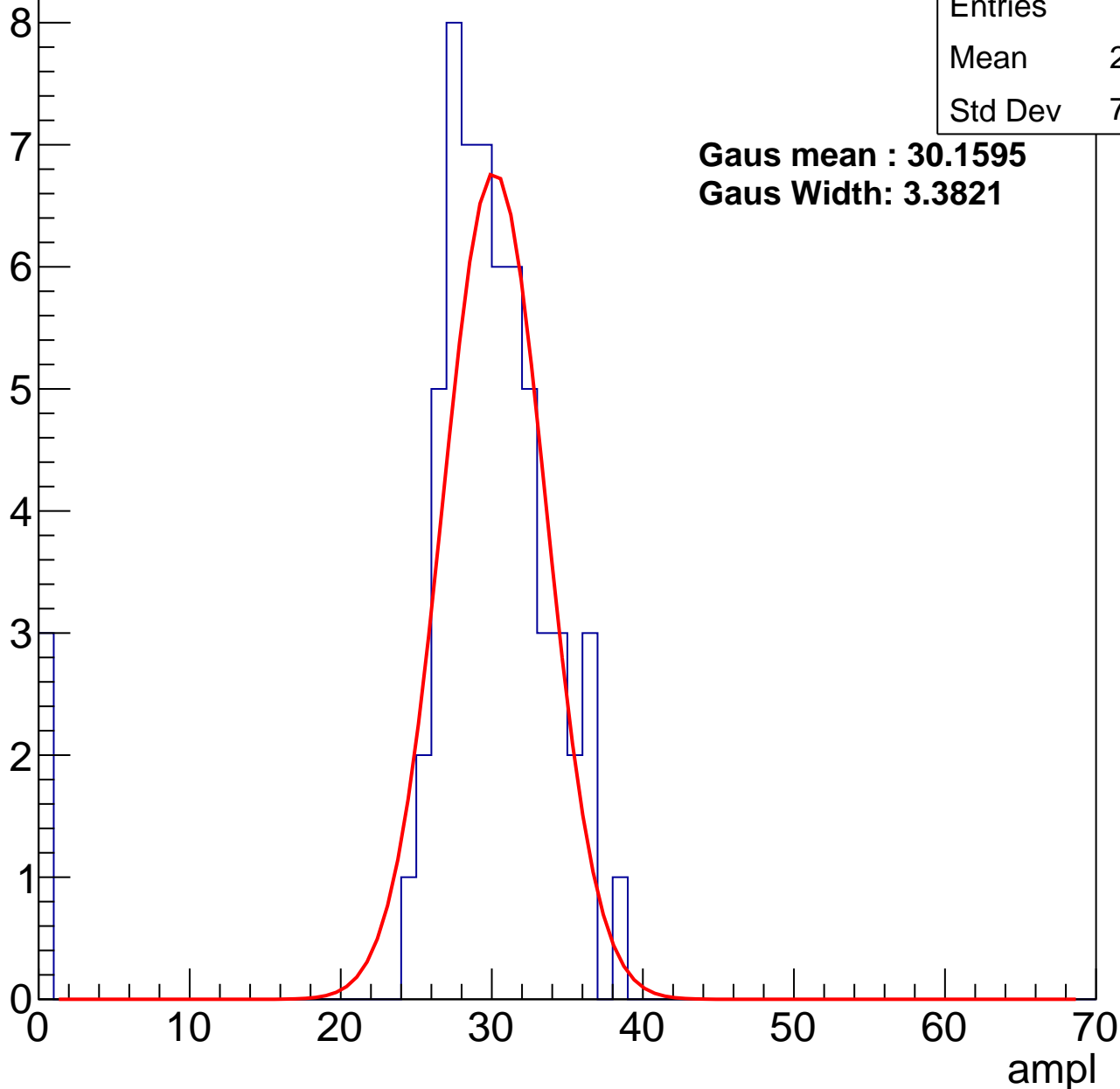
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	28.42
Std Dev	7.118

Gaus mean : 30.1595

Gaus Width: 3.3821



B1L103S, U18-ch13, adc1

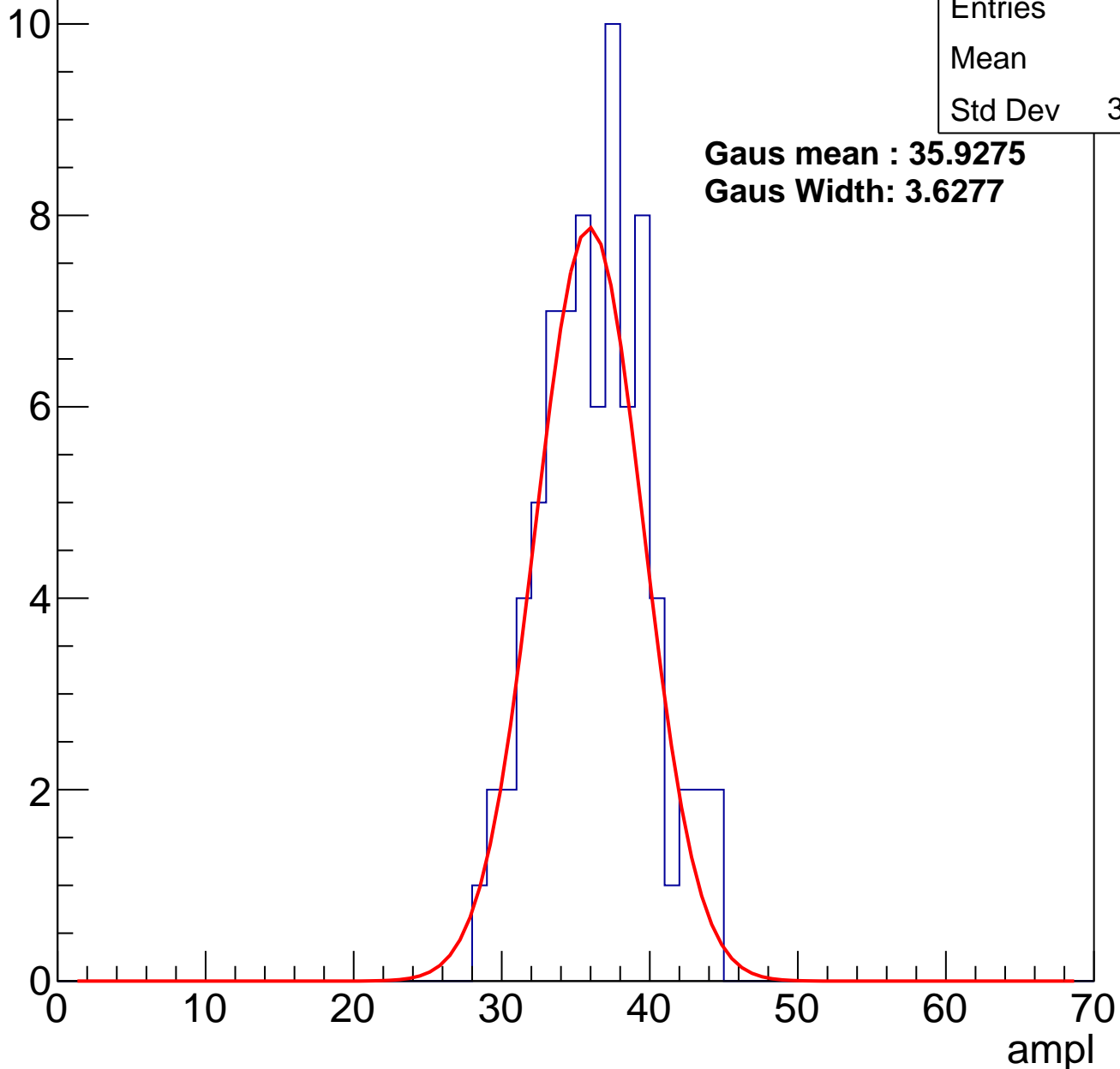
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	35.9
Std Dev	3.613

Gaus mean : 35.9275

Gaus Width: 3.6277

Entry

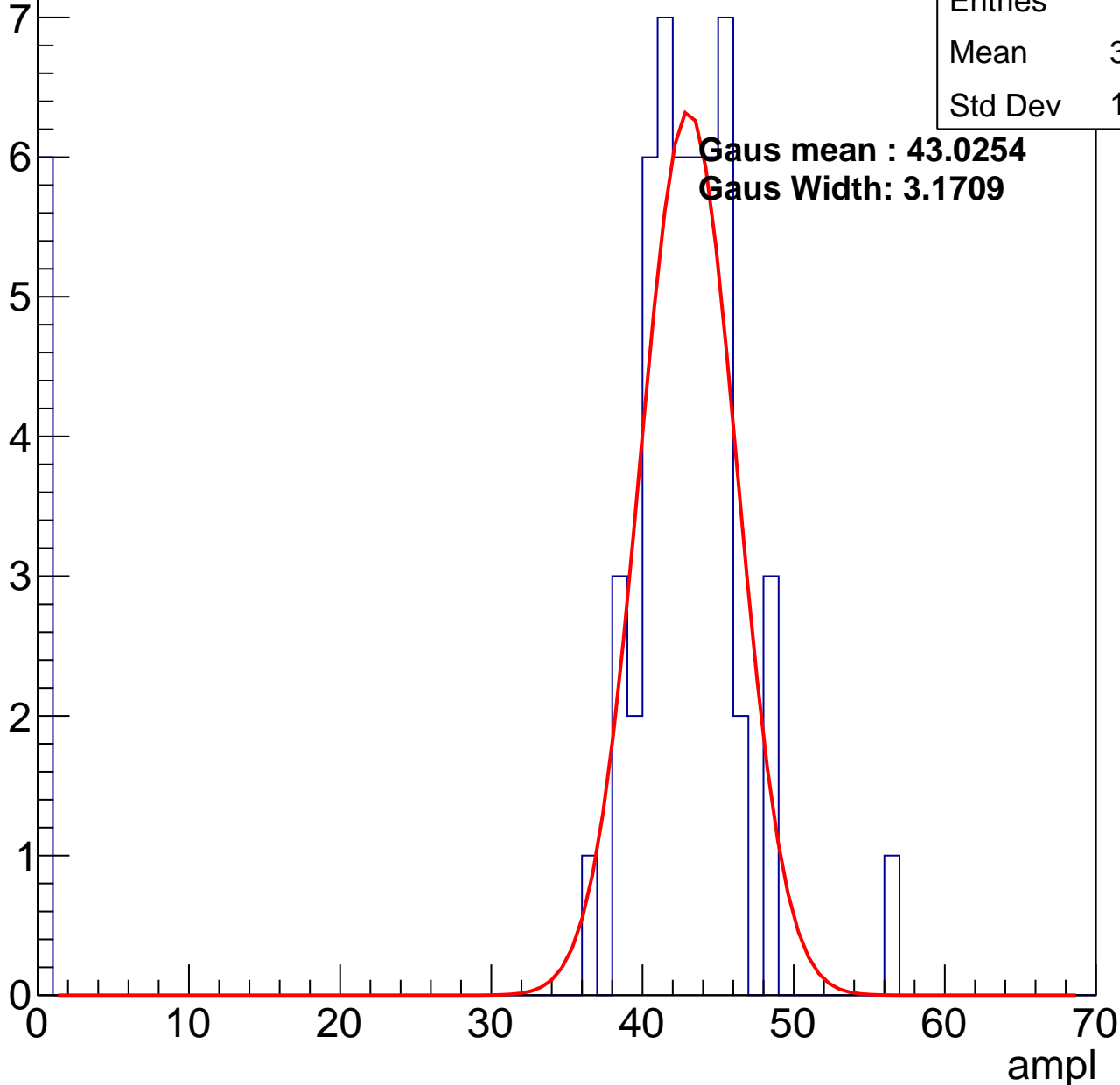


B1L103S, U18-ch13, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	38.14
Std Dev	13.57



B1L103S, U18-ch13, adc3

calib_packv5_041523_1651.root, FC#0, port C2

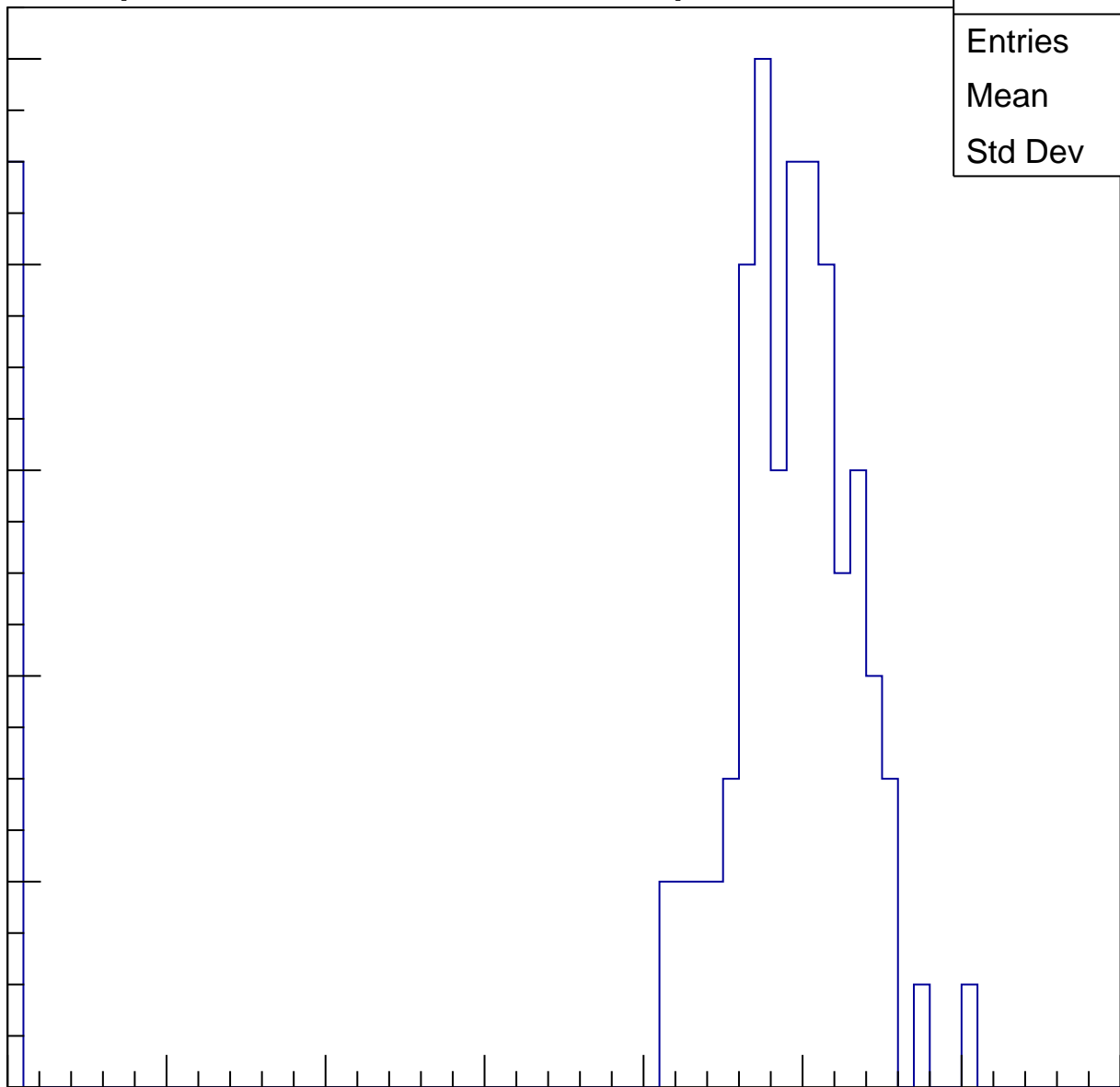
Entries	90
Mean	44.18
Std Dev	15.13

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

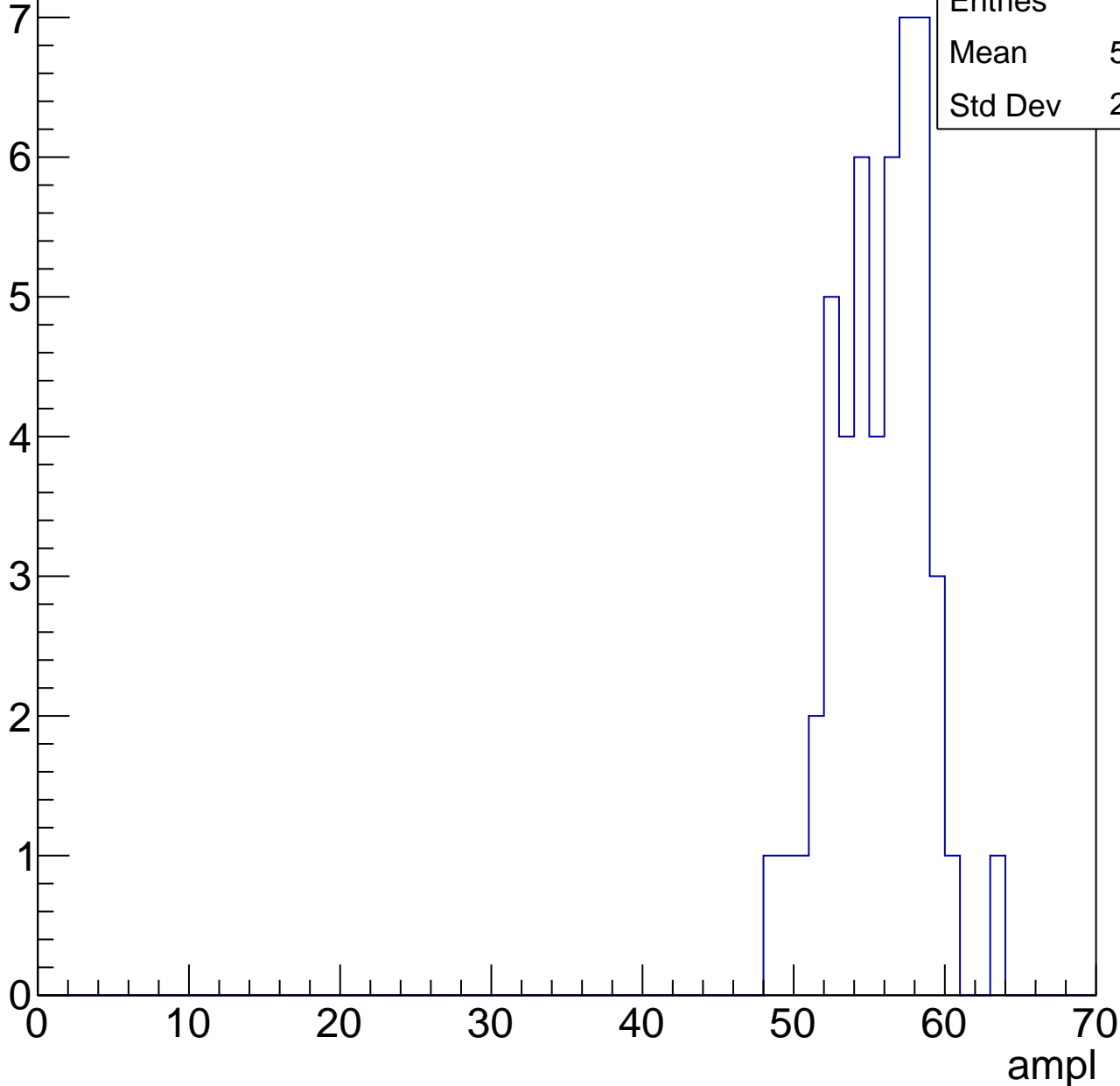


B1L103S, U18-ch13, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.22
Std Dev	2.998

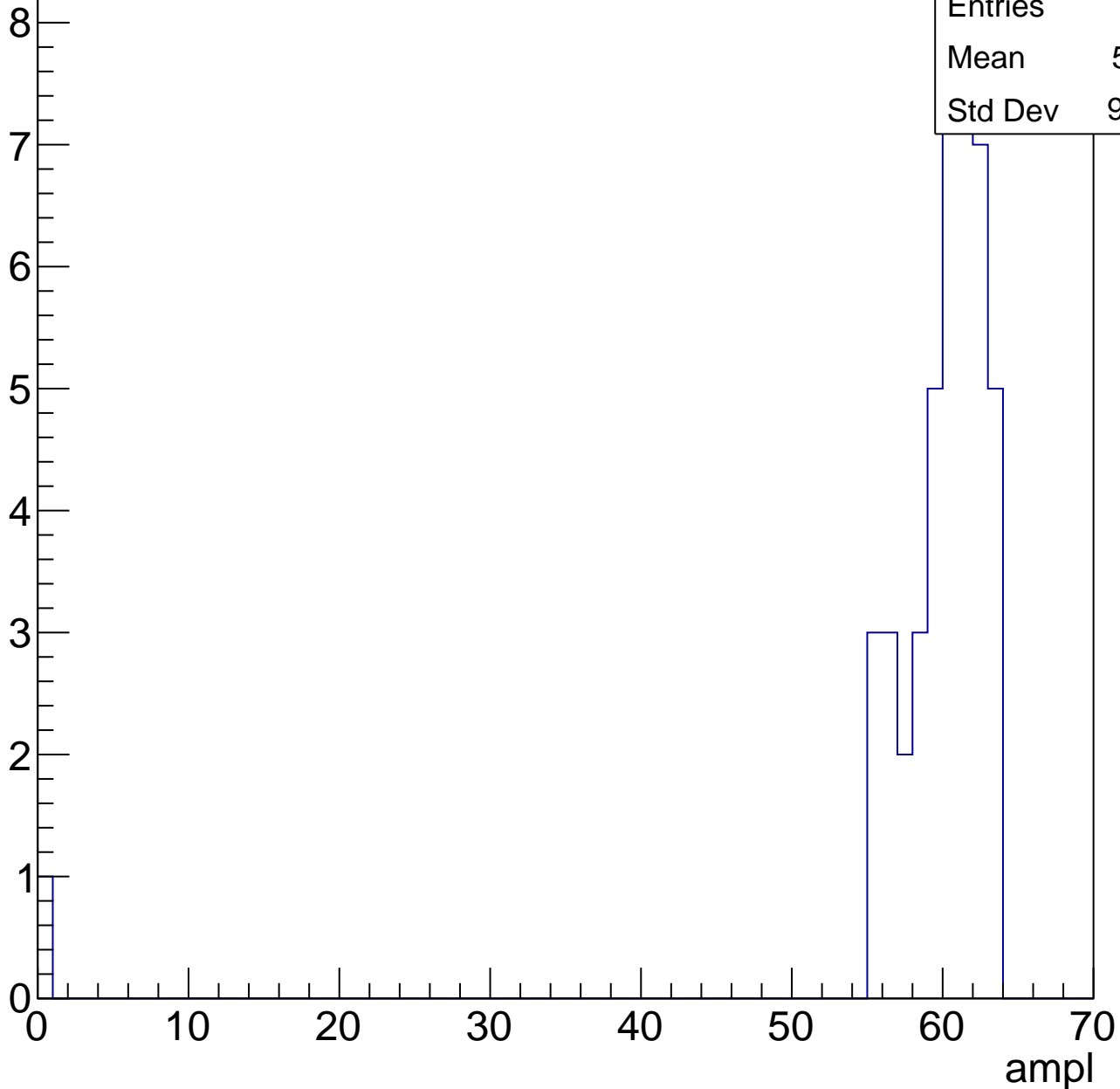


B1L103S, U18-ch13, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

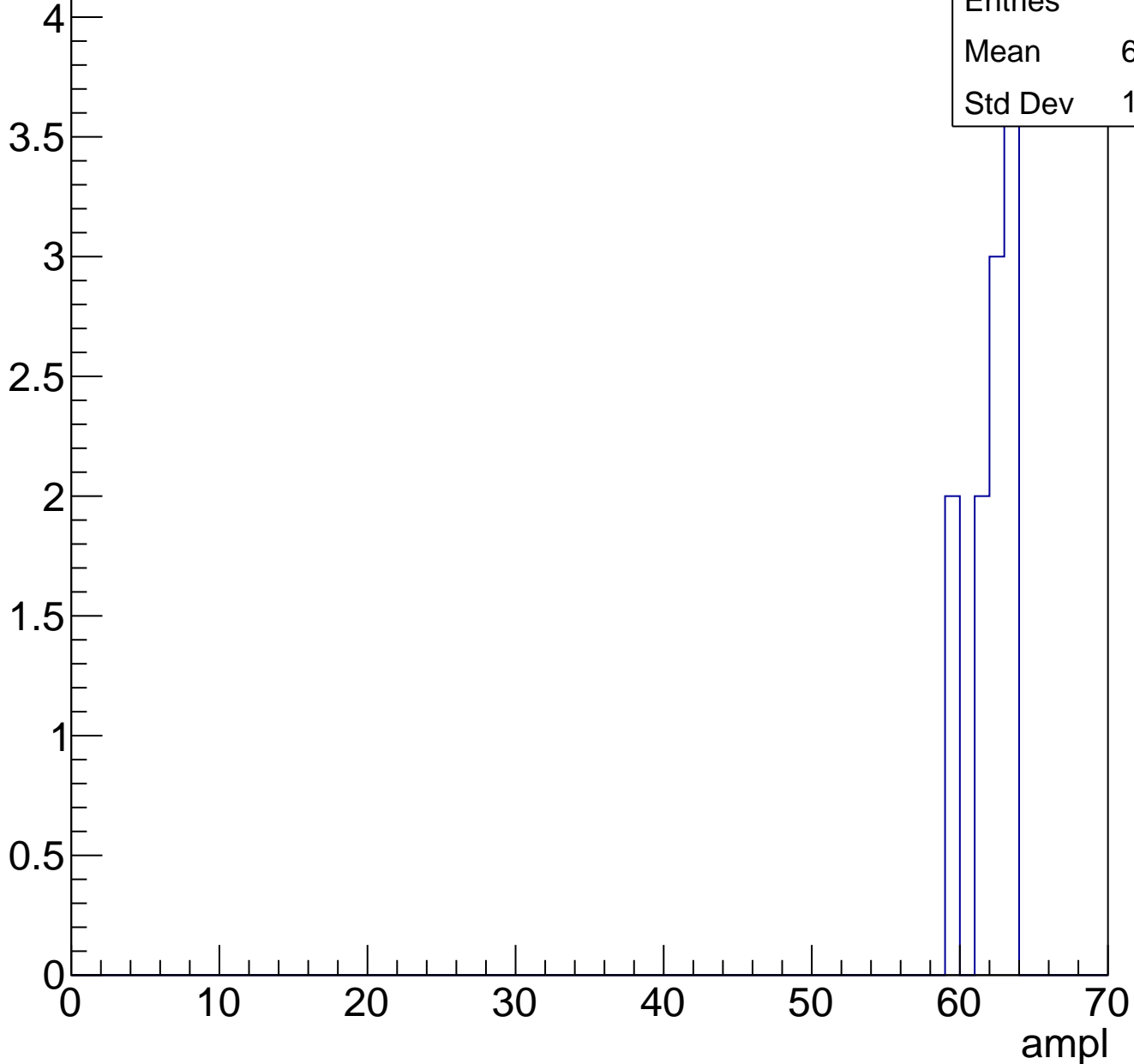
Entries	45
Mean	58.51
Std Dev	9.116



B1L103S, U18-ch13, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



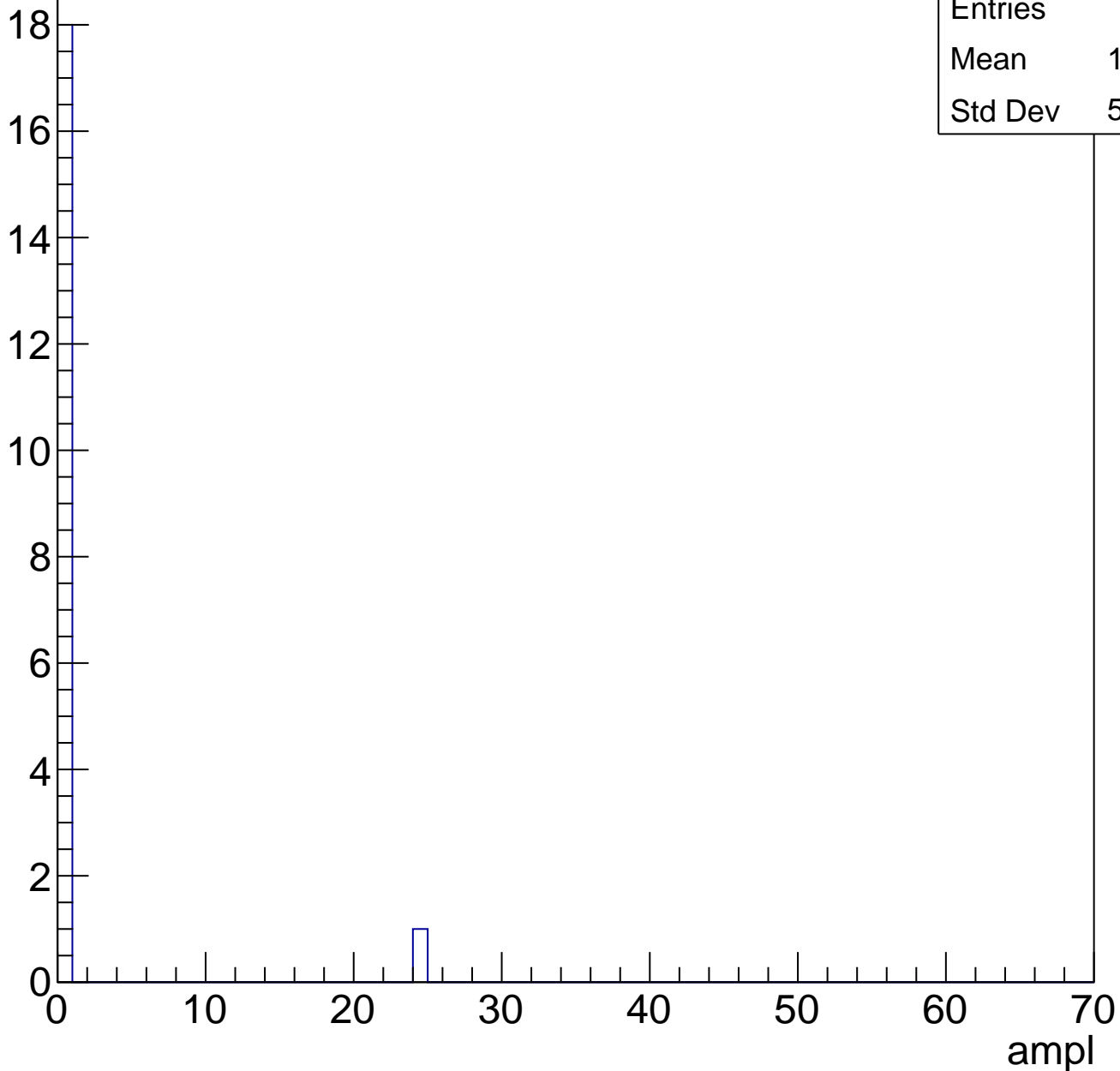
Entries	11
Mean	61.64
Std Dev	1.432

B1L103S, U18-ch13, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.263
Std Dev	5.359

Entry



B1L103S, U18-ch14, adc0

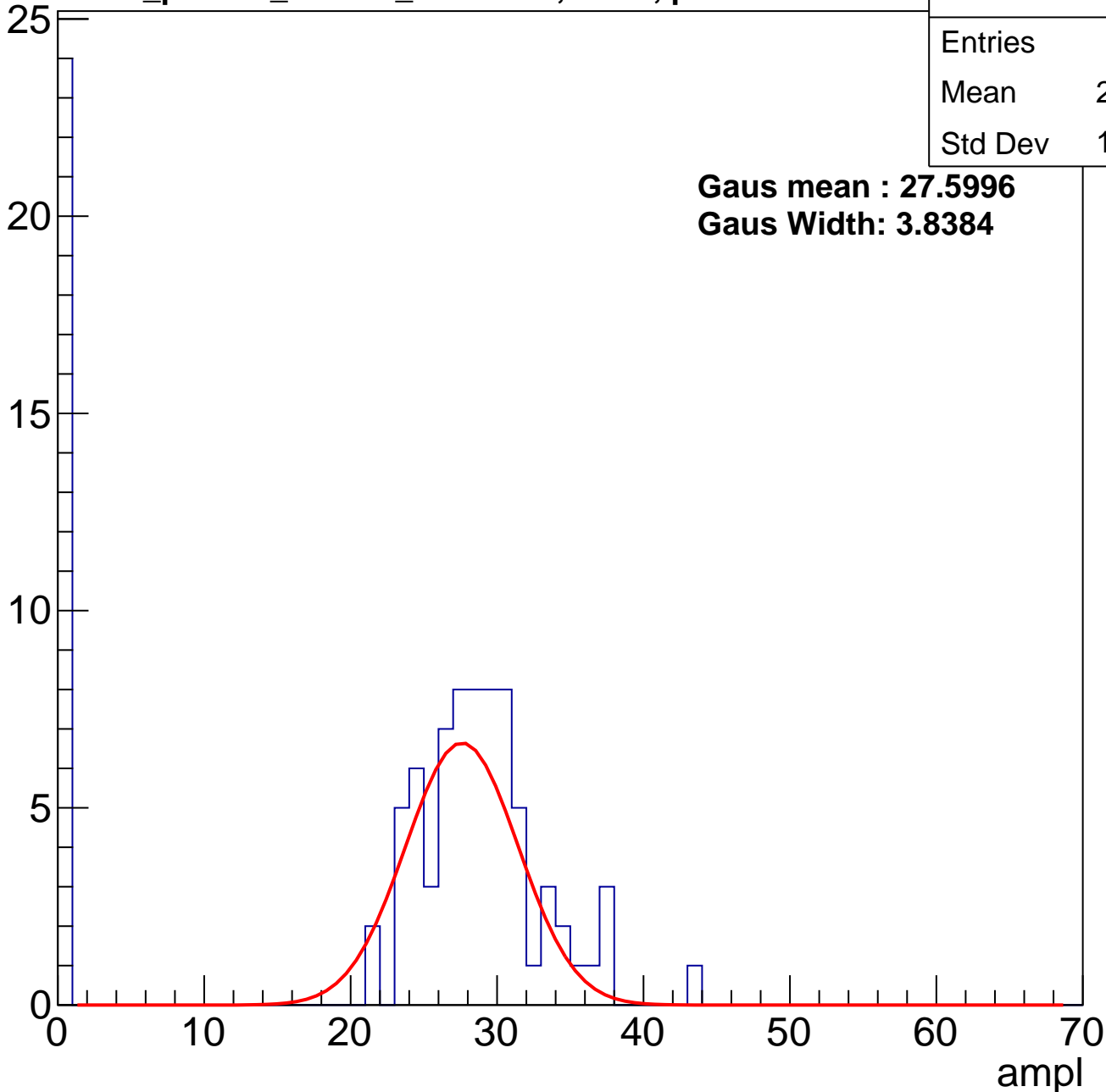
calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	21.34
Std Dev	12.82

Gaus mean : 27.5996

Gaus Width: 3.8384

Entry



B1L103S, U18-ch14, adc1

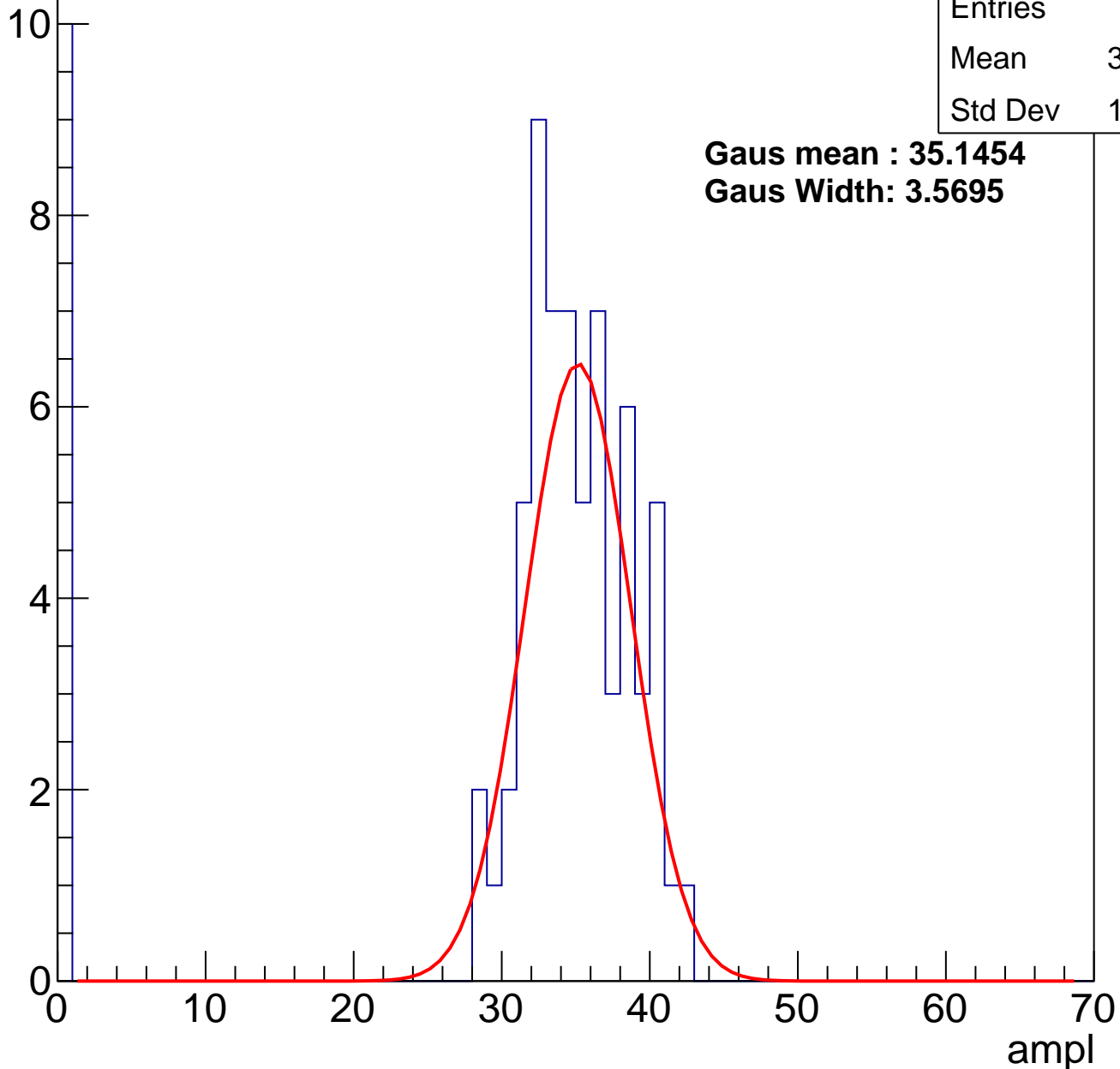
calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	30.04
Std Dev	12.27

Gaus mean : 35.1454

Gaus Width: 3.5695

Entry



B1L103S, U18-ch14, adc2

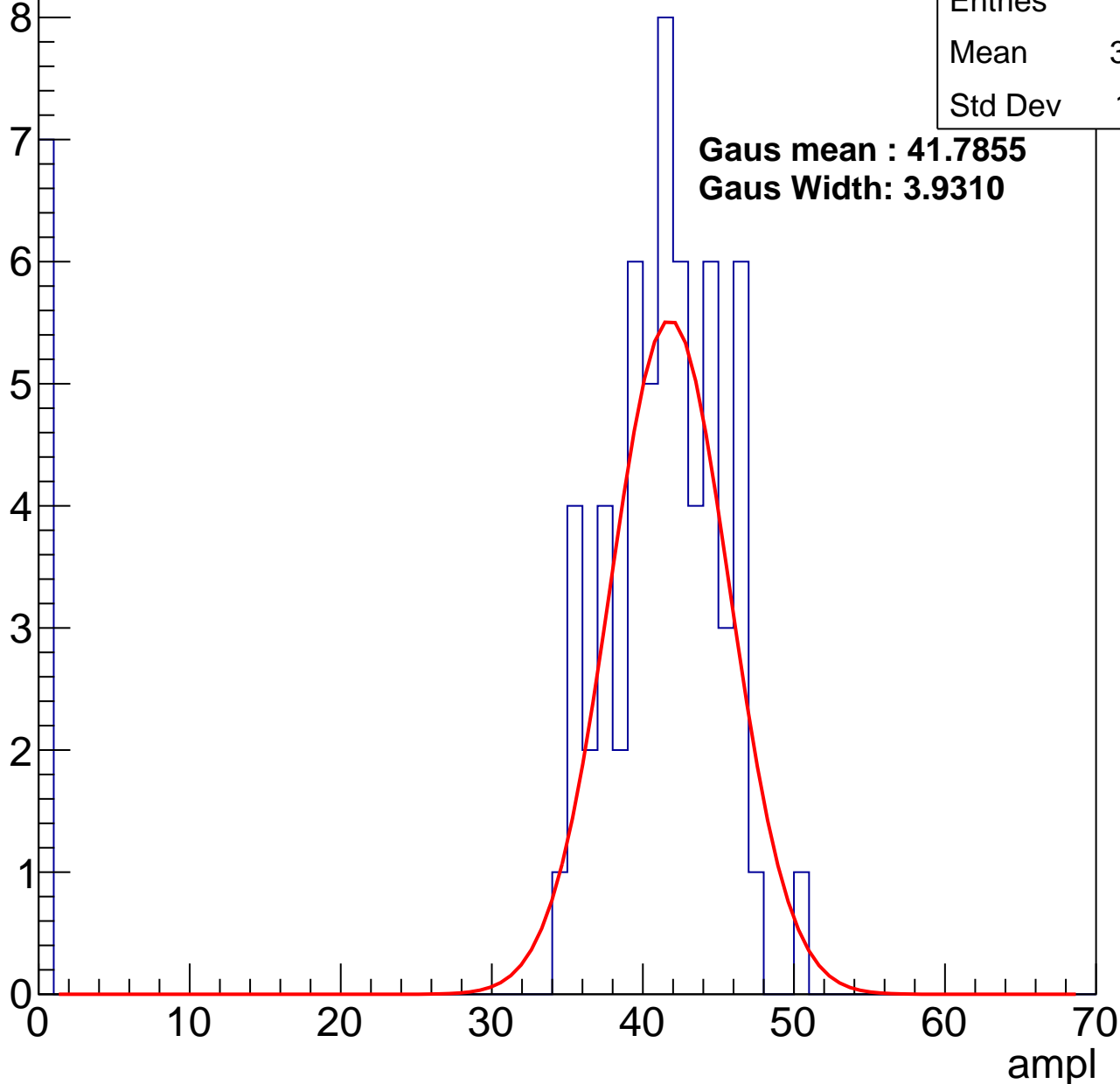
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.79
Std Dev	13.11

Gaus mean : 41.7855

Gaus Width: 3.9310

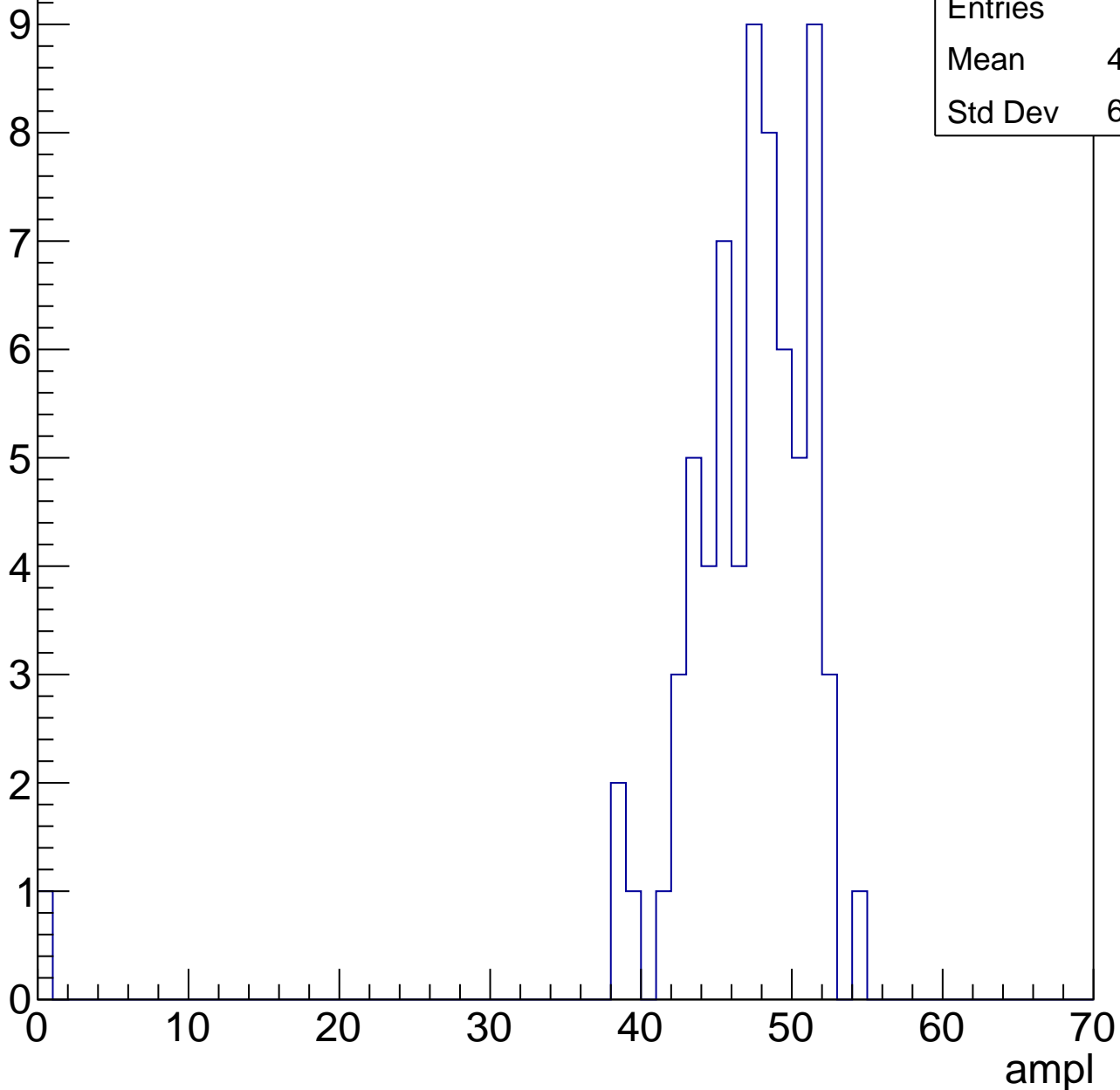


B1L103S, U18-ch14, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	46.26
Std Dev	6.595

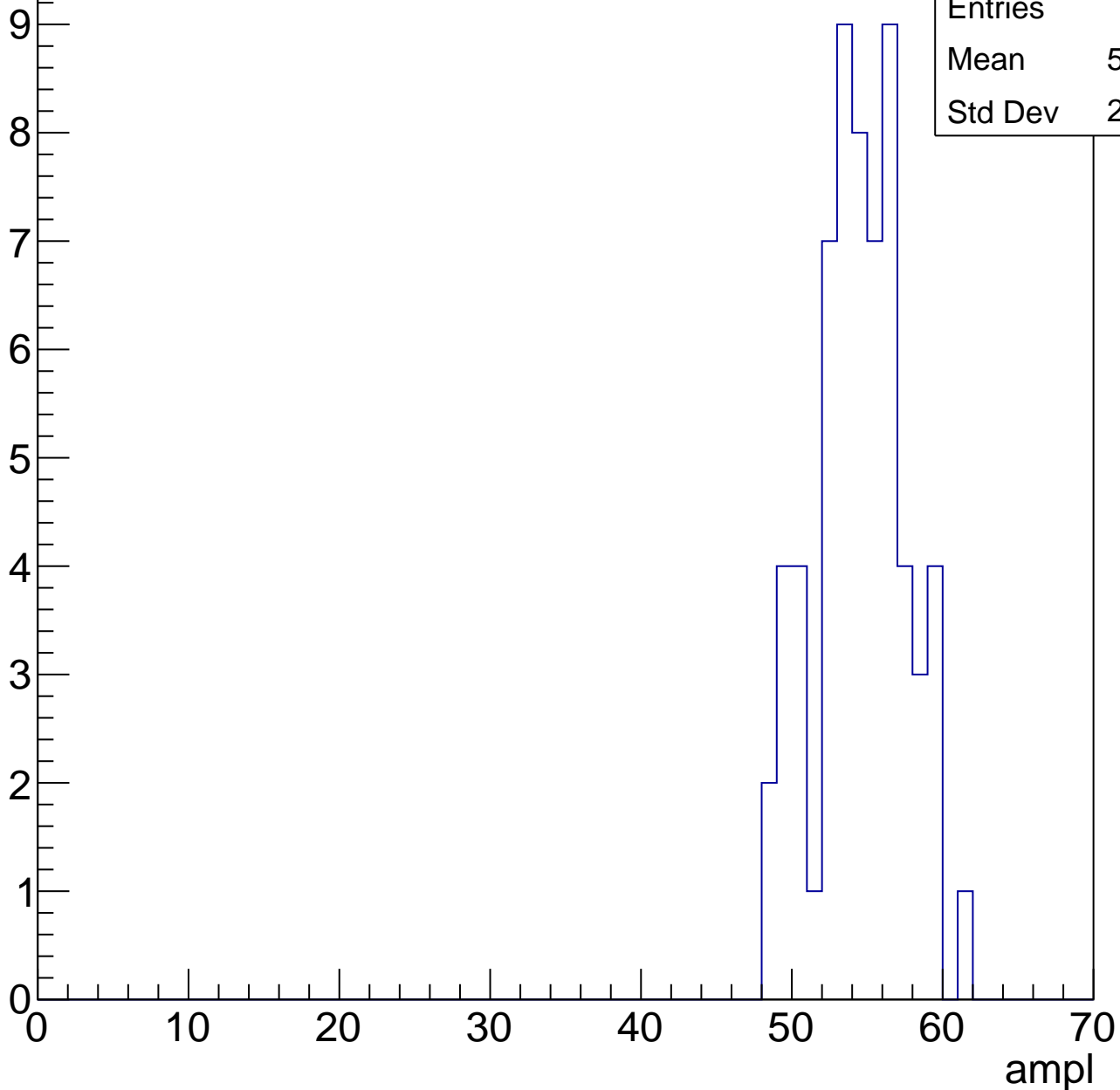


B1L103S, U18-ch14, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	54.03
Std Dev	2.976

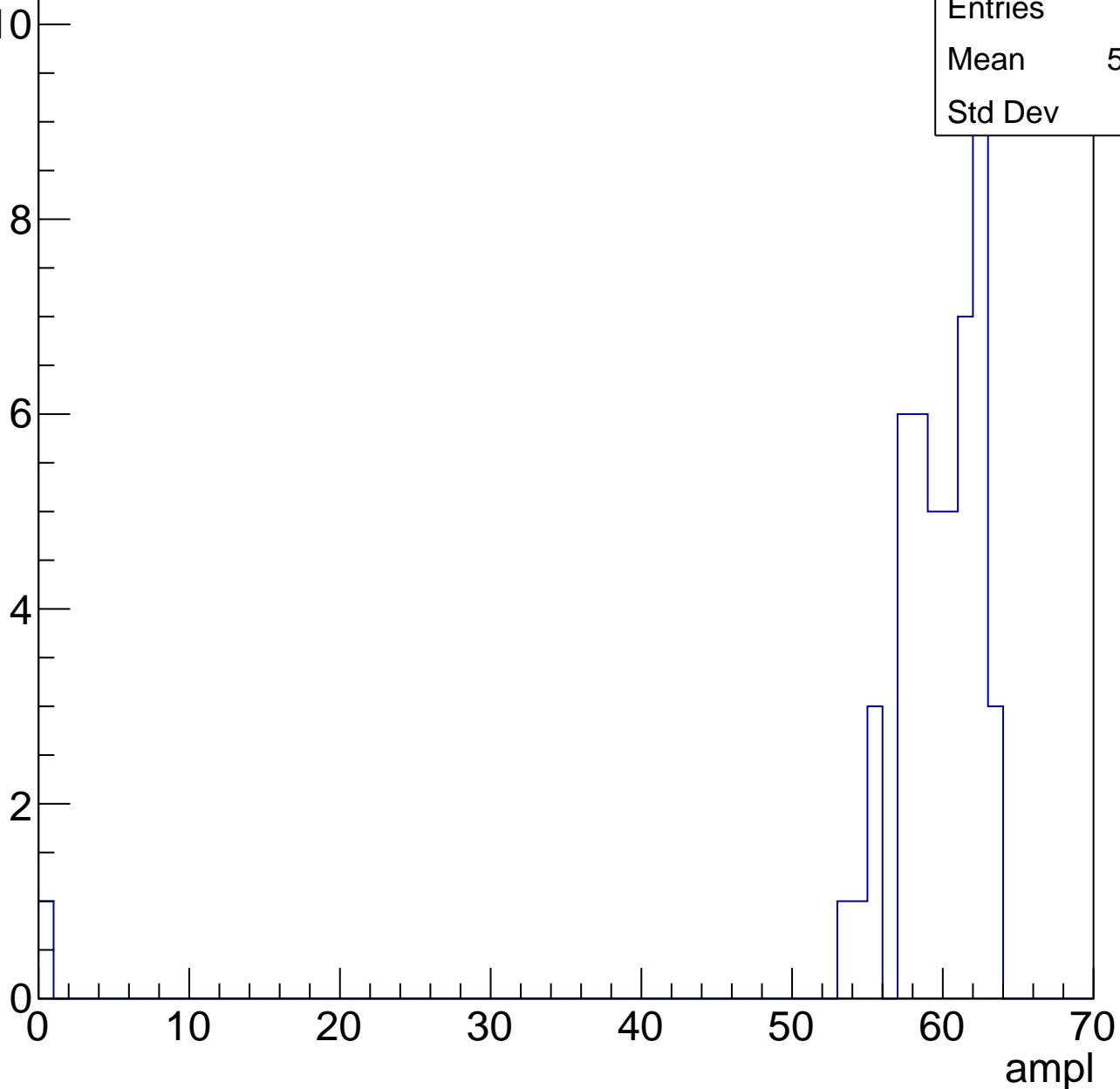


B1L103S, U18-ch14, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	58.19
Std Dev	8.85

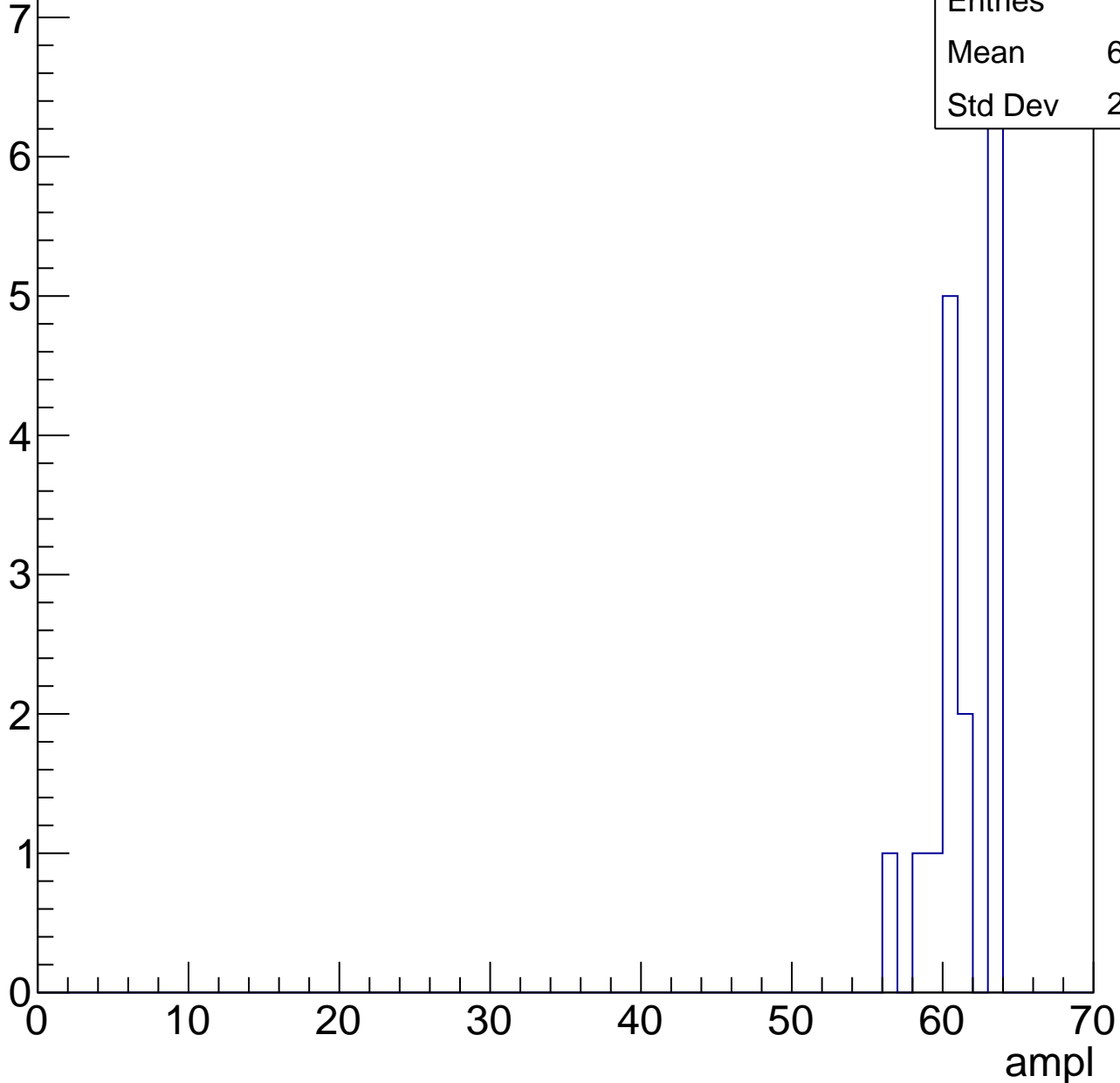


B1L103S, U18-ch14, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	60.94
Std Dev	2.043



B1L103S, U18-ch14, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch15, adc0

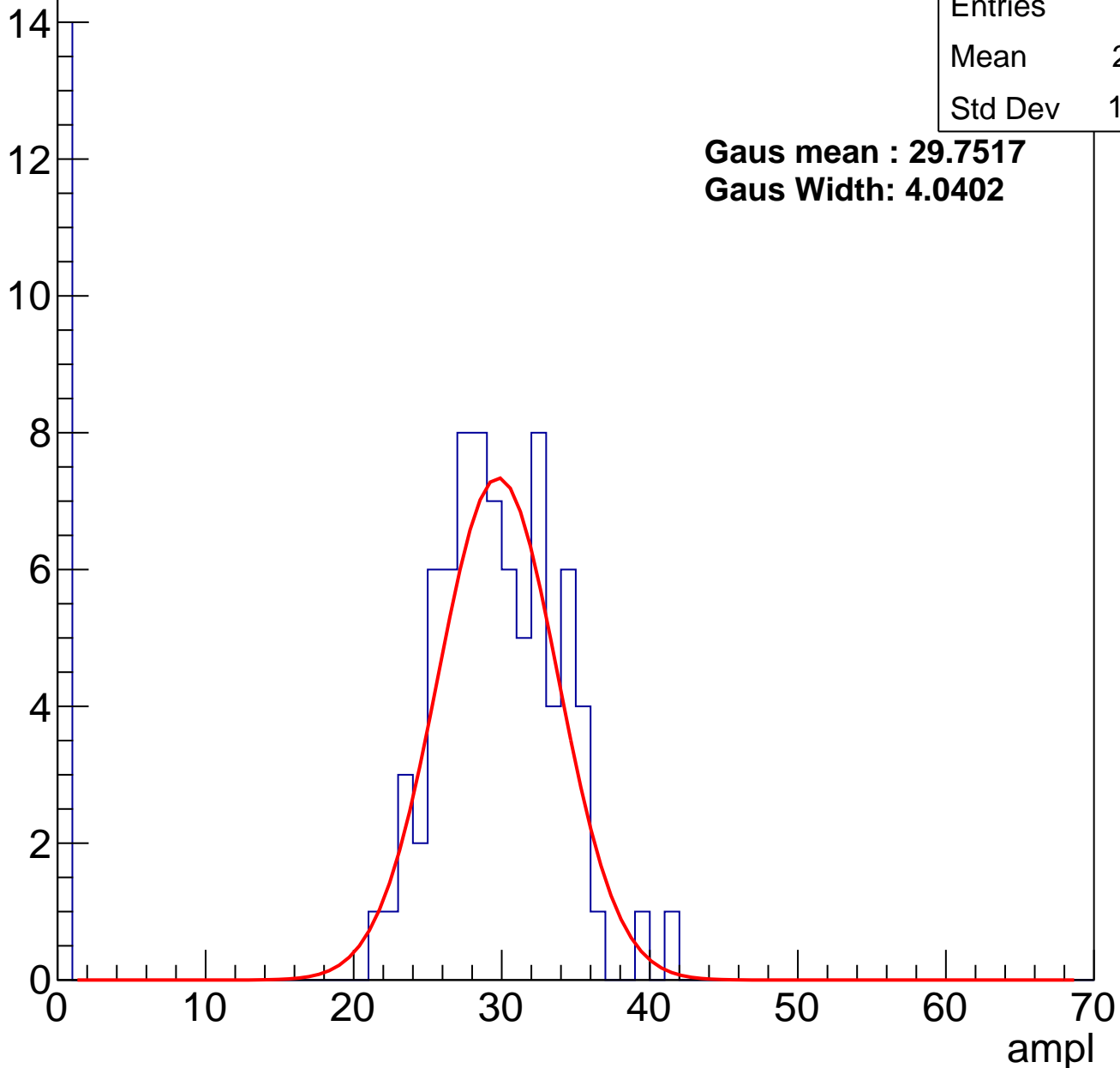
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	24.91
Std Dev	11.15

Gaus mean : 29.7517

Gaus Width: 4.0402

Entry



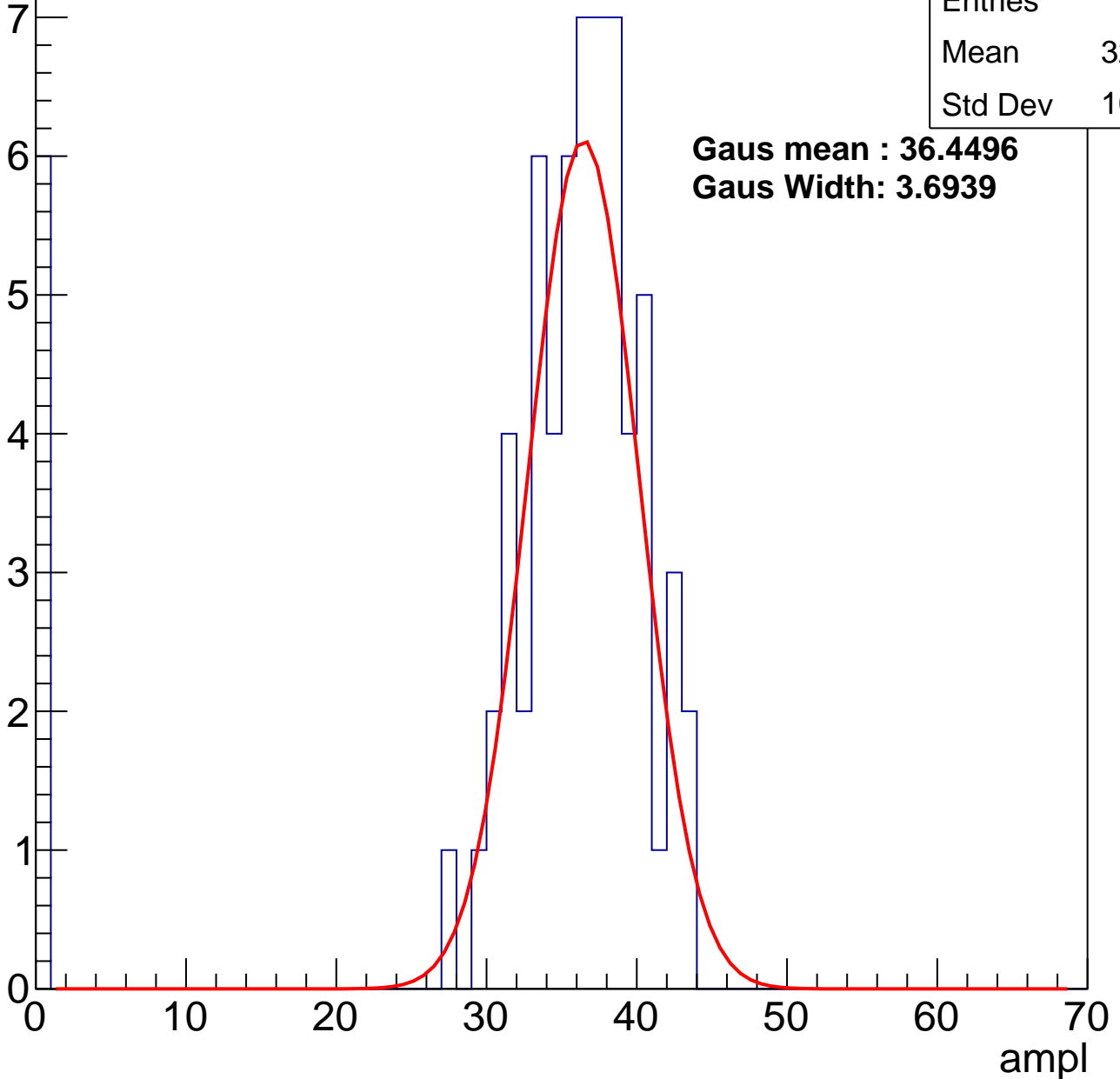
B1L103S, U18-ch15, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	32.85
Std Dev	10.77

Gaus mean : 36.4496
Gaus Width: 3.6939



B1L103S, U18-ch15, adc2

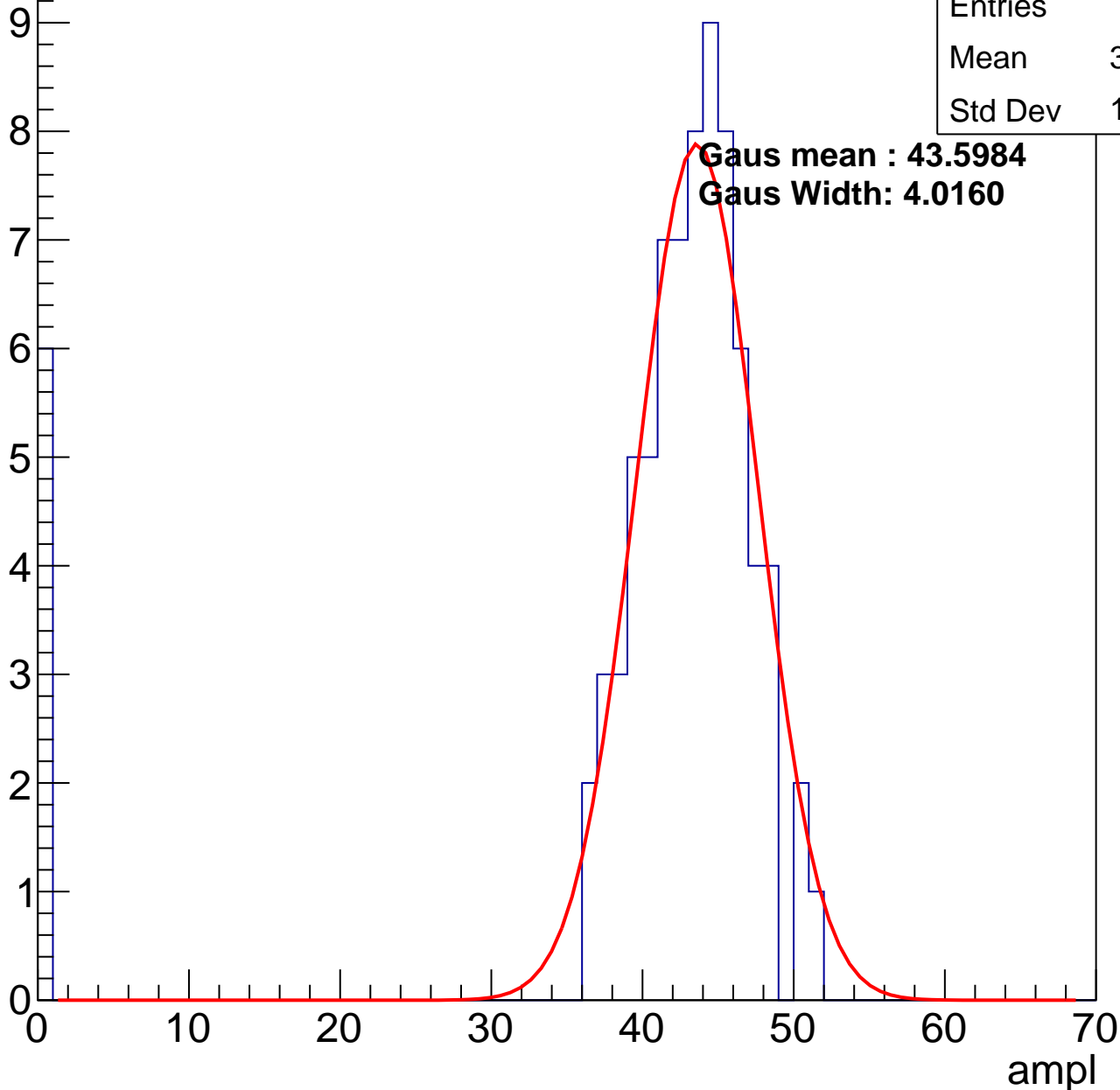
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	39.75
Std Dev	11.79

Gaus mean : 43.5984

Gaus Width: 4.0160

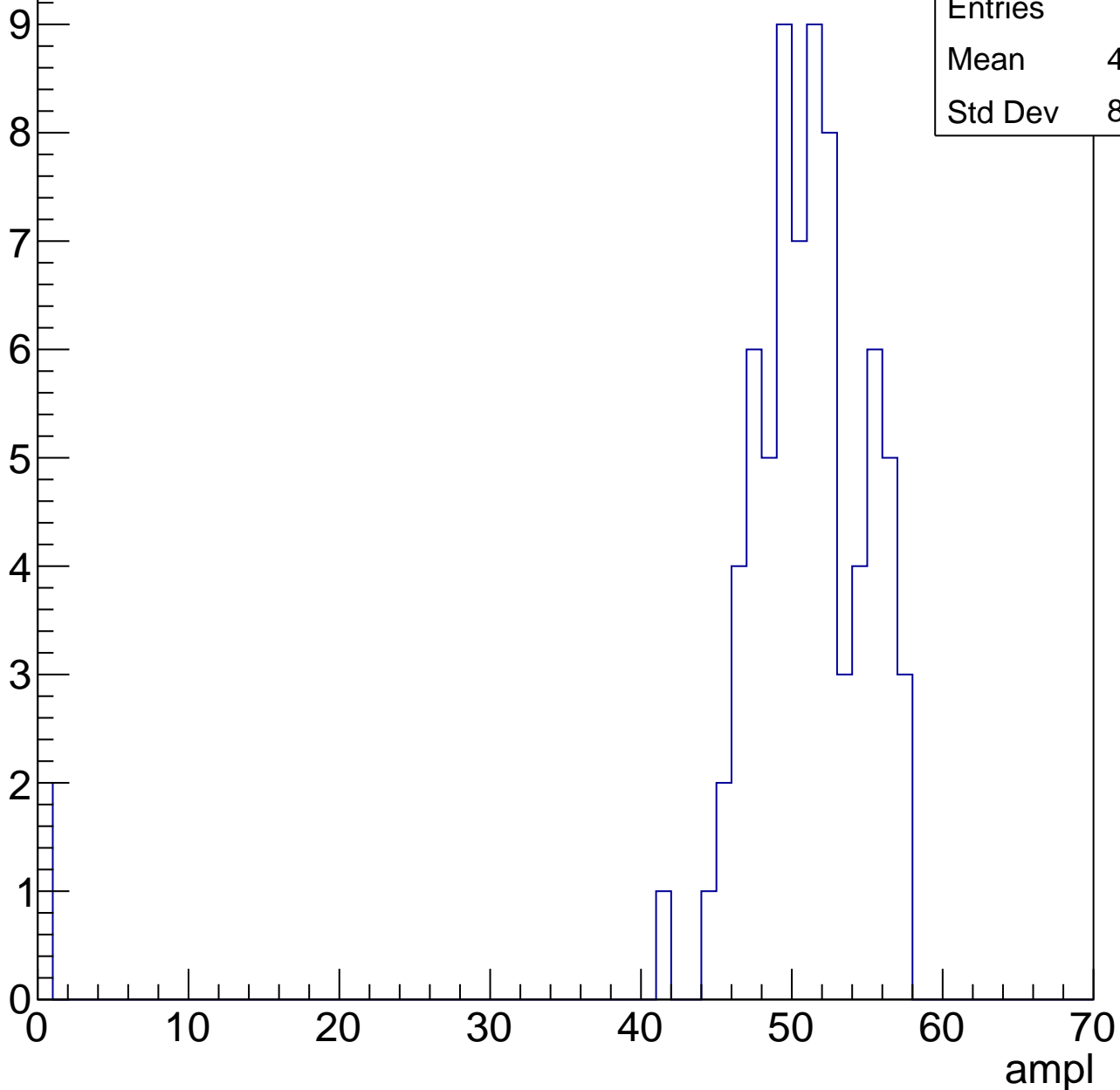


B1L103S, U18-ch15, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	49.37
Std Dev	8.865

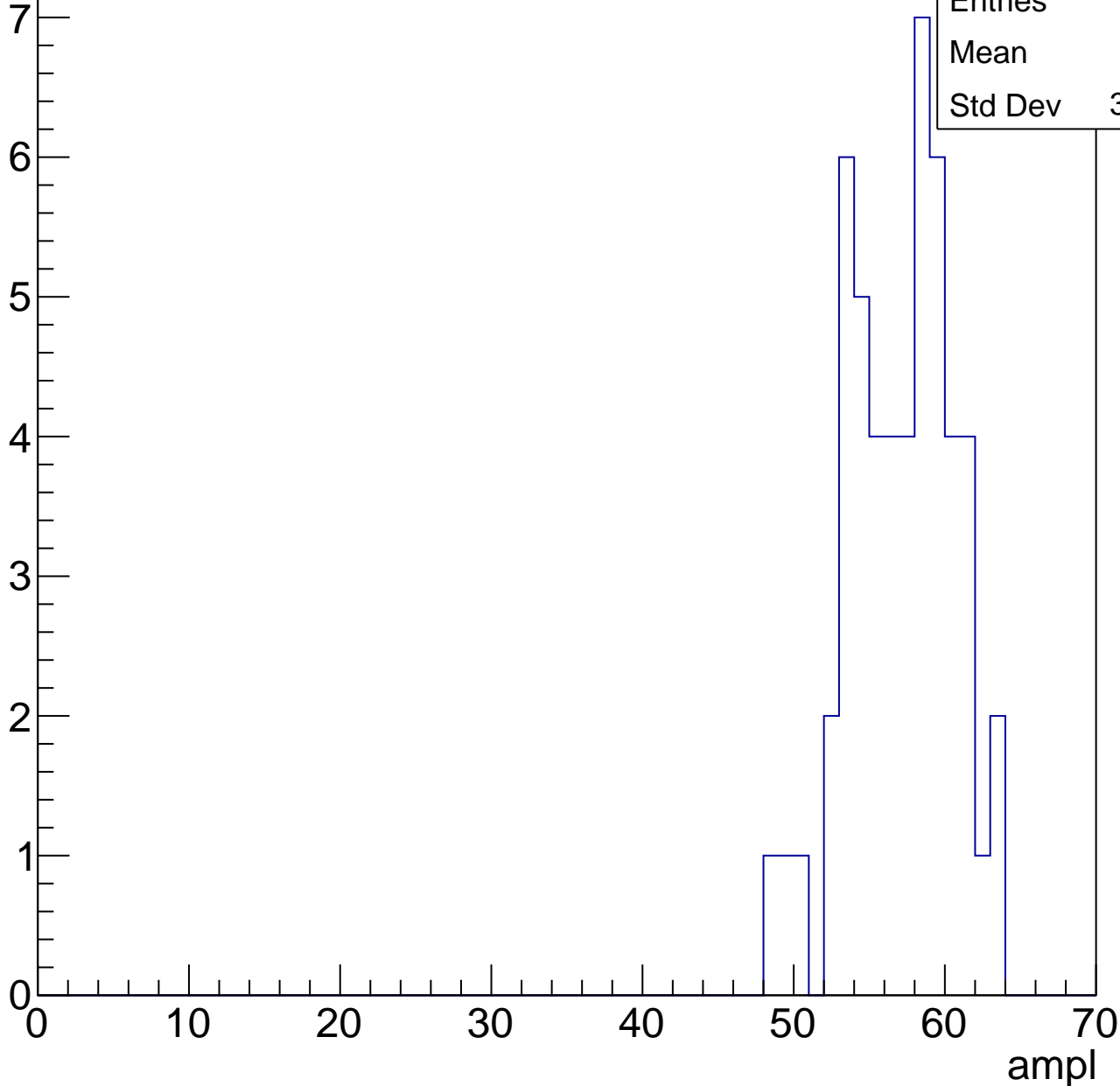


B1L103S, U18-ch15, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	56.6
Std Dev	3.466

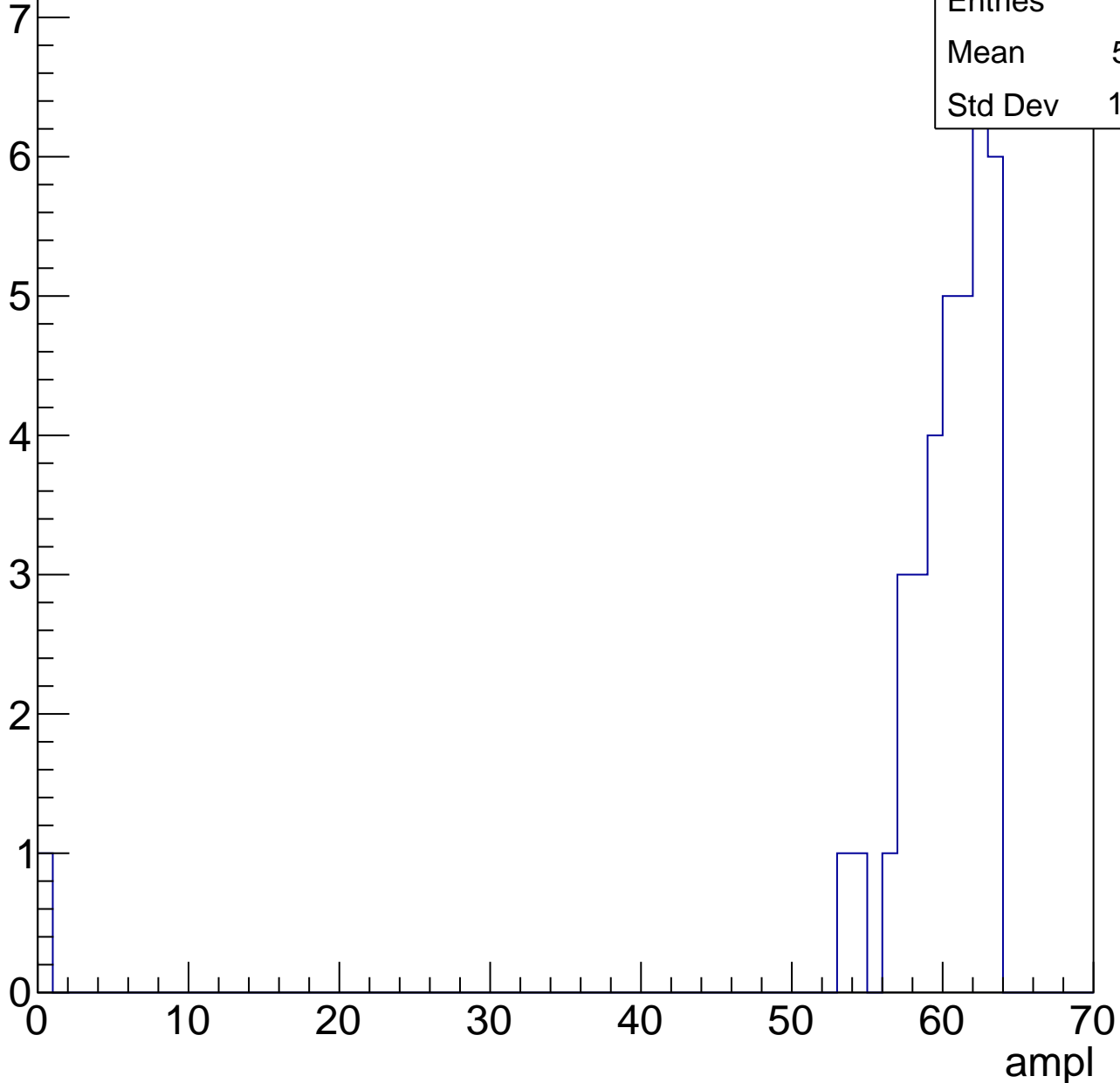


B1L103S, U18-ch15, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	58.41
Std Dev	10.05



B1L103S, U18-ch15, adc6

calib_packv5_041523_1651.root, FC#0, port C2

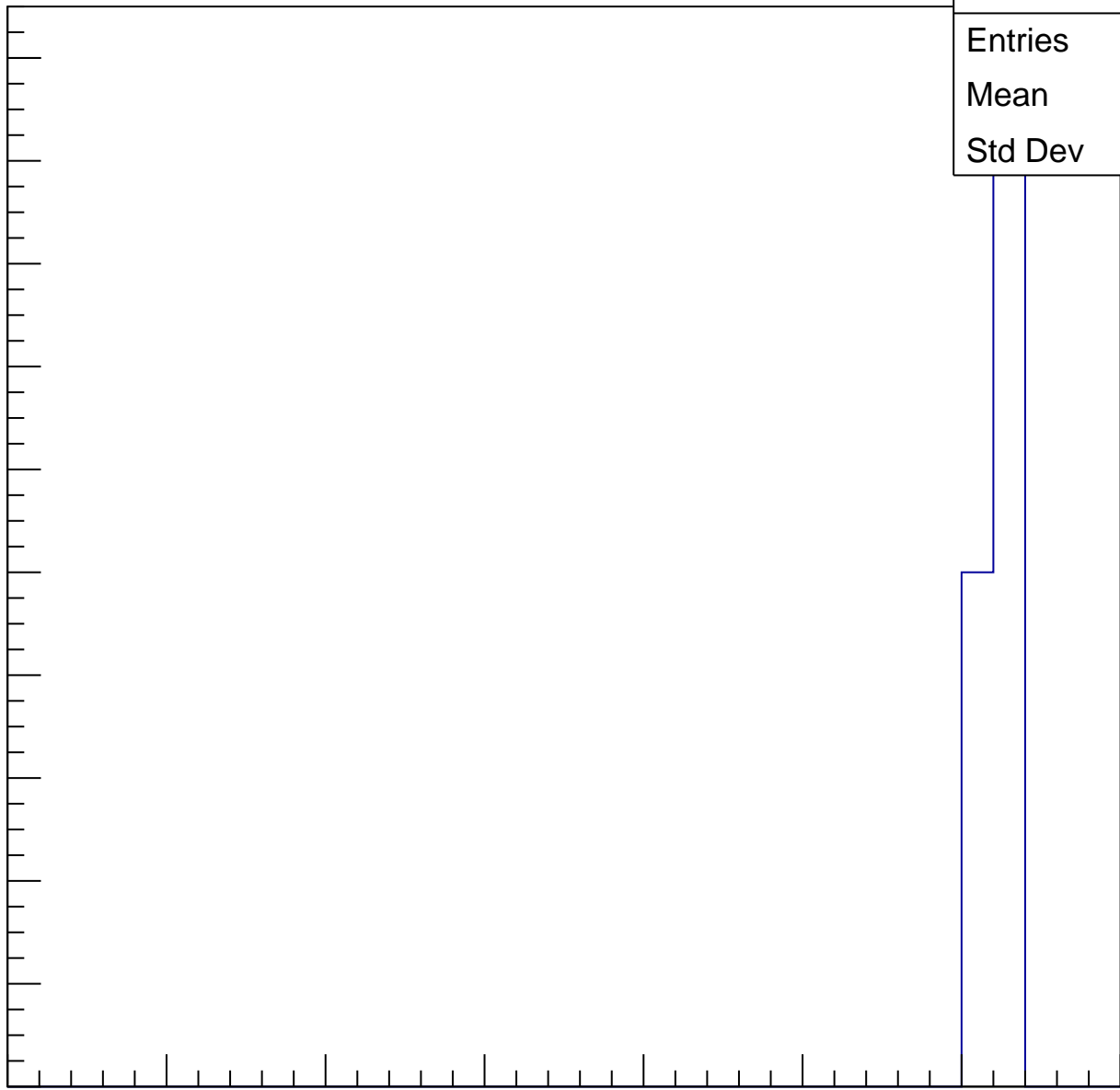
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch15, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch16, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	22.48
Std Dev	11.2

Gaus mean : 28.7449

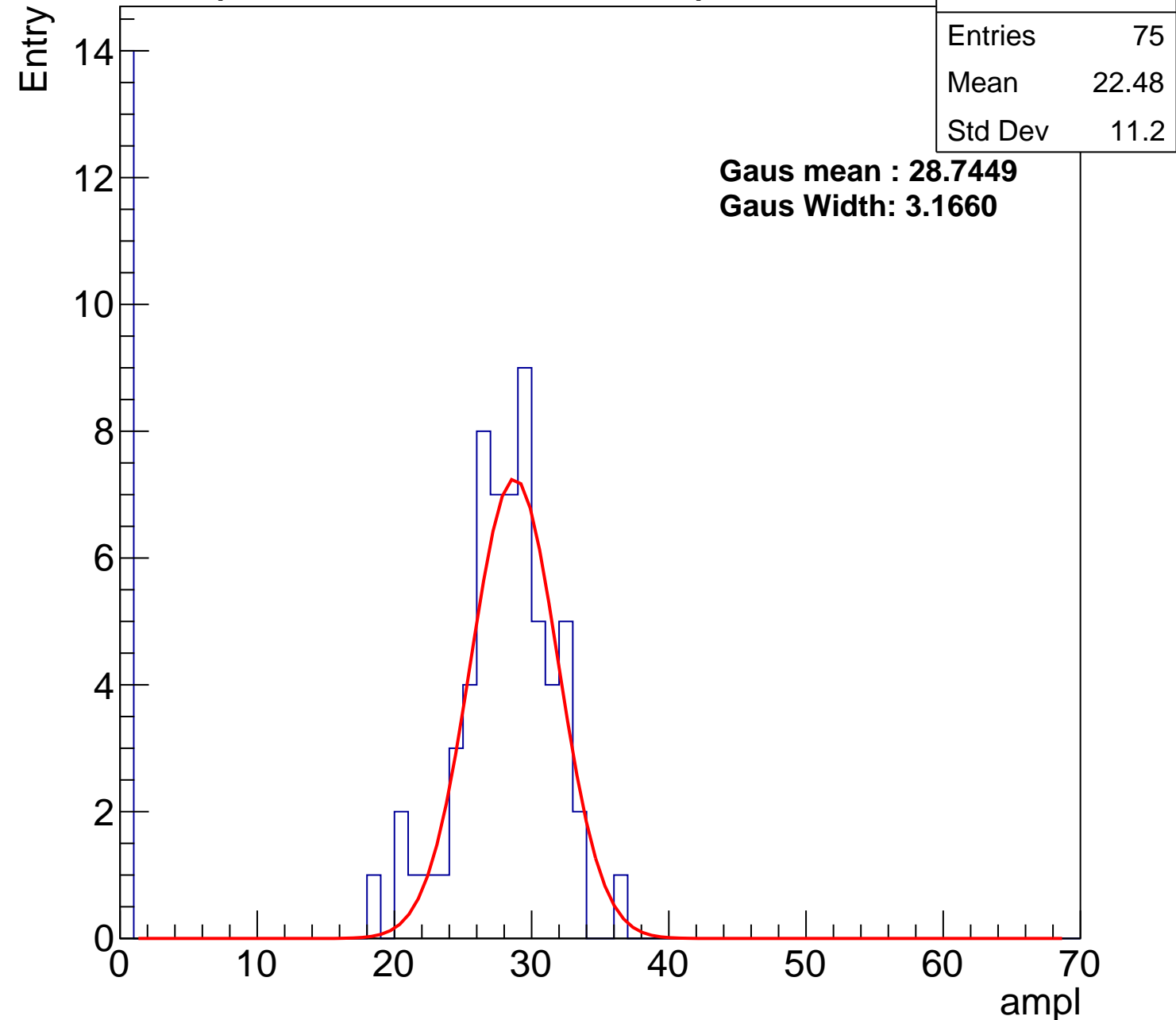
Gaus Width: 3.1660

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch16, adc1

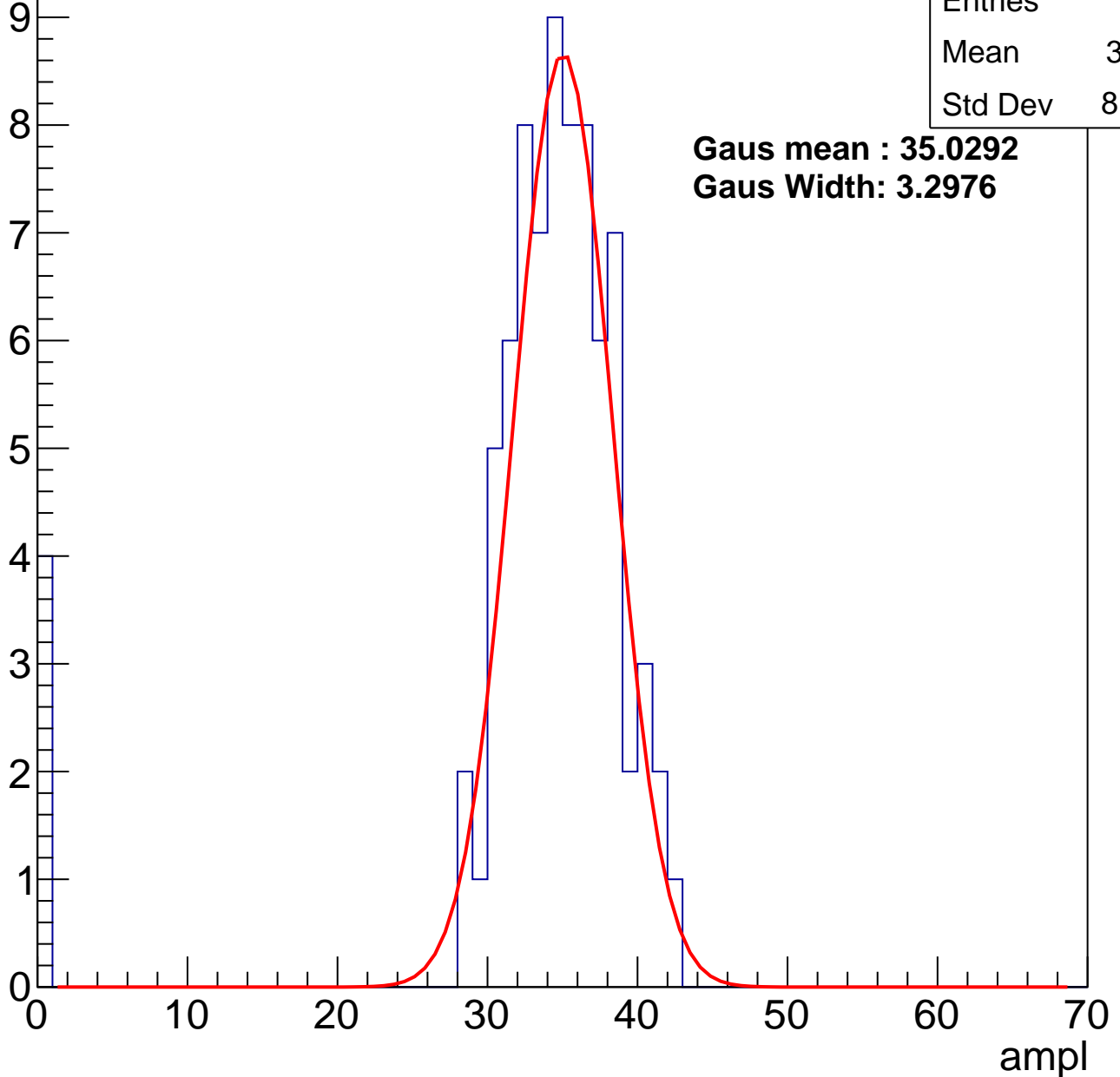
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	32.81
Std Dev	8.197

Gaus mean : 35.0292

Gaus Width: 3.2976



B1L103S, U18-ch16, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	35.8
Std Dev	14.43

Gaus mean : 42.1996

Gaus Width: 3.5218

Entry

10

8

6

4

2

0

0

10

20

30

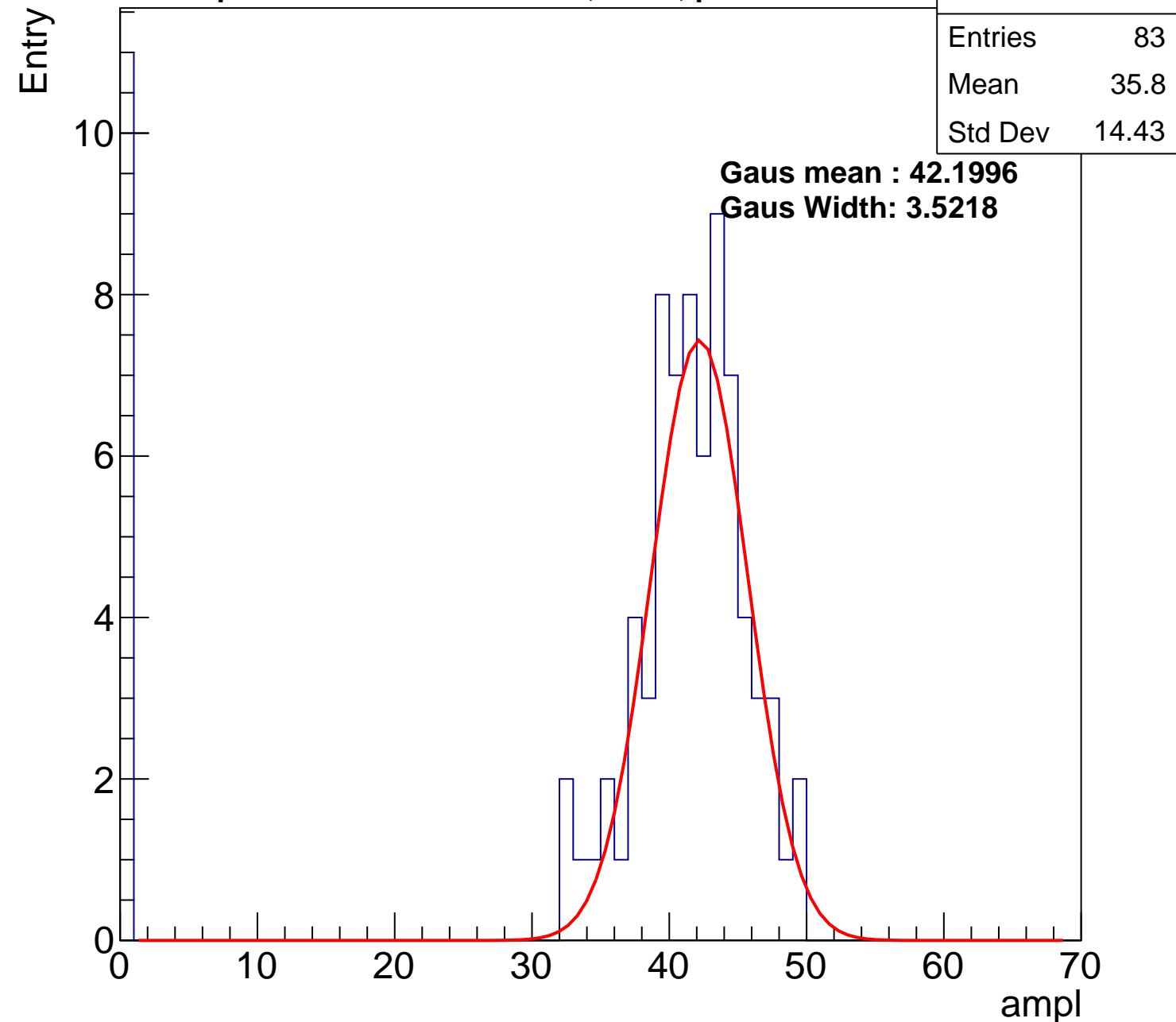
40

50

60

70

ampl

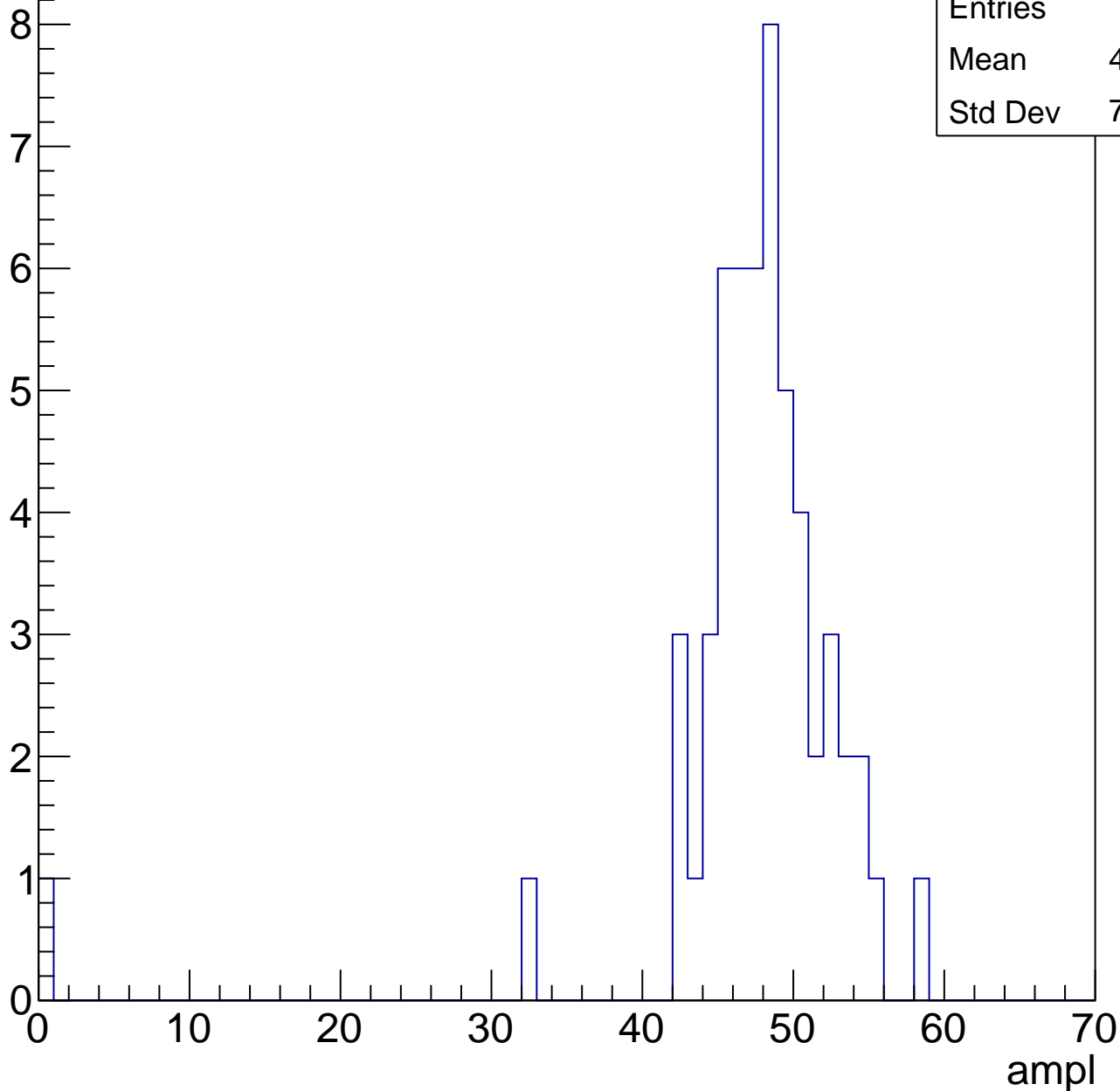


B1L103S, U18-ch16, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	46.82
Std Dev	7.515

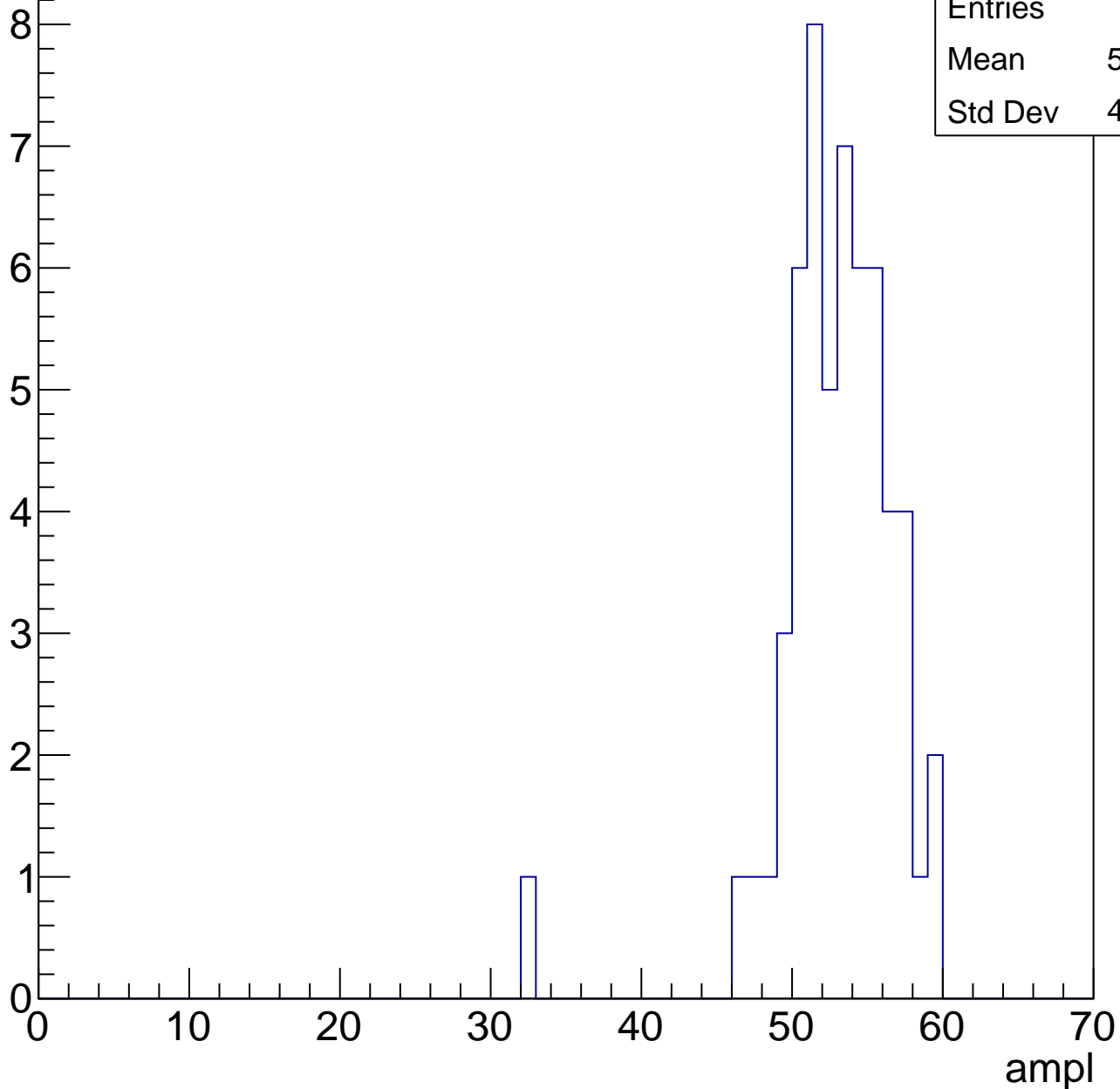


B1L103S, U18-ch16, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.52
Std Dev	4.018

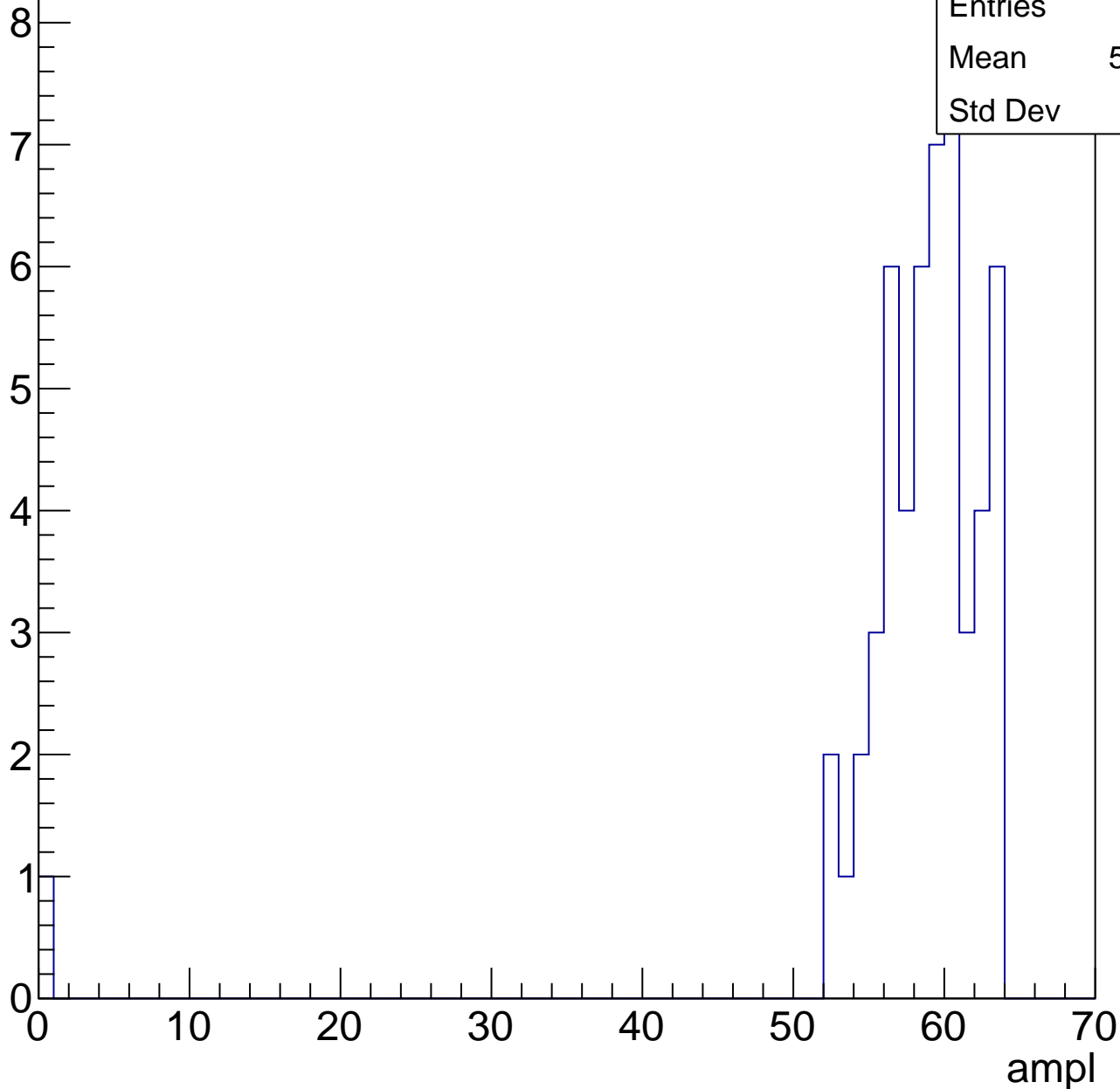


B1L103S, U18-ch16, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.43
Std Dev	8.48

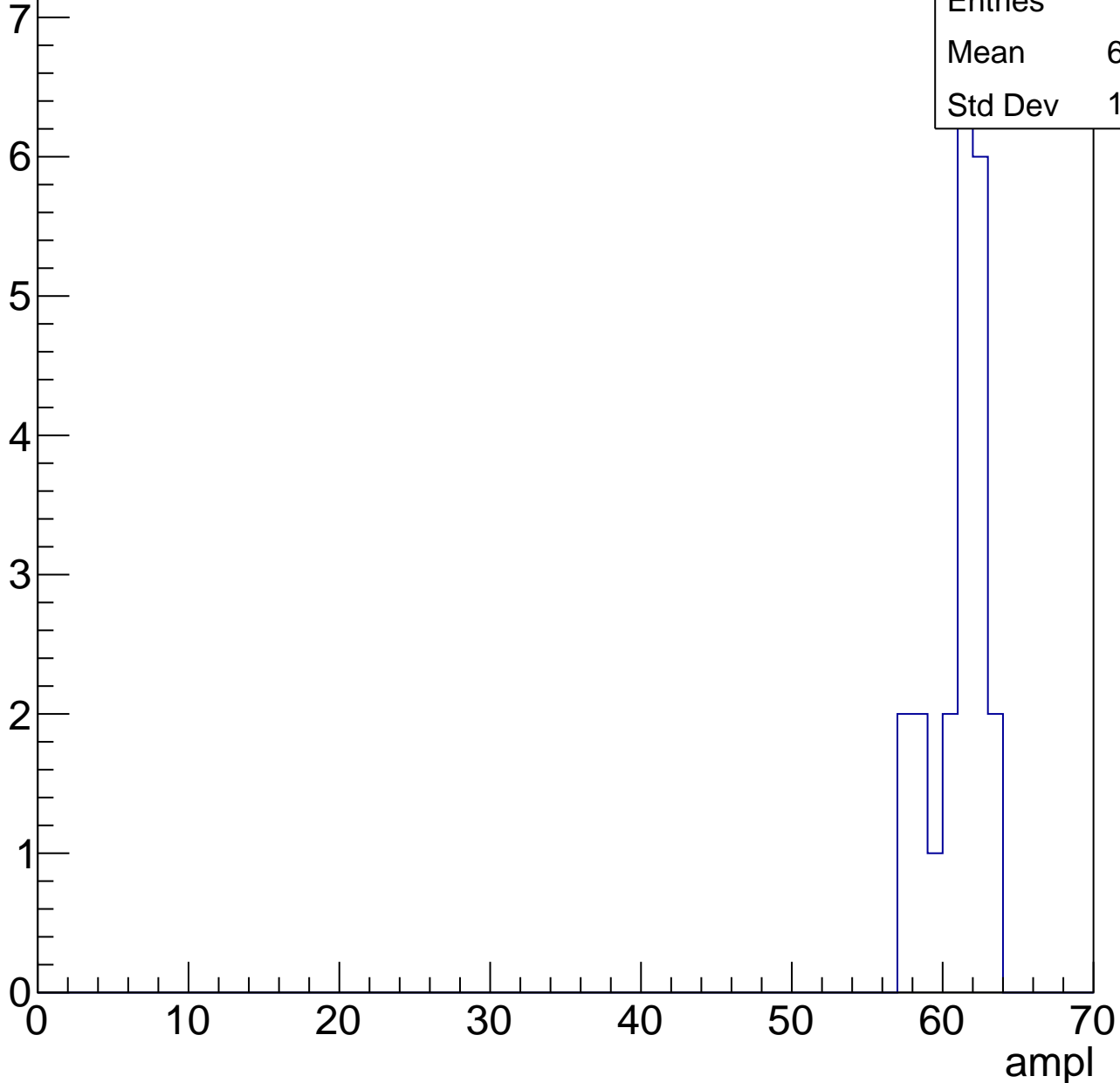


B1L103S, U18-ch16, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	60.64
Std Dev	1.746



B1L103S, U18-ch16, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U18-ch17, adc0

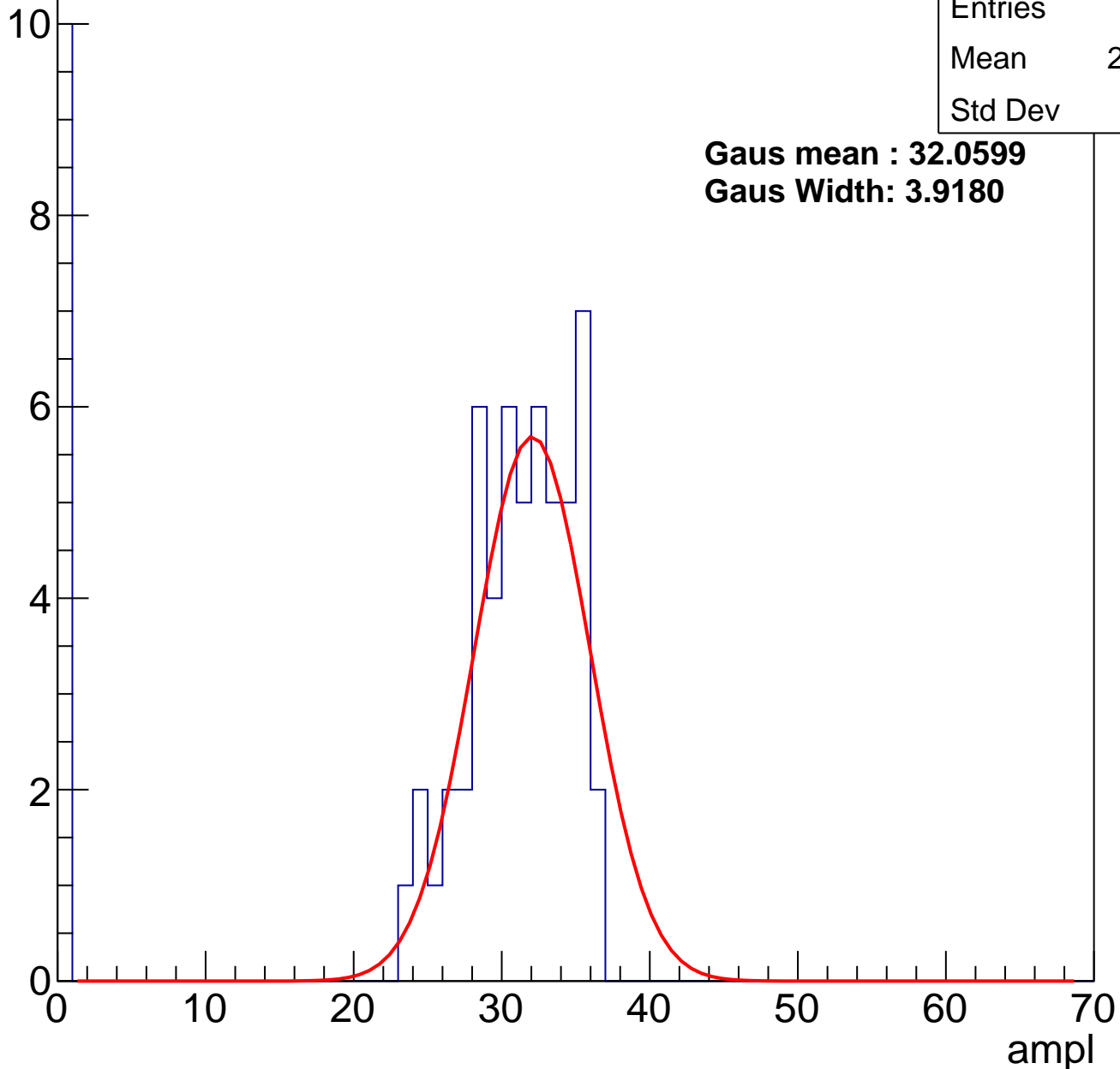
calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	26.02
Std Dev	11.6

Gaus mean : 32.0599

Gaus Width: 3.9180

Entry



B1L103S, U18-ch17, adc1

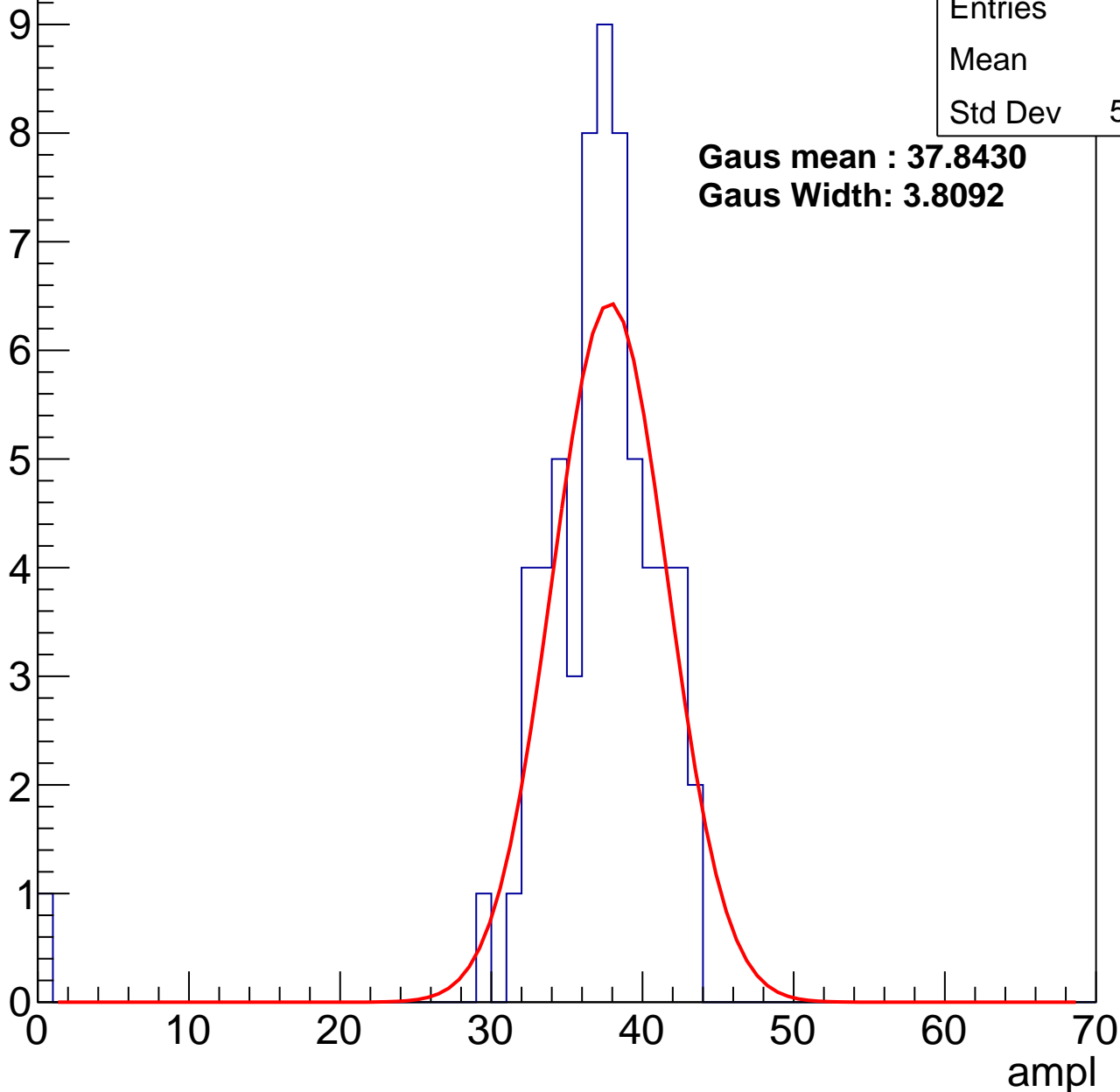
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.4
Std Dev	5.599

Gaus mean : 37.8430

Gaus Width: 3.8092



B1L103S, U18-ch17, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	42.47
Std Dev	9.35

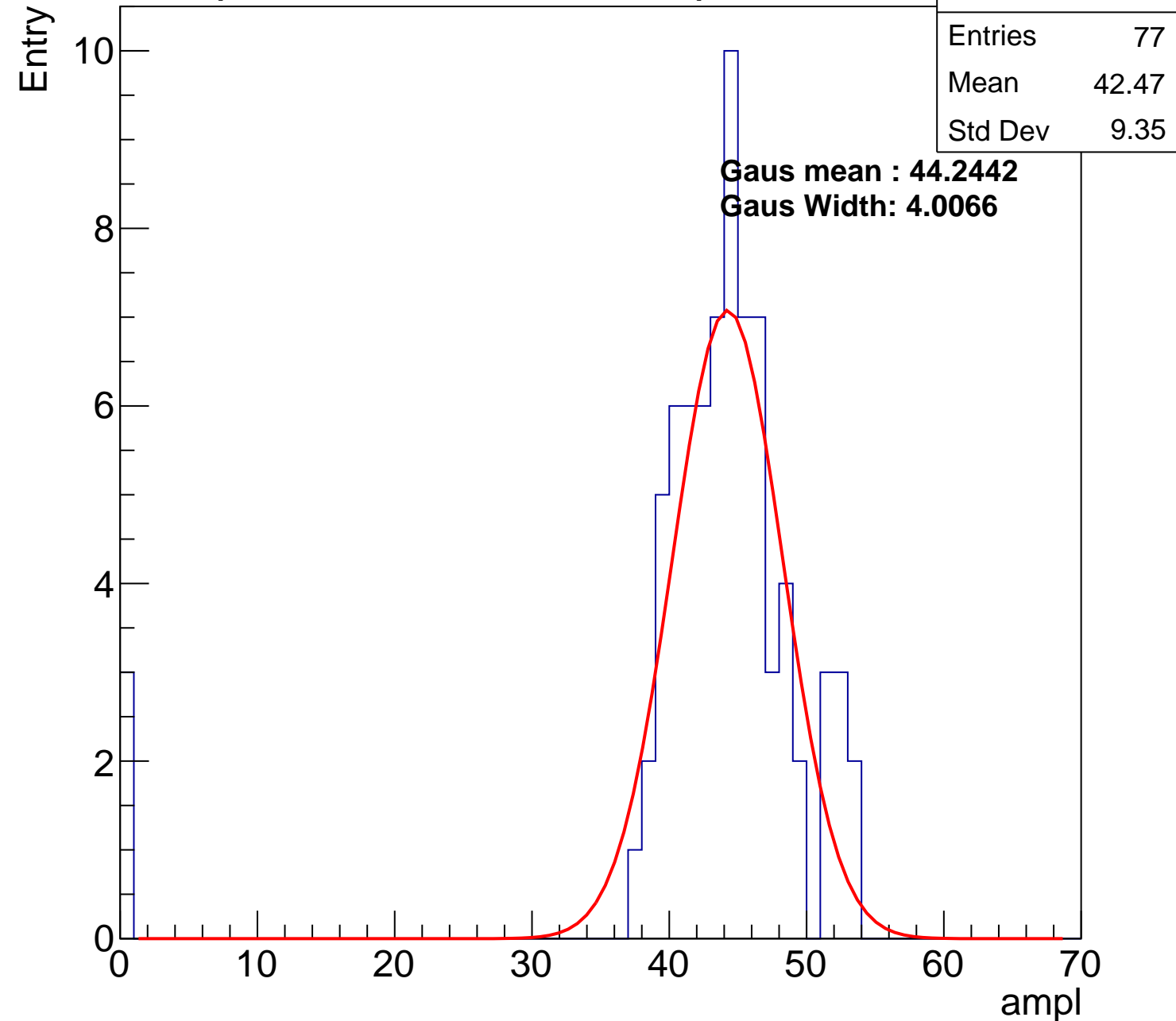
Gaus mean : 44.2442

Gaus Width: 4.0066

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

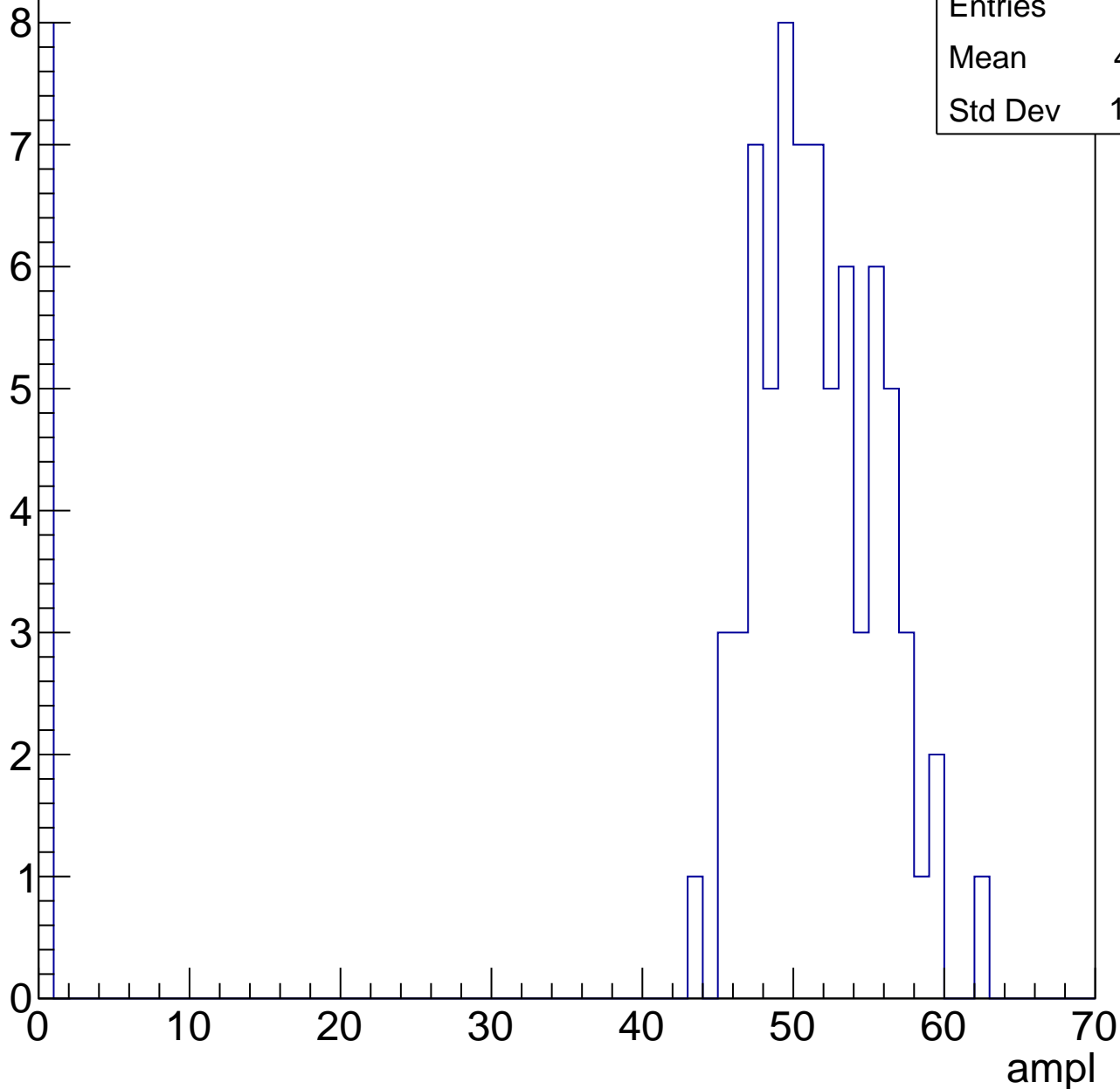


B1L103S, U18-ch17, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	46.21
Std Dev	15.74

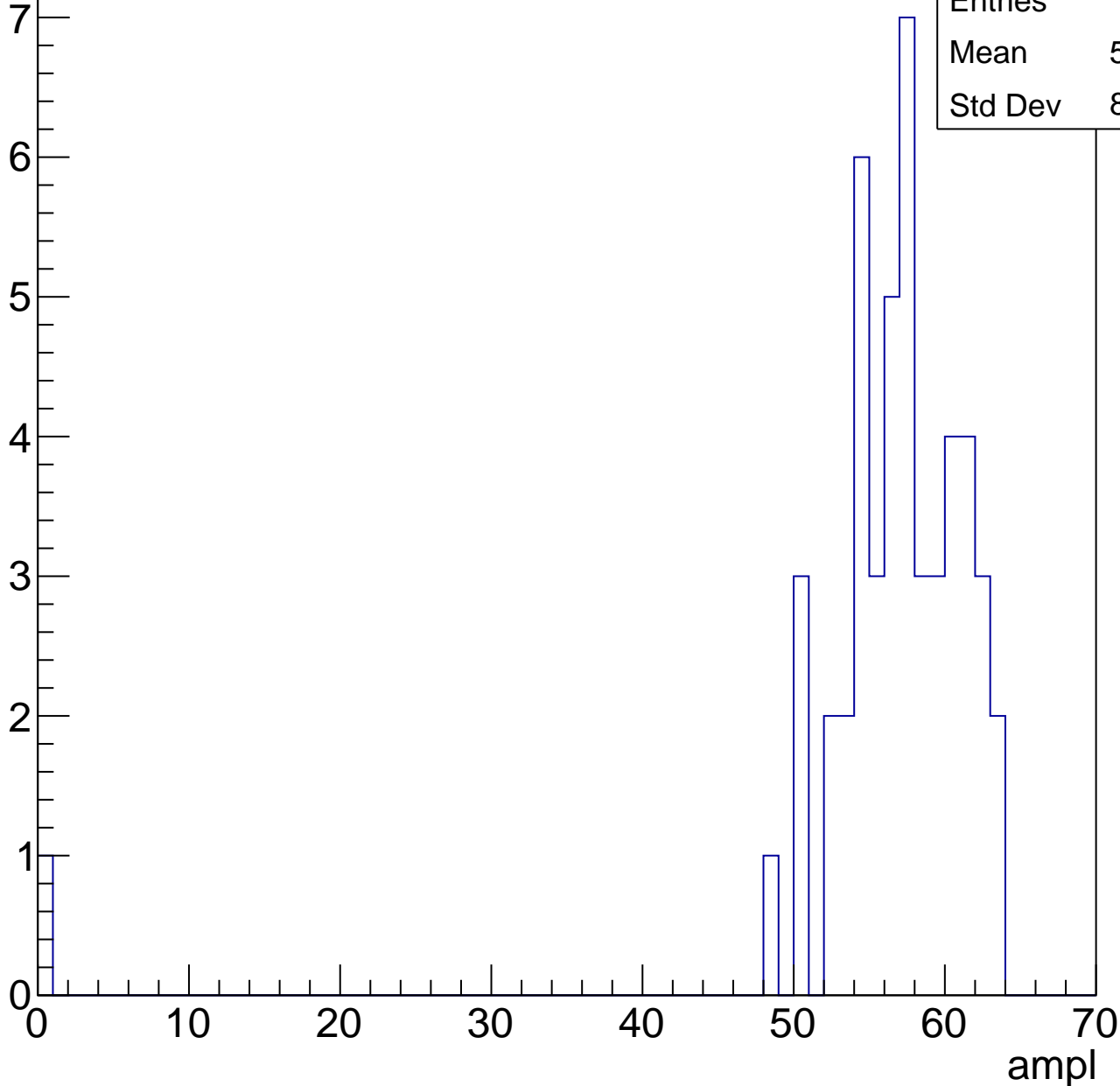


B1L103S, U18-ch17, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.57
Std Dev	8.797

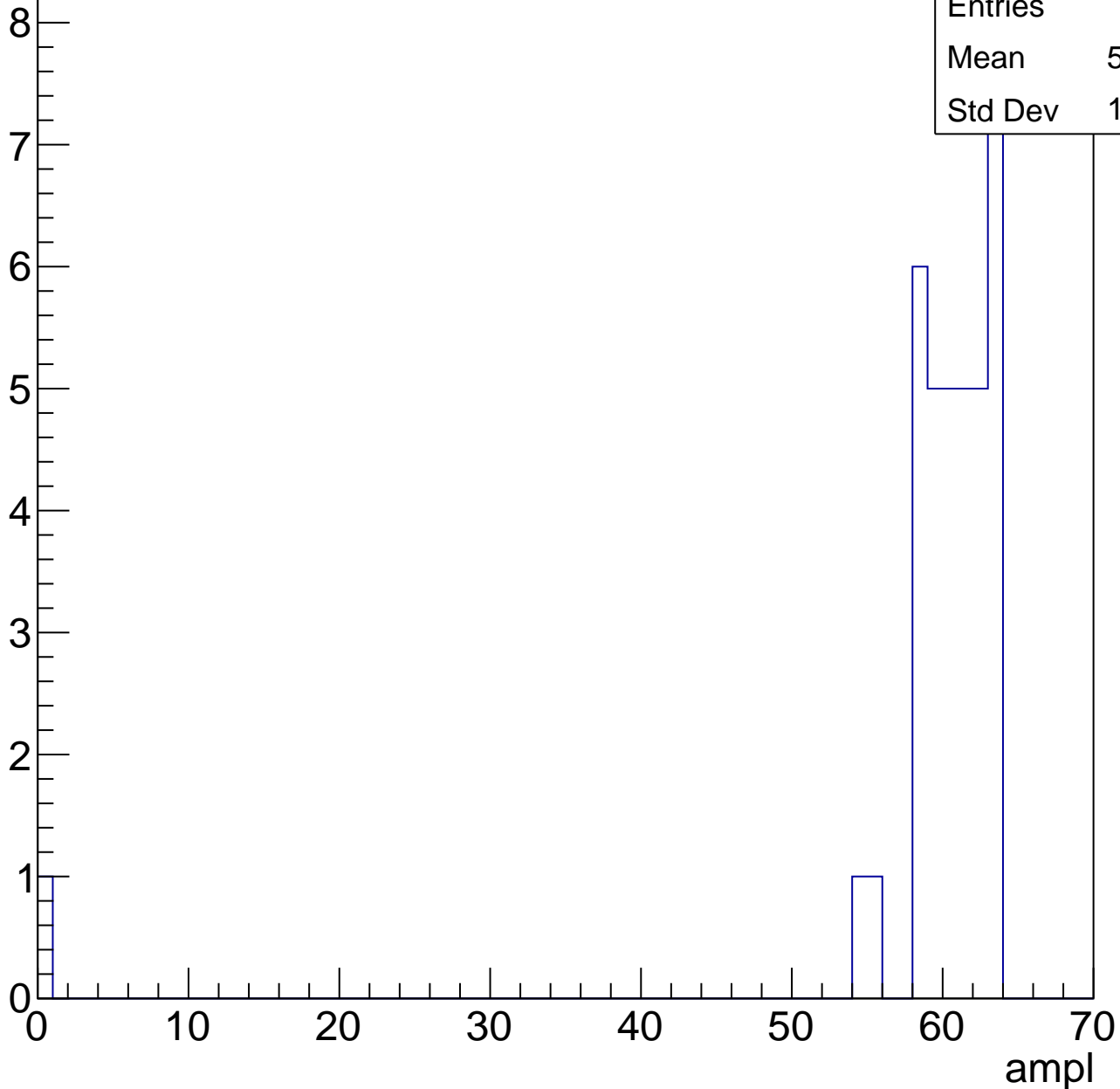


B1L103S, U18-ch17, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	58.68
Std Dev	10.03



B1L103S, U18-ch17, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

61

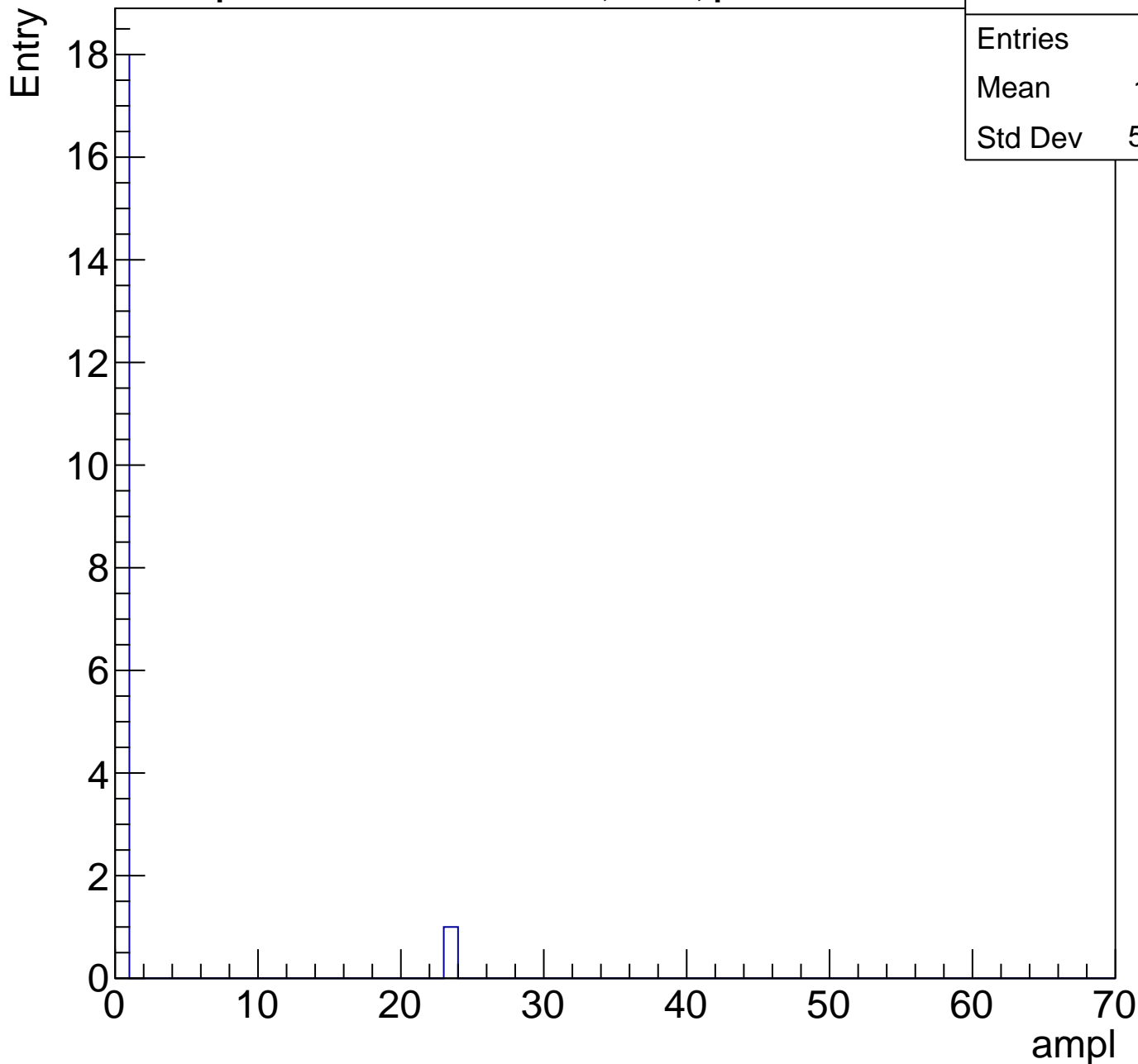
Std Dev

0.8165

B1L103S, U18-ch17, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136



B1L103S, U18-ch18, adc0

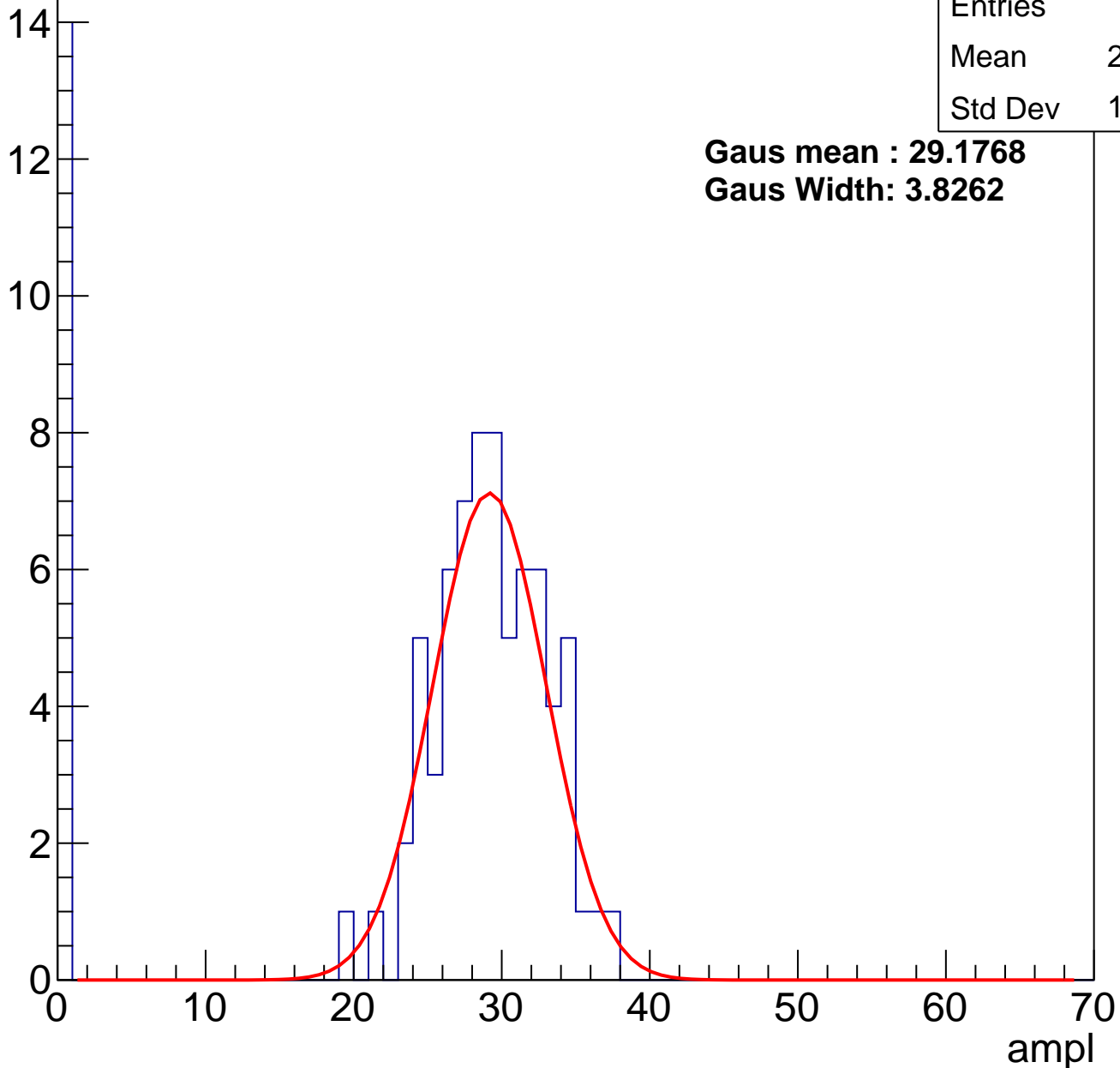
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	24.05
Std Dev	11.25

Gaus mean : 29.1768

Gaus Width: 3.8262

Entry



B1L103S, U18-ch18, adc1

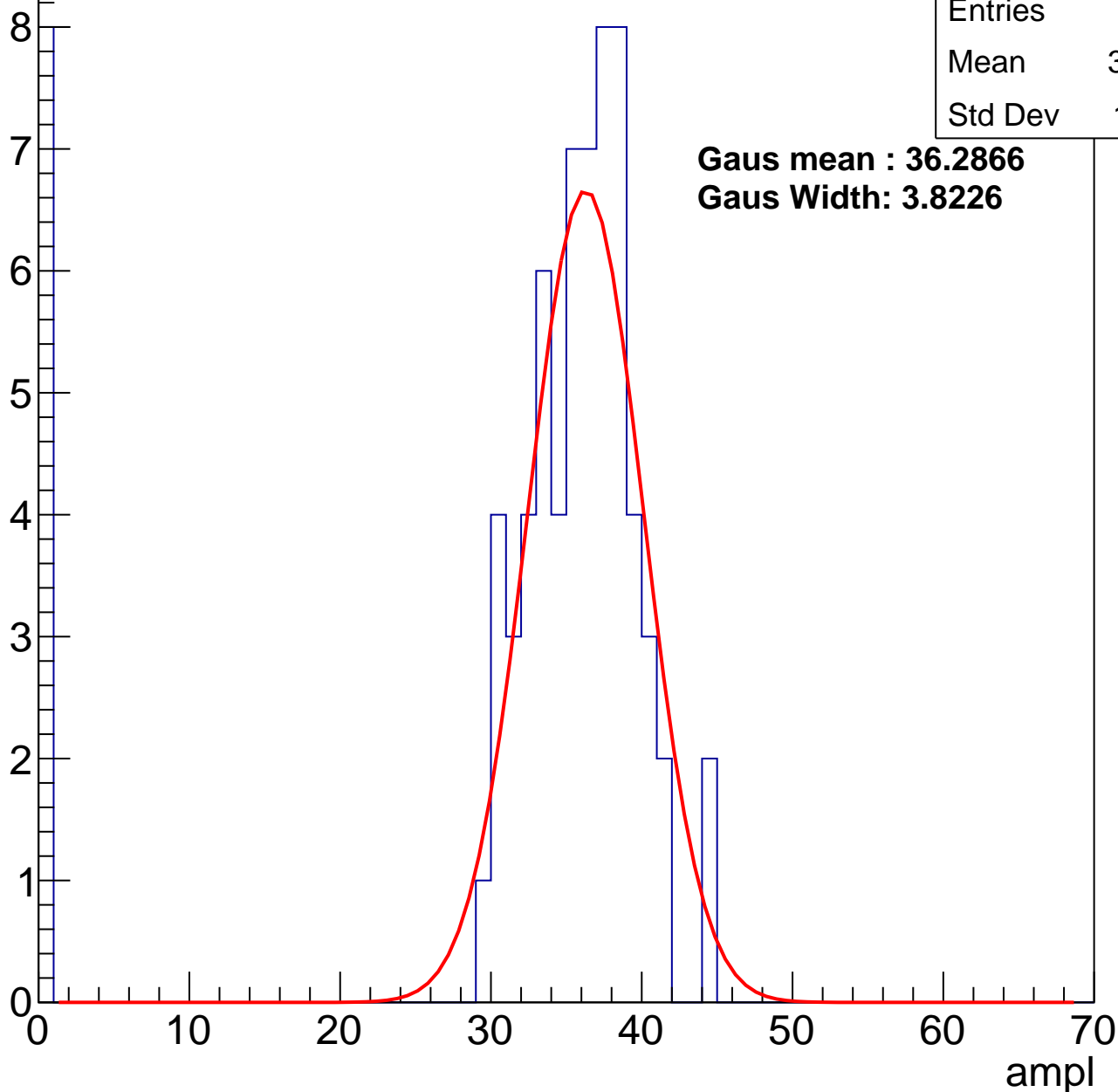
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	31.65
Std Dev	11.71

Gaus mean : 36.2866

Gaus Width: 3.8226



B1L103S, U18-ch18, adc2

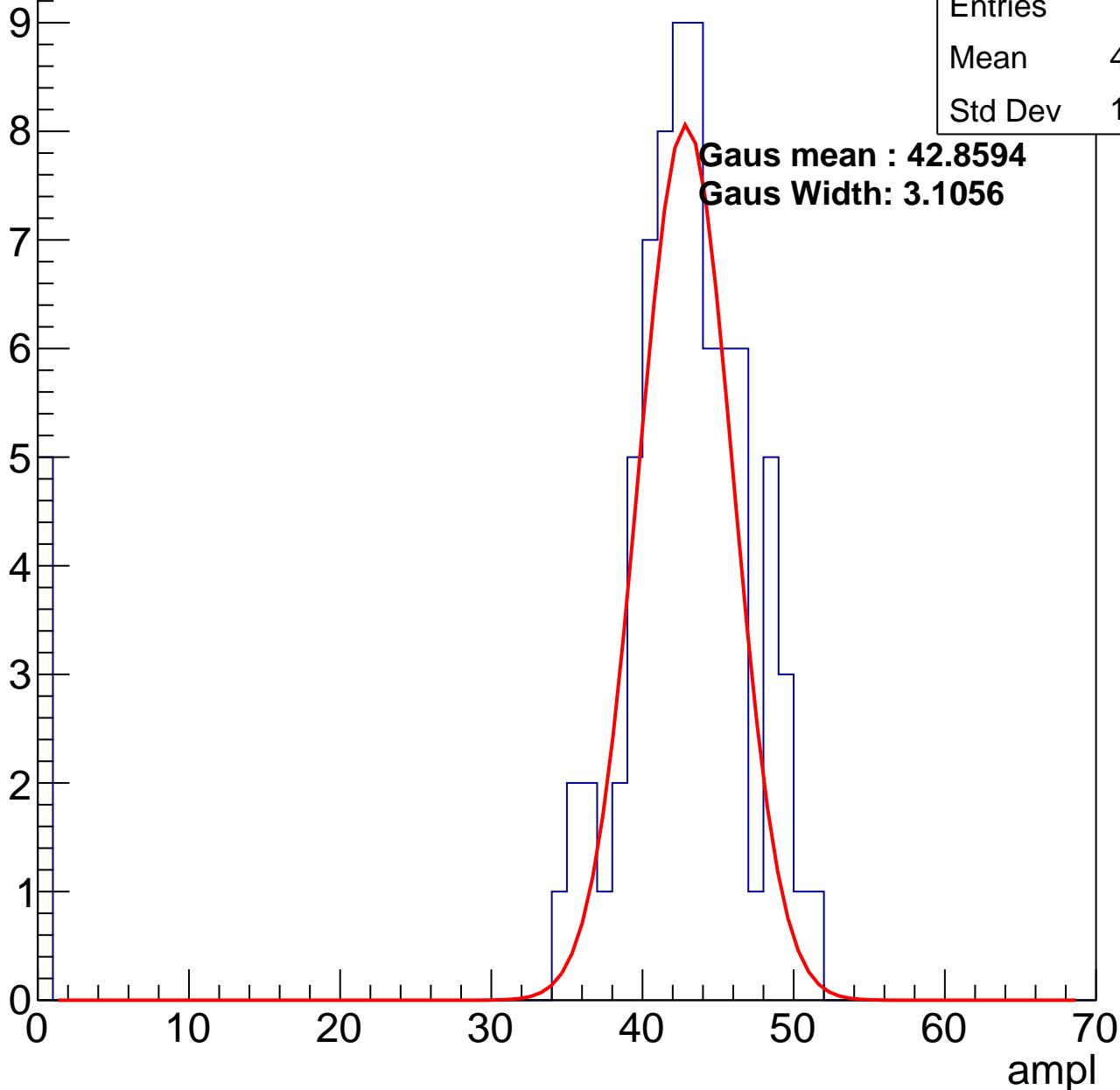
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	40.02
Std Dev	10.93

Gaus mean : 42.8594

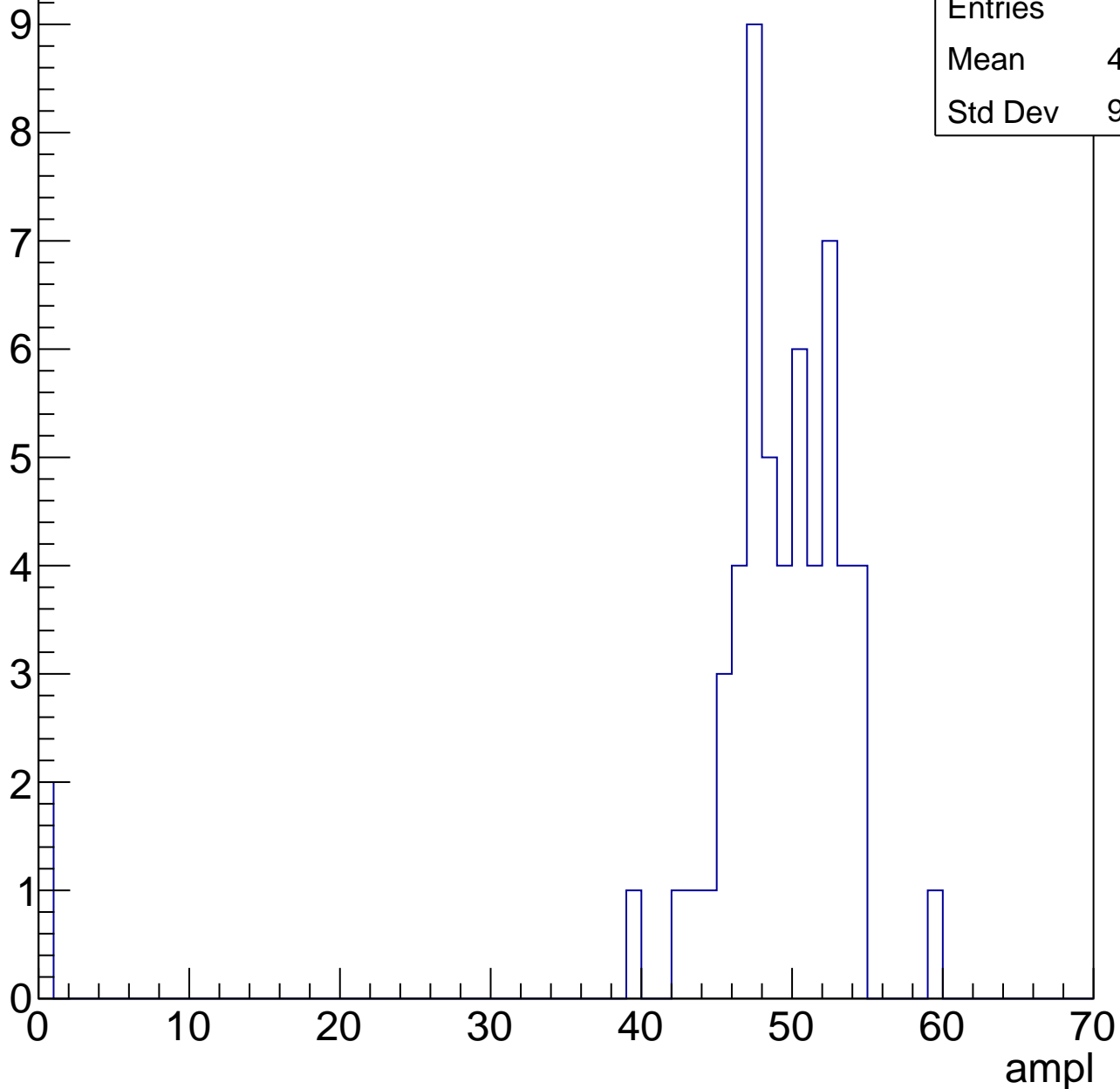
Gaus Width: 3.1056



B1L103S, U18-ch18, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

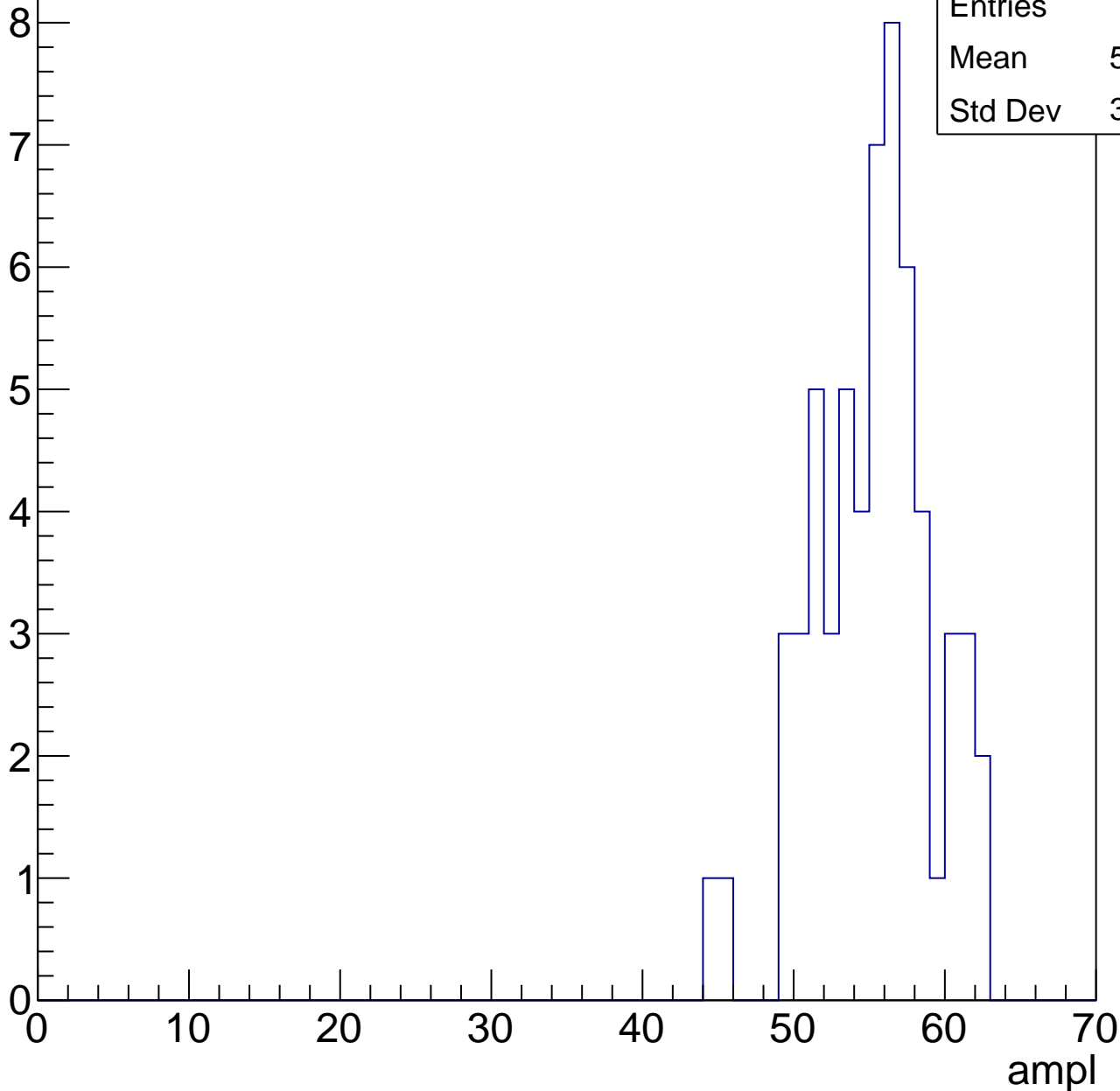


B1L103S, U18-ch18, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

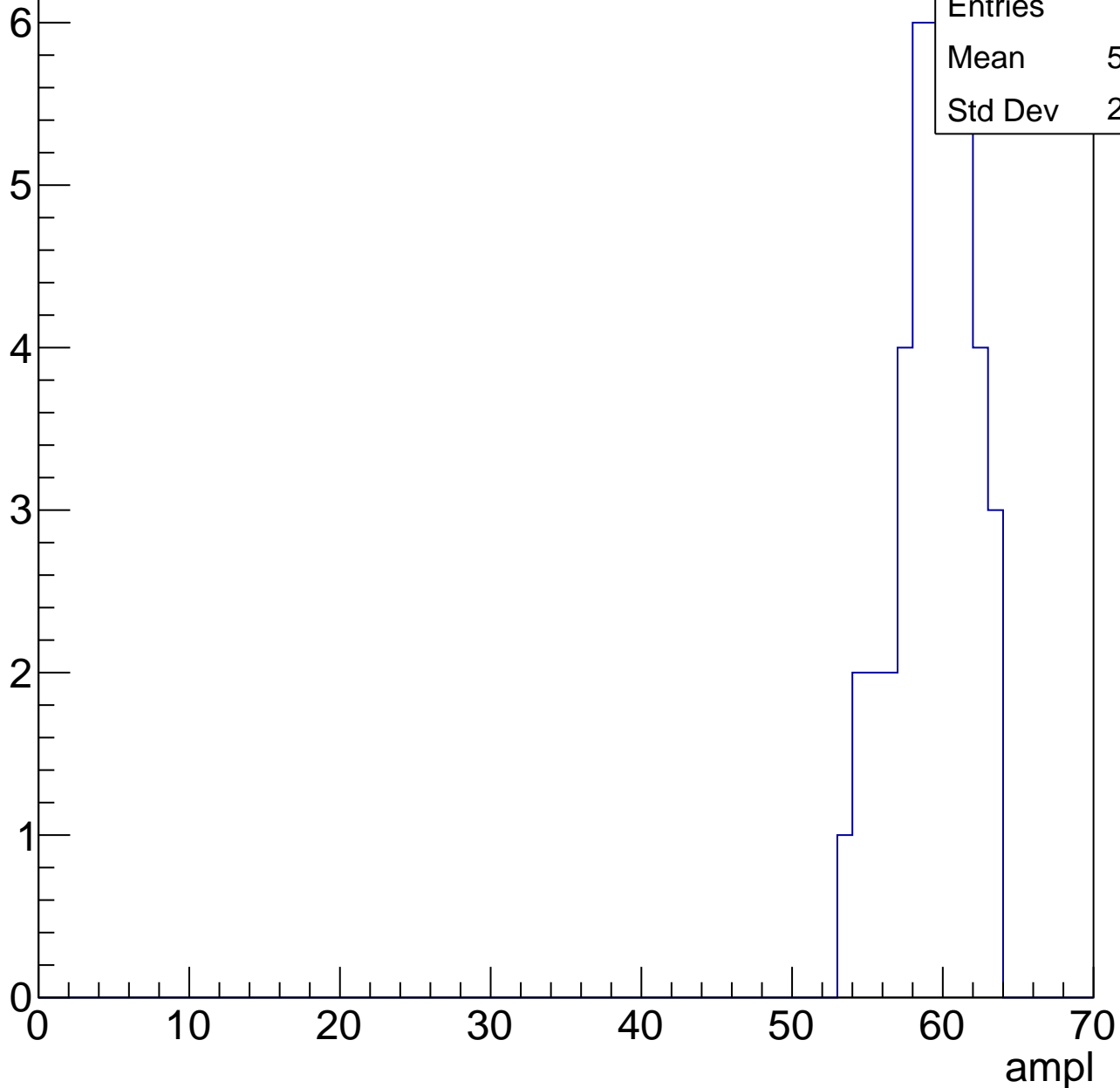
Entries	59
Mean	54.76
Std Dev	3.898



B1L103S, U18-ch18, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

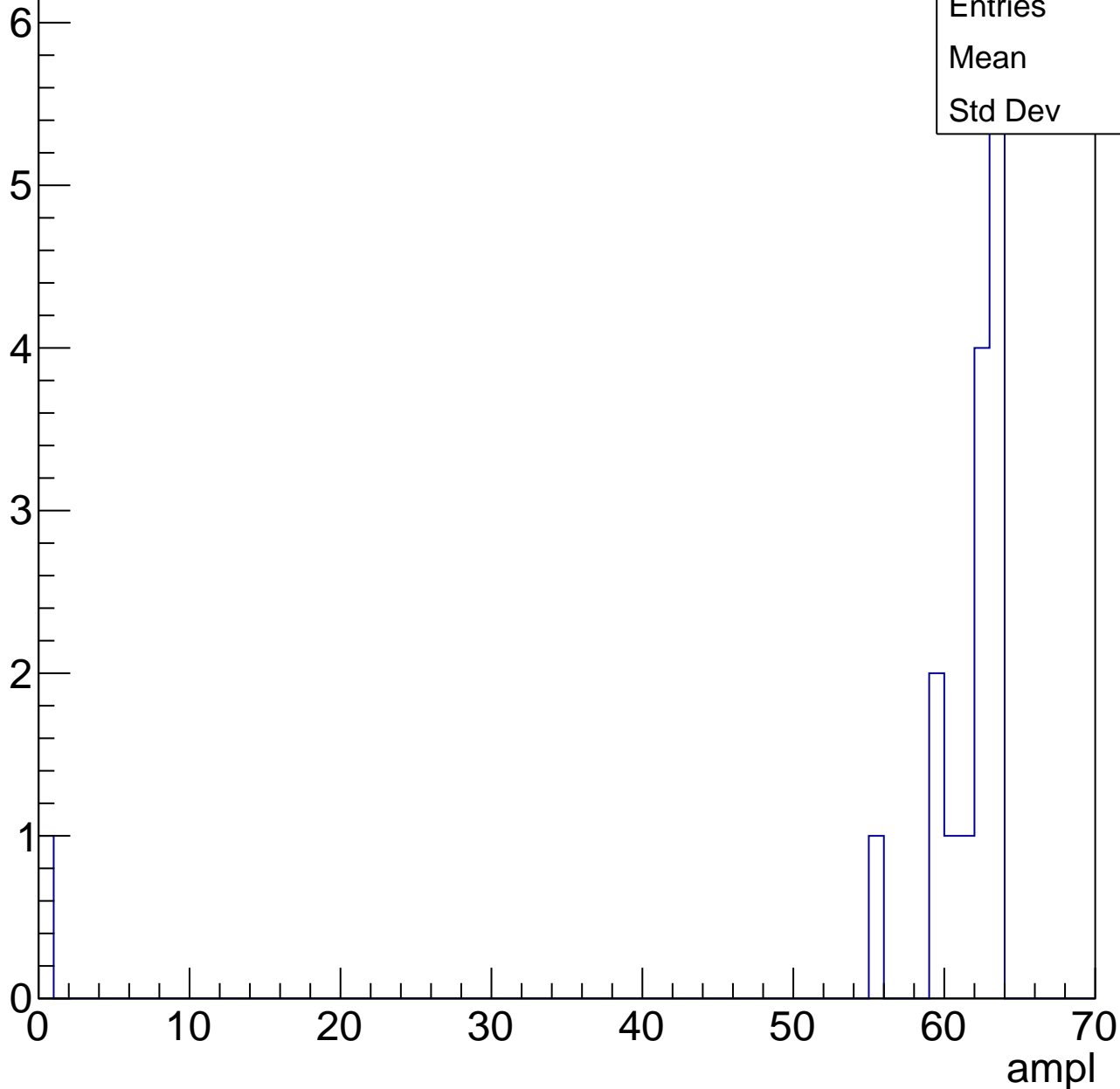


B1L103S, U18-ch18, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.5
Std Dev	15



B1L103S, U18-ch18, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U18-ch19, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	96
Mean	25.81
Std Dev	12.5

Gaus mean : 32.3016

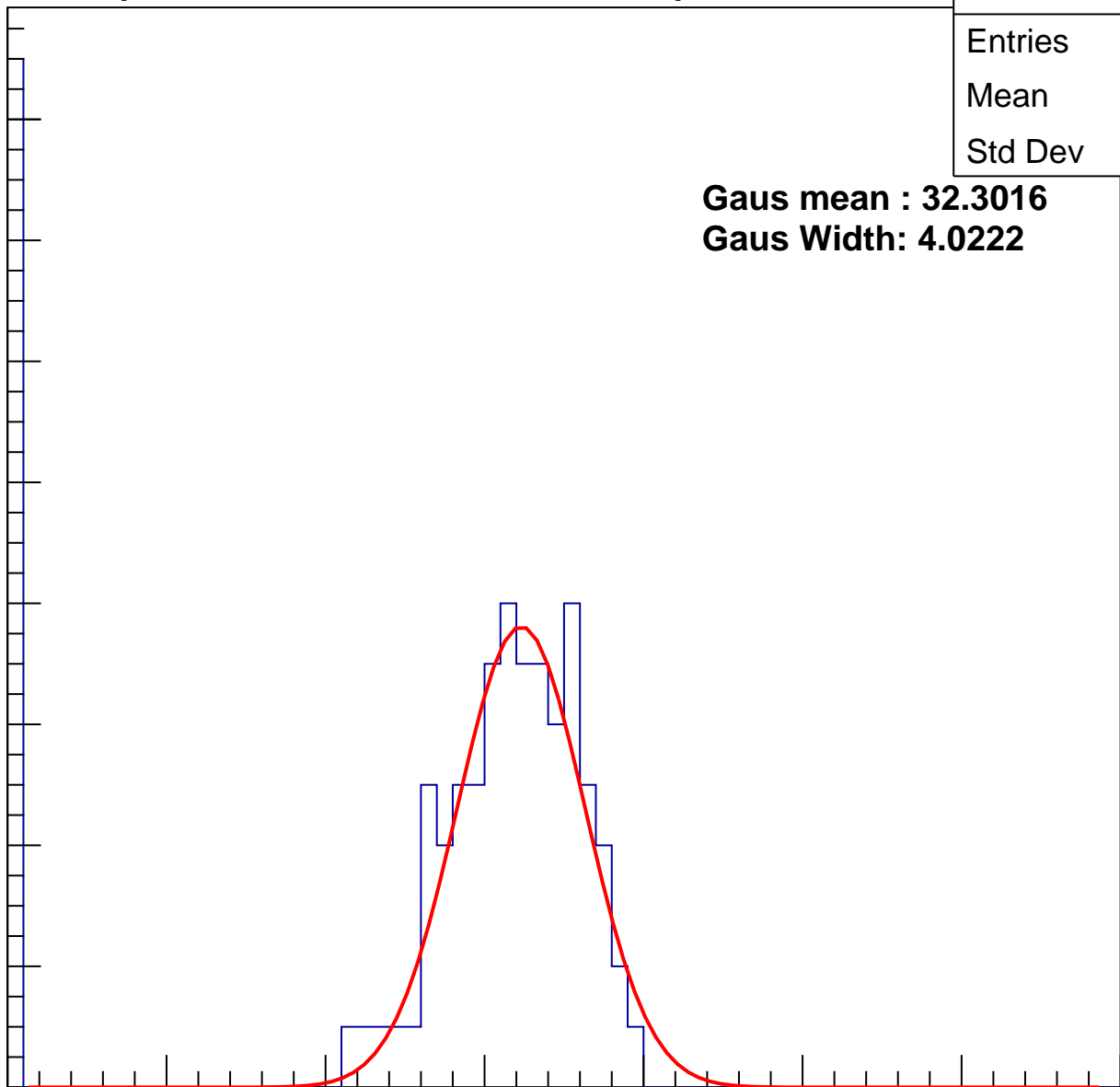
Gaus Width: 4.0222

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch19, adc1

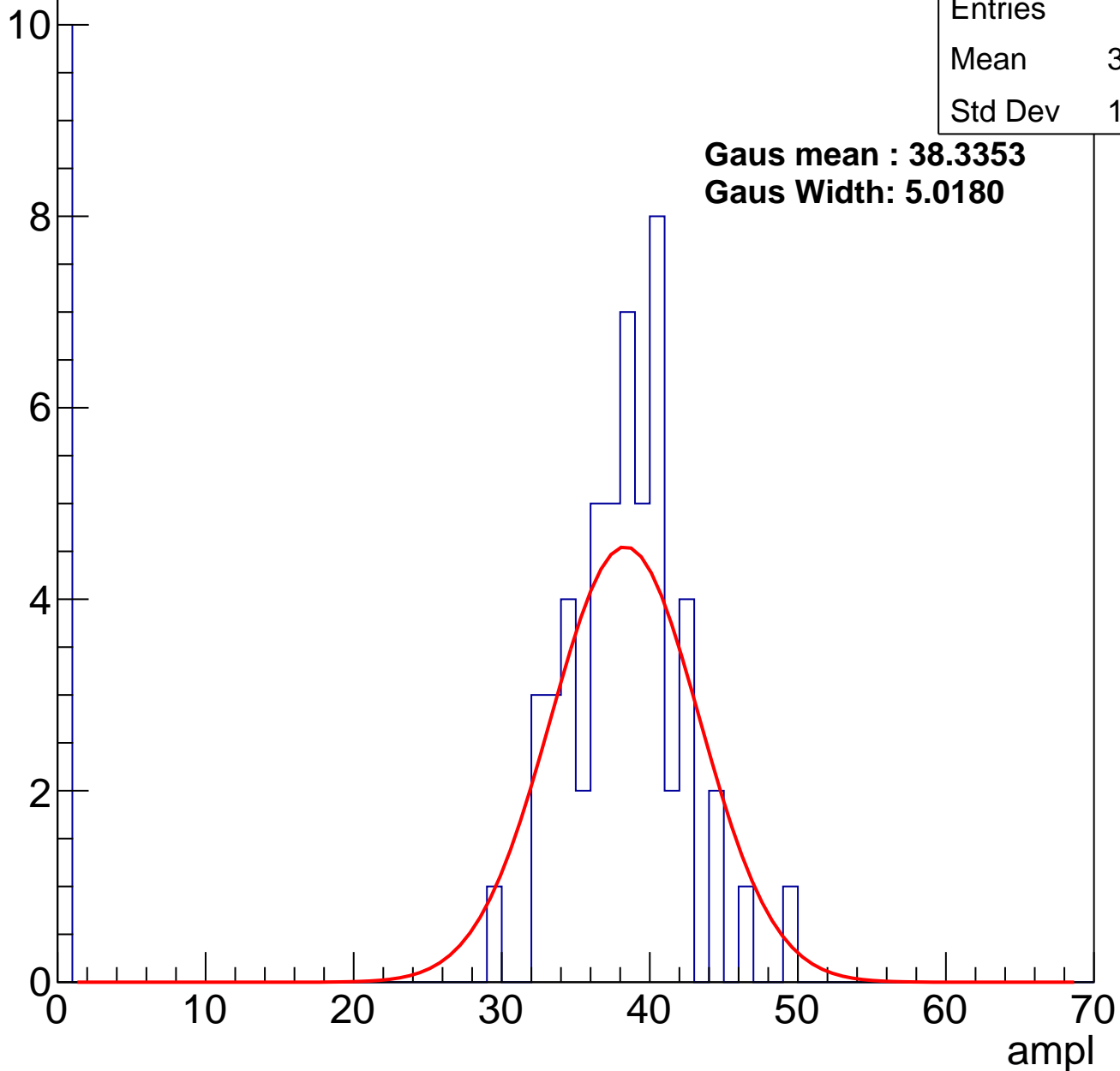
calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	31.89
Std Dev	14.27

Gaus mean : 38.3353

Gaus Width: 5.0180

Entry



B1L103S, U18-ch19, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	41.91
Std Dev	9.569

Gaus mean : 44.6857

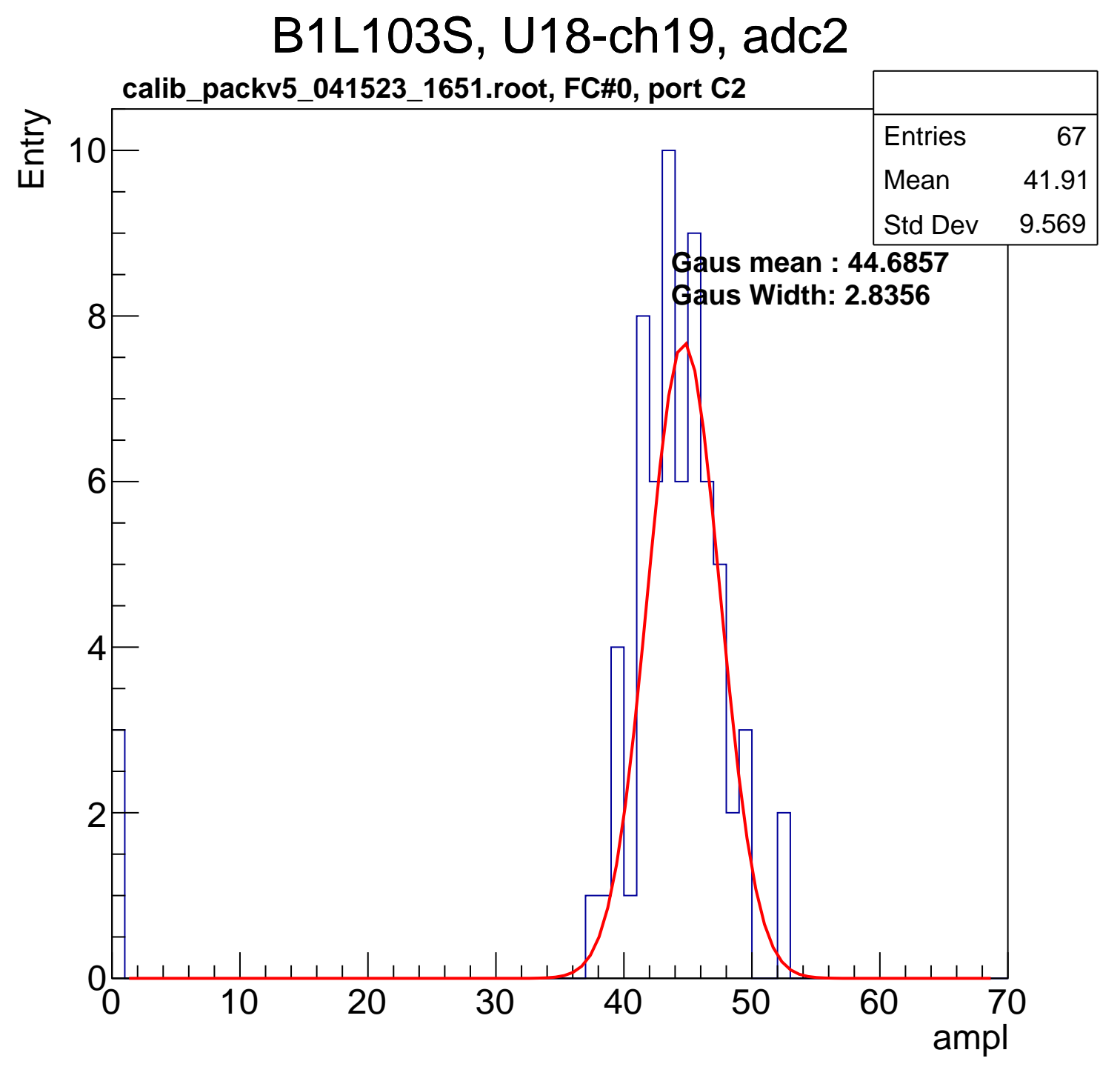
Gaus Width: 2.8356

Entry

10
8
6
4
2
0

ampl

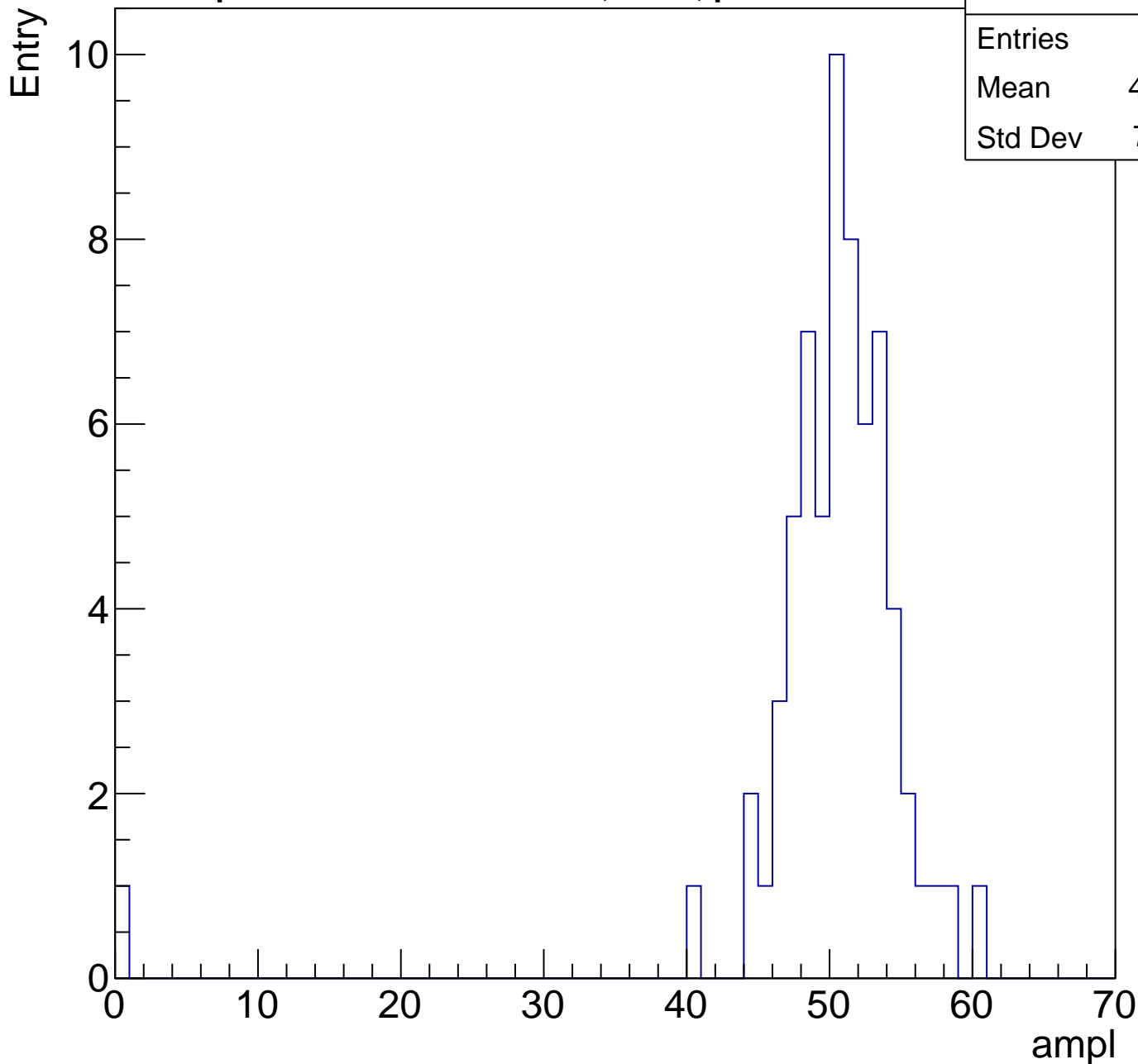
0 10 20 30 40 50 60 70



B1L103S, U18-ch19, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	49.62
Std Dev	7.041

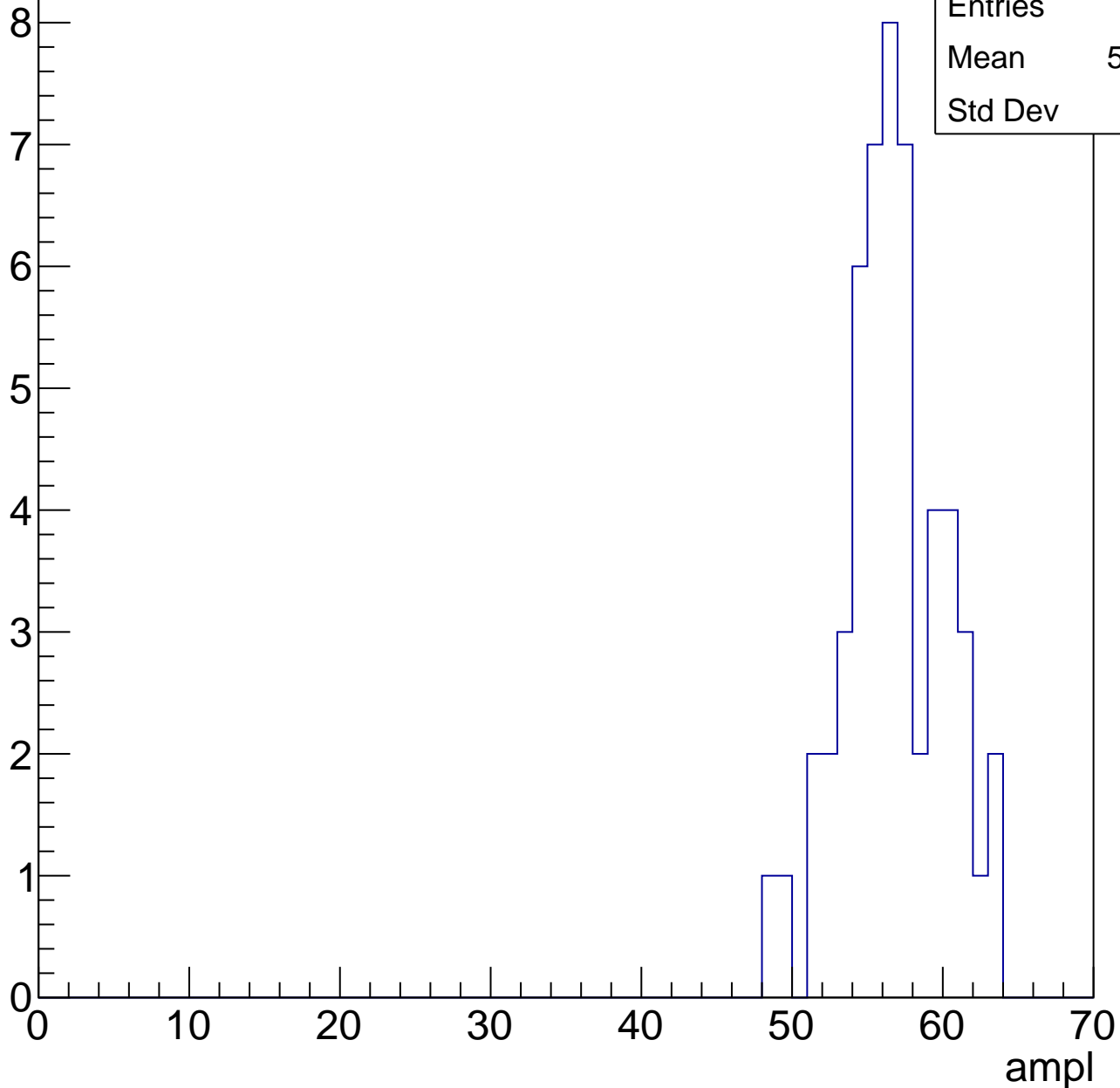


B1L103S, U18-ch19, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

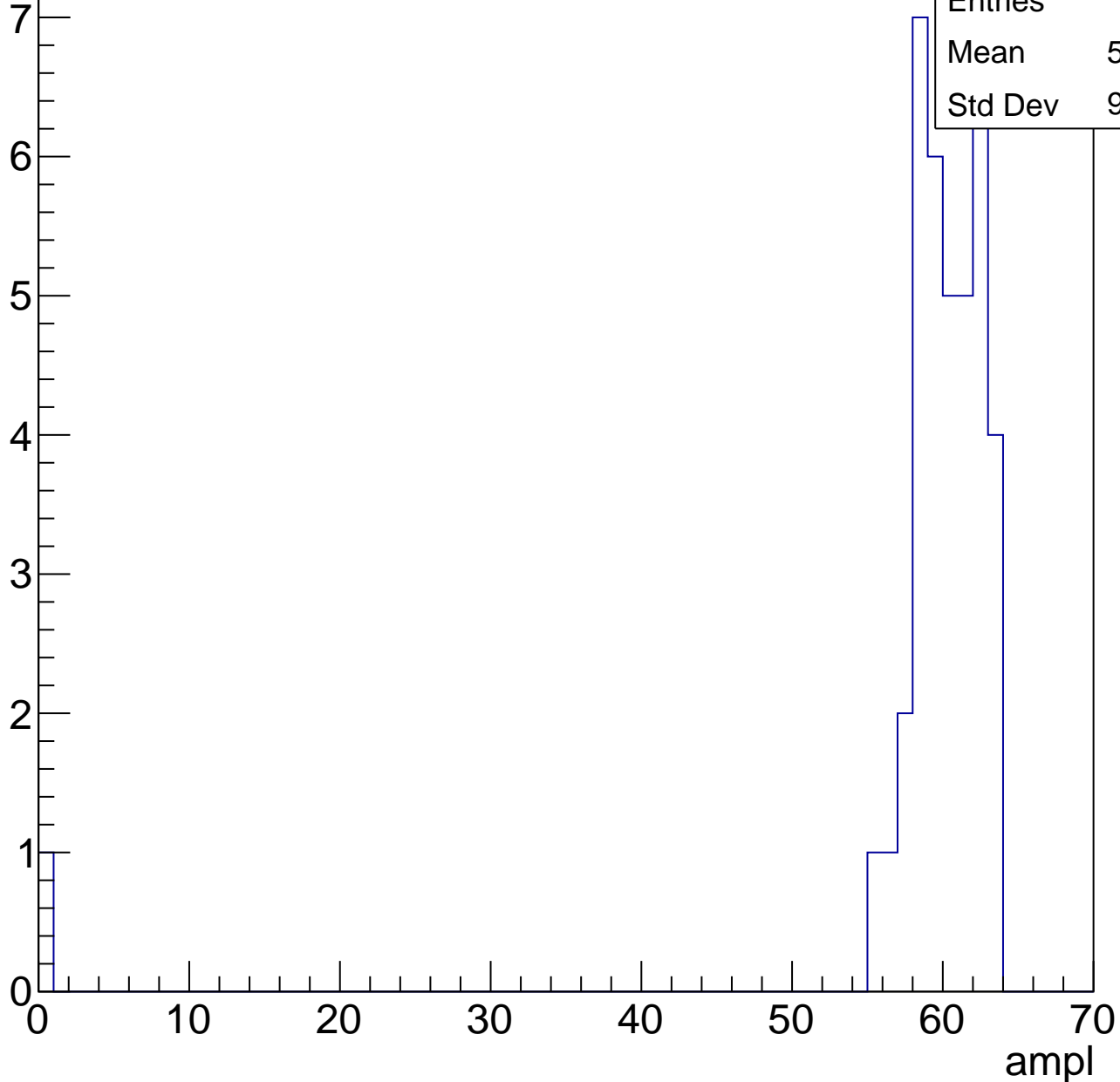
Entries	53
Mean	56.25
Std Dev	3.29



B1L103S, U18-ch19, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch19, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch19, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch20, adc0

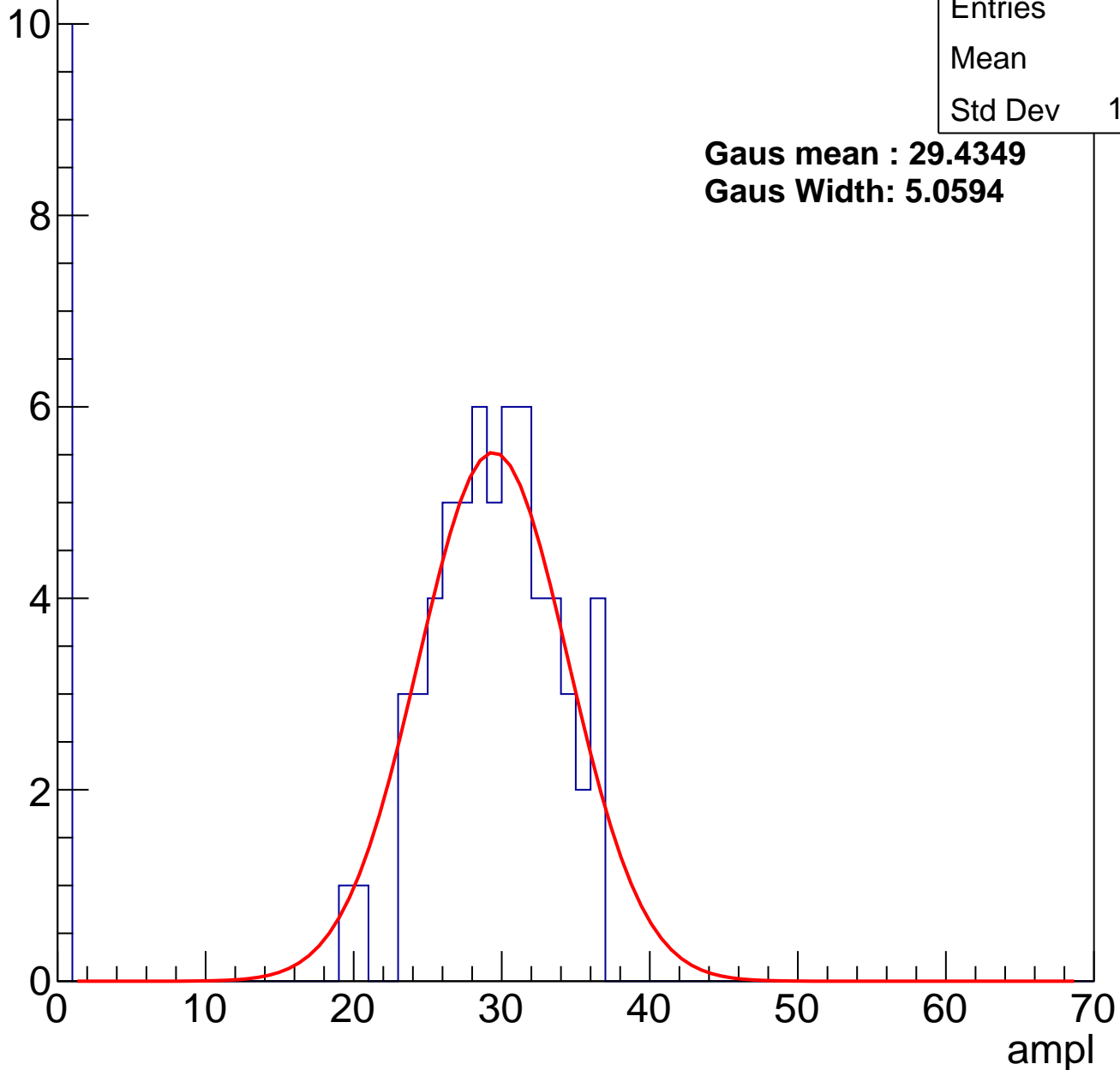
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	25
Std Dev	10.69

Gaus mean : 29.4349

Gaus Width: 5.0594

Entry



B1L103S, U18-ch20, adc1

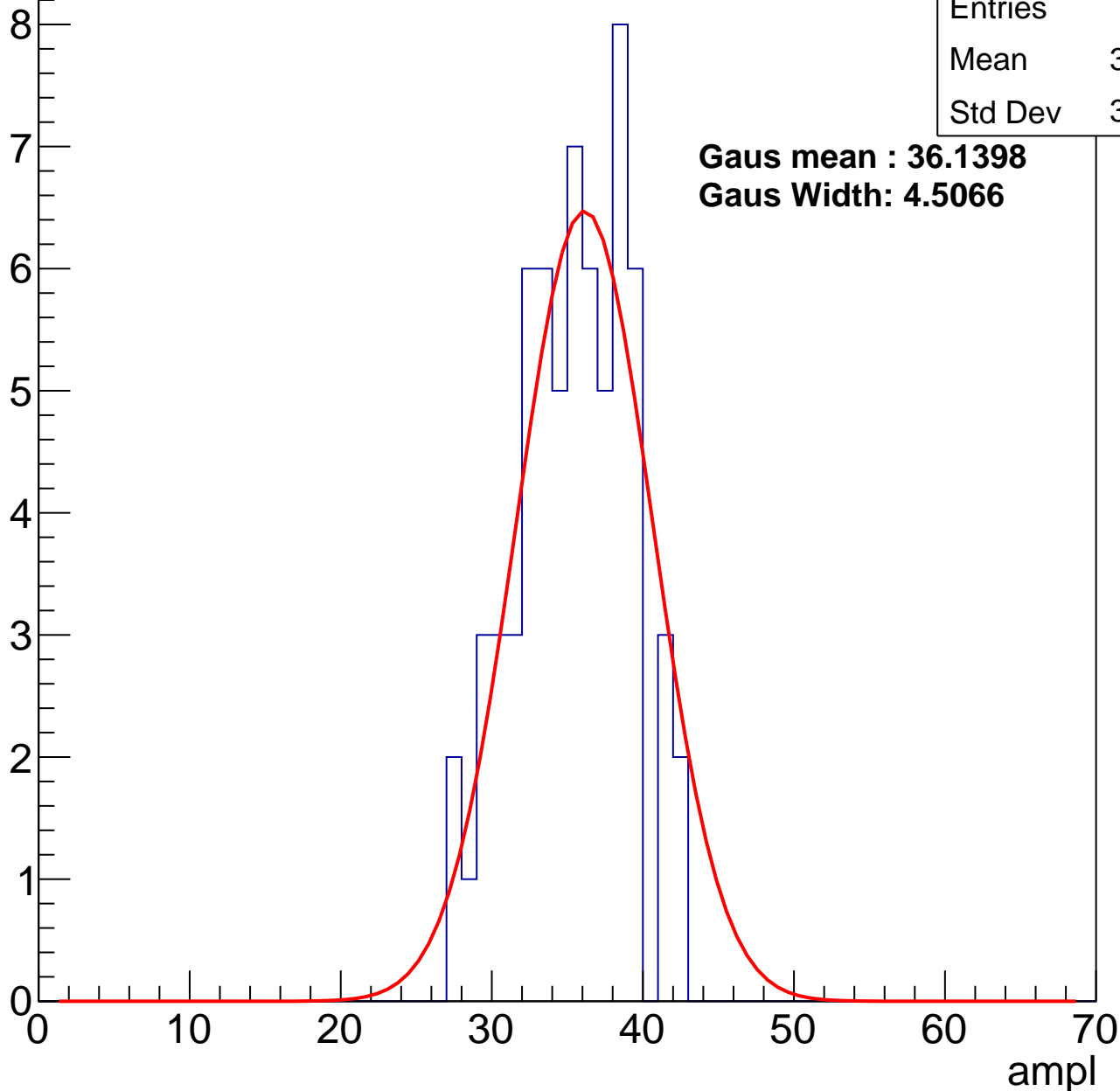
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	34.89
Std Dev	3.673

Gaus mean : 36.1398

Gaus Width: 4.5066



B1L103S, U18-ch20, adc2

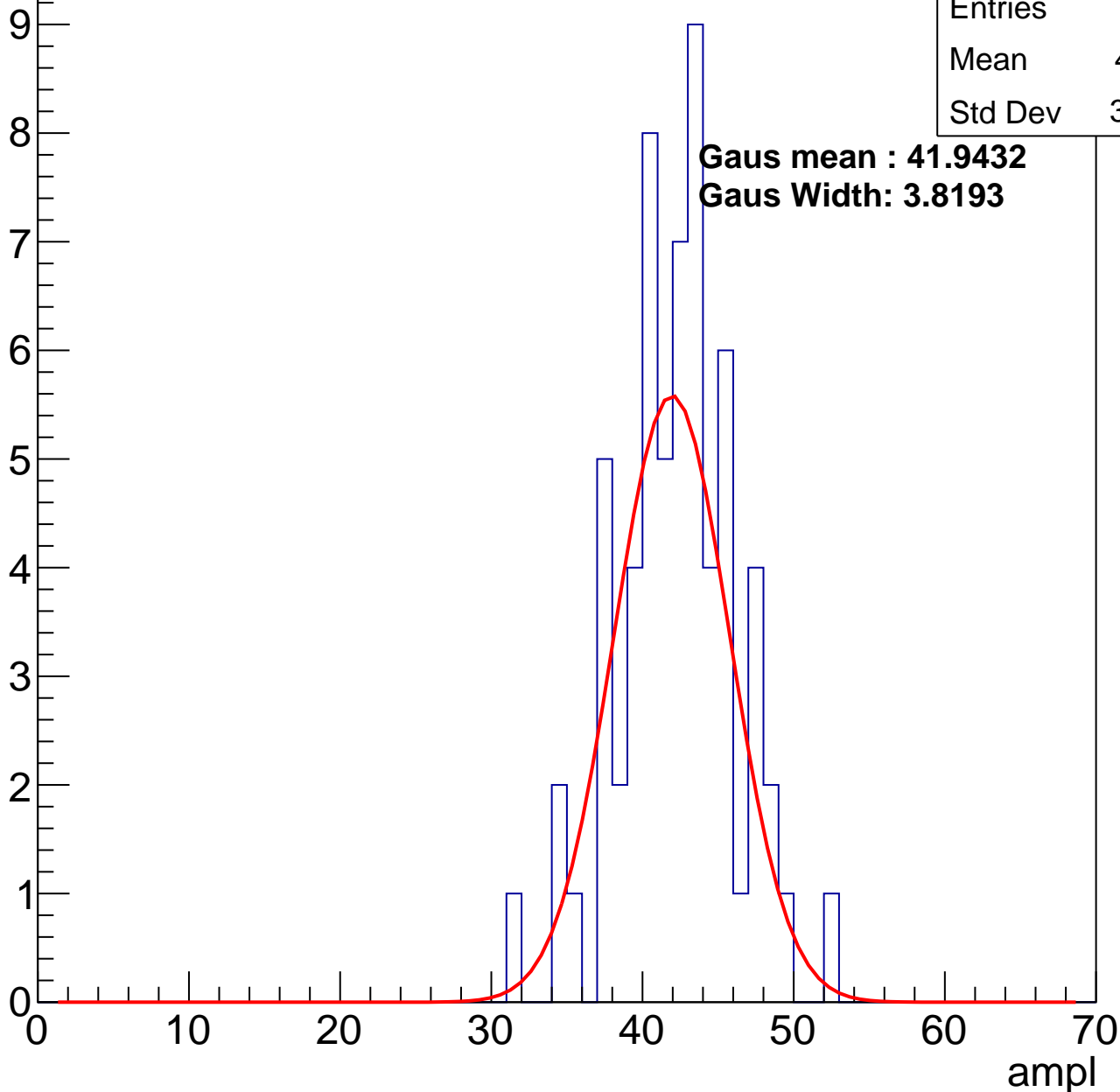
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	41.81
Std Dev	3.866

Gaus mean : 41.9432

Gaus Width: 3.8193

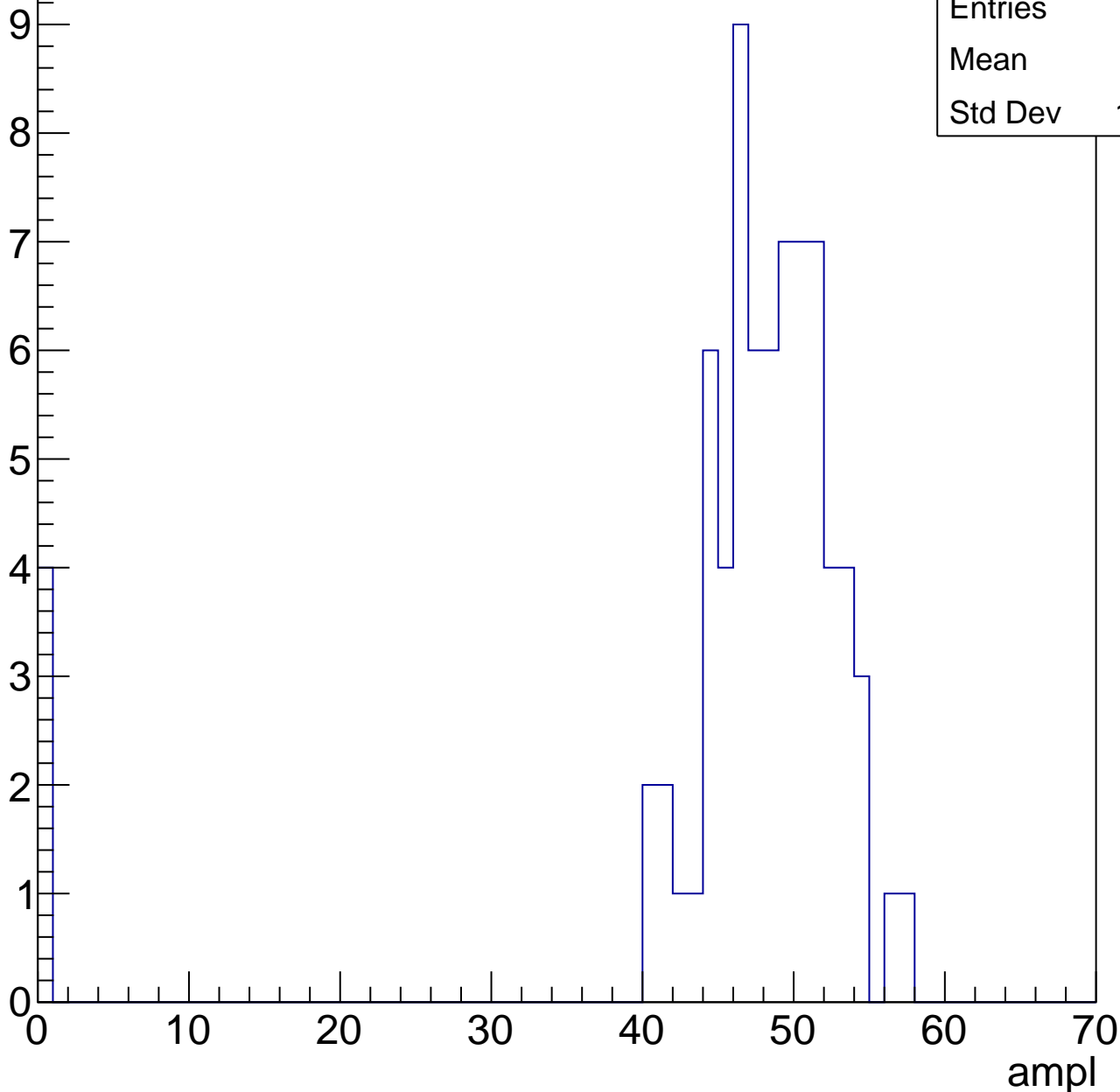


B1L103S, U18-ch20, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	45.6
Std Dev	11.41

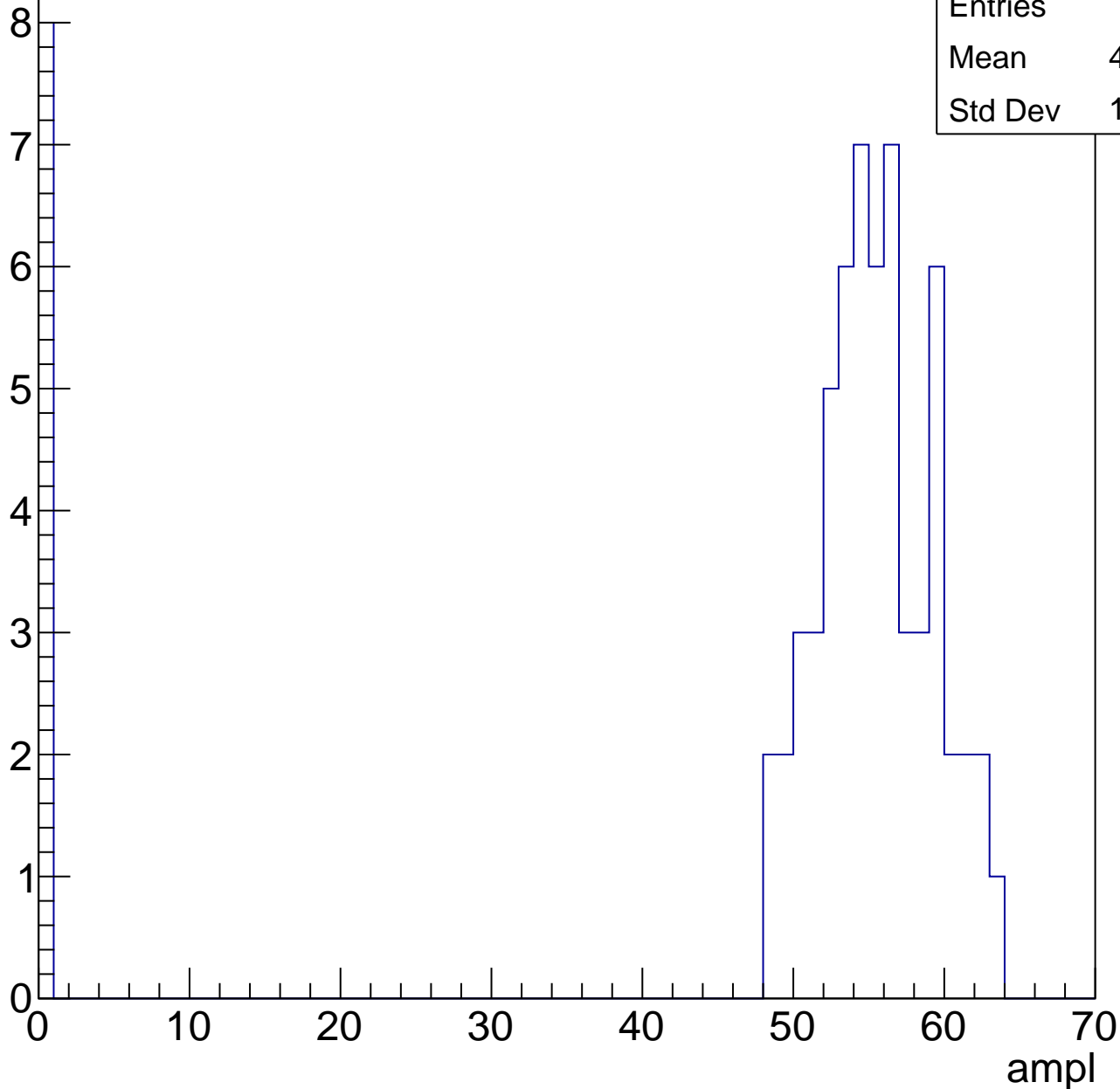


B1L103S, U18-ch20, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

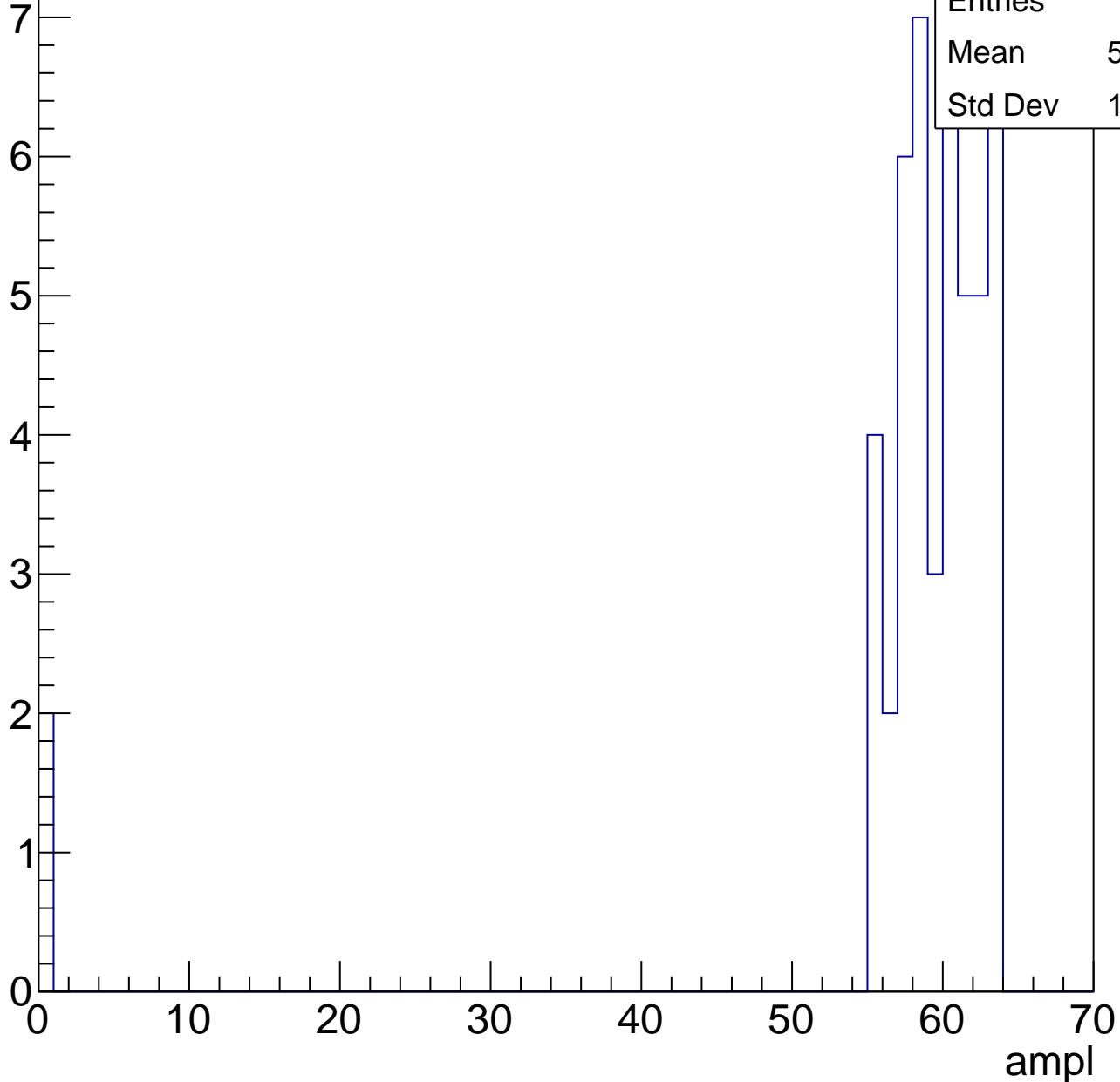
Entries	68
Mean	48.57
Std Dev	18.06



B1L103S, U18-ch20, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch20, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

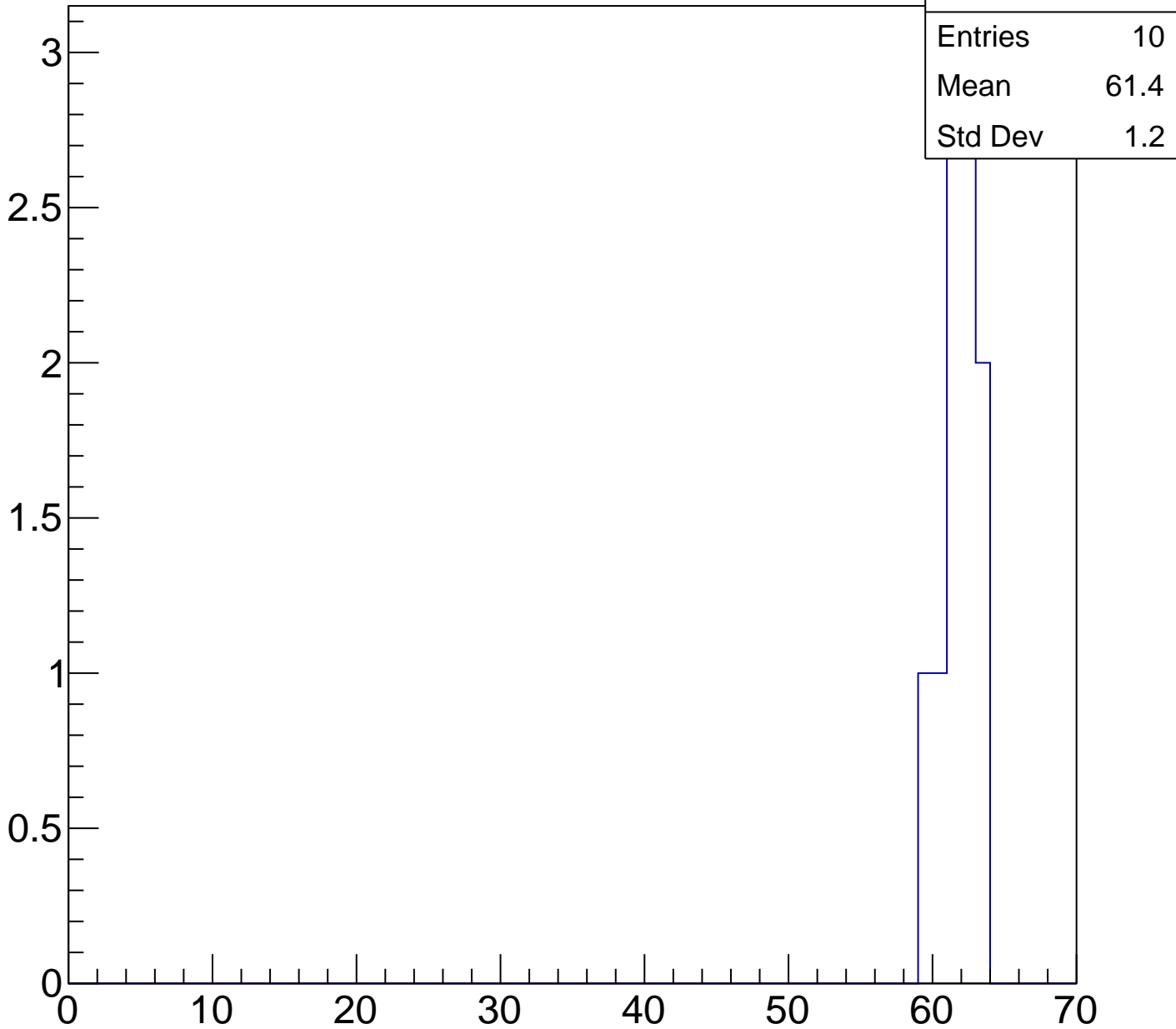
1

0.5

0

ampl

Entries	10
Mean	61.4
Std Dev	1.2



B1L103S, U18-ch20, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch21, adc0

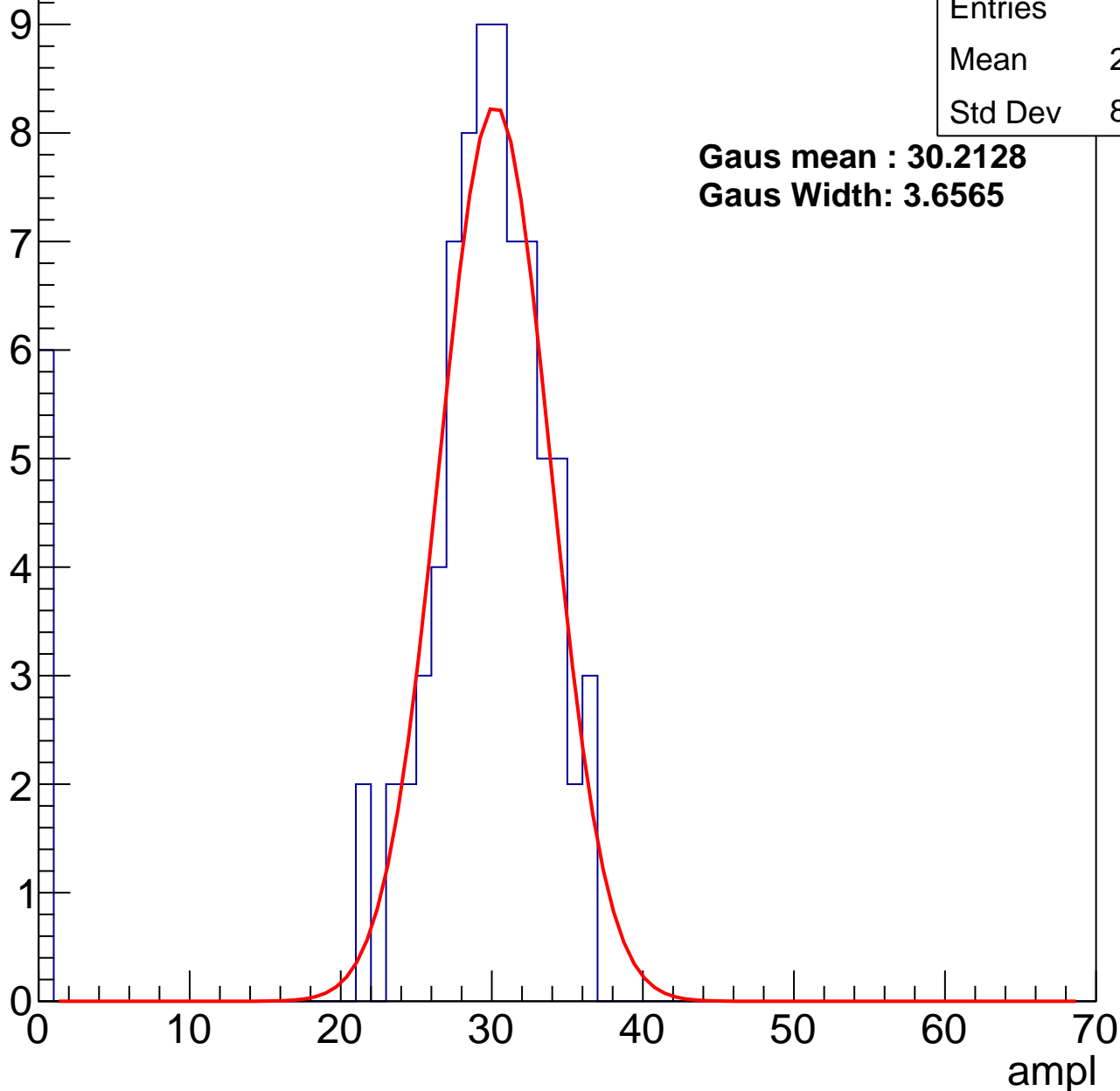
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	27.32
Std Dev	8.397

Gaus mean : 30.2128

Gaus Width: 3.6565



B1L103S, U18-ch21, adc1

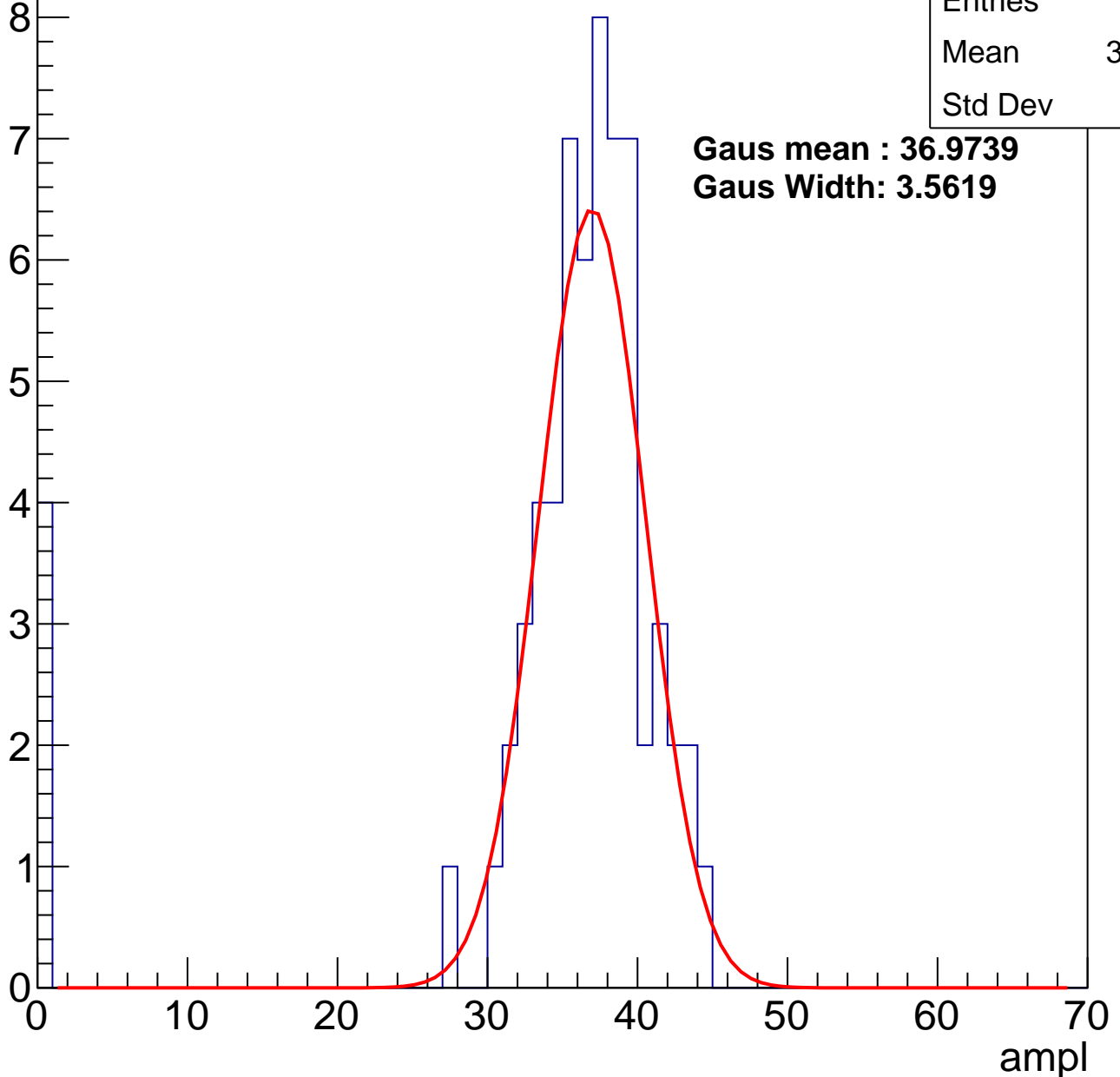
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.31
Std Dev	9.45

Gaus mean : 36.9739

Gaus Width: 3.5619



B1L103S, U18-ch21, adc2

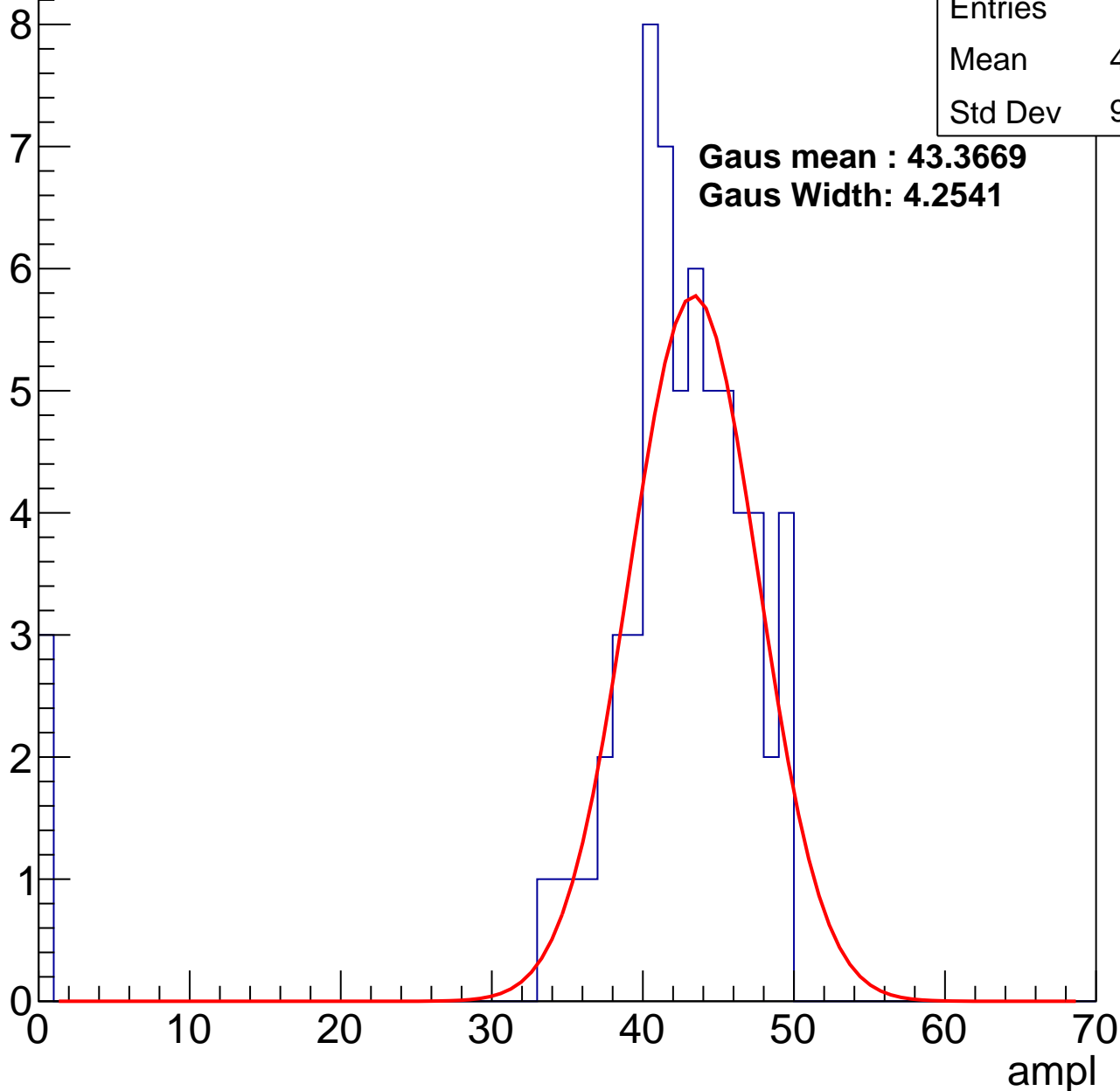
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	40.42
Std Dev	9.628

Gaus mean : 43.3669

Gaus Width: 4.2541

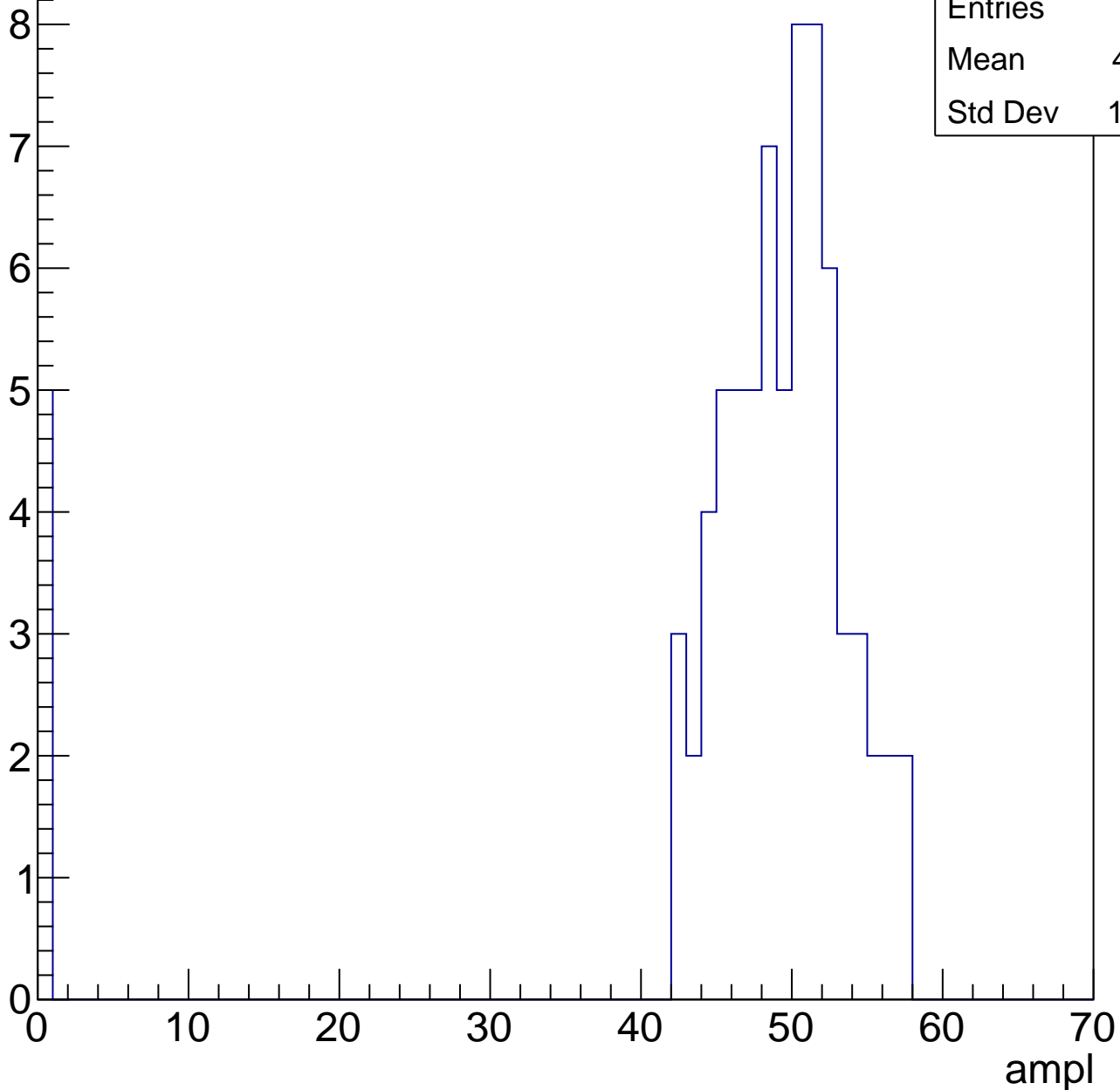


B1L103S, U18-ch21, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	45.81
Std Dev	12.77

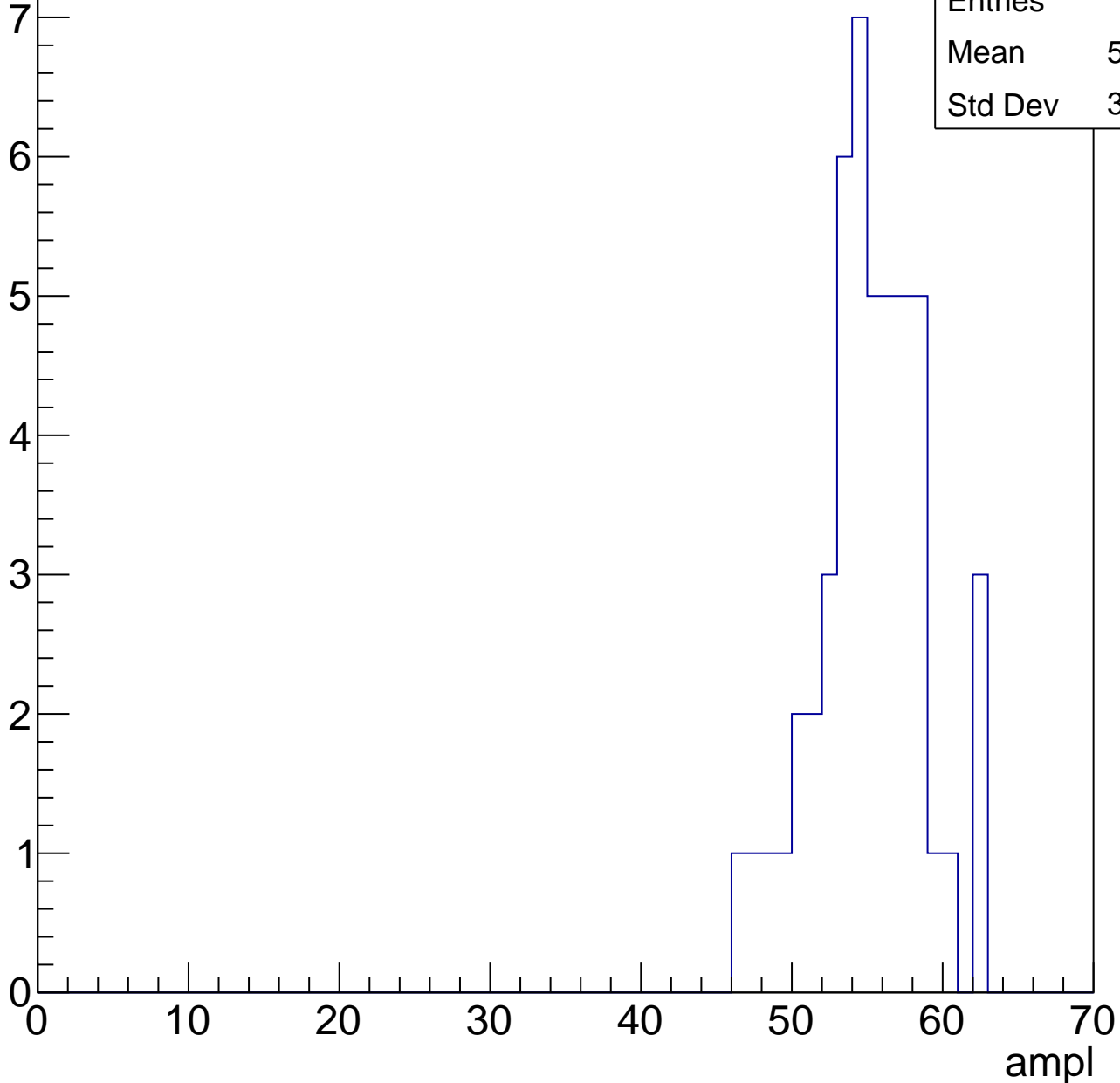


B1L103S, U18-ch21, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	54.67
Std Dev	3.565

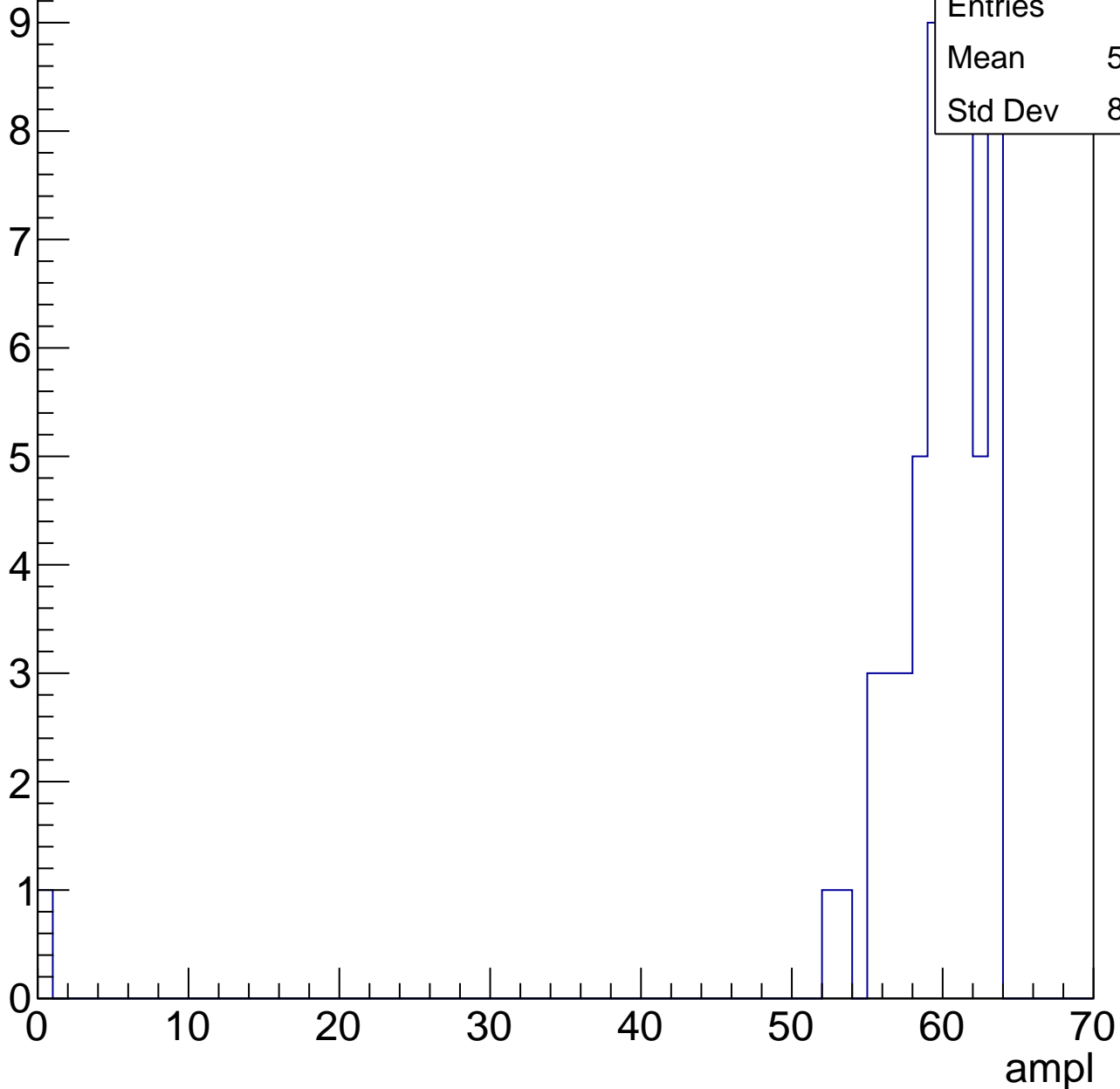


B1L103S, U18-ch21, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.47
Std Dev	8.229



B1L103S, U18-ch21, adc6

calib_packv5_041523_1651.root, FC#0, port C2

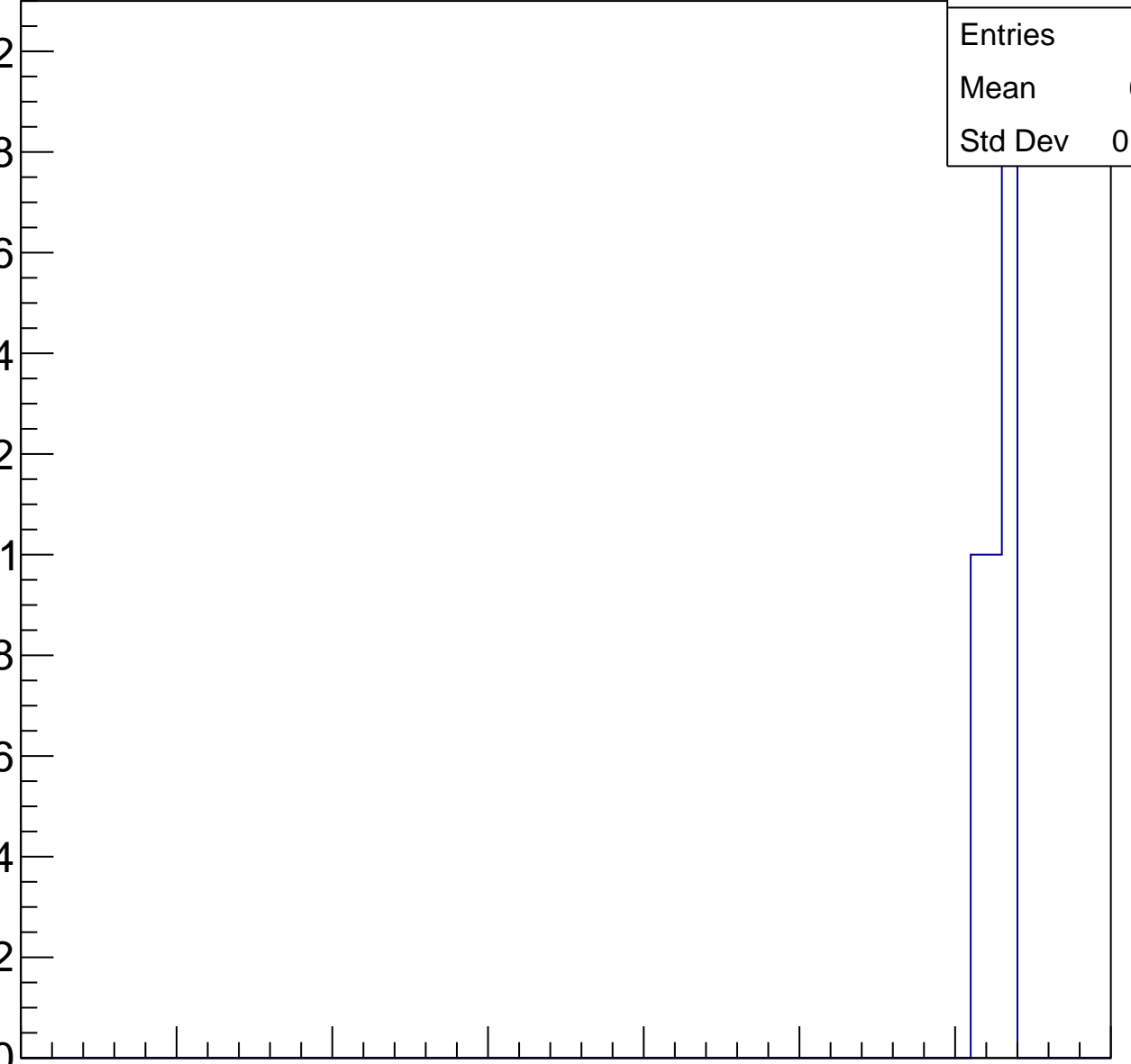
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch21, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U18-ch22, adc0

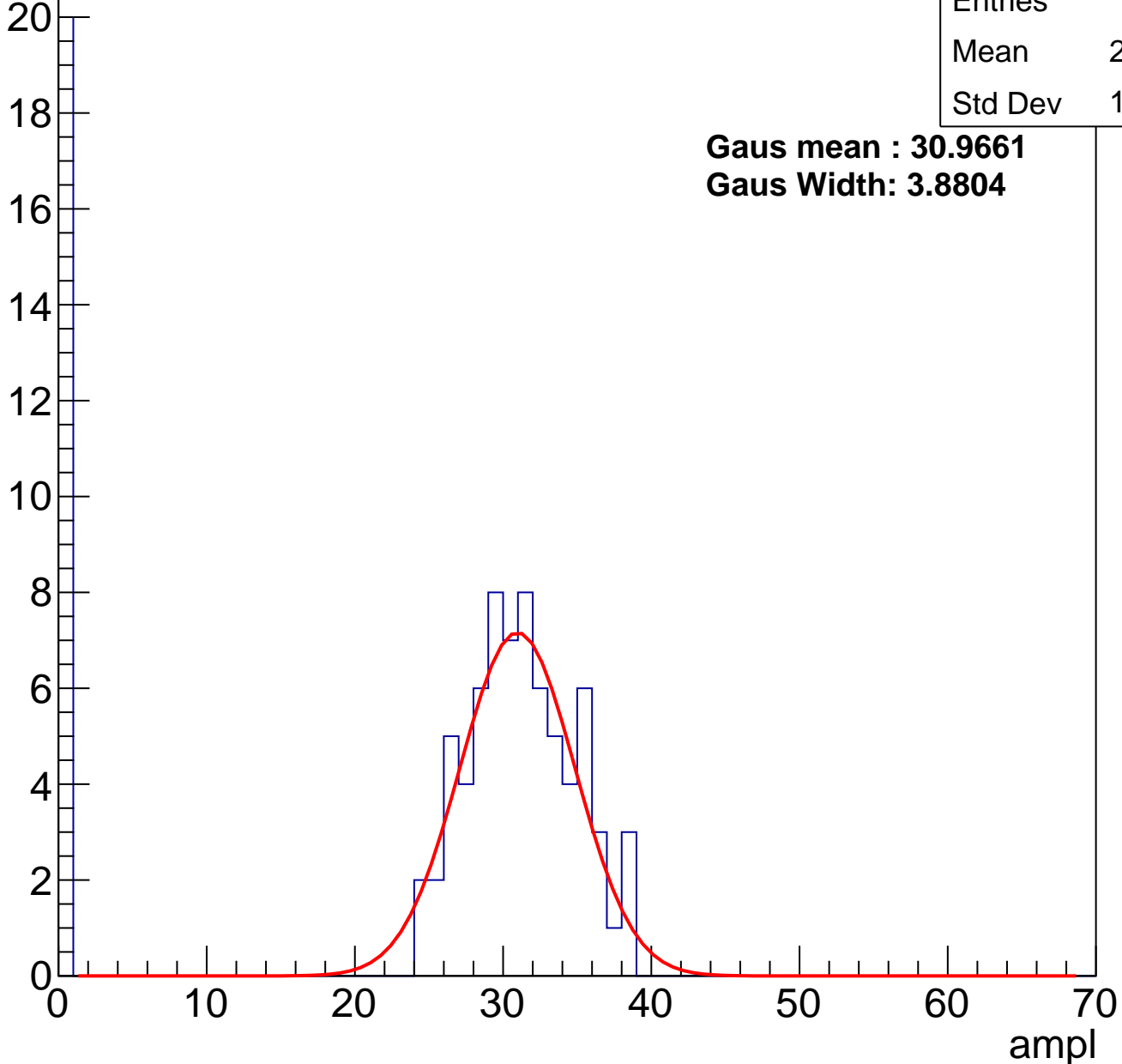
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	23.96
Std Dev	13.17

Gaus mean : 30.9661

Gaus Width: 3.8804

Entry



B1L103S, U18-ch22, adc1

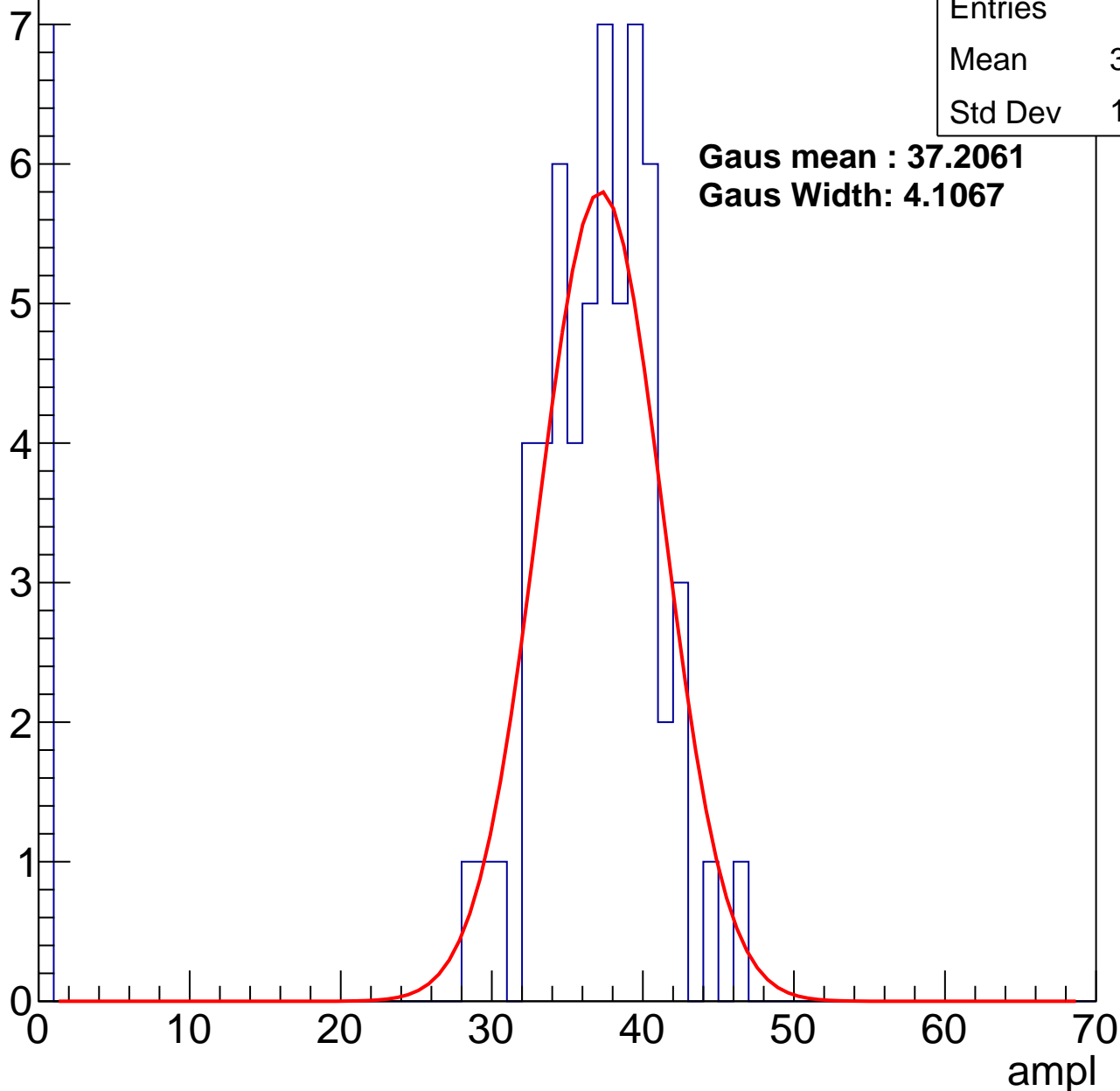
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	32.78
Std Dev	11.89

Gaus mean : 37.2061

Gaus Width: 4.1067



B1L103S, U18-ch22, adc2

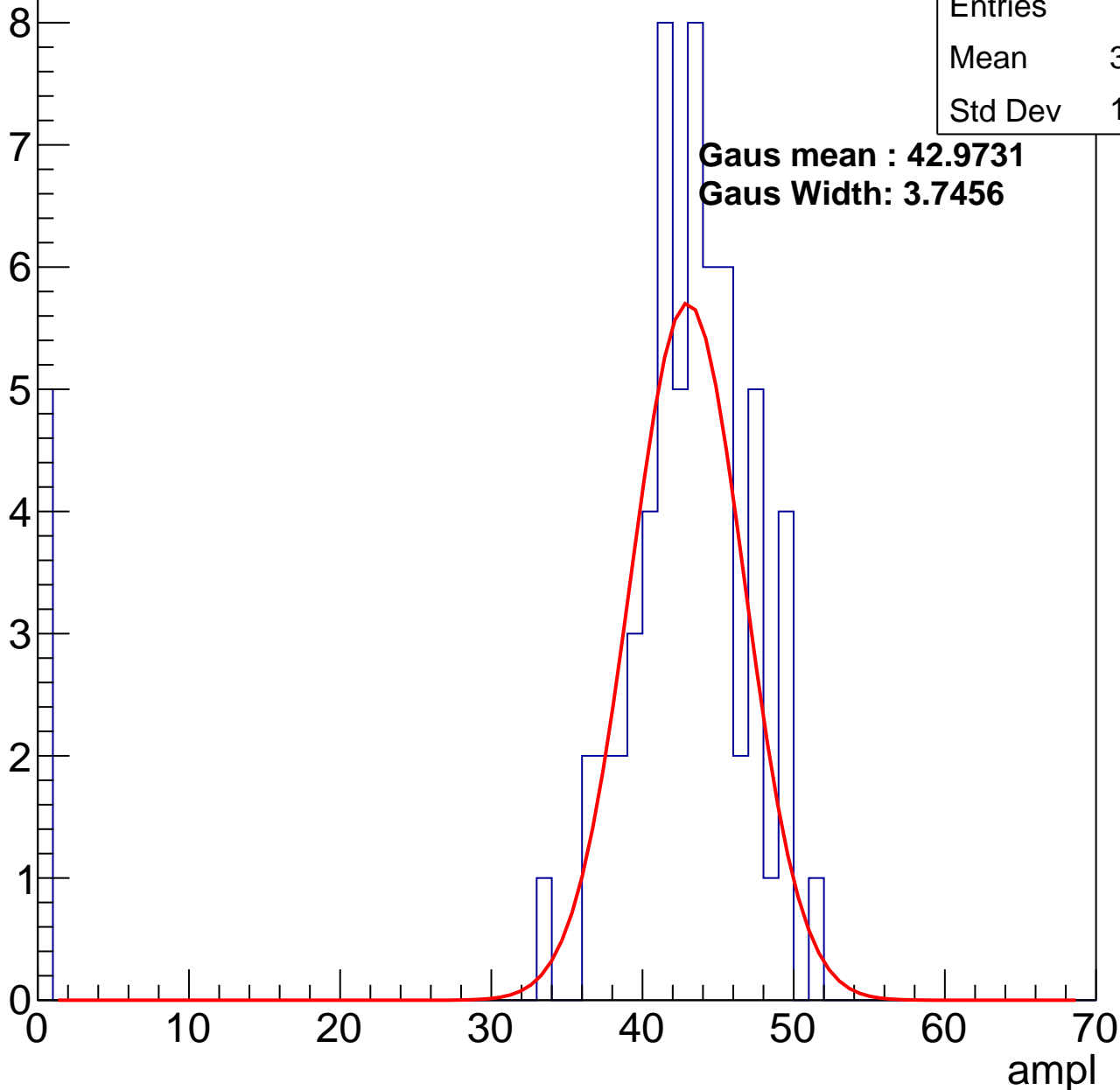
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	39.54
Std Dev	11.94

Gaus mean : 42.9731

Gaus Width: 3.7456

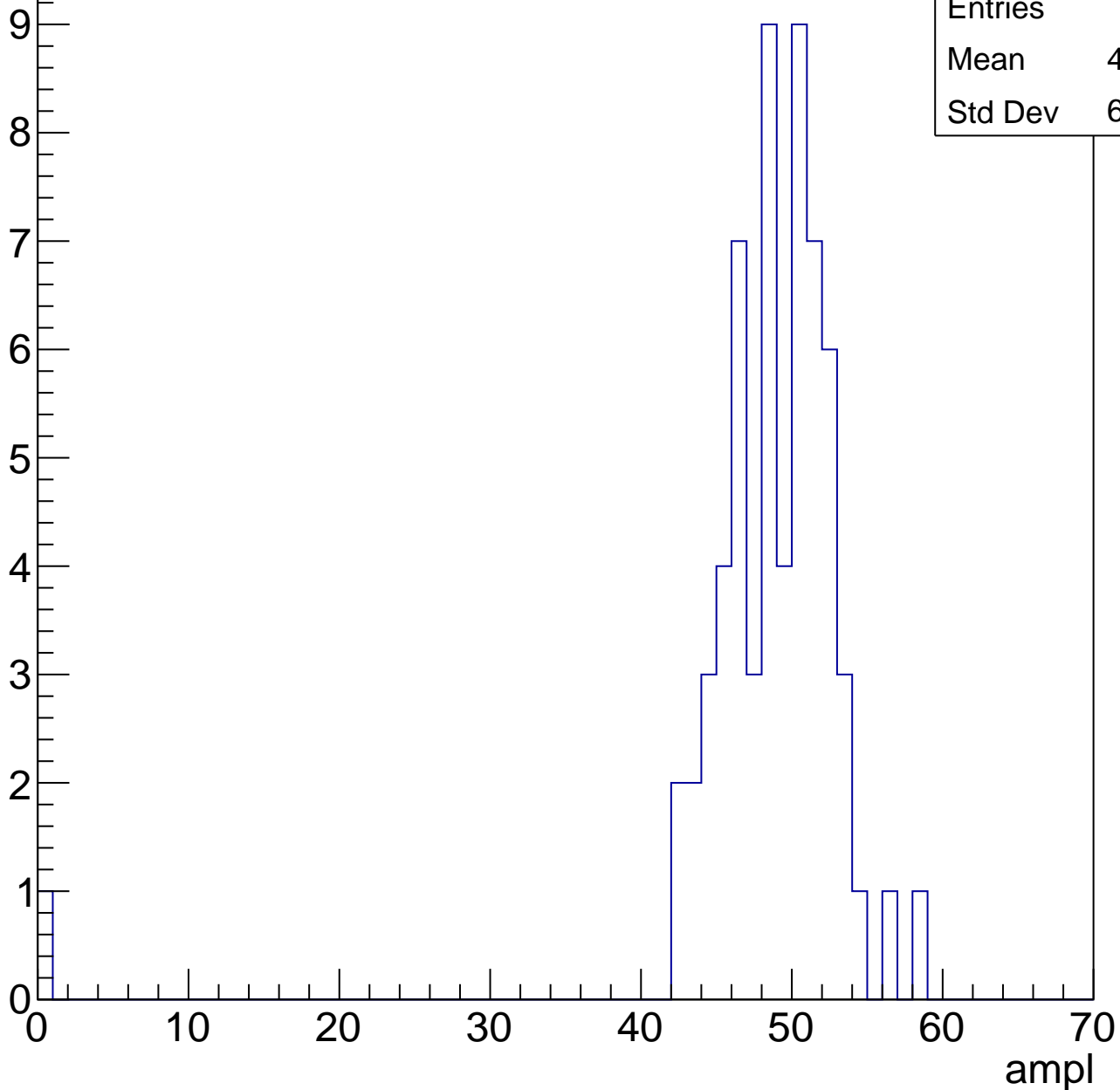


B1L103S, U18-ch22, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.92
Std Dev	6.917

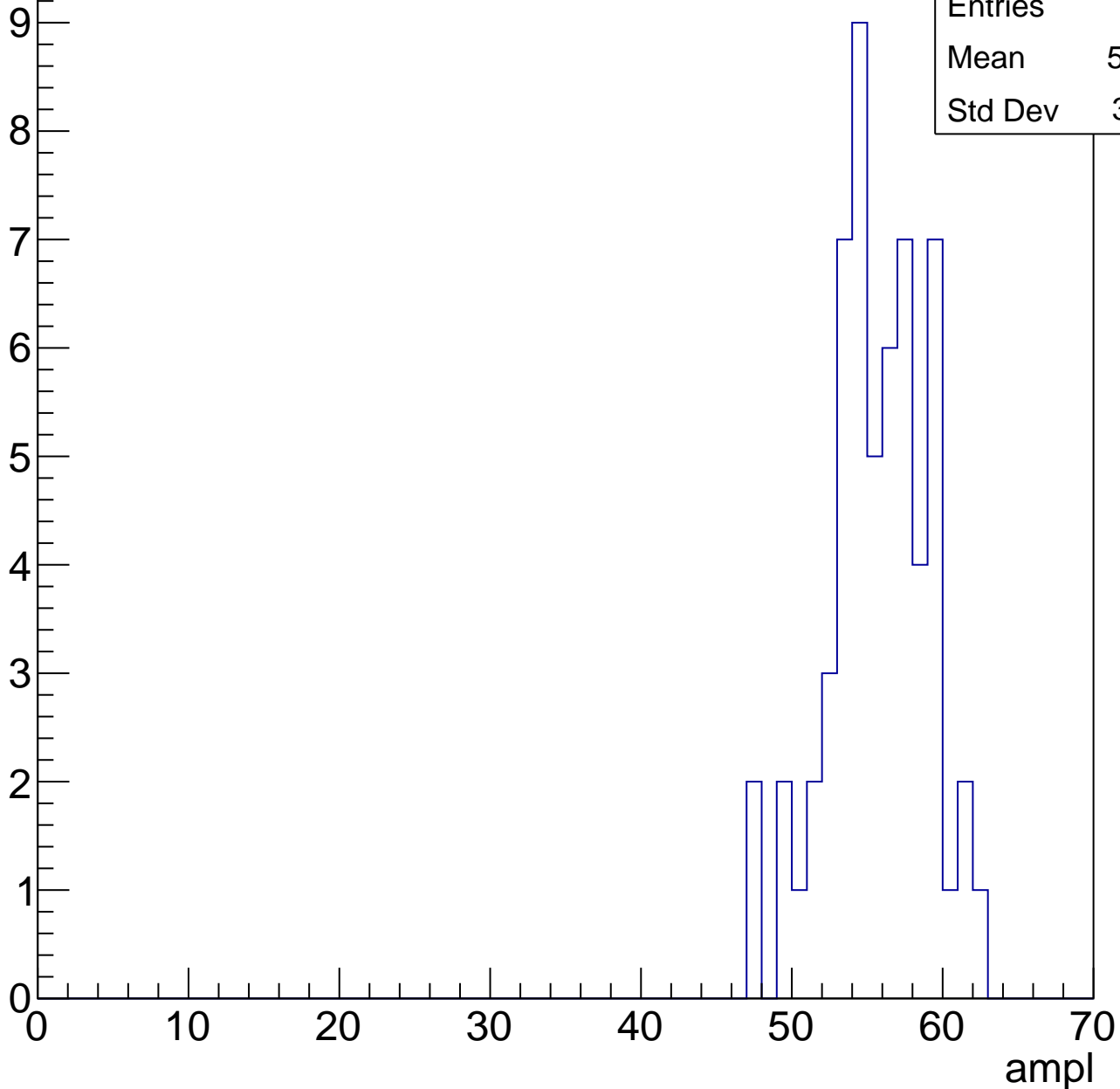


B1L103S, U18-ch22, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

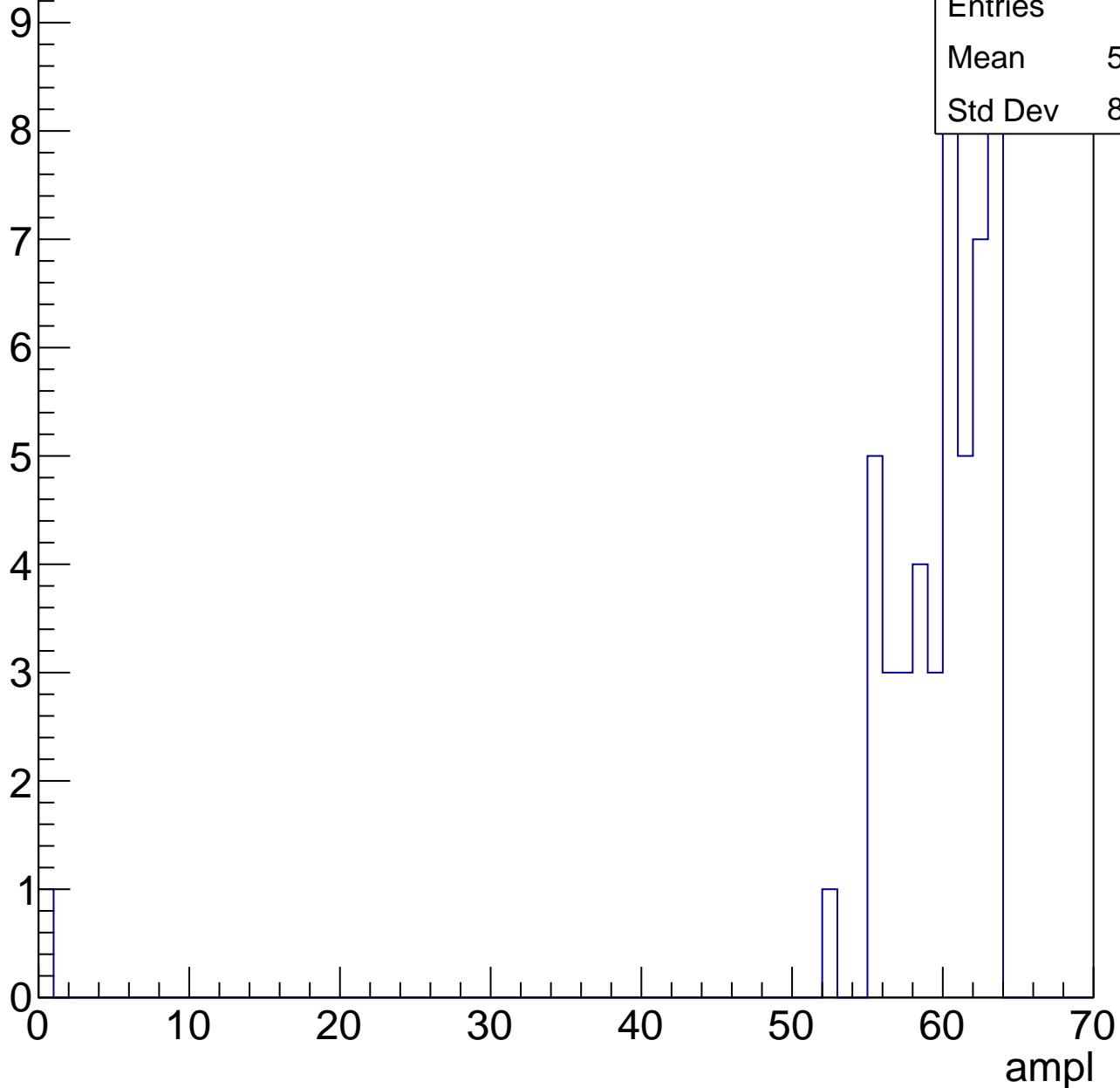
Entries	59
Mean	55.19
Std Dev	3.311



B1L103S, U18-ch22, adc5

calib_packv5_041523_1651.root, FC#0, port C2

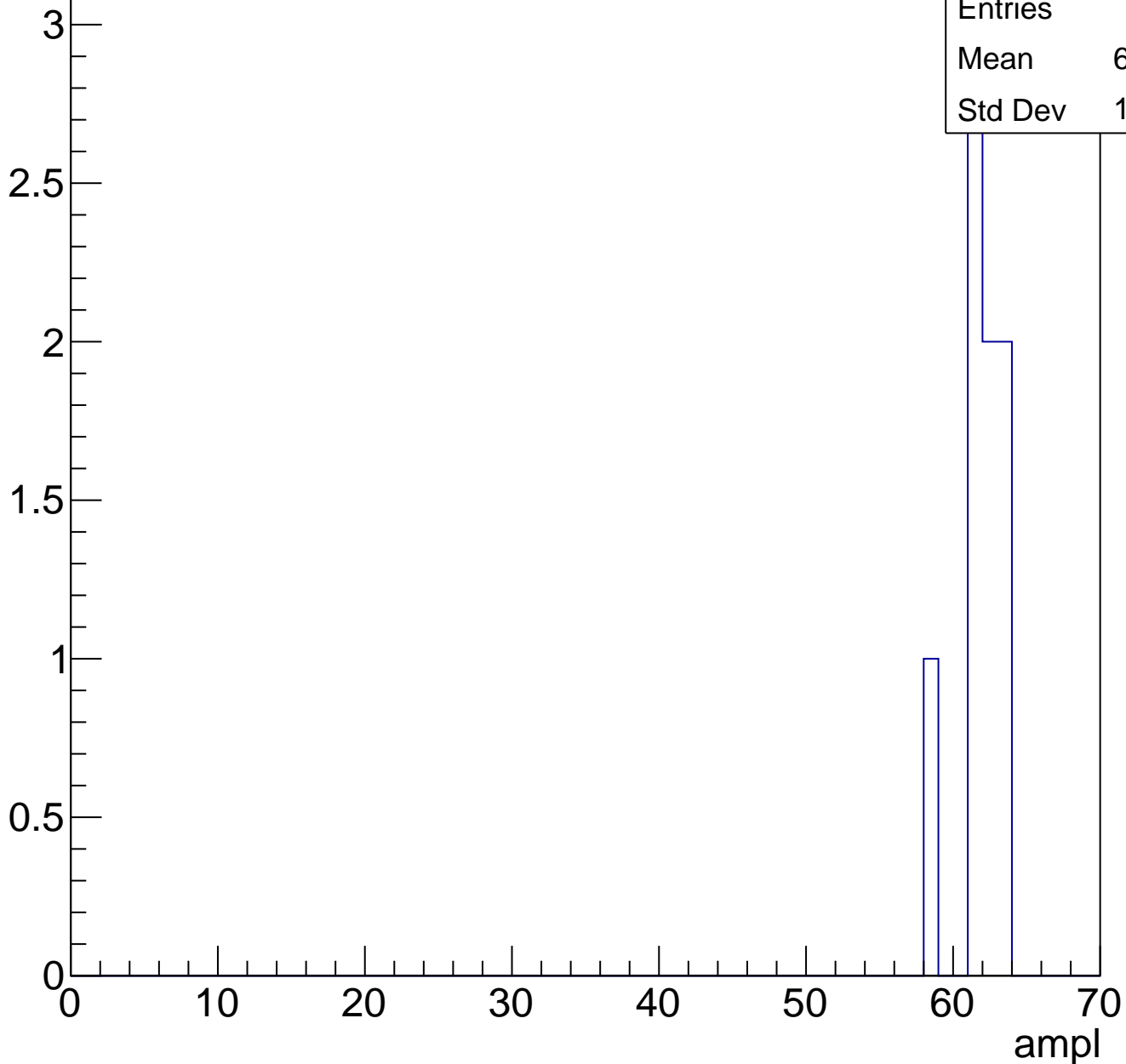
Entry



B1L103S, U18-ch22, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch22, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch23, adc0

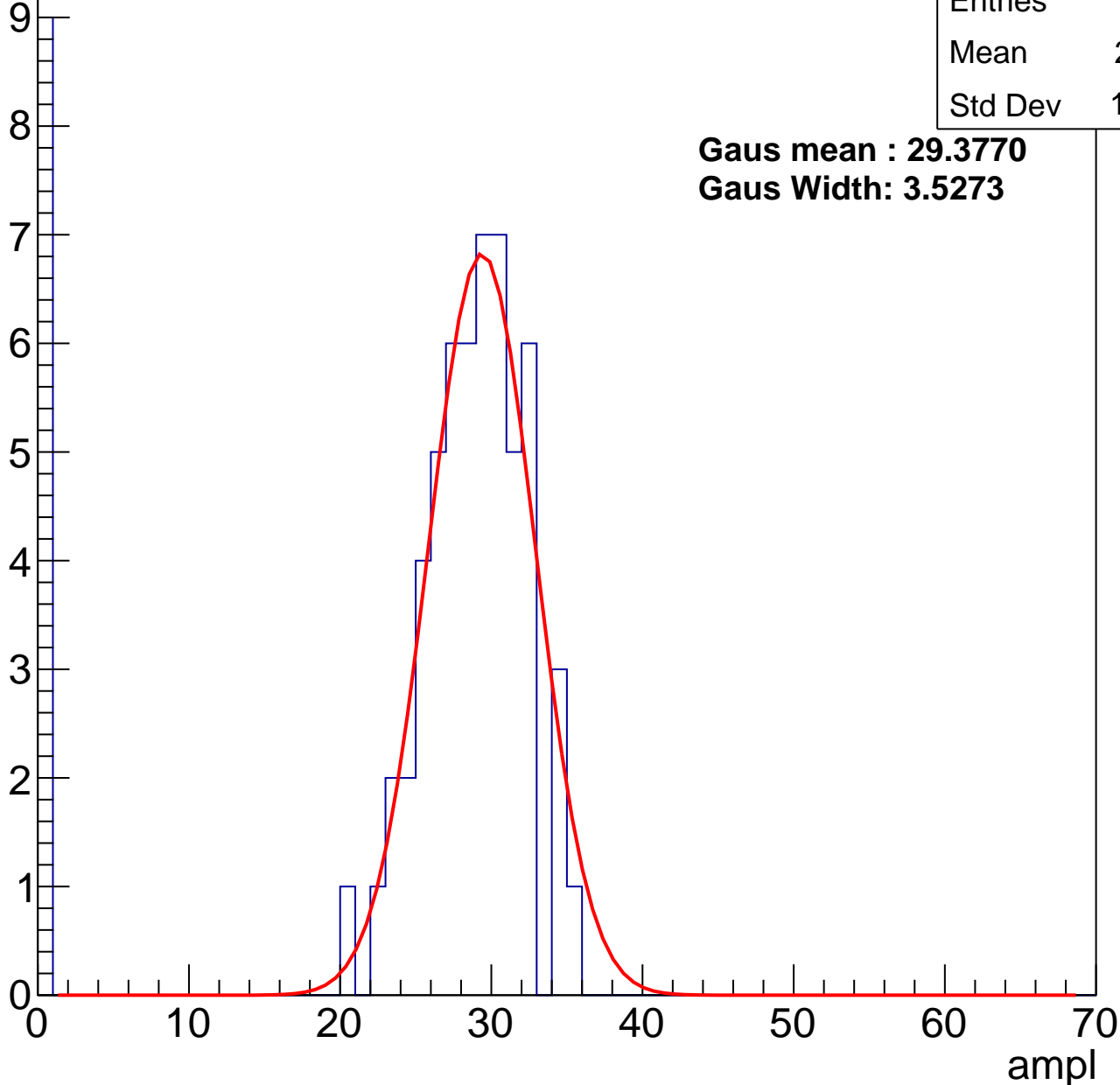
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	24.51
Std Dev	10.26

Gaus mean : 29.3770

Gaus Width: 3.5273

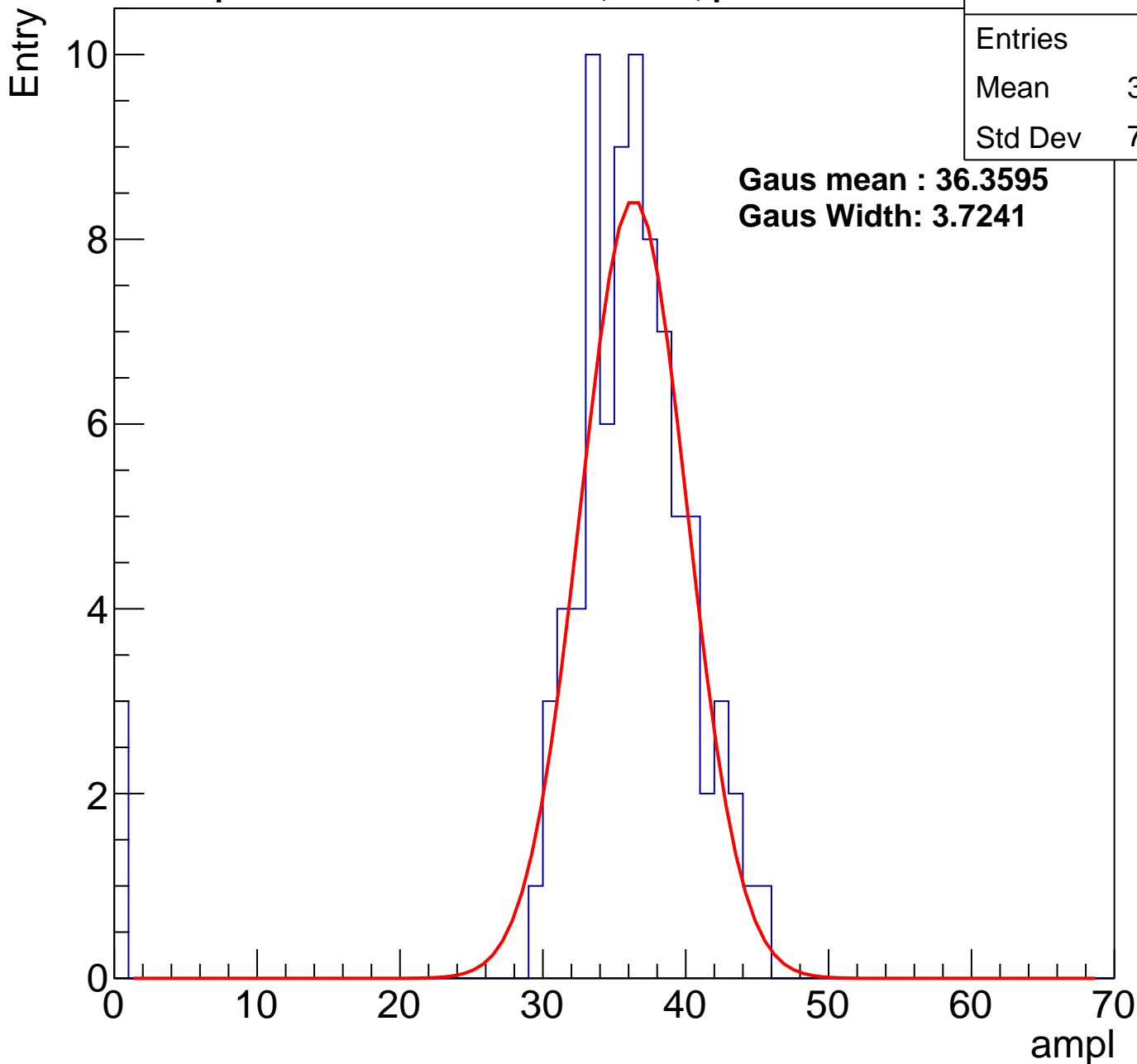


B1L103S, U18-ch23, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	34.76
Std Dev	7.532

Gaus mean : 36.3595
Gaus Width: 3.7241



B1L103S, U18-ch23, adc2

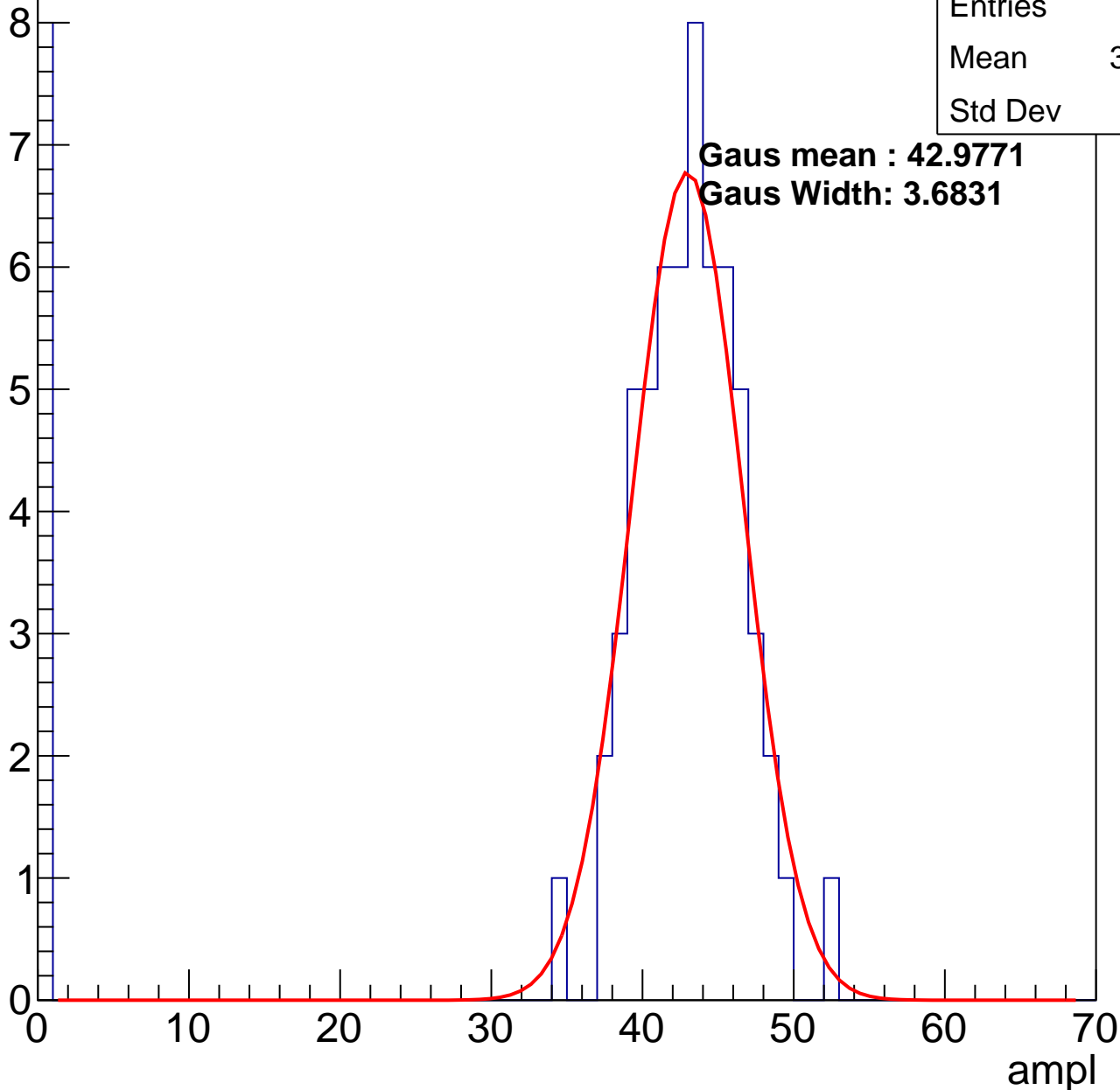
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	37.66
Std Dev	14.1

Gaus mean : 42.9771

Gaus Width: 3.6831

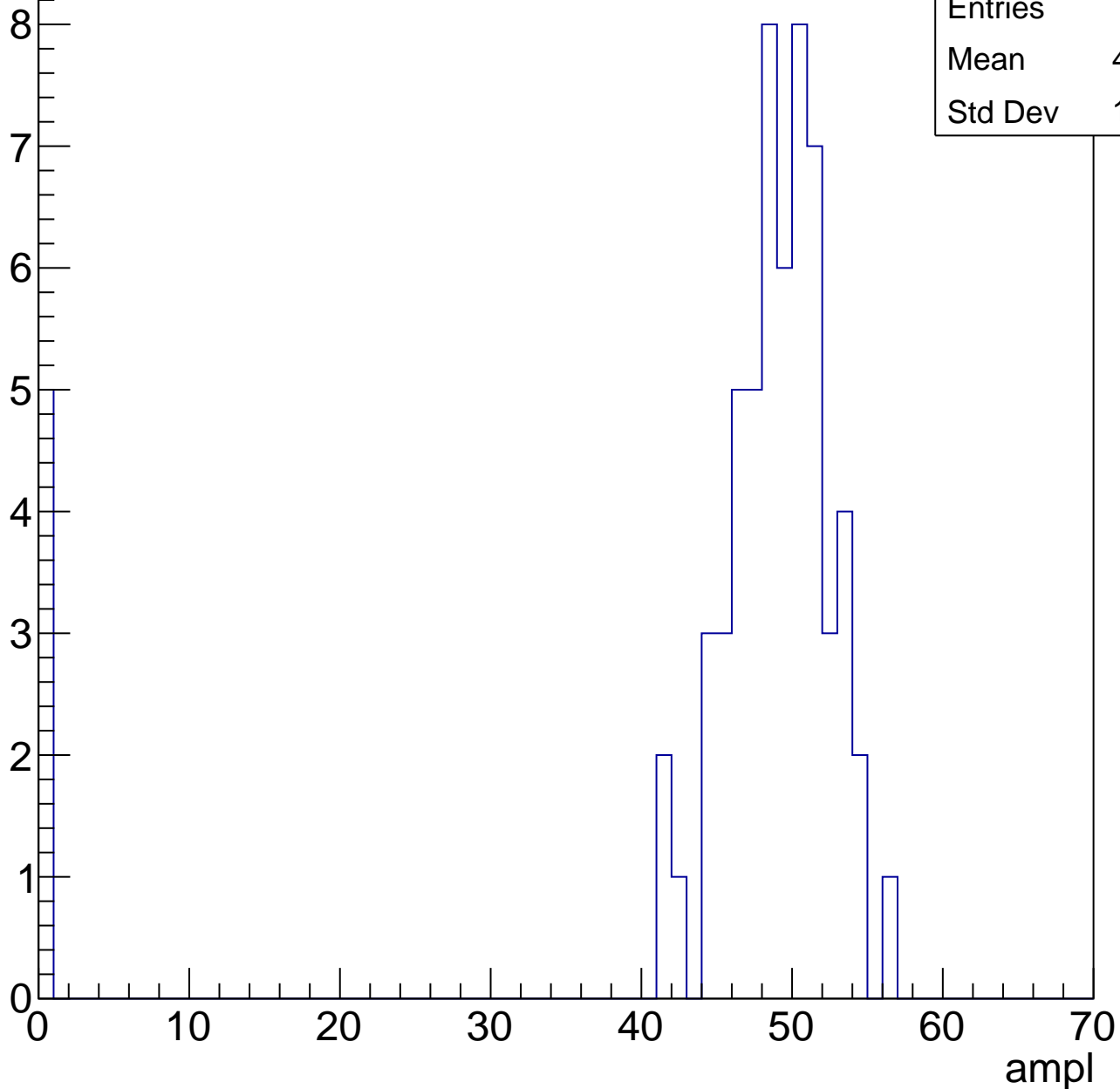


B1L103S, U18-ch23, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	44.81
Std Dev	13.51

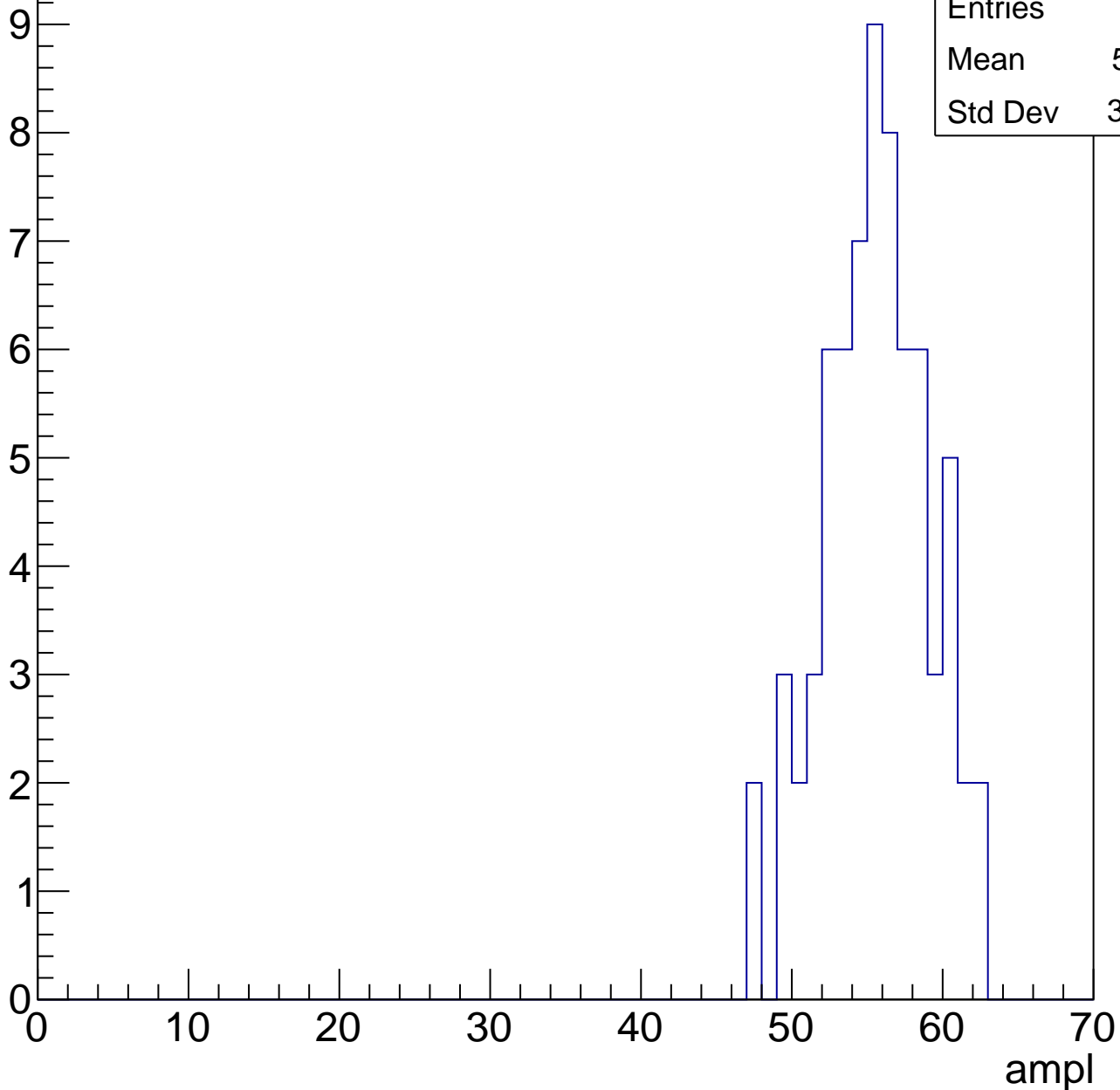


B1L103S, U18-ch23, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	55.11
Std Dev	3.479

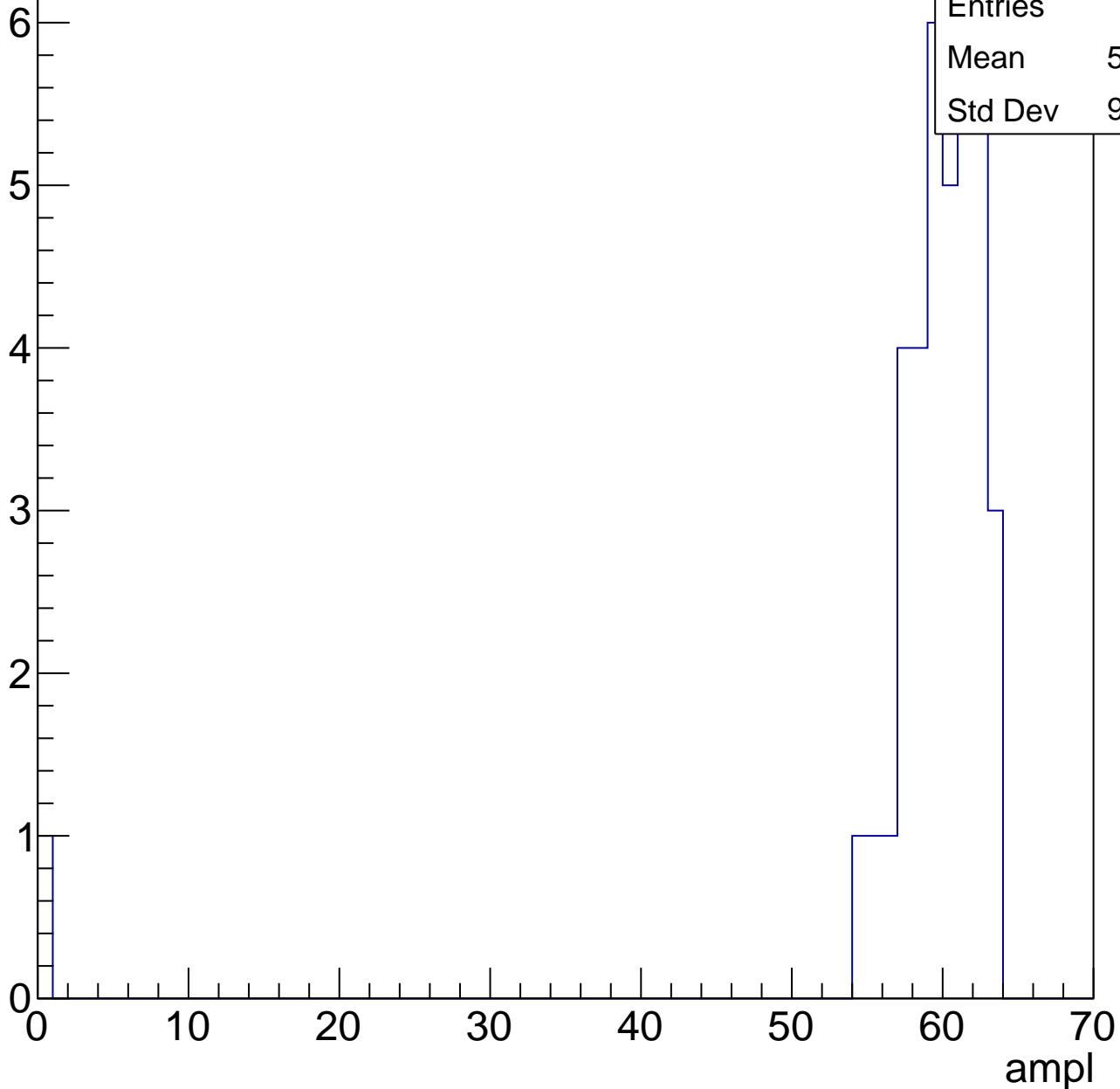


B1L103S, U18-ch23, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.05
Std Dev	9.798

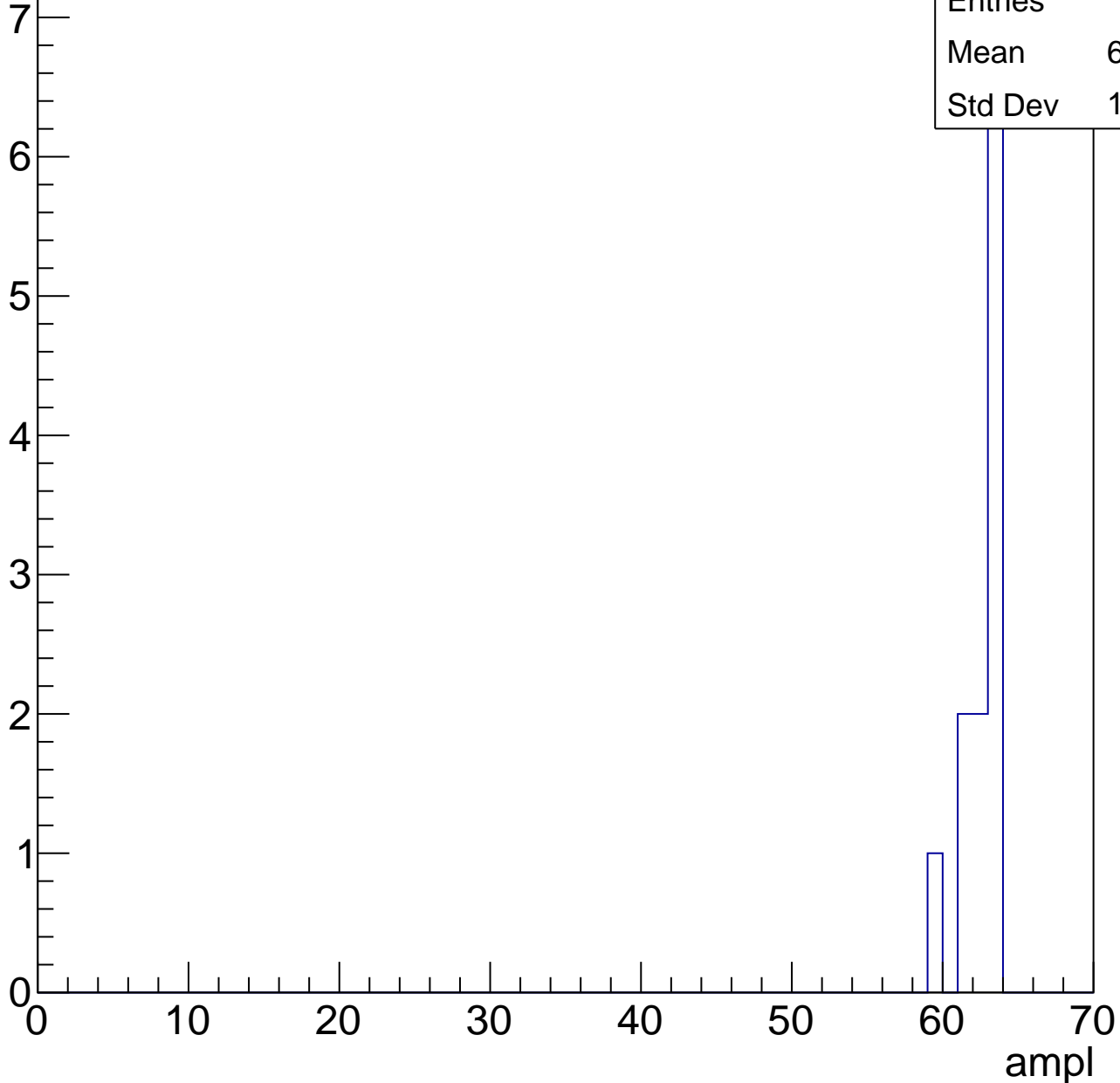


B1L103S, U18-ch23, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	62.17
Std Dev	1.213



B1L103S, U18-ch23, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch24, adc0

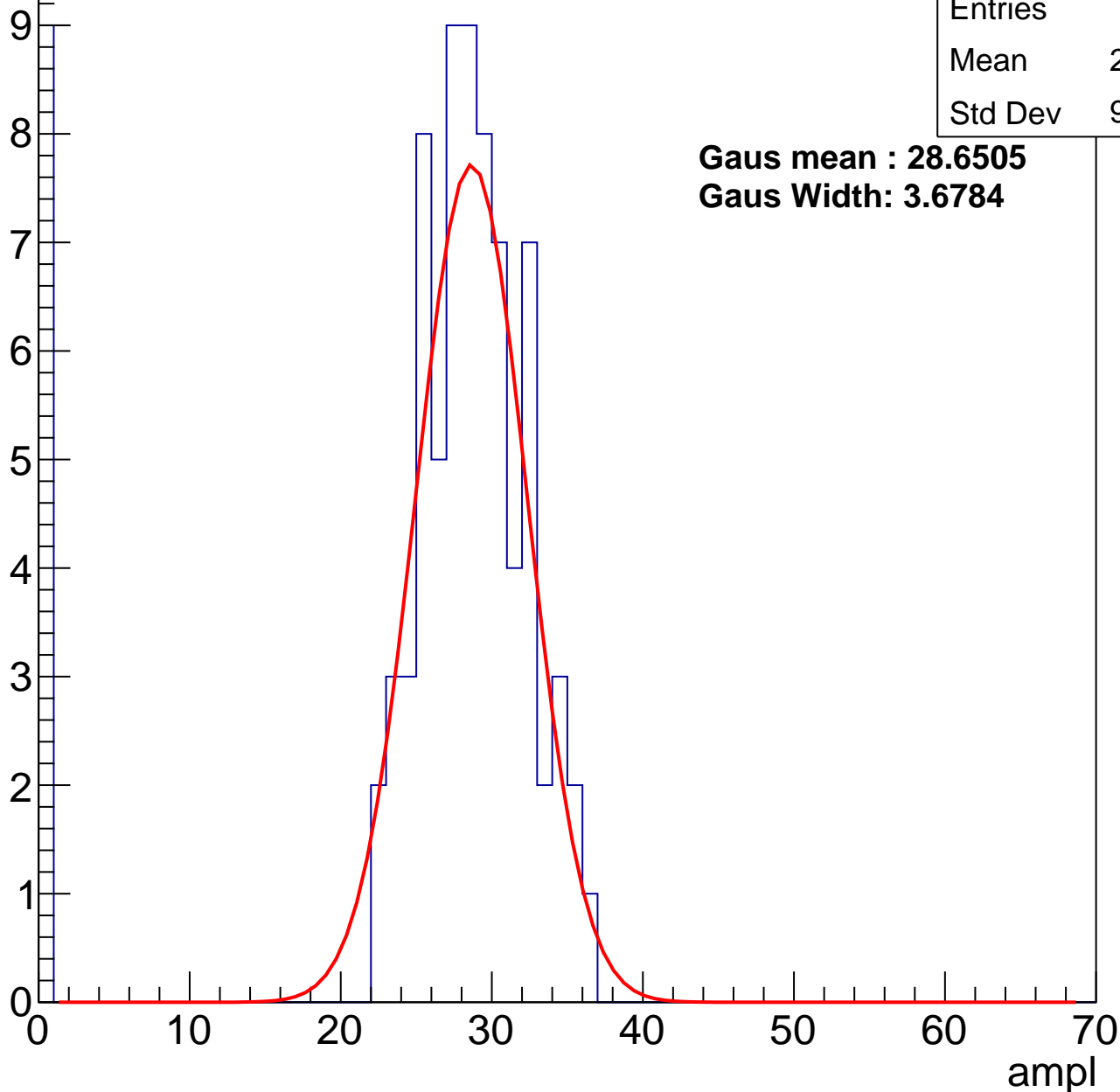
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	25.29
Std Dev	9.406

Gaus mean : 28.6505

Gaus Width: 3.6784



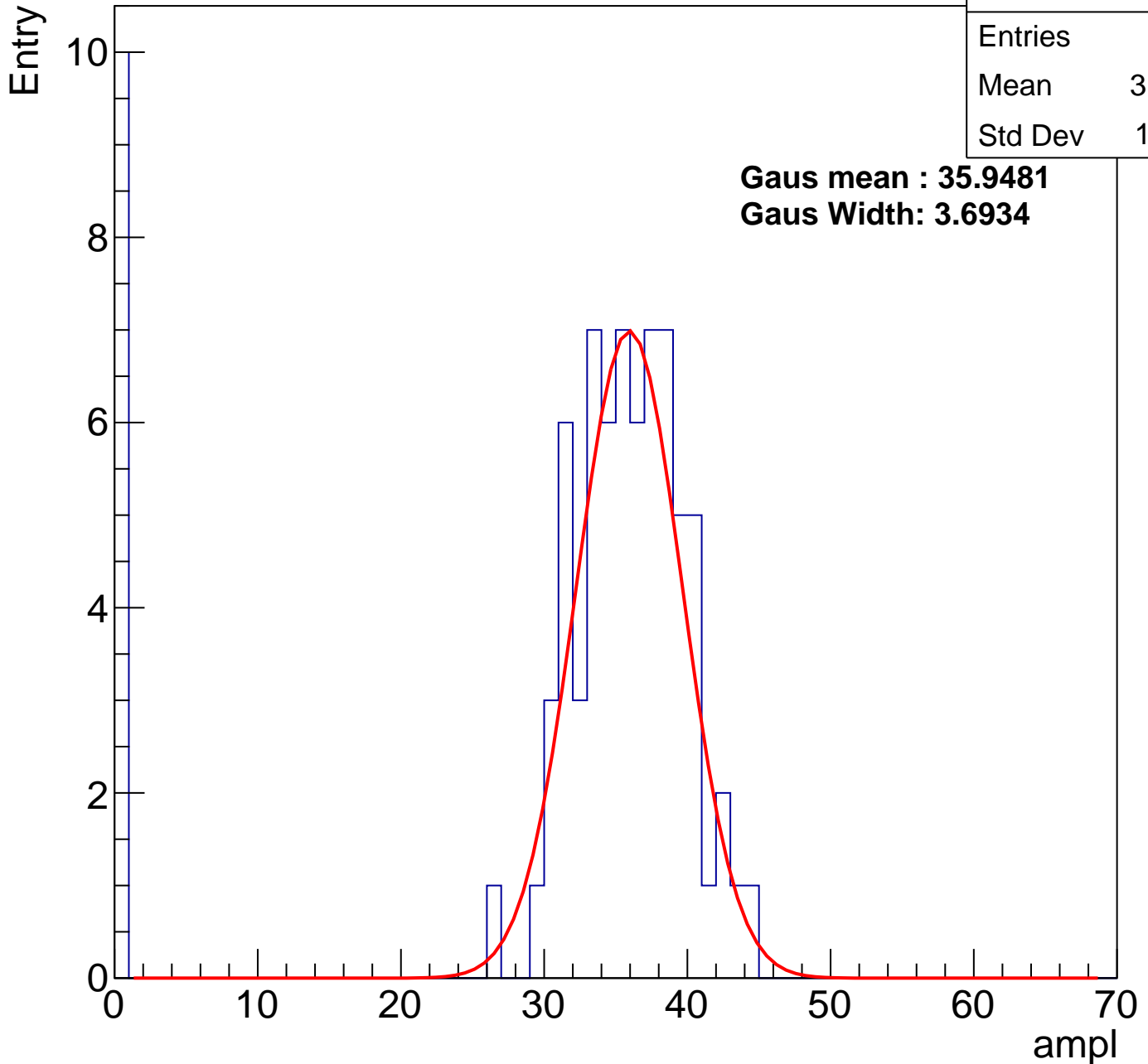
B1L103S, U18-ch24, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	31.08
Std Dev	12.31

Gaus mean : 35.9481

Gaus Width: 3.6934



B1L103S, U18-ch24, adc2

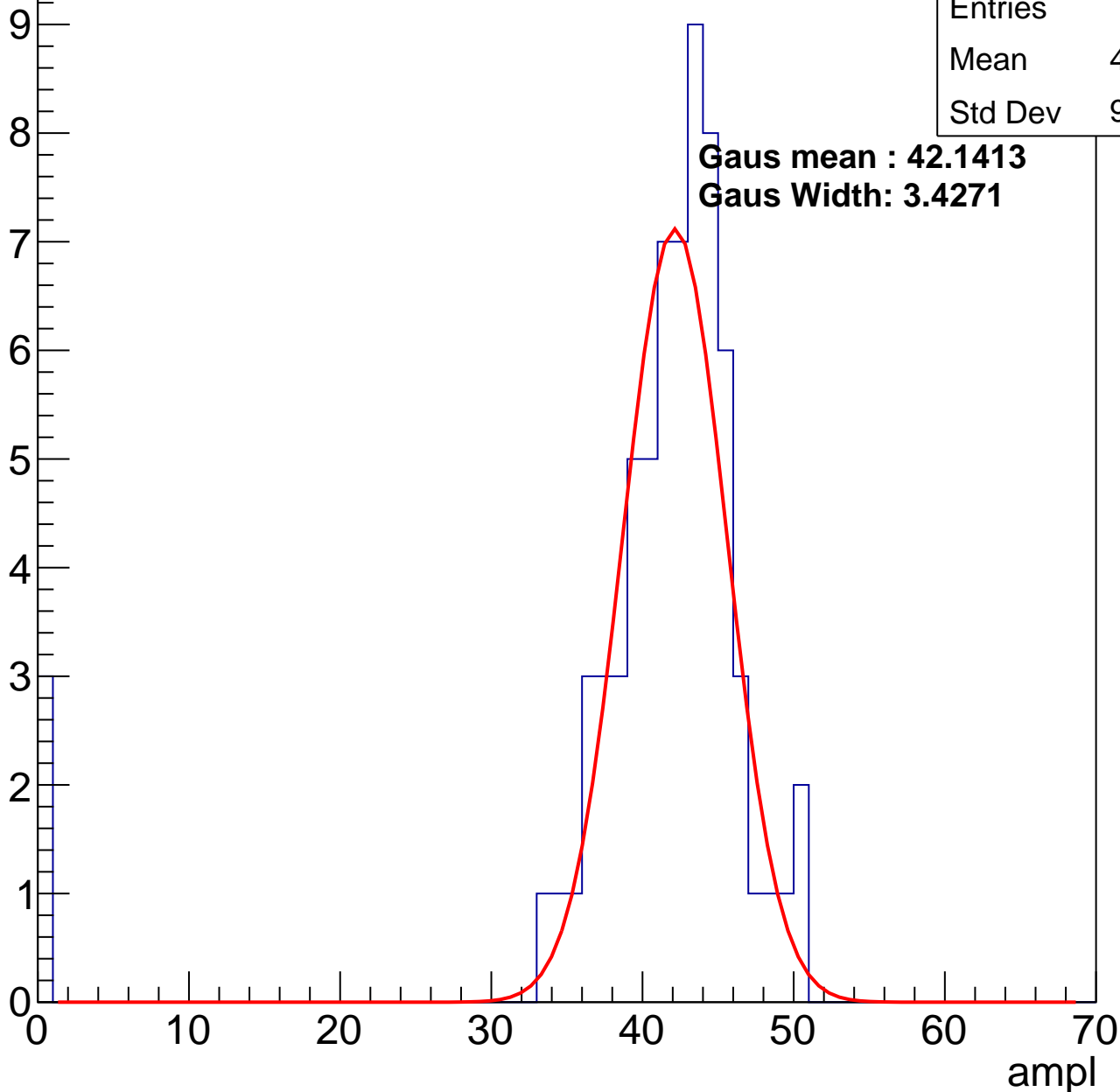
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	40.03
Std Dev	9.179

Gaus mean : 42.1413

Gaus Width: 3.4271

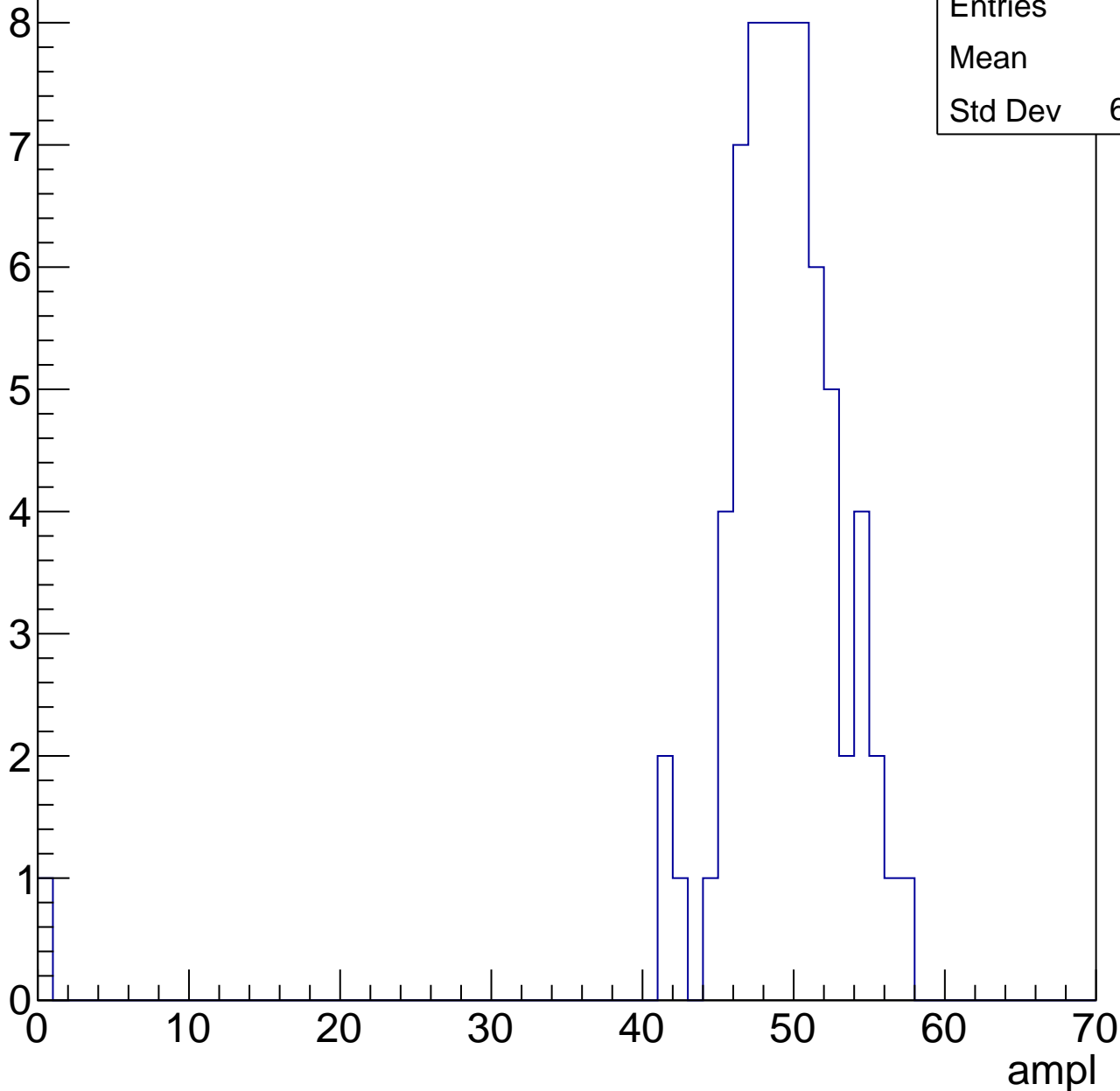


B1L103S, U18-ch24, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	48.3
Std Dev	6.736

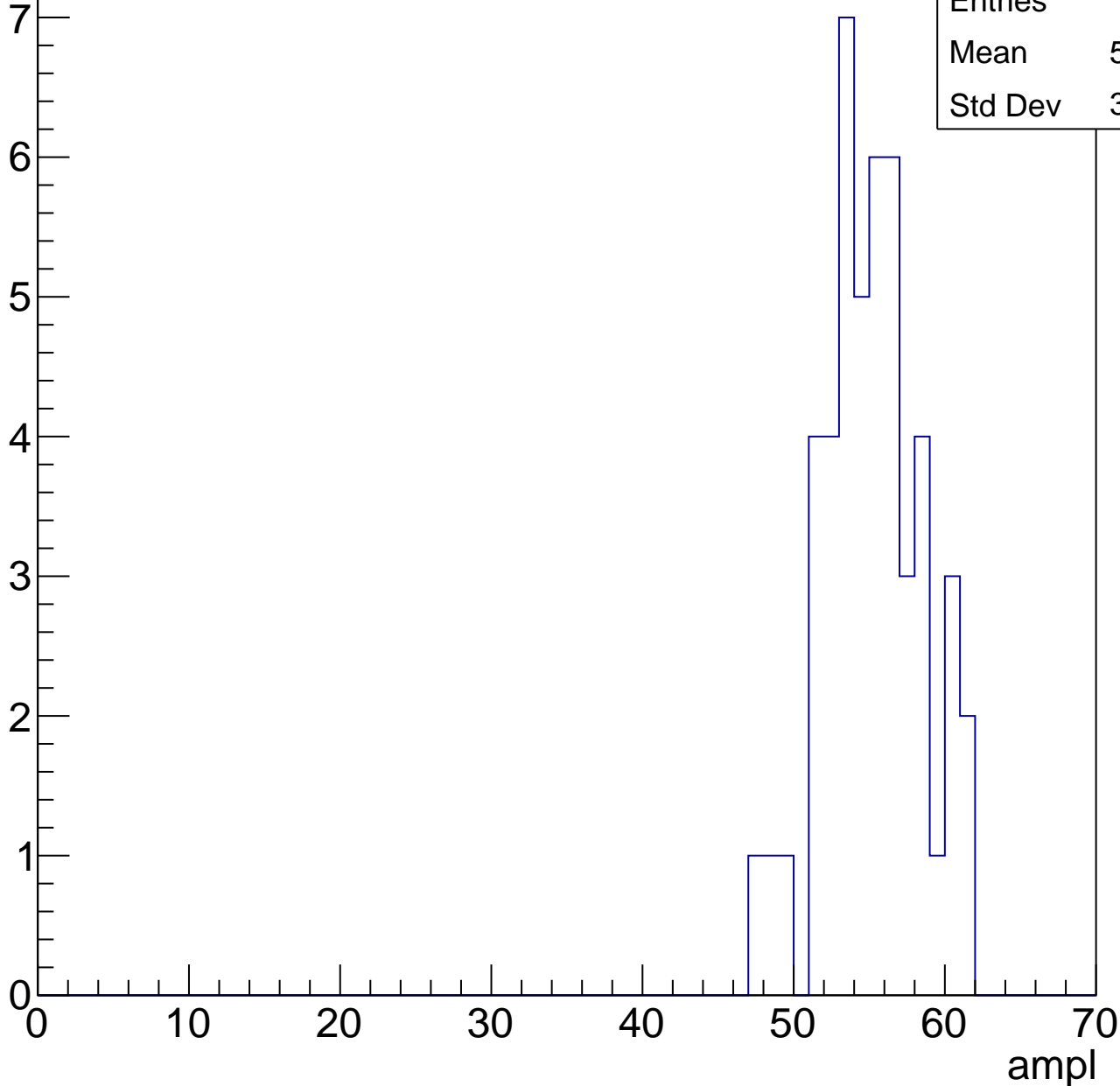


B1L103S, U18-ch24, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

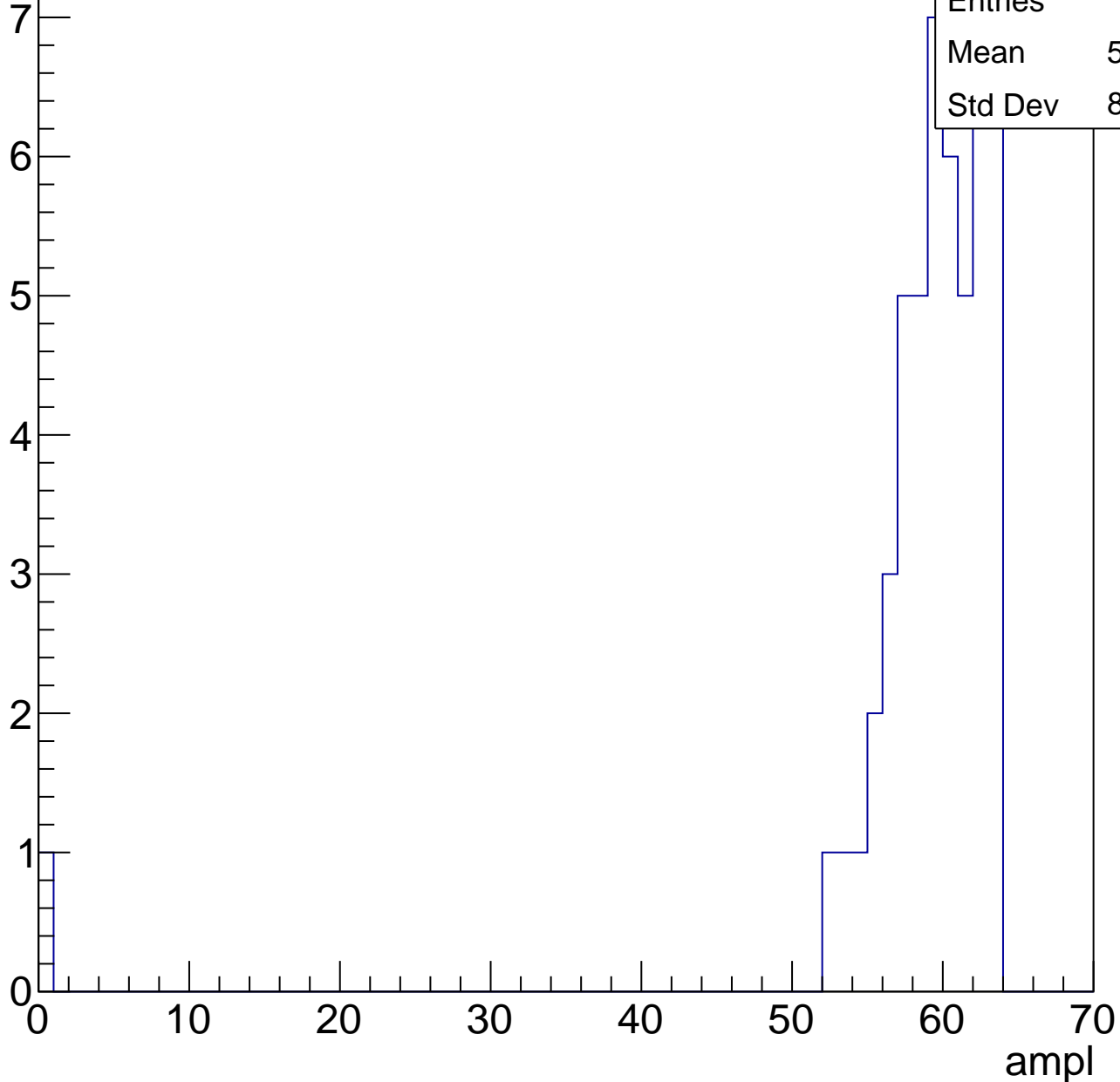
Entries	48
Mean	54.73
Std Dev	3.213



B1L103S, U18-ch24, adc5

calib_packv5_041523_1651.root, FC#0, port C2

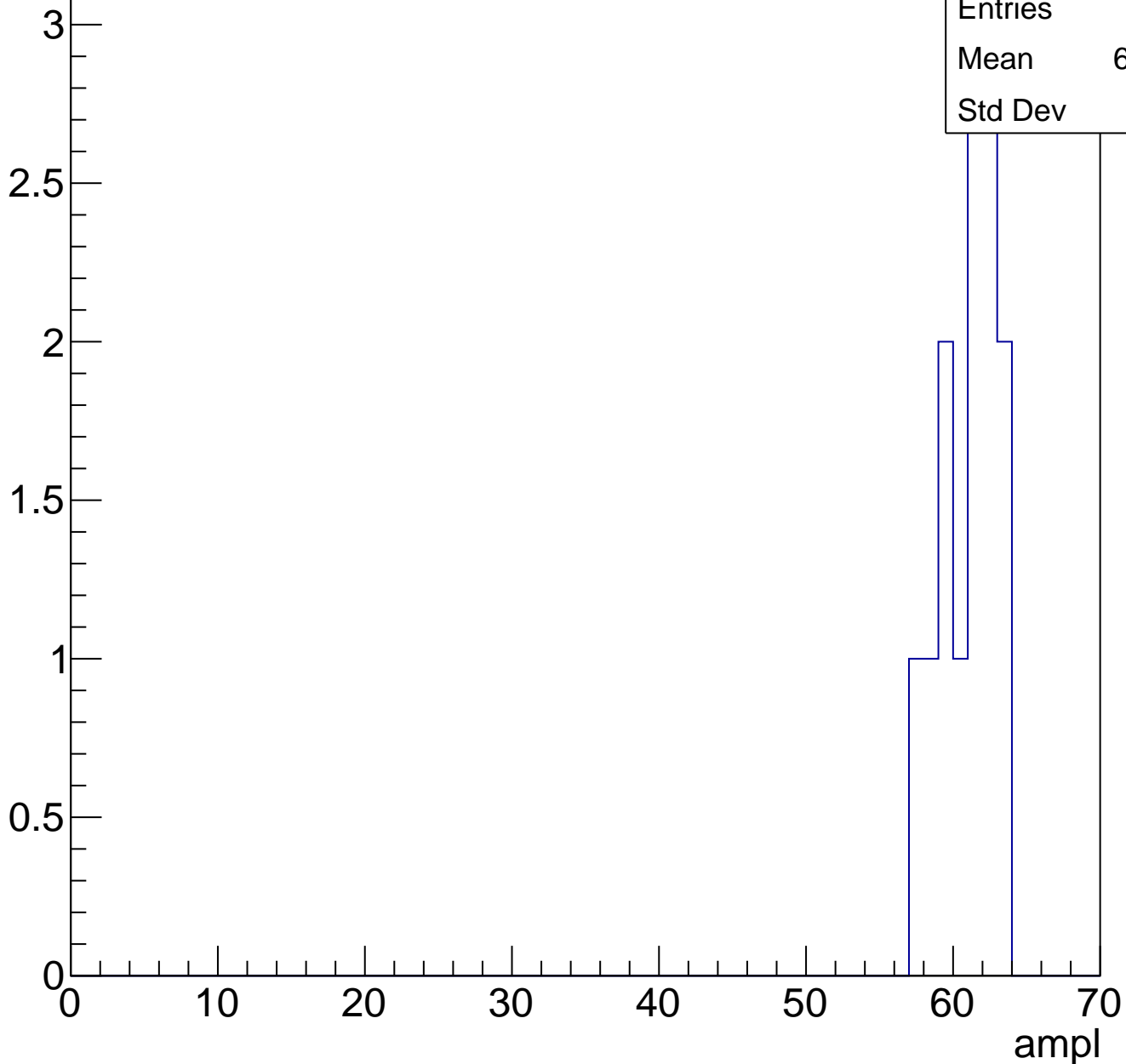
Entry



B1L103S, U18-ch24, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch24, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U18-ch25, adc0

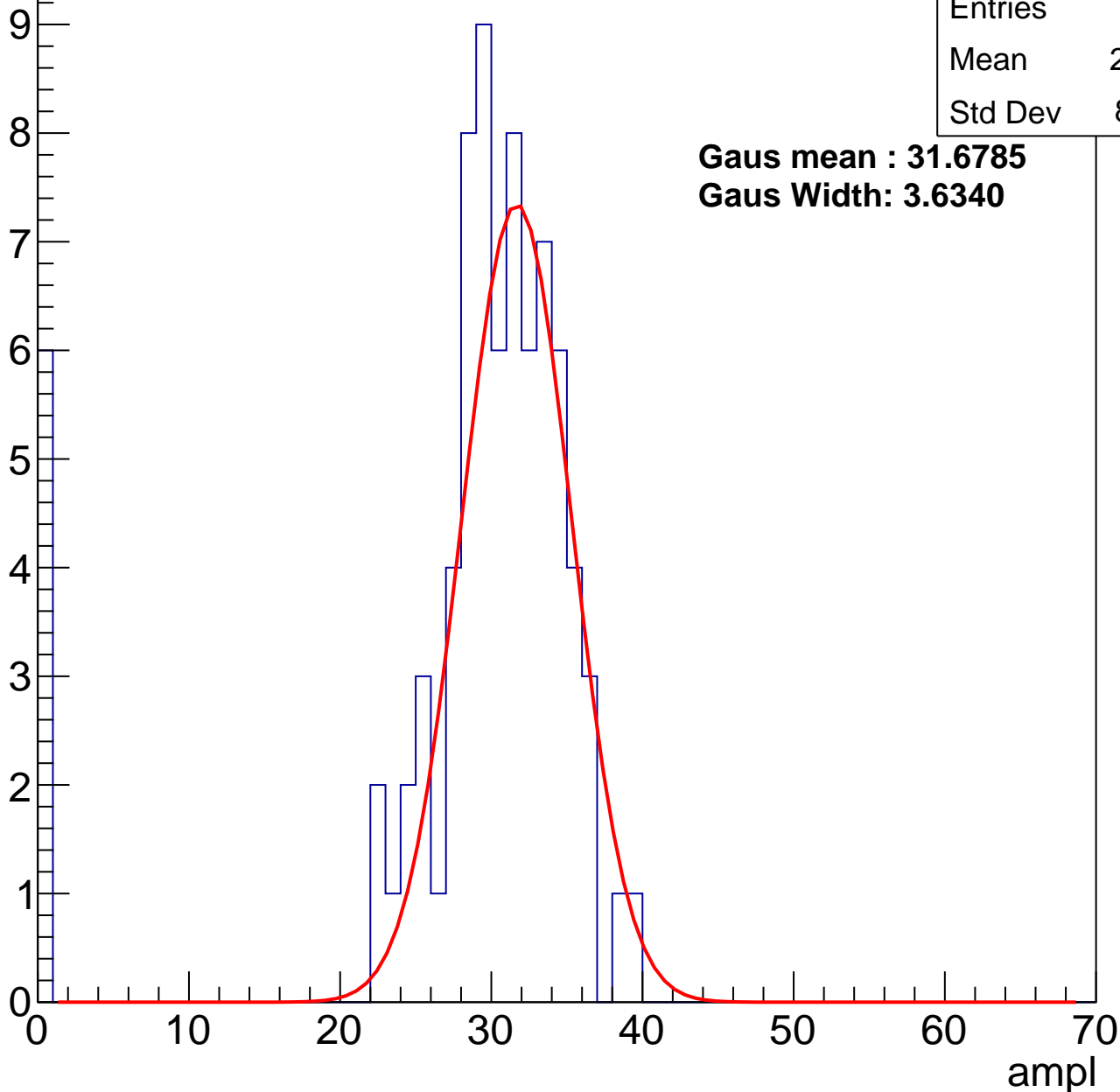
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	28.06
Std Dev	8.821

Gaus mean : 31.6785

Gaus Width: 3.6340



B1L103S, U18-ch25, adc1

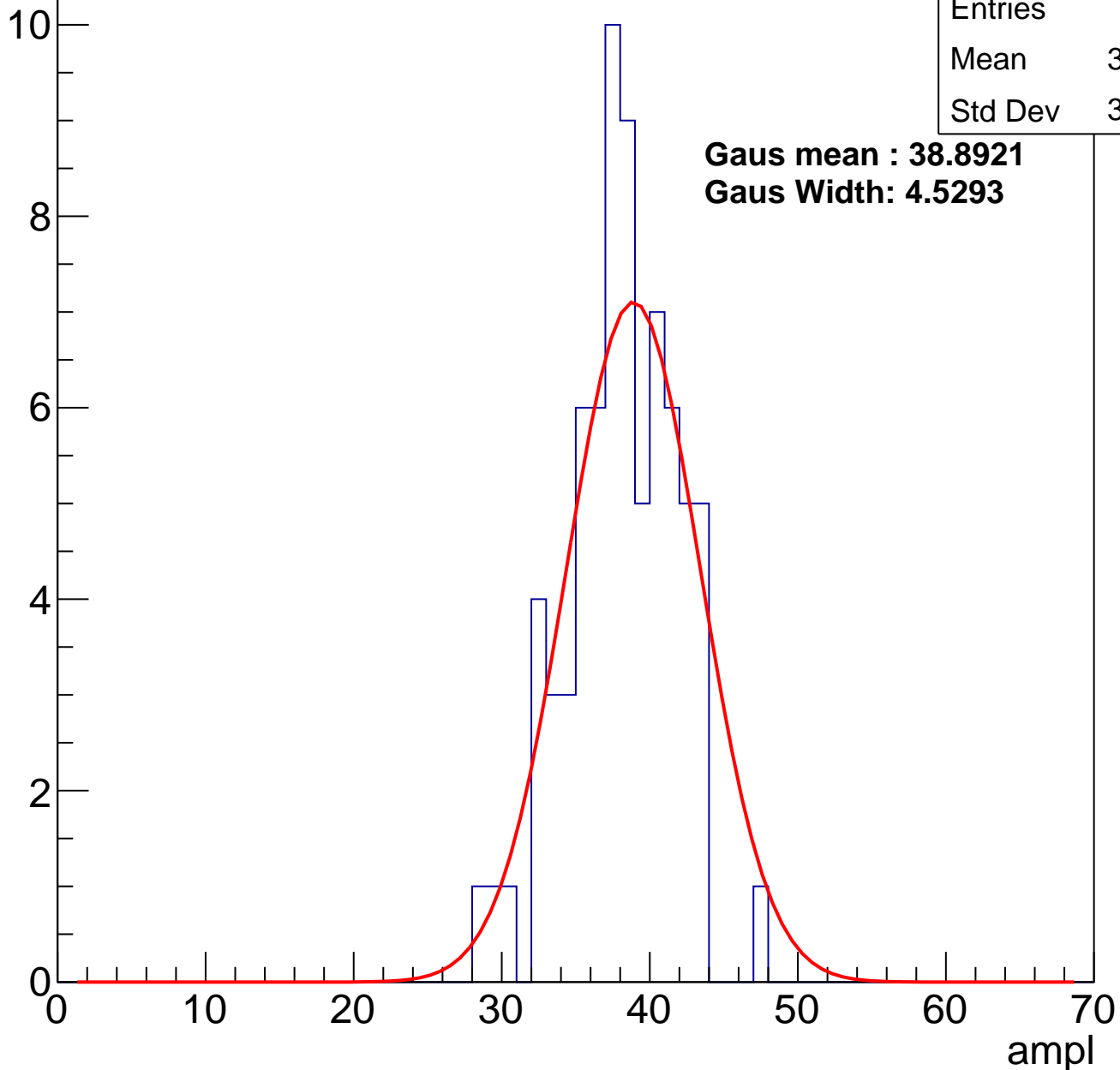
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	37.63
Std Dev	3.632

Gaus mean : 38.8921

Gaus Width: 4.5293

Entry



B1L103S, U18-ch25, adc2

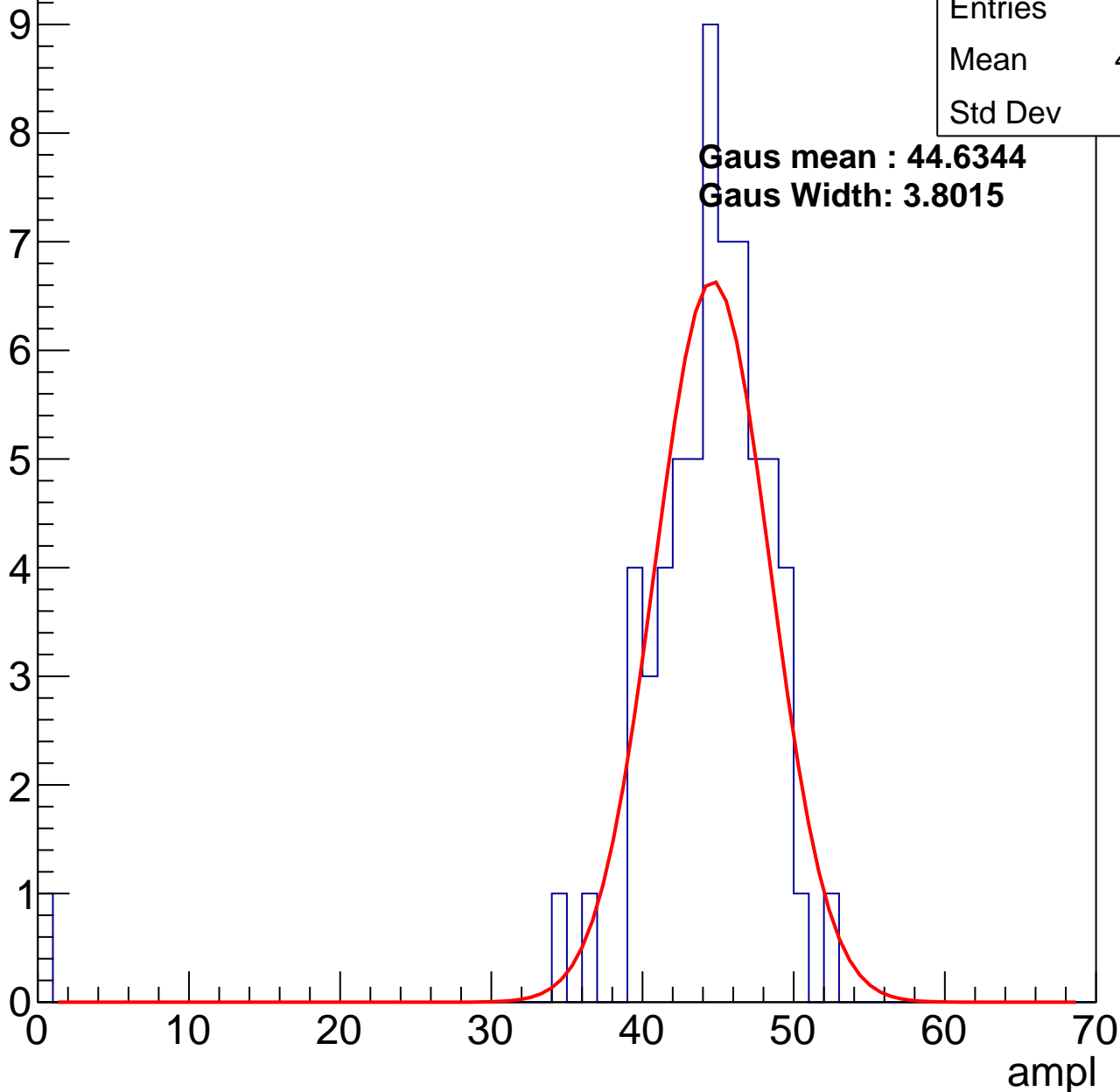
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	43.51
Std Dev	6.49

Gaus mean : 44.6344

Gaus Width: 3.8015



B1L103S, U18-ch25, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	43.87
Std Dev	17.9

Entry

10

8

6

4

2

0

0

10

20

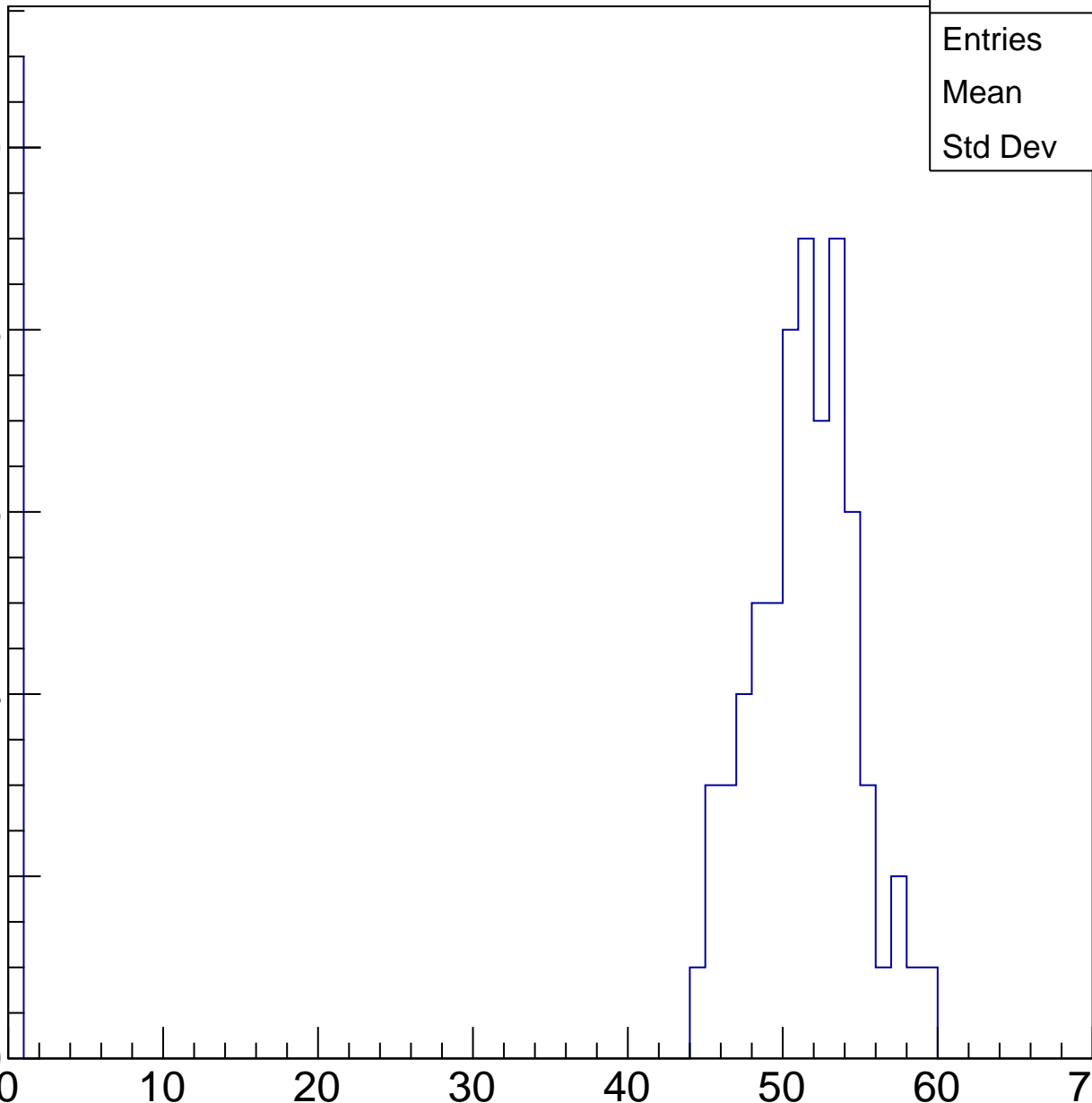
30

40

50

60

ampl

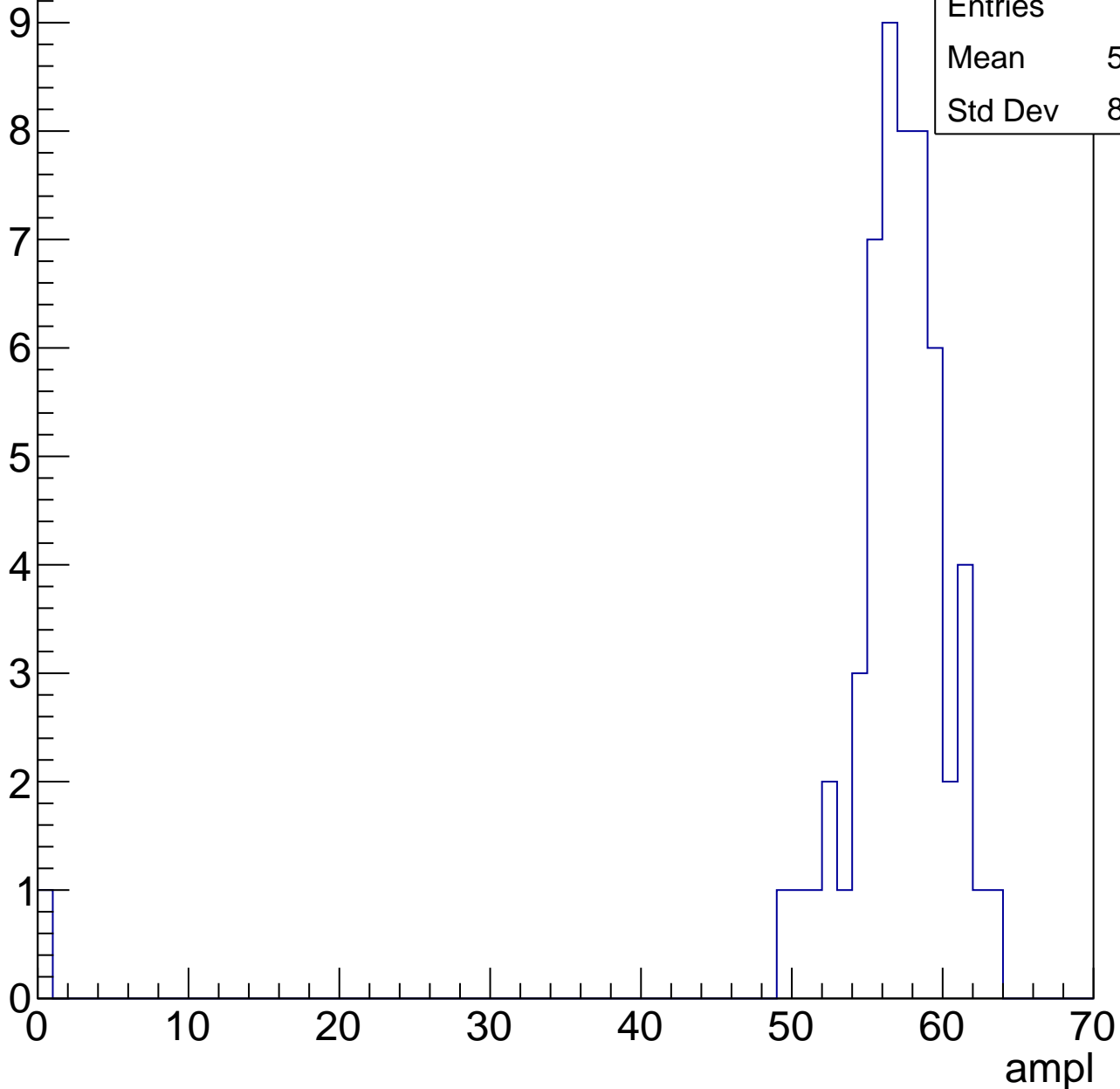


B1L103S, U18-ch25, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	55.73
Std Dev	8.032

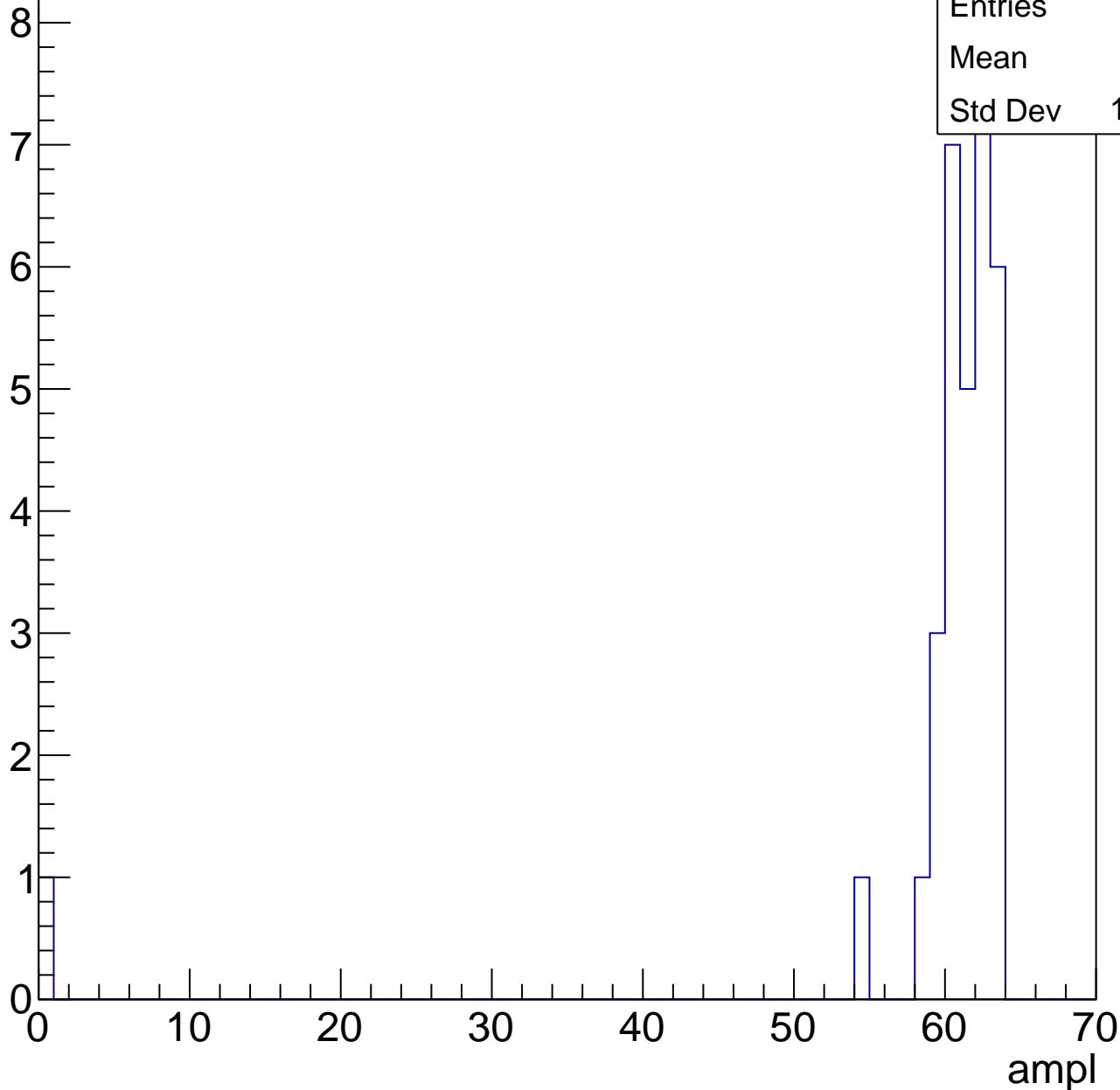


B1L103S, U18-ch25, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	32
Mean	59
Std Dev	10.76



B1L103S, U18-ch25, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch25, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch26, adc0

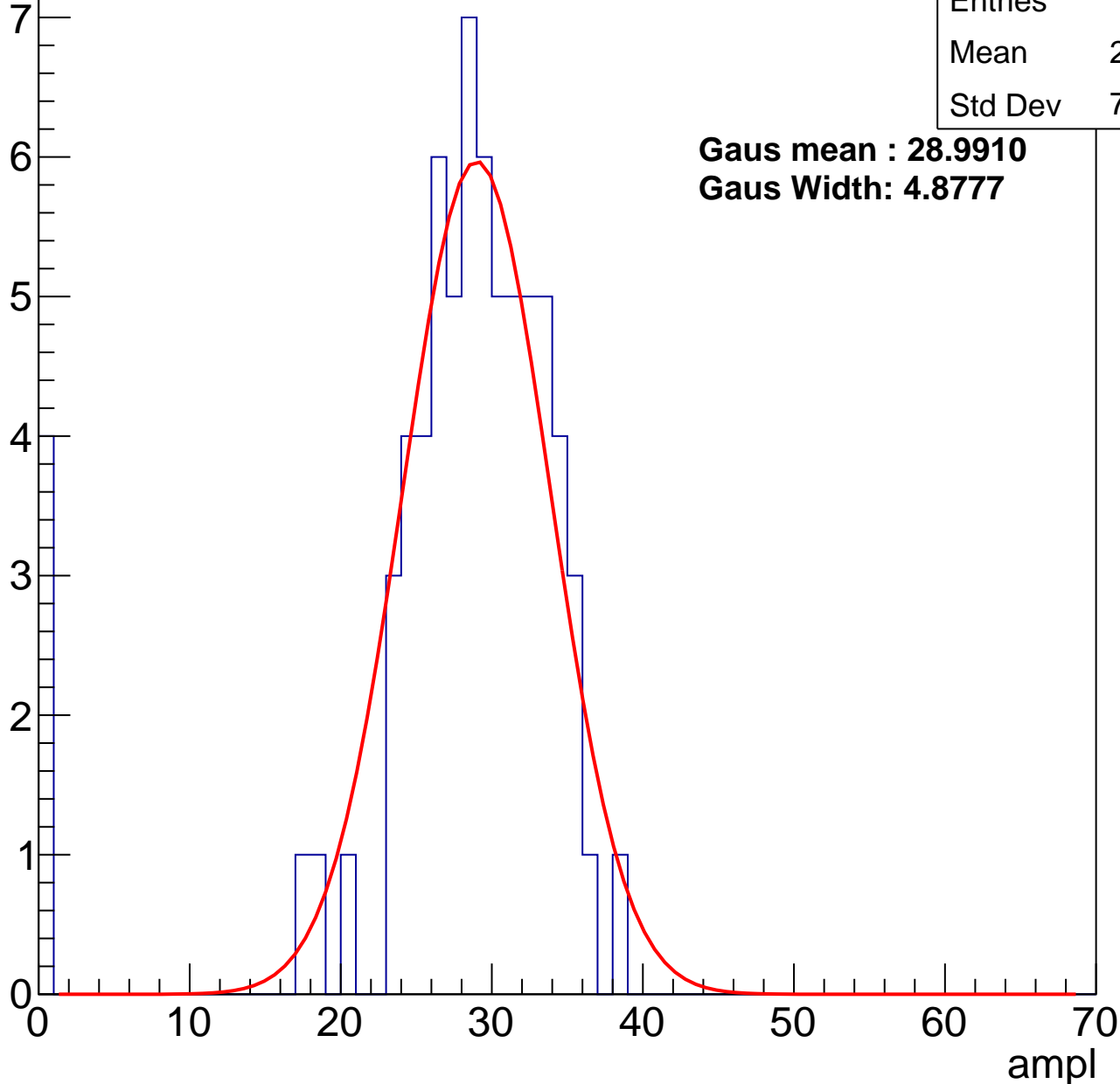
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	27.13
Std Dev	7.784

Gaus mean : 28.9910

Gaus Width: 4.8777



B1L103S, U18-ch26, adc1

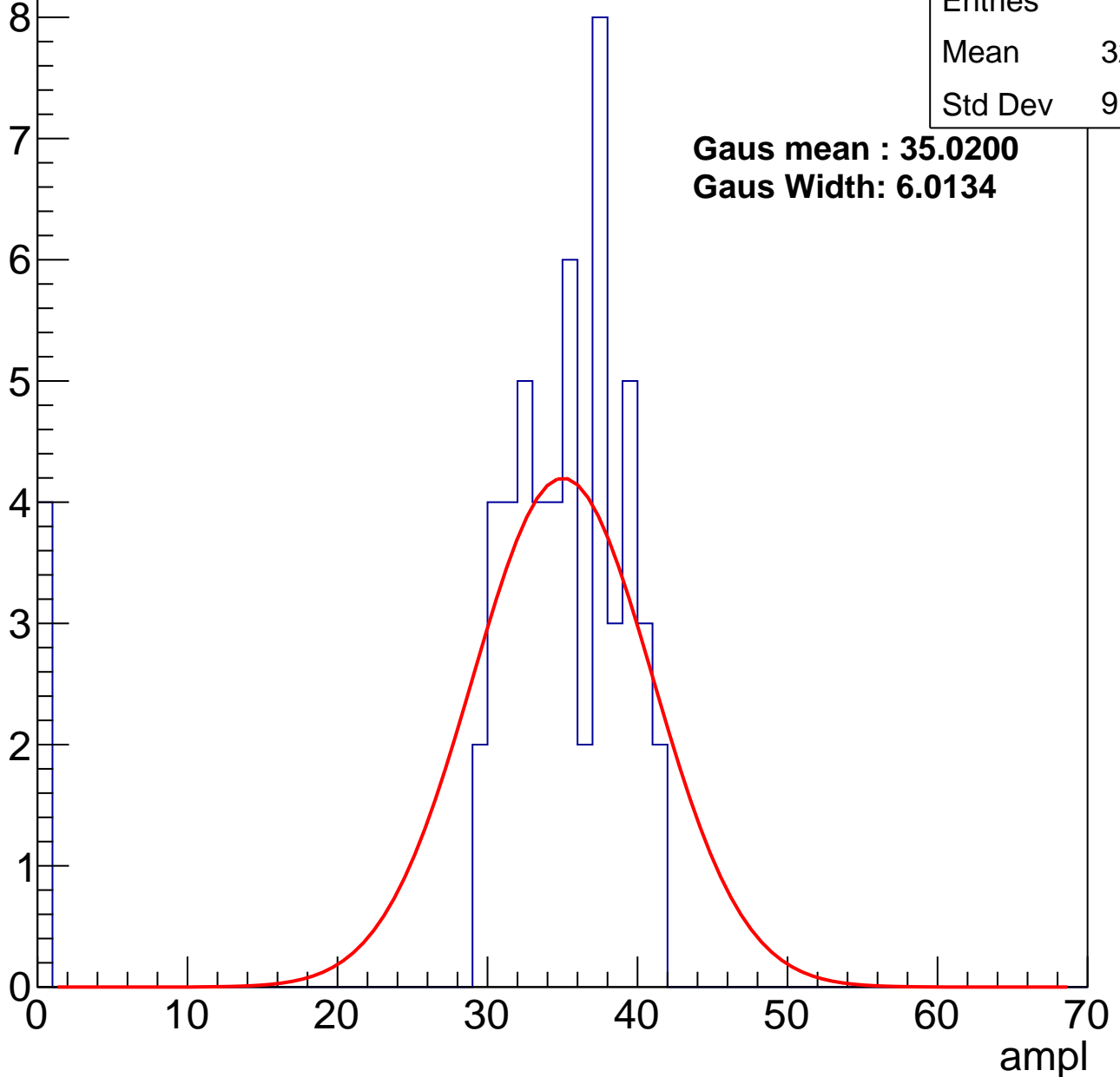
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	32.48
Std Dev	9.575

Gaus mean : 35.0200

Gaus Width: 6.0134



B1L103S, U18-ch26, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	37.95
Std Dev	11.57

Gaus mean : 42.4000

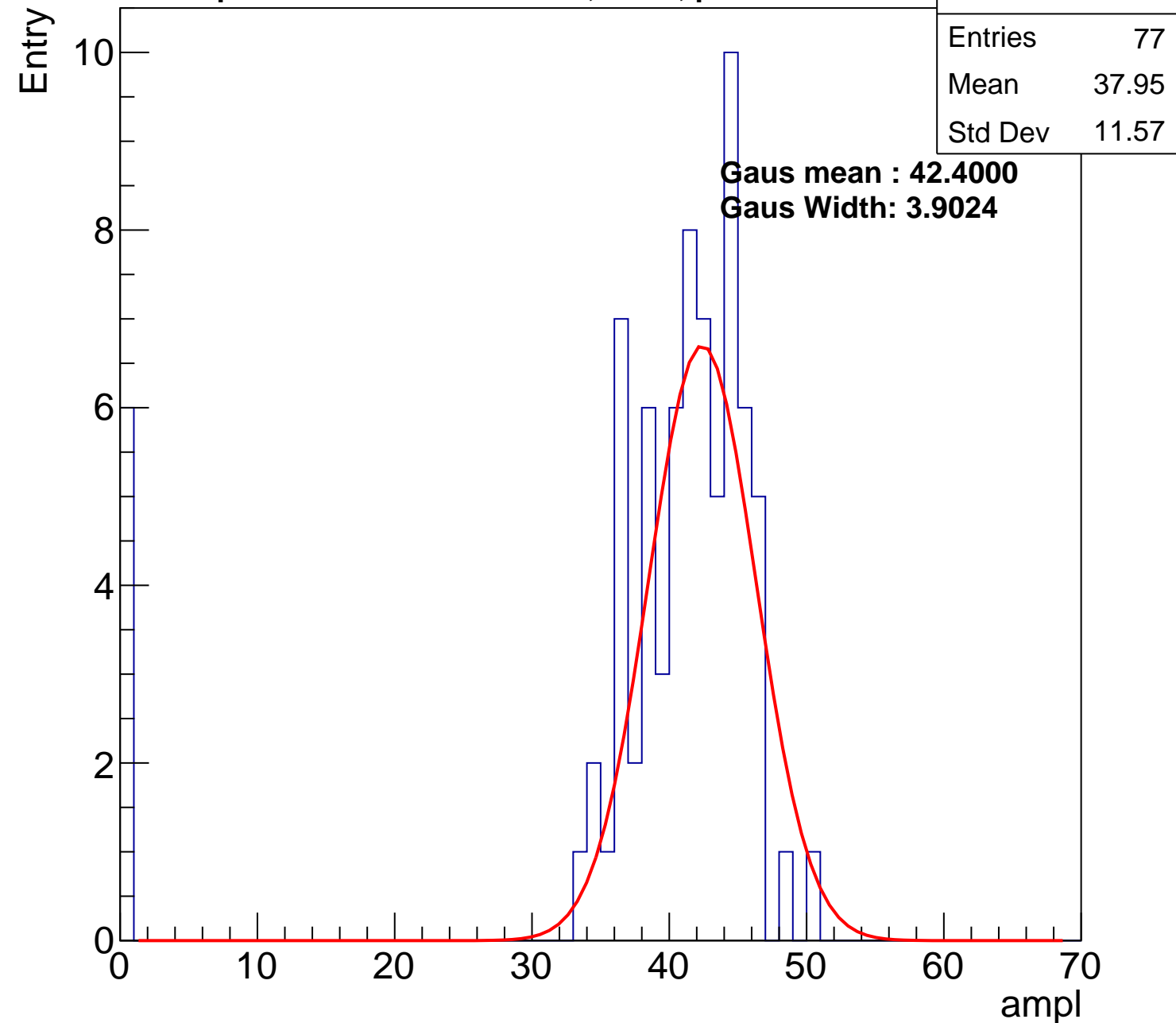
Gaus Width: 3.9024

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

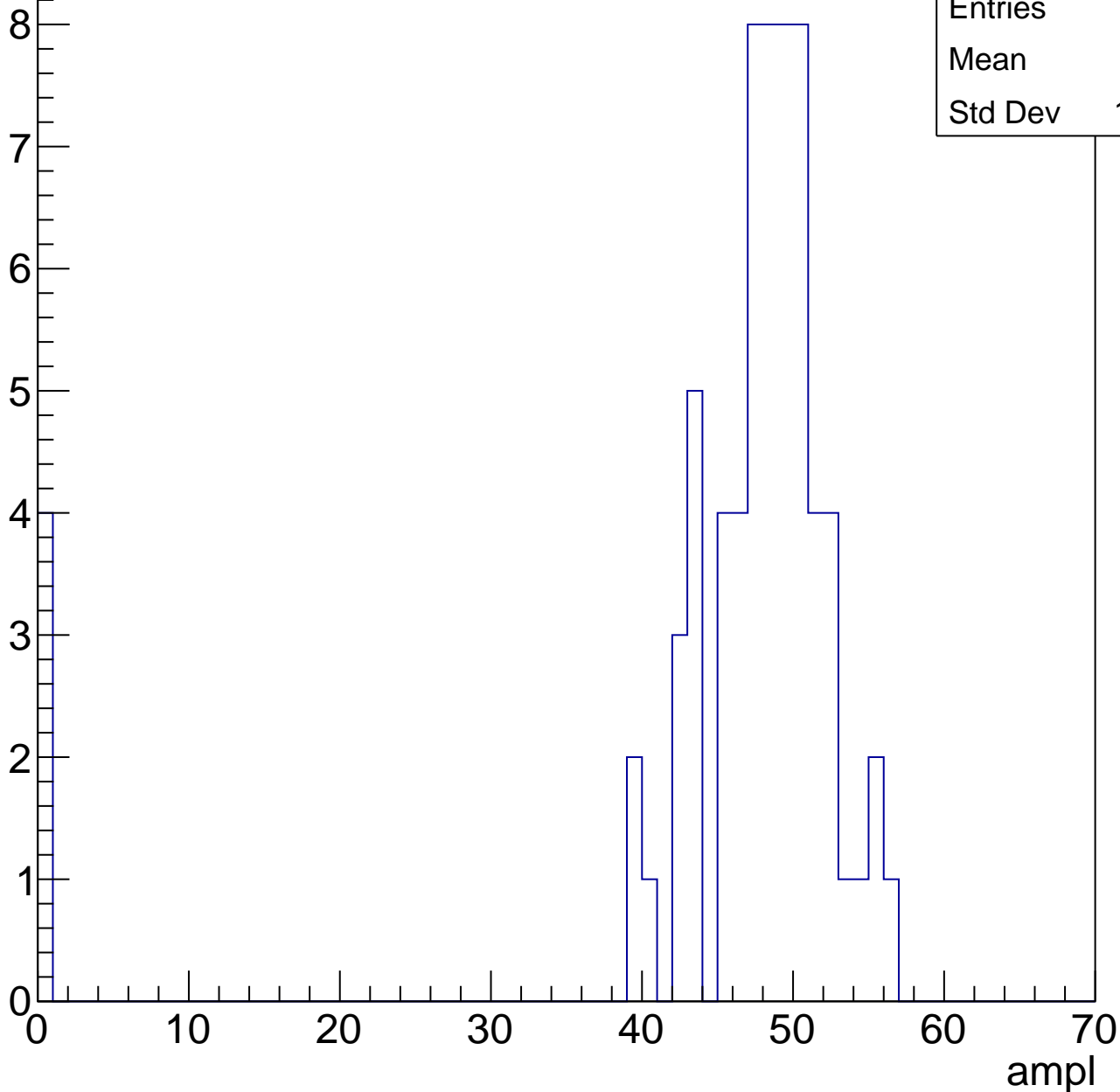


B1L103S, U18-ch26, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	45
Std Dev	11.81

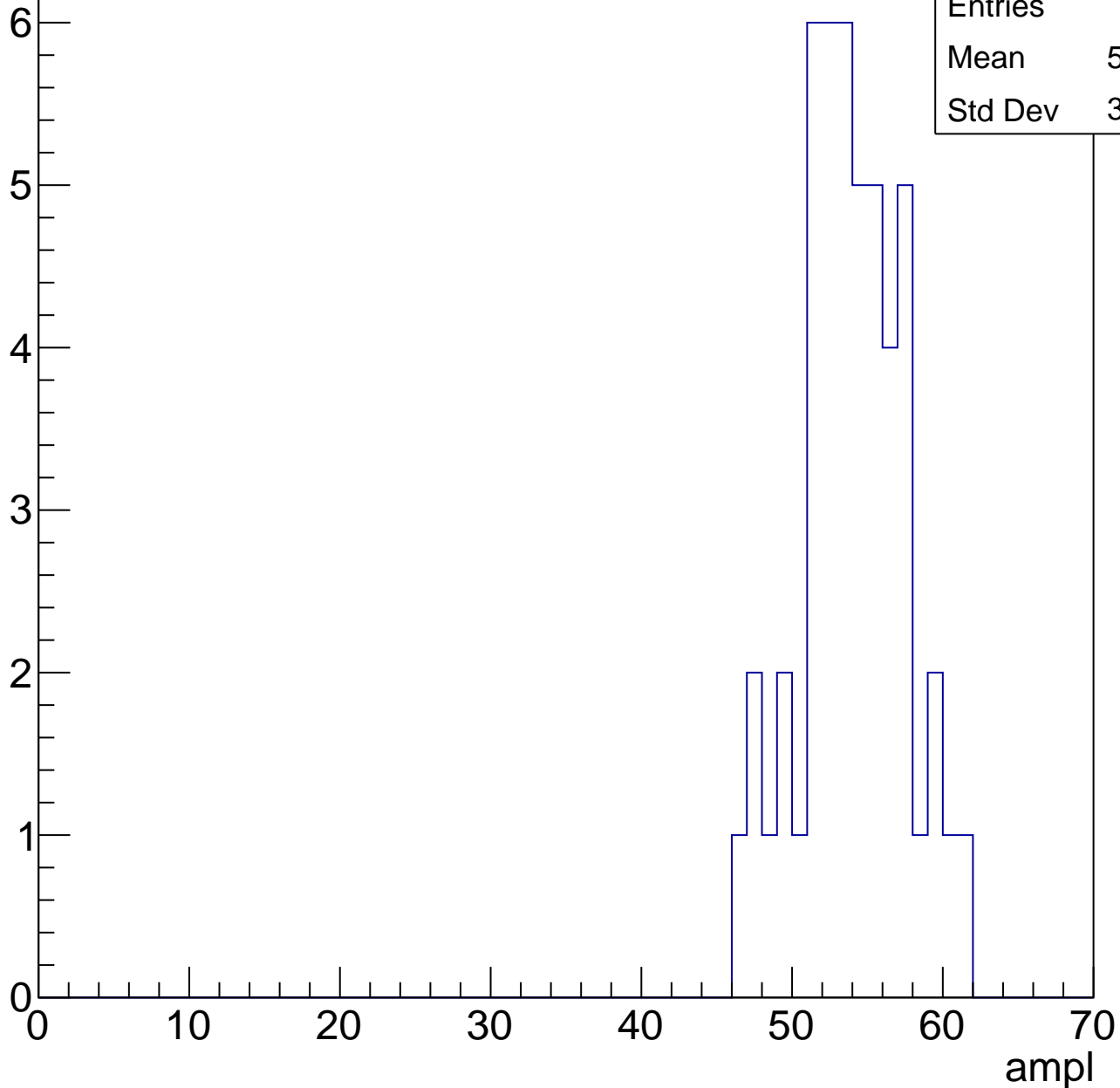


B1L103S, U18-ch26, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

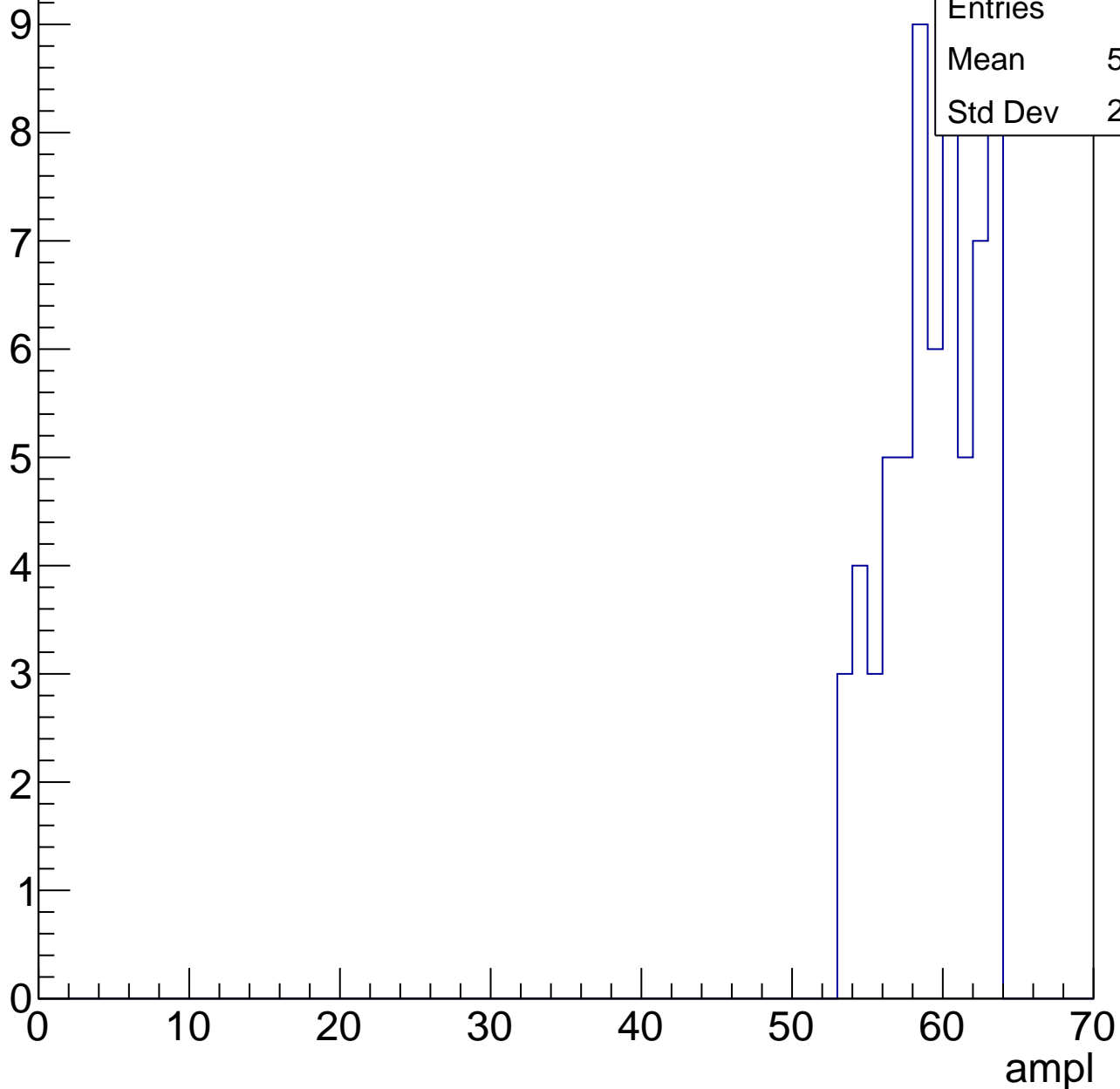
Entries	49
Mean	53.53
Std Dev	3.363



B1L103S, U18-ch26, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

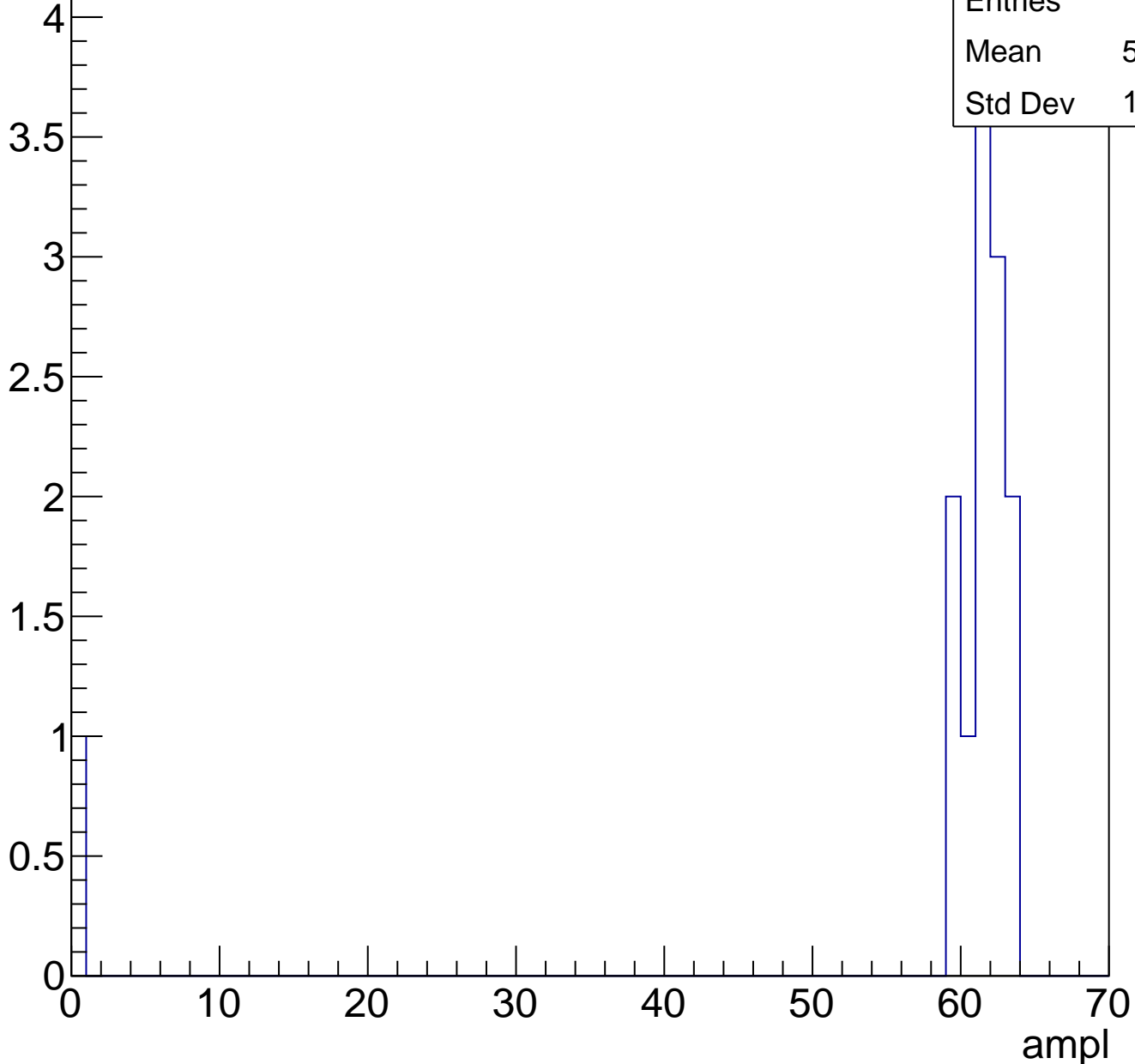


Entries	63
Mean	58.79
Std Dev	2.945

B1L103S, U18-ch26, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	13
Mean	56.46
Std Dev	16.35

B1L103S, U18-ch26, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch27, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	26.08
Std Dev	11.57

Gaus mean : 31.2287

Gaus Width: 3.6855

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

B1L103S, U18-ch27, adc1

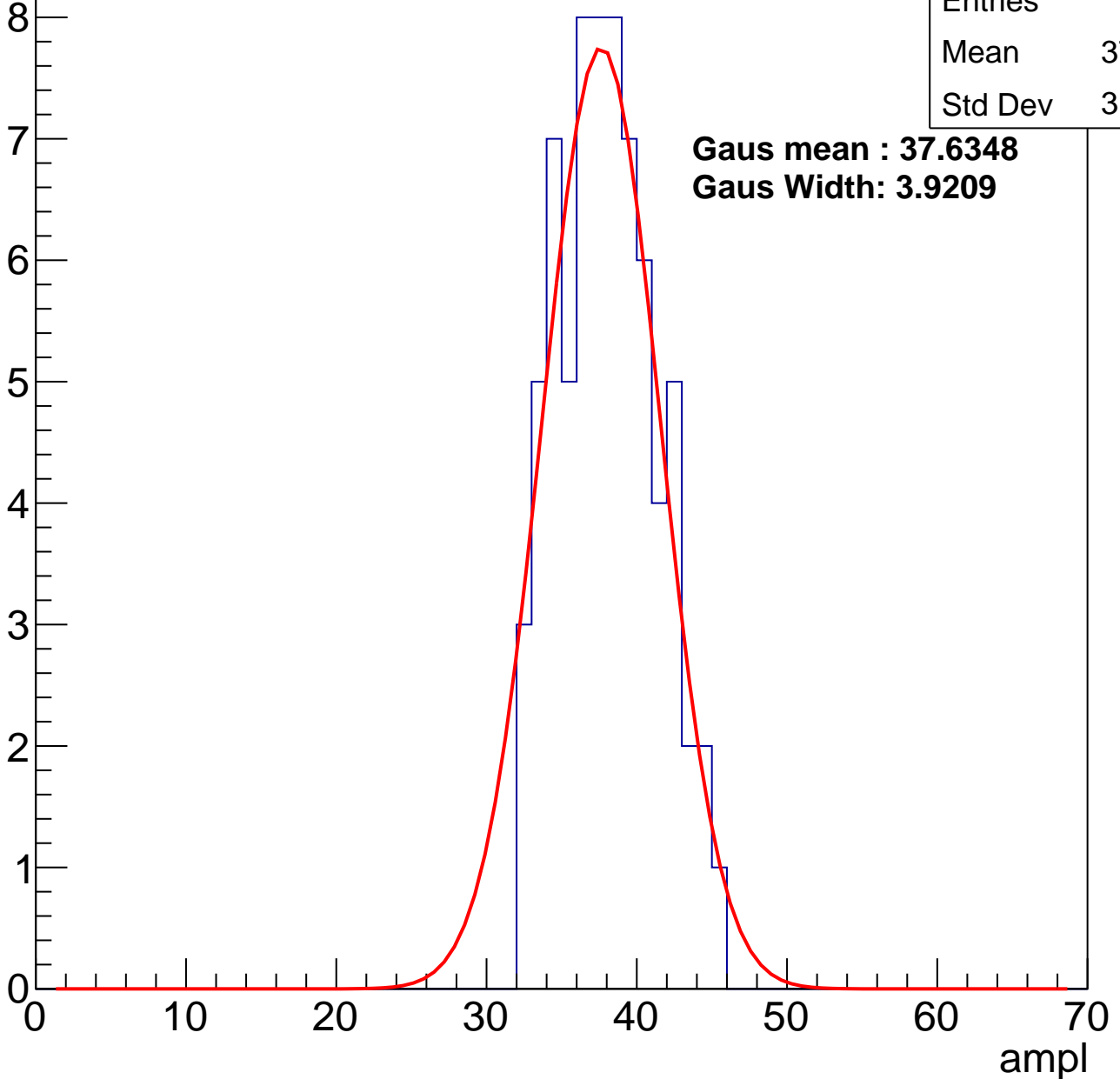
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	37.58
Std Dev	3.218

Gaus mean : 37.6348

Gaus Width: 3.9209



B1L103S, U18-ch27, adc2

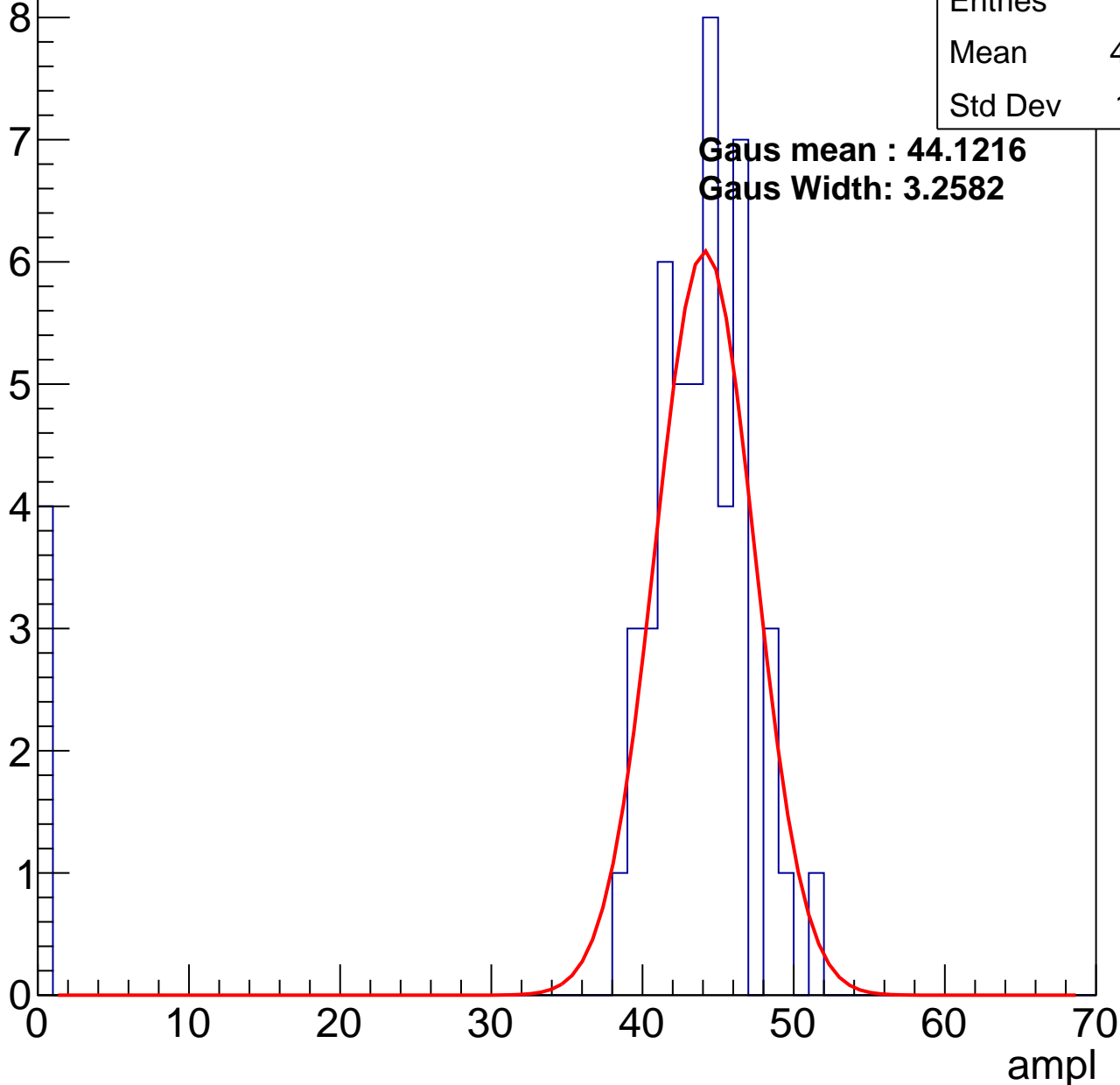
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	40.08
Std Dev	12.01

Gaus mean : 44.1216

Gaus Width: 3.2582



B1L103S, U18-ch27, adc3

calib_packv5_041523_1651.root, FC#0, port C2

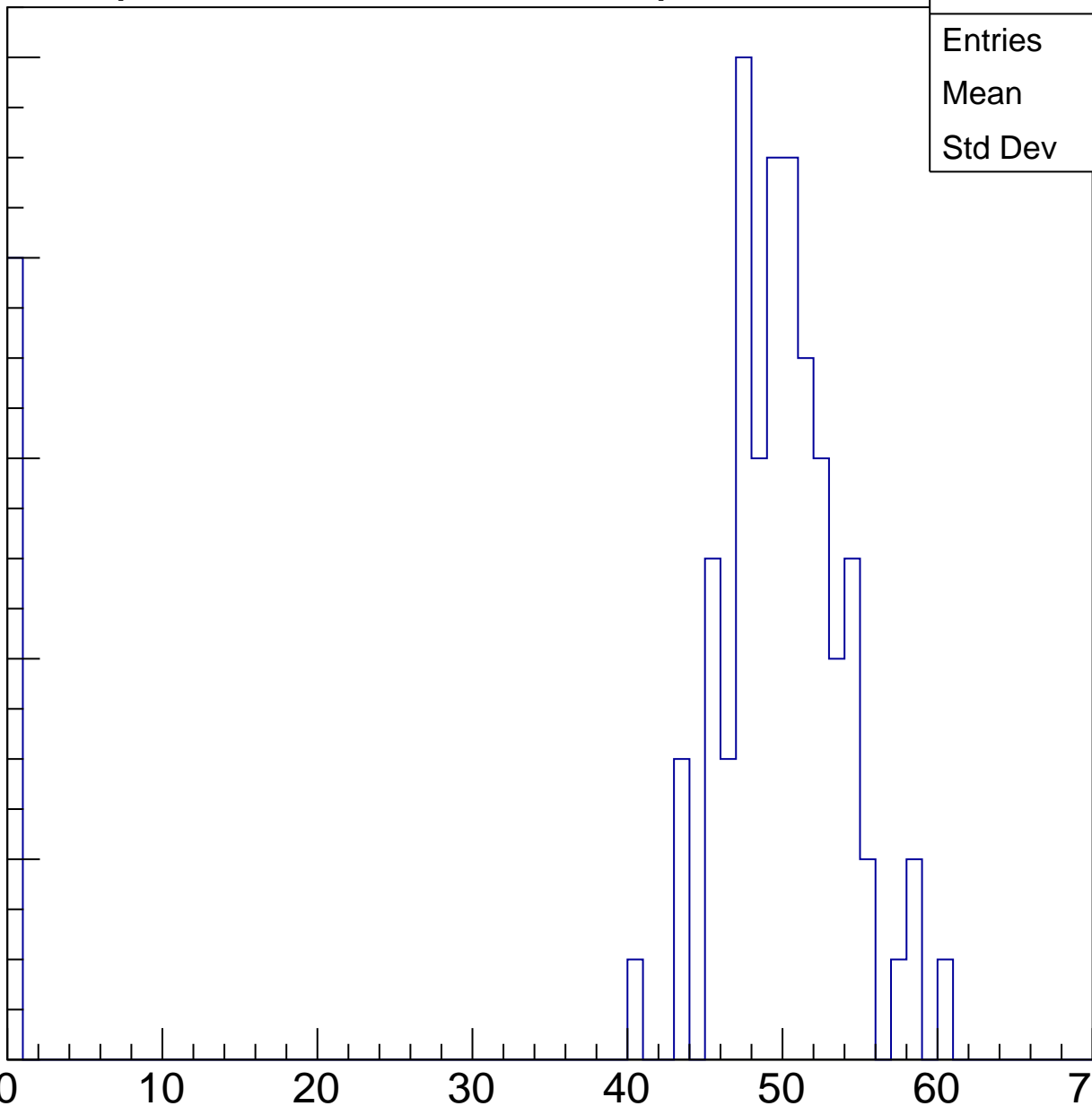
Entries	82
Mean	44.82
Std Dev	15.15

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

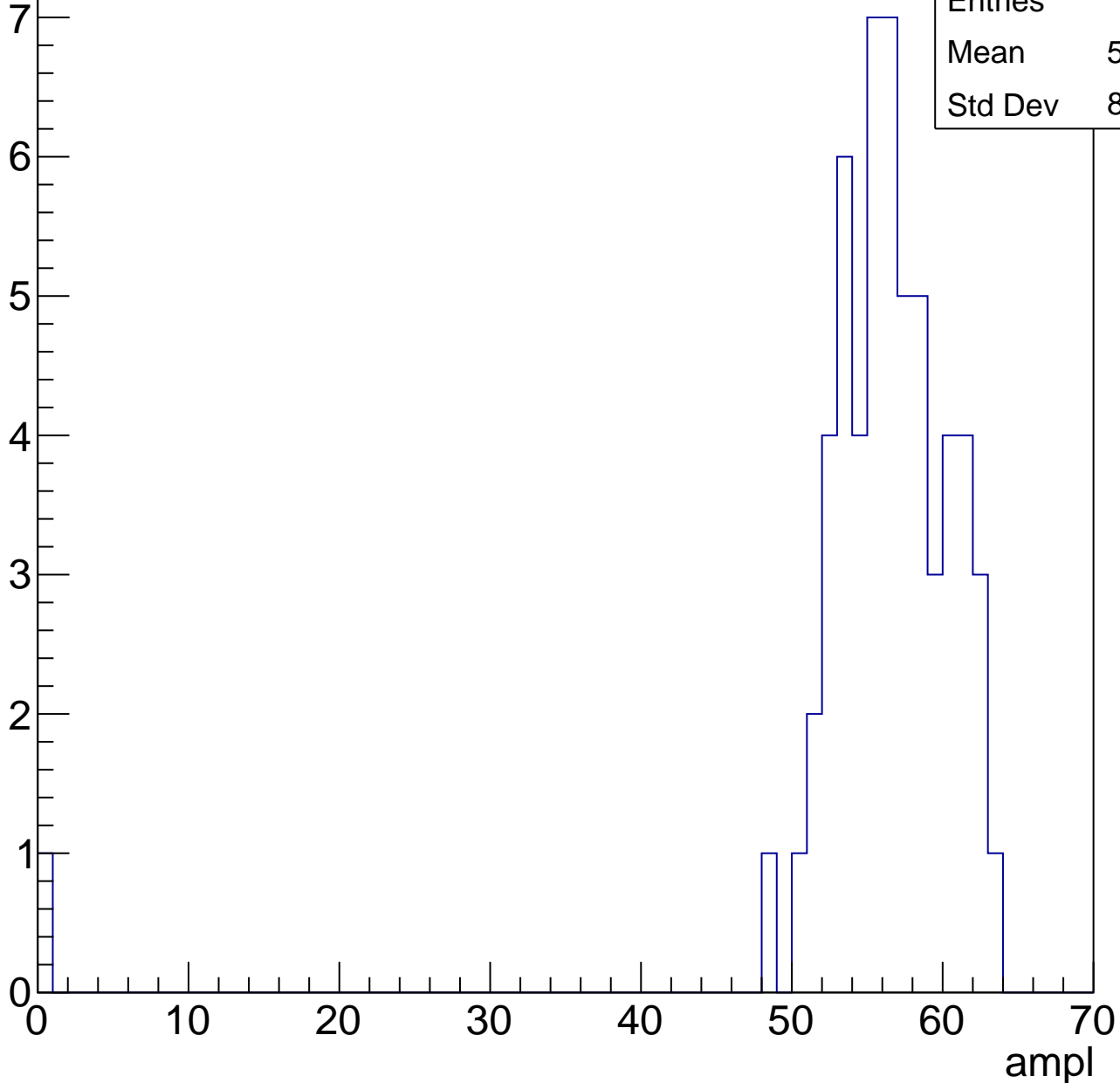


B1L103S, U18-ch27, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

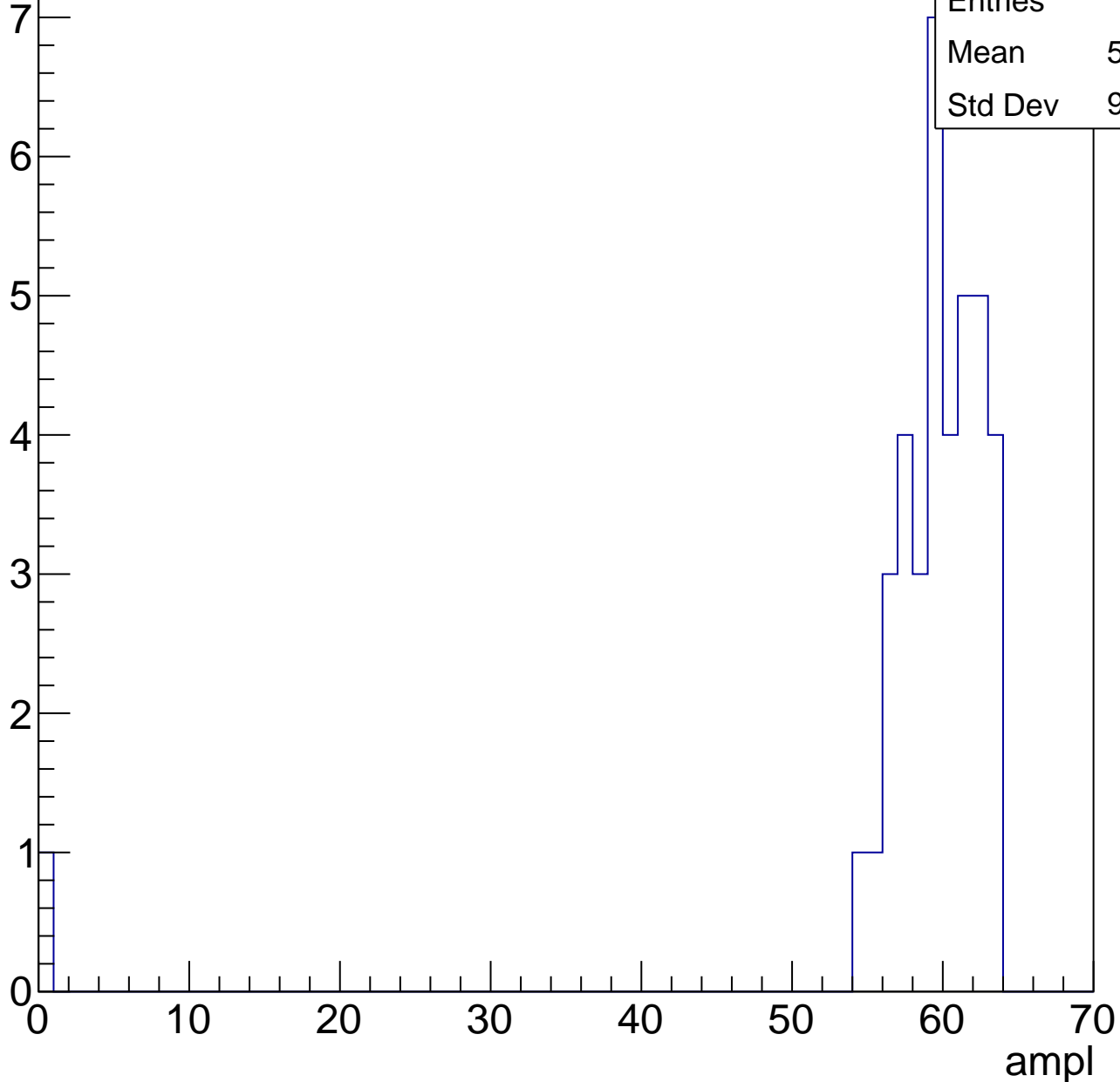
Entries	58
Mean	55.24
Std Dev	8.059



B1L103S, U18-ch27, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

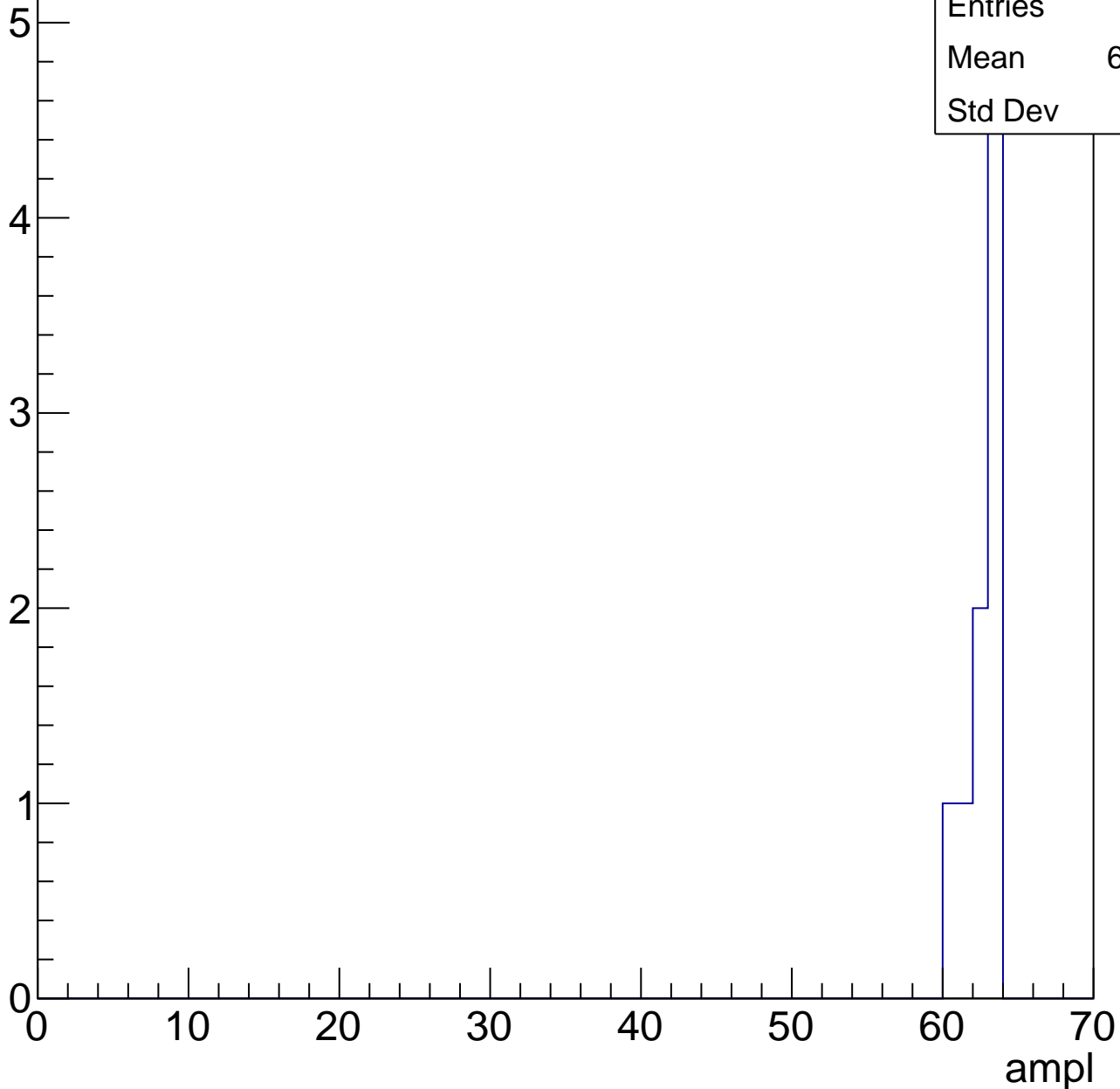


B1L103S, U18-ch27, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	9
Mean	62.22
Std Dev	1.03



B1L103S, U18-ch27, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch28, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	24.17
Std Dev	10.35

Gaus mean : 28.6358

Gaus Width: 4.2647

Entry

12

10

8

6

4

2

0

0

10

20

30

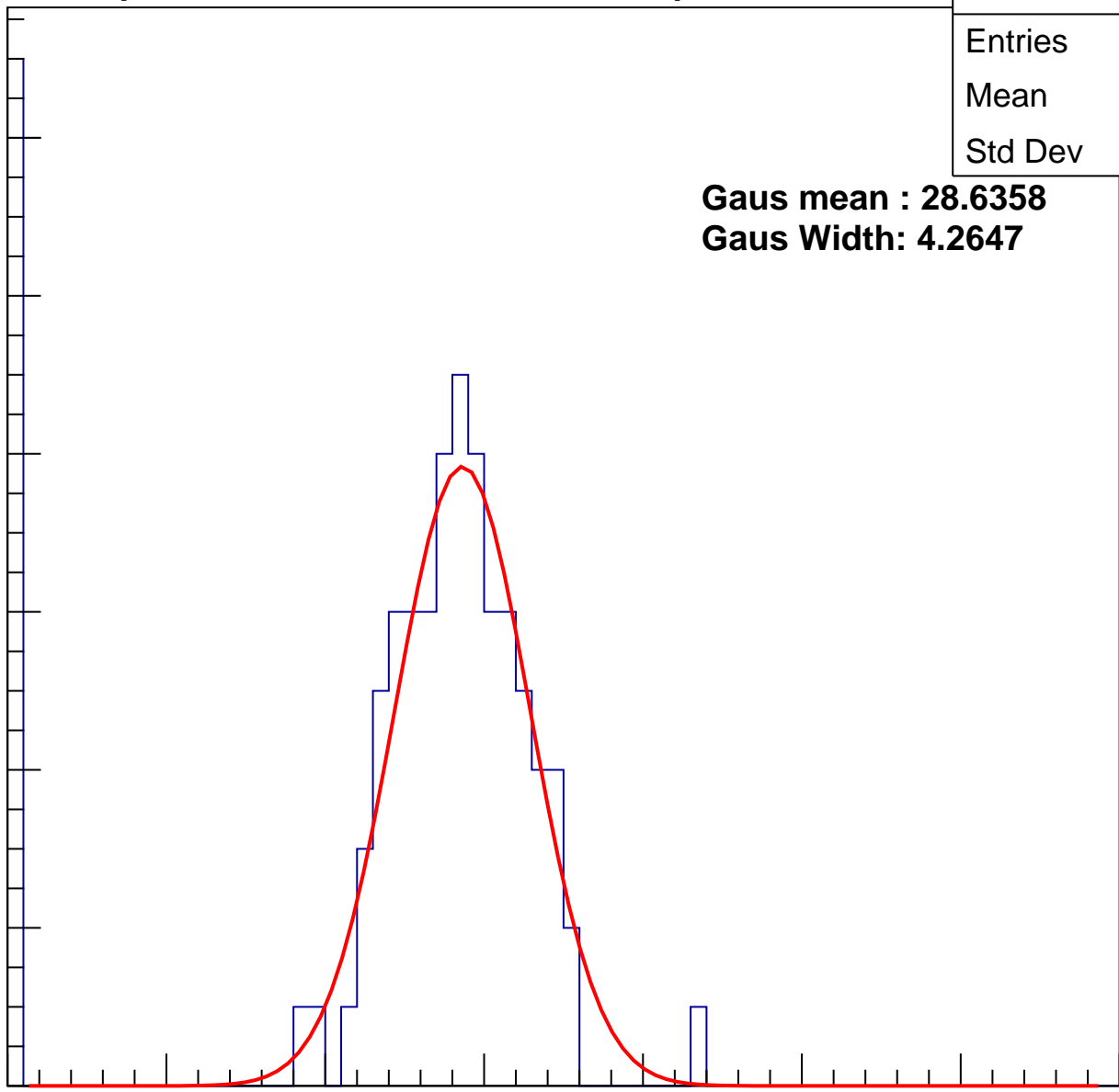
40

50

60

70

ampl



B1L103S, U18-ch28, adc1

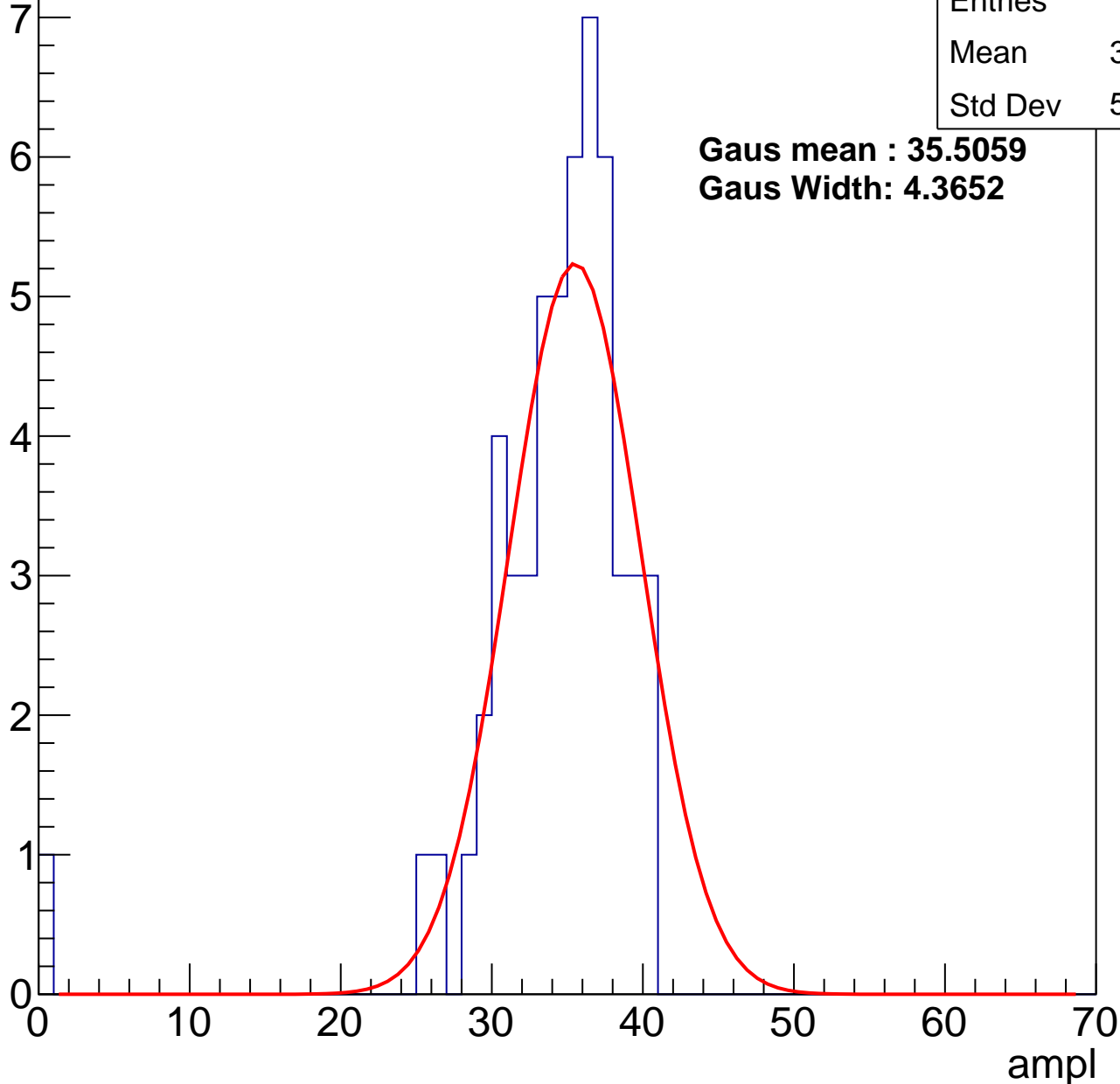
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	33.63
Std Dev	5.784

Gaus mean : 35.5059

Gaus Width: 4.3652



B1L103S, U18-ch28, adc2

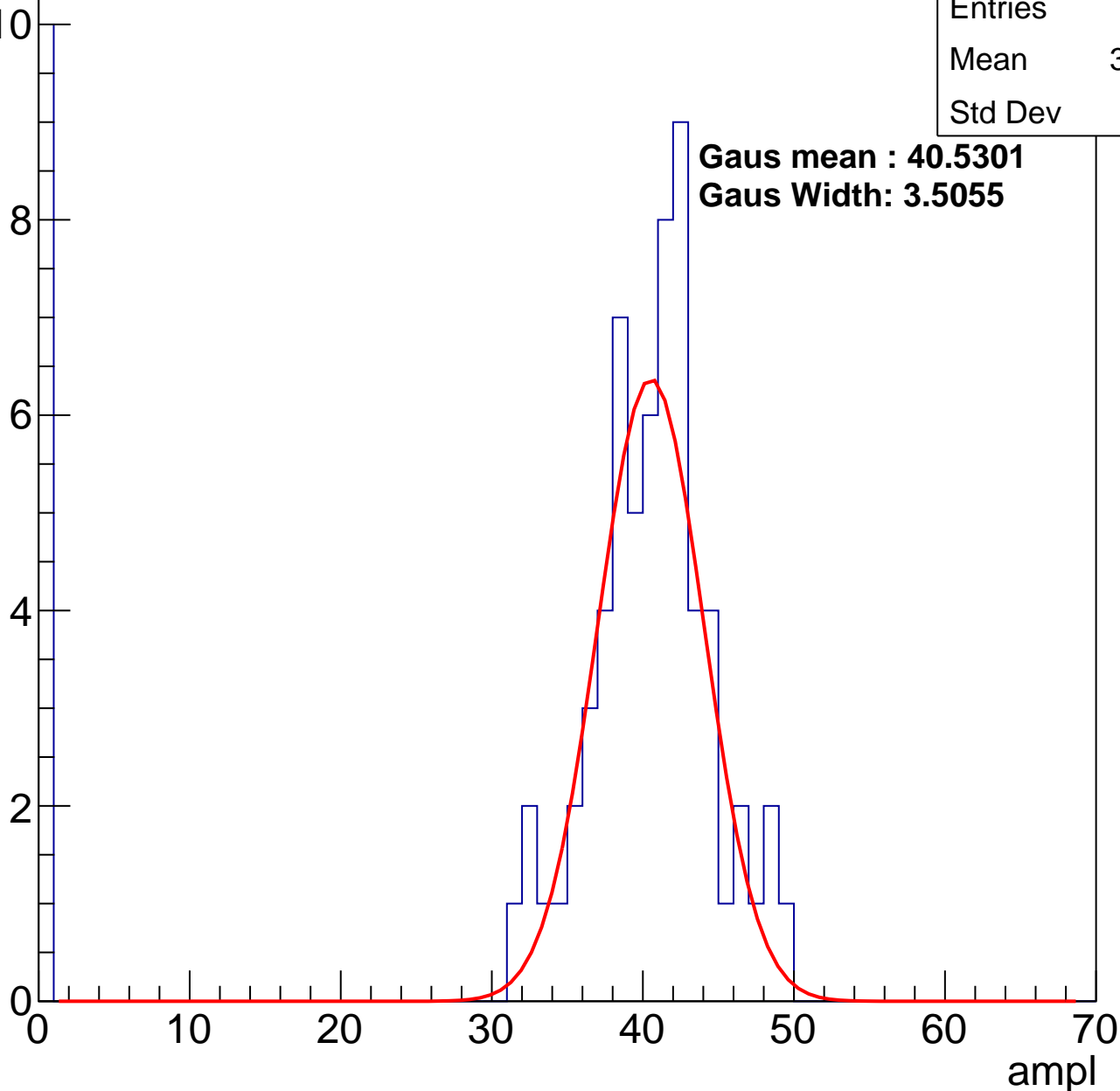
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	34.76
Std Dev	14.2

Gaus mean : 40.5301

Gaus Width: 3.5055

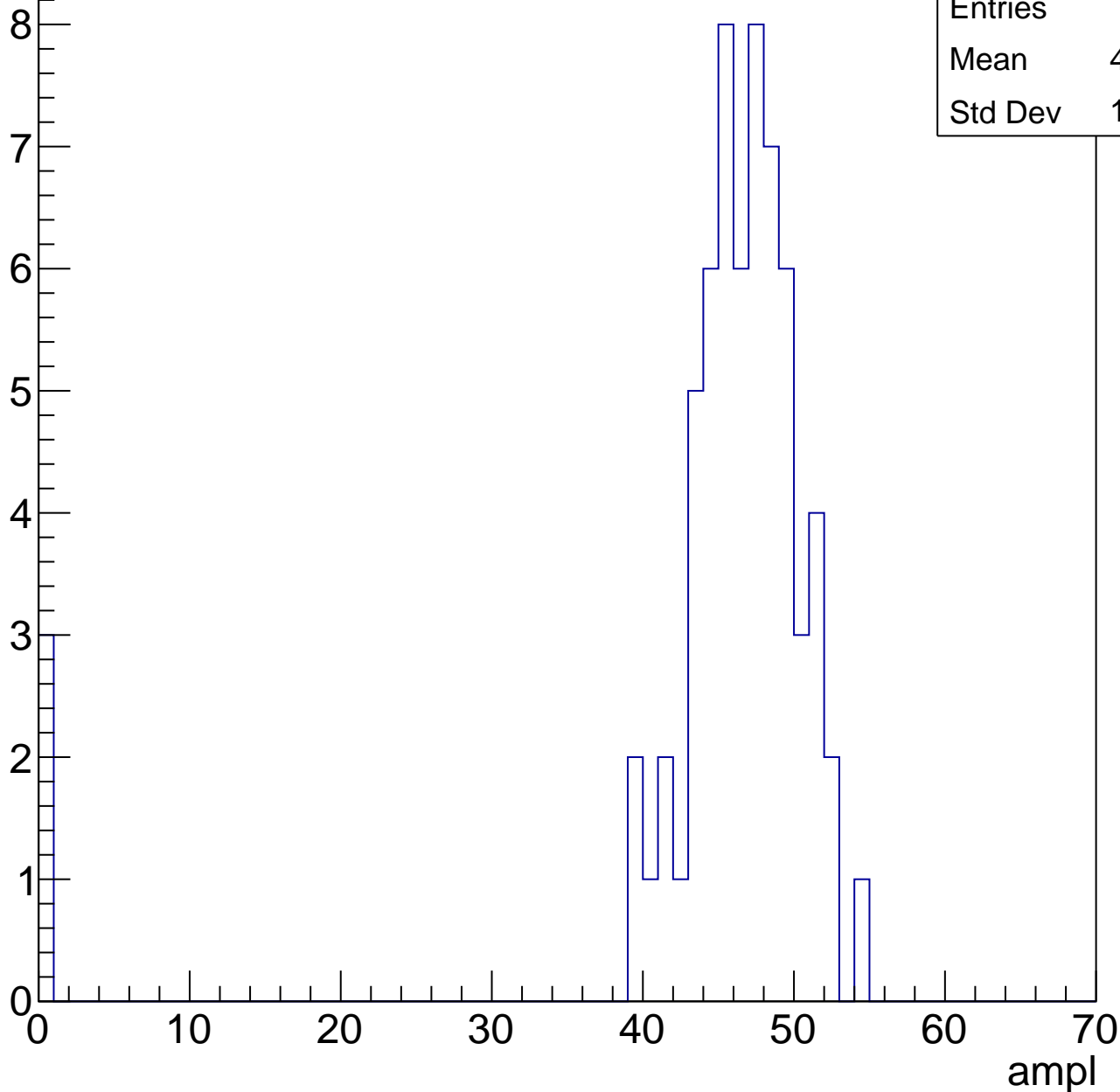


B1L103S, U18-ch28, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	44.23
Std Dev	10.22

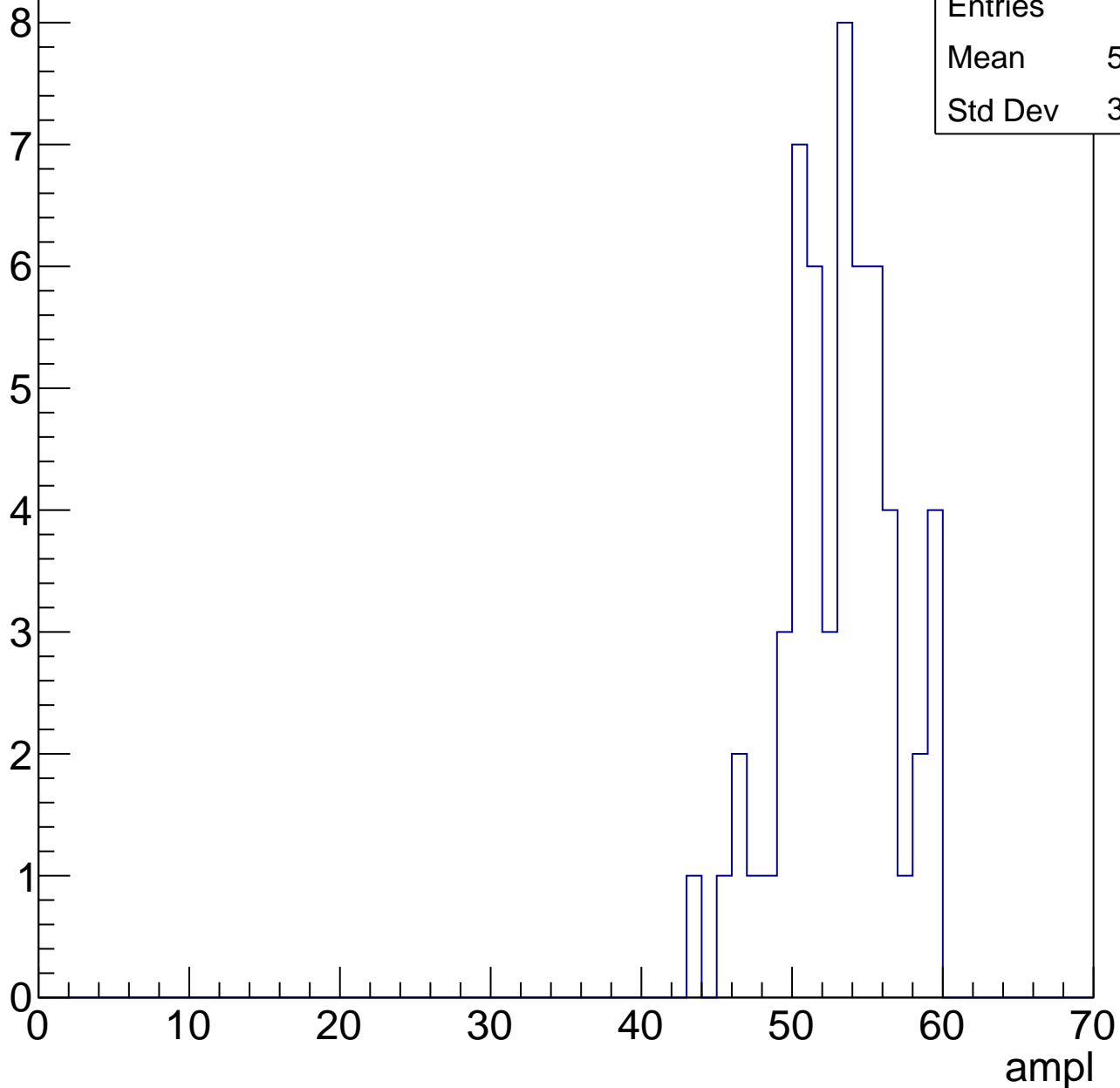


B1L103S, U18-ch28, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.59
Std Dev	3.604

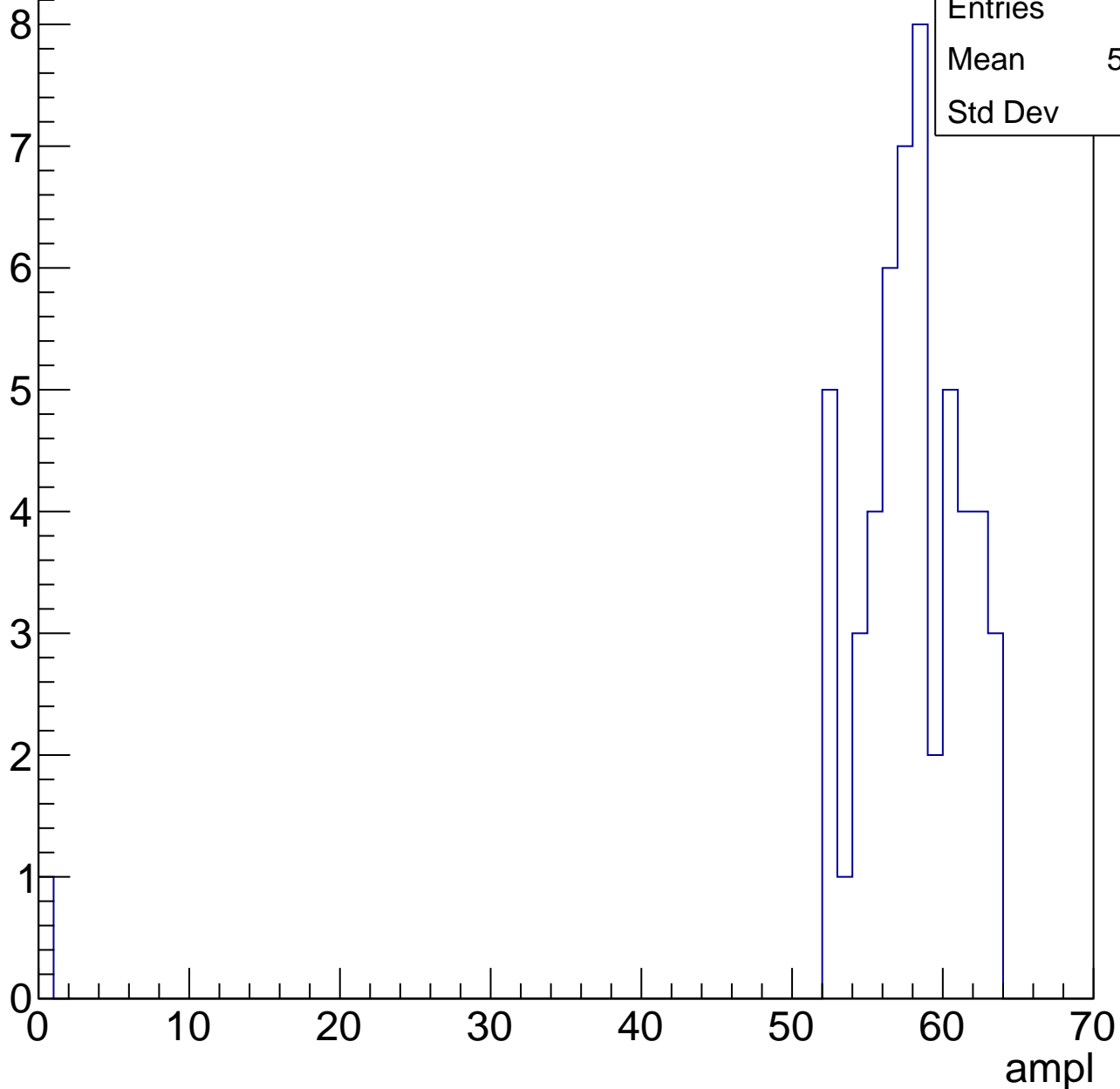


B1L103S, U18-ch28, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	56.47
Std Dev	8.42

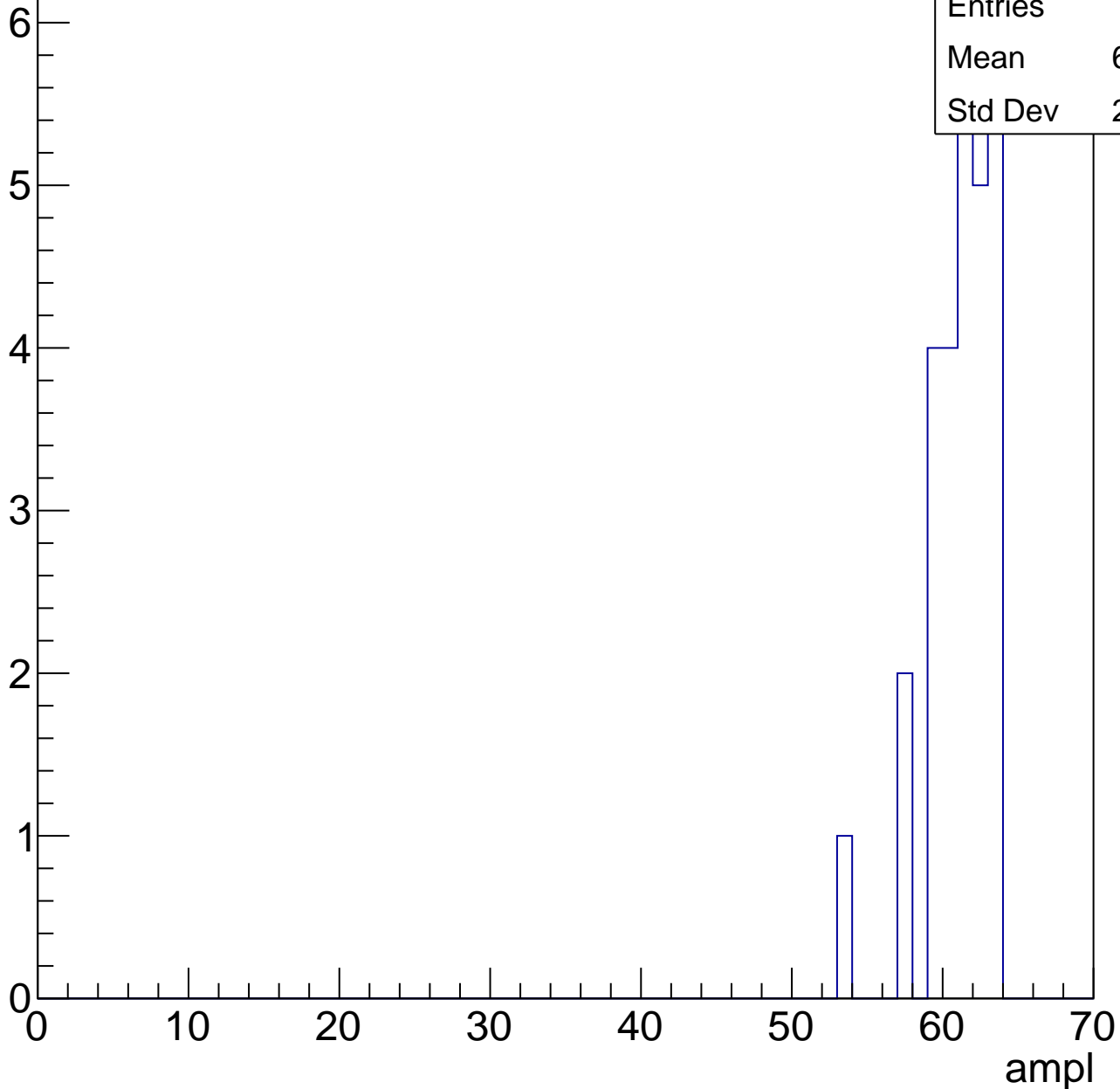


B1L103S, U18-ch28, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	60.61
Std Dev	2.241



B1L103S, U18-ch28, adc7

calib_packv5_041523_1651.root, FC#0, port C2

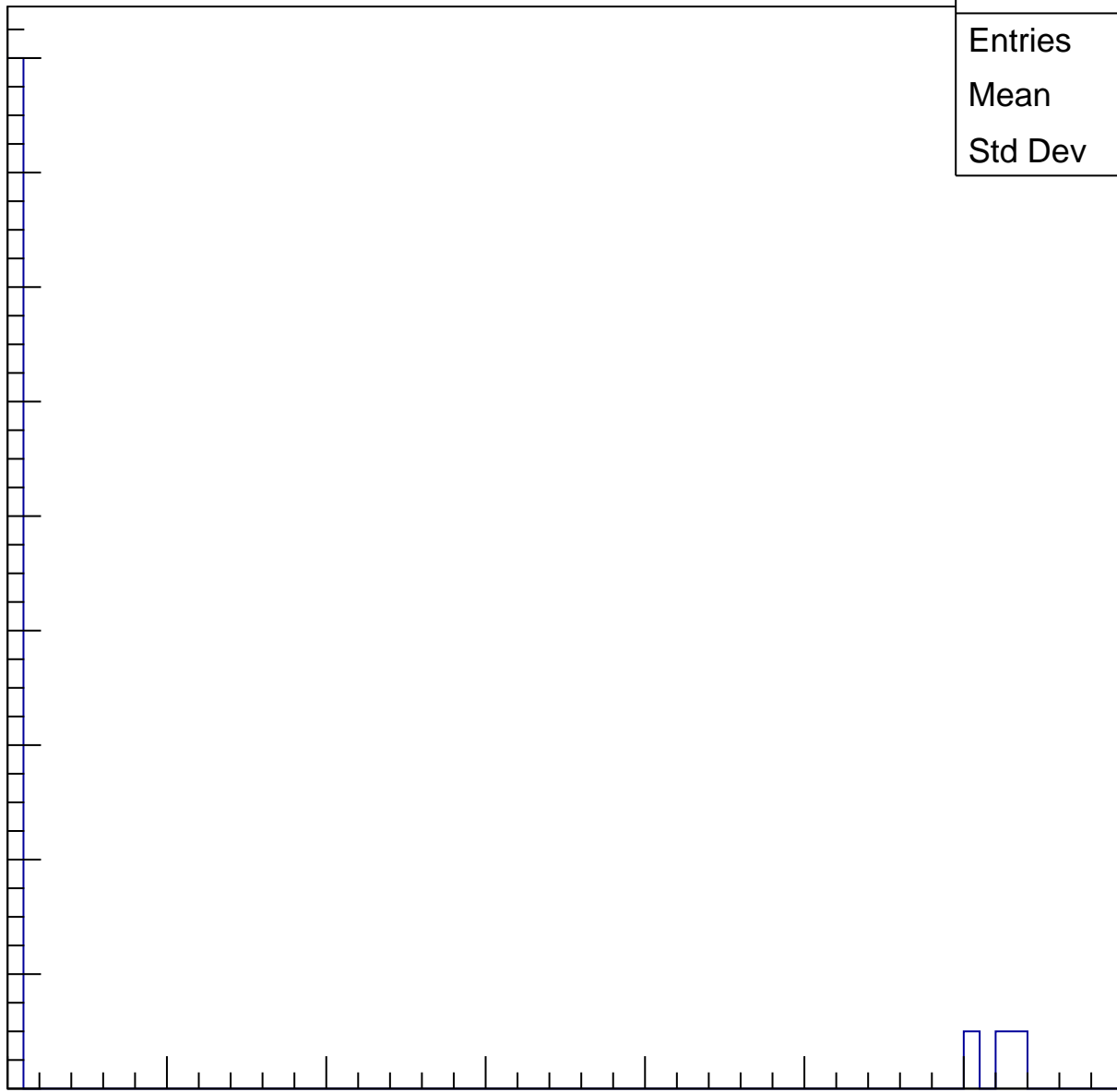
Entry

18
16
14
12
10
8
6
4
2
0

Entries	21
Mean	8.81
Std Dev	21.58

0 10 20 30 40 50 60 70

ampl



B1L103S, U18-ch29, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	27.45
Std Dev	6.561

Gaus mean : 28.8026

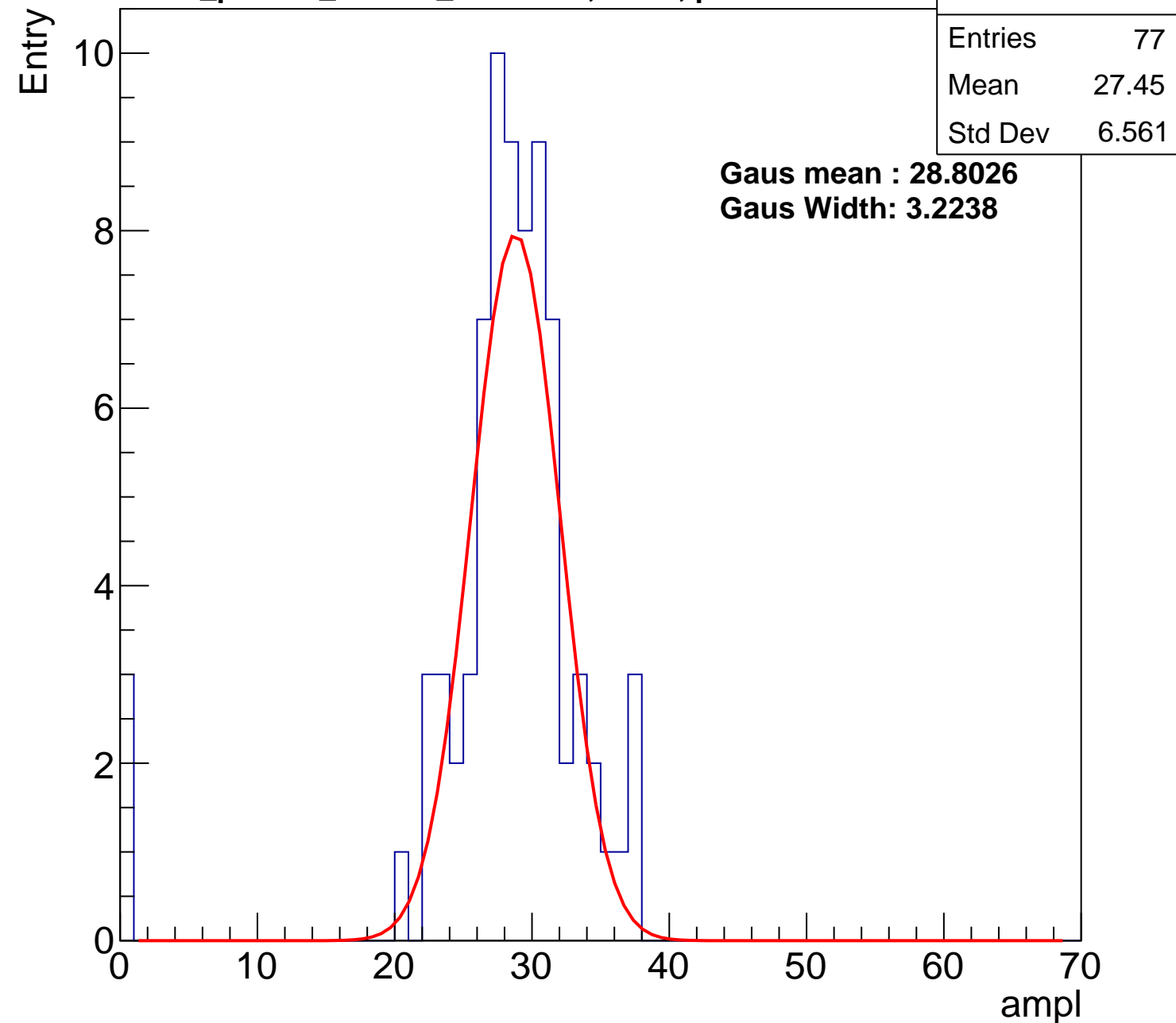
Gaus Width: 3.2238

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch29, adc1

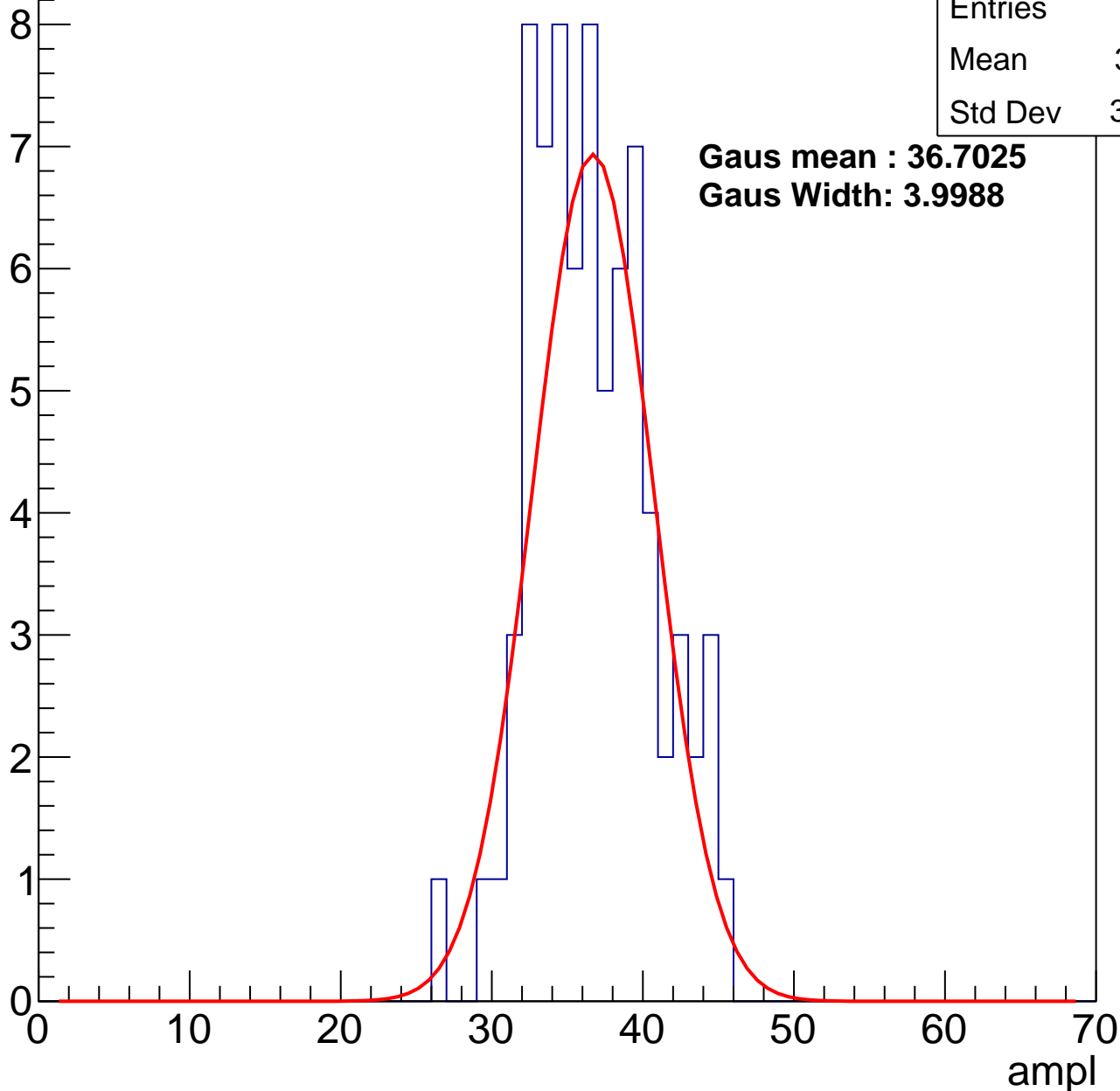
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	36.21
Std Dev	3.925

Gaus mean : 36.7025

Gaus Width: 3.9988



B1L103S, U18-ch29, adc2

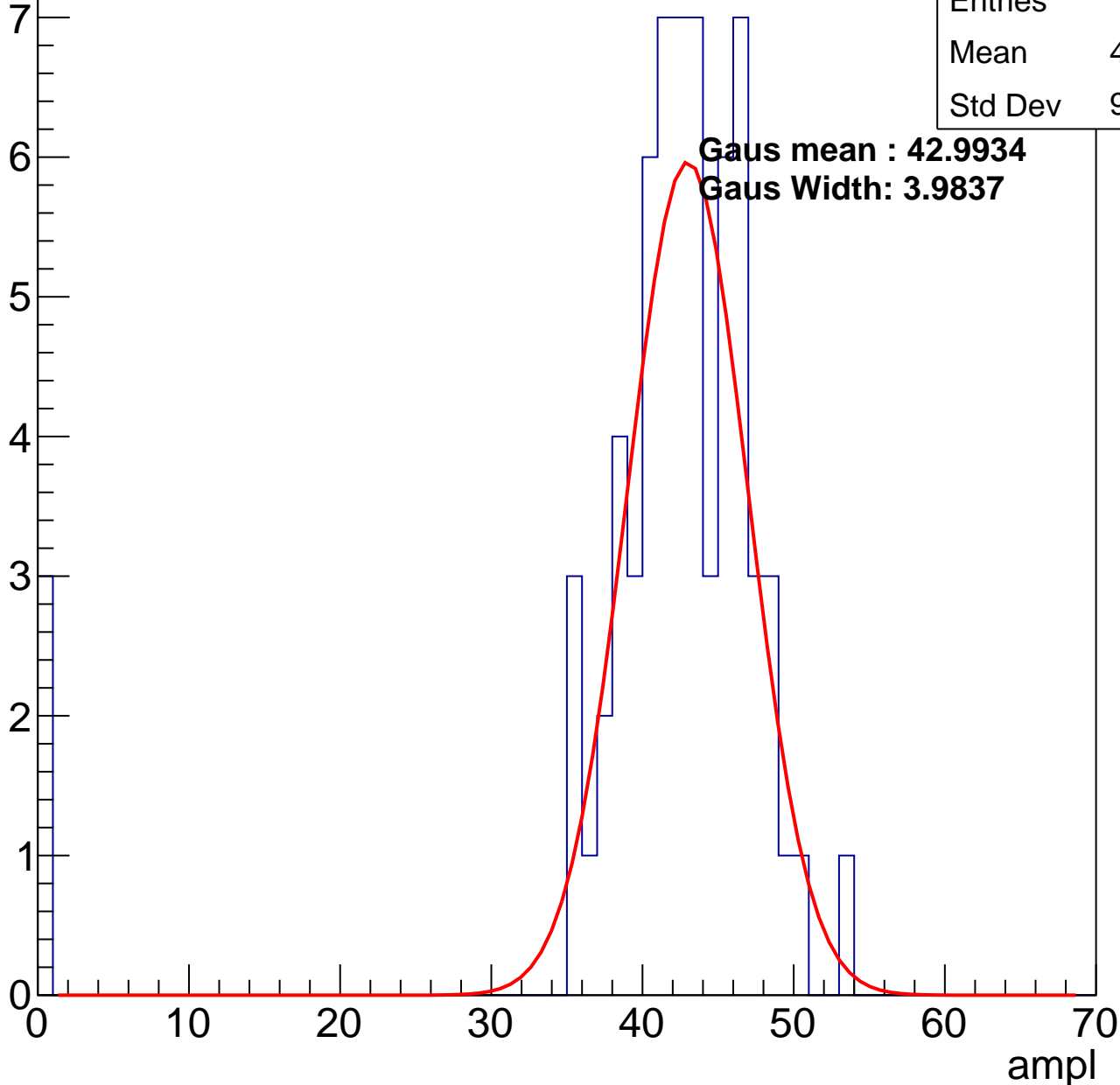
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.69
Std Dev	9.497

Gaus mean : 42.9934

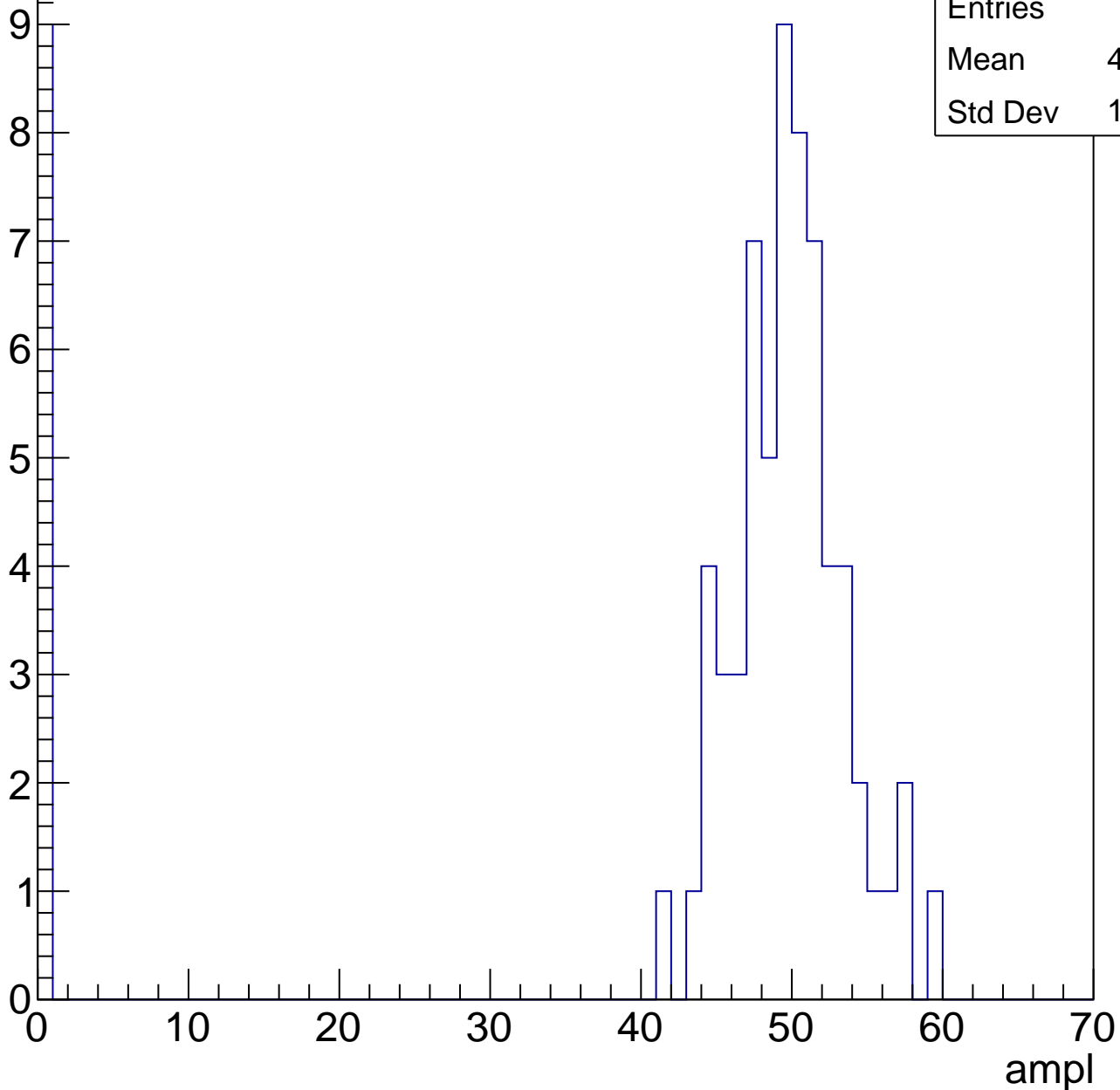
Gaus Width: 3.9837



B1L103S, U18-ch29, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

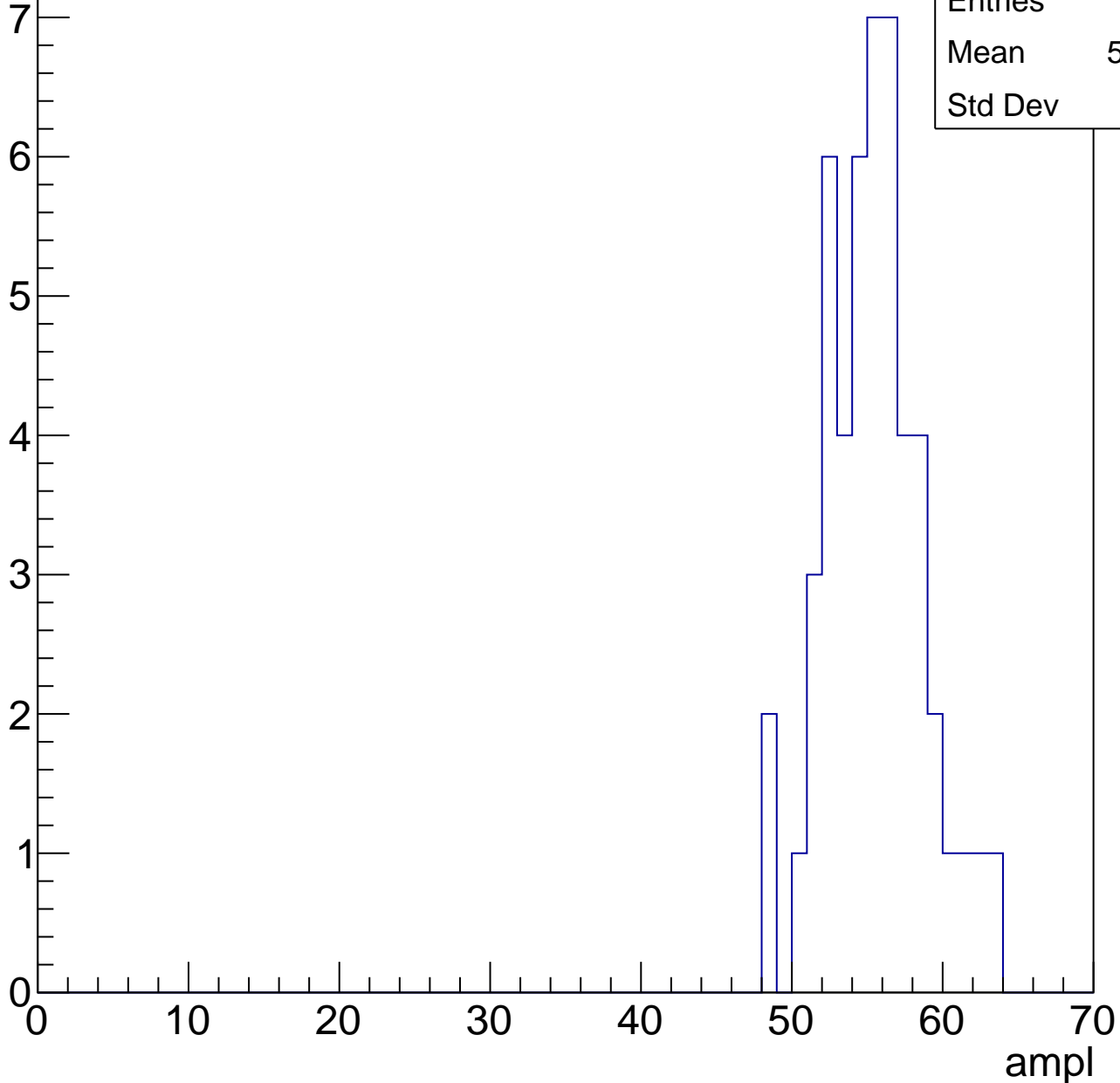


B1L103S, U18-ch29, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	54.96
Std Dev	3.2

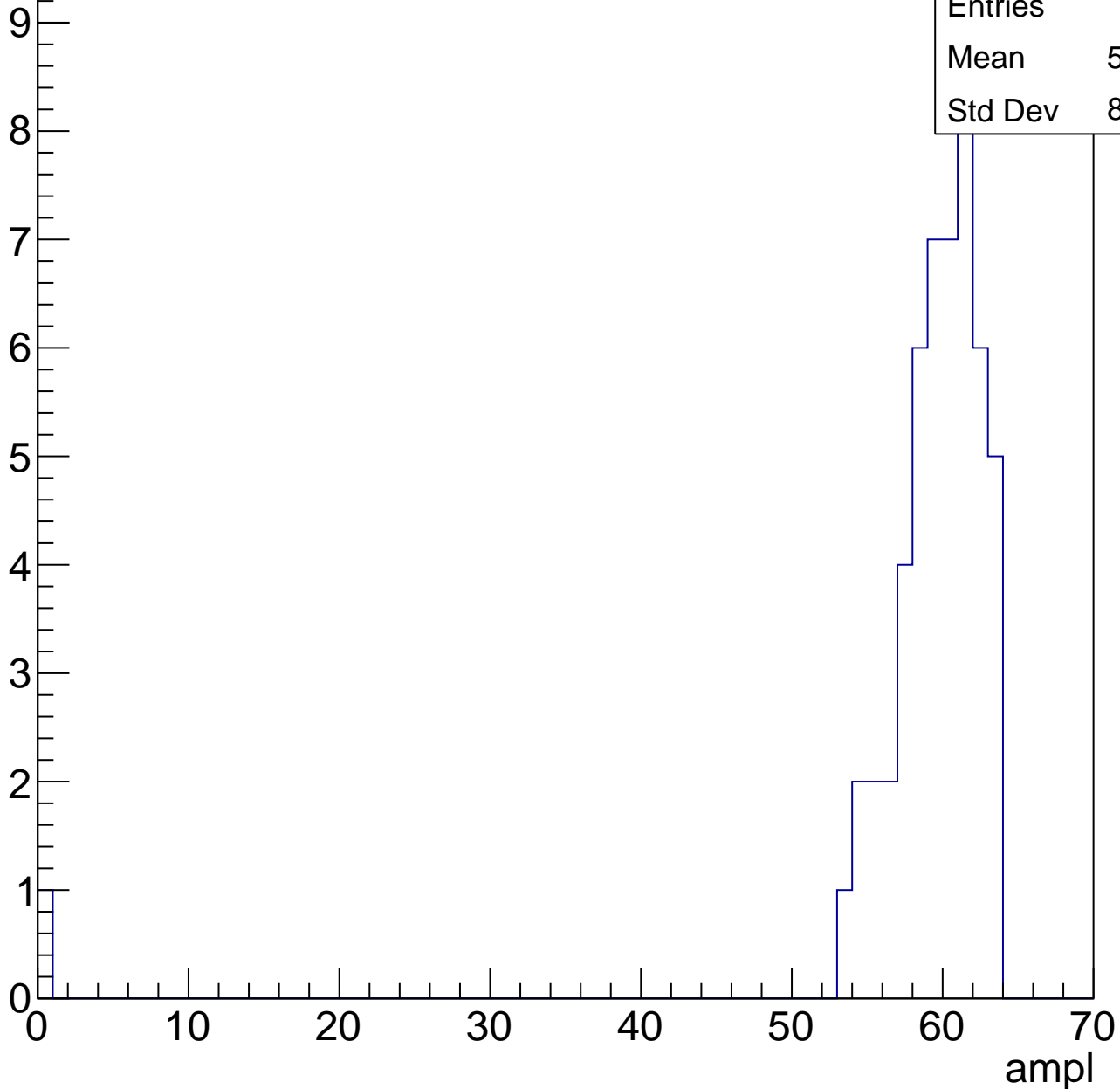


B1L103S, U18-ch29, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

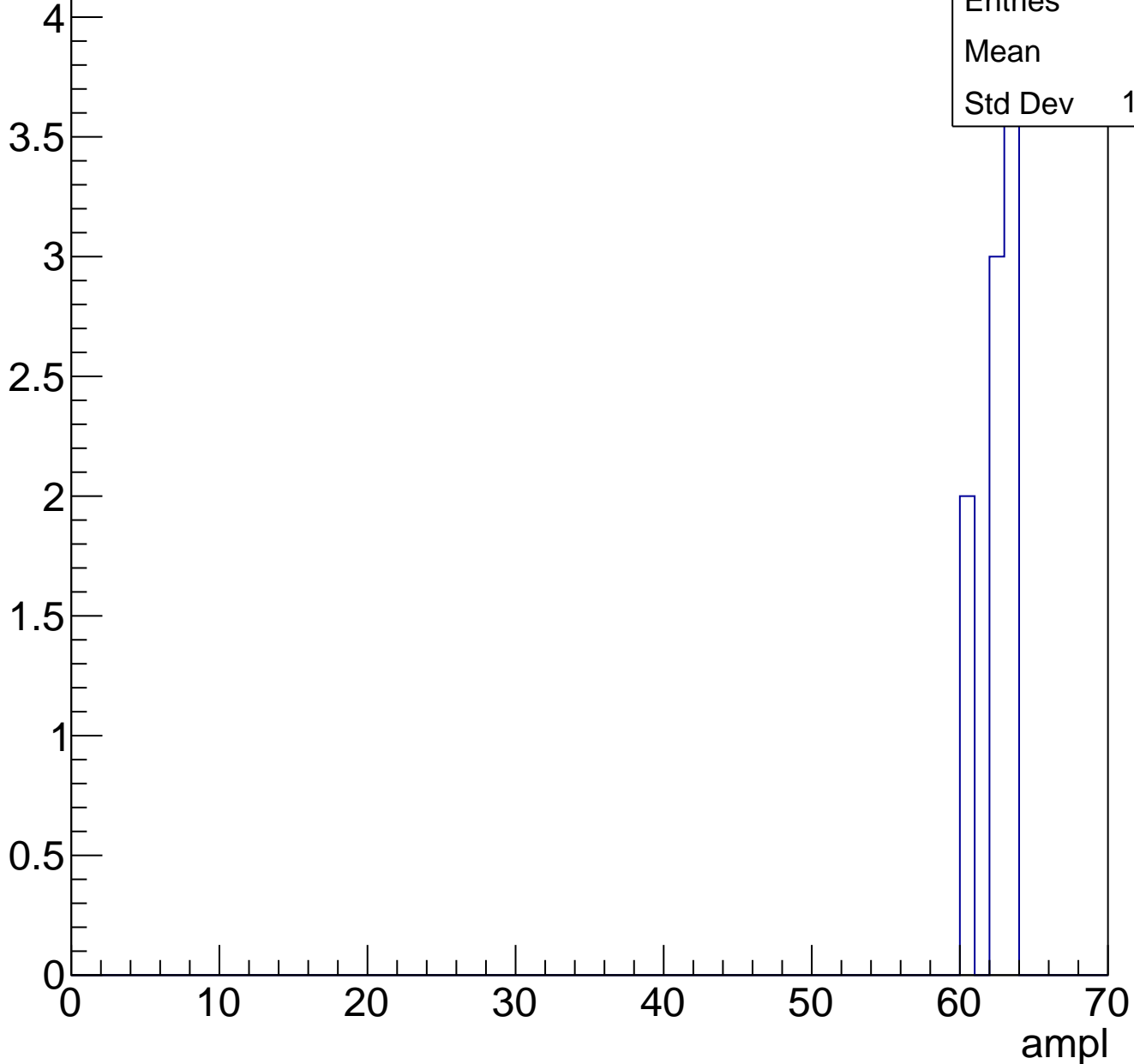
Entries	52
Mean	58.23
Std Dev	8.532



B1L103S, U18-ch29, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch29, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch30, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	19.7
Std Dev	12.75

Gaus mean : 28.0724

Gaus Width: 4.0502

Entry

25

20

15

10

5

0

0

10

20

30

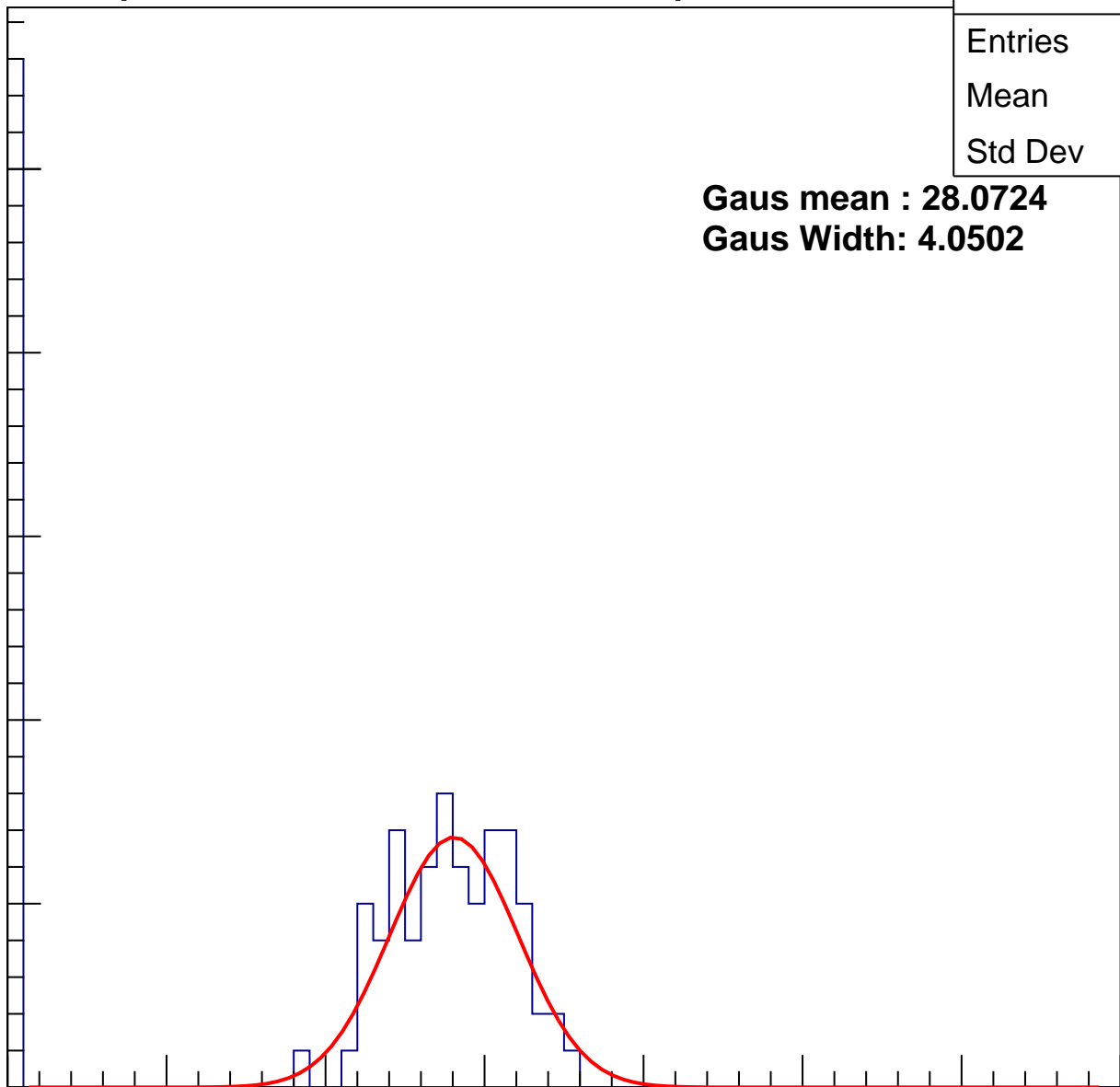
40

50

60

70

ampl



B1L103S, U18-ch30, adc1

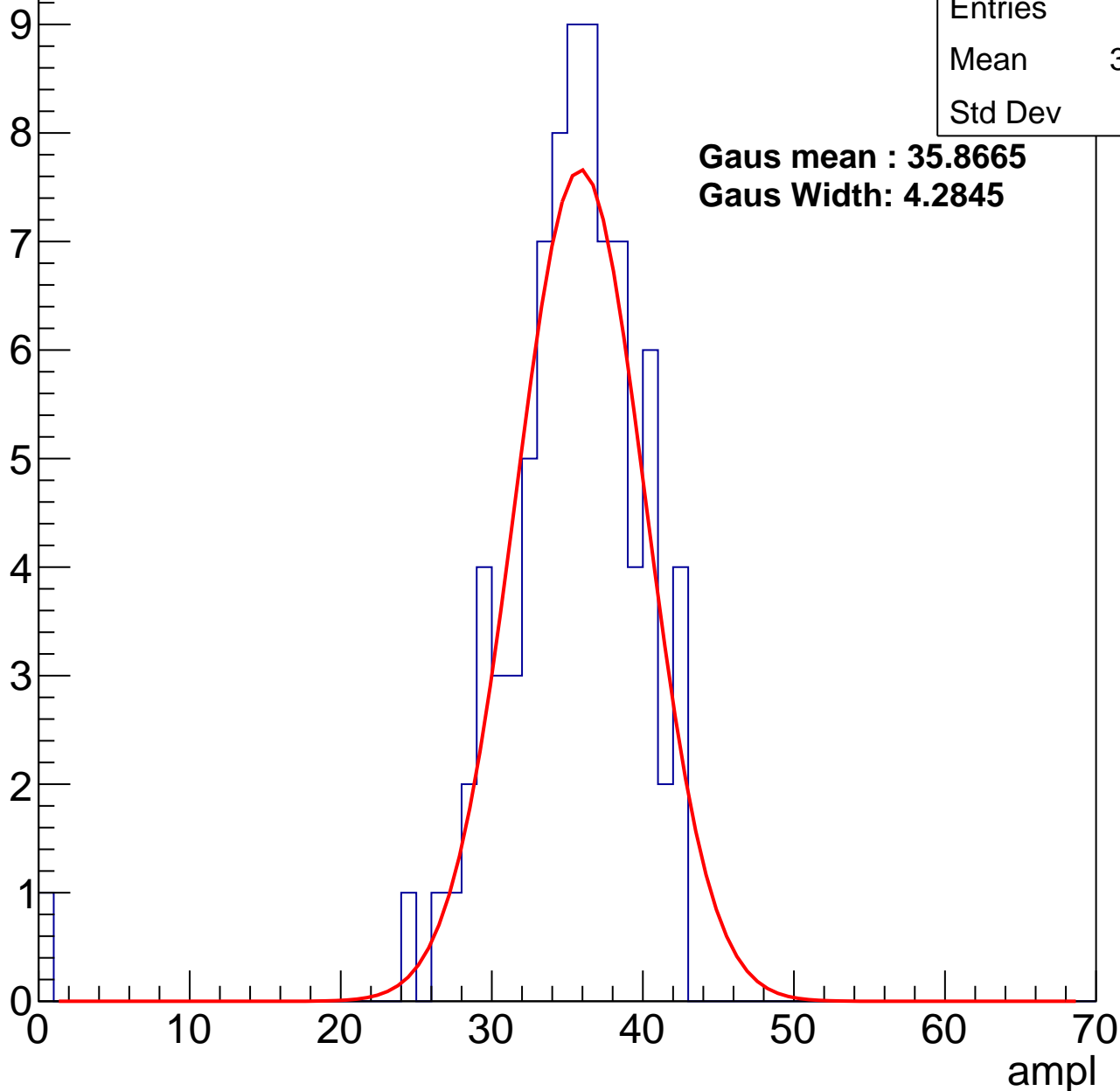
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	34.58
Std Dev	5.46

Gaus mean : 35.8665

Gaus Width: 4.2845



B1L103S, U18-ch30, adc2

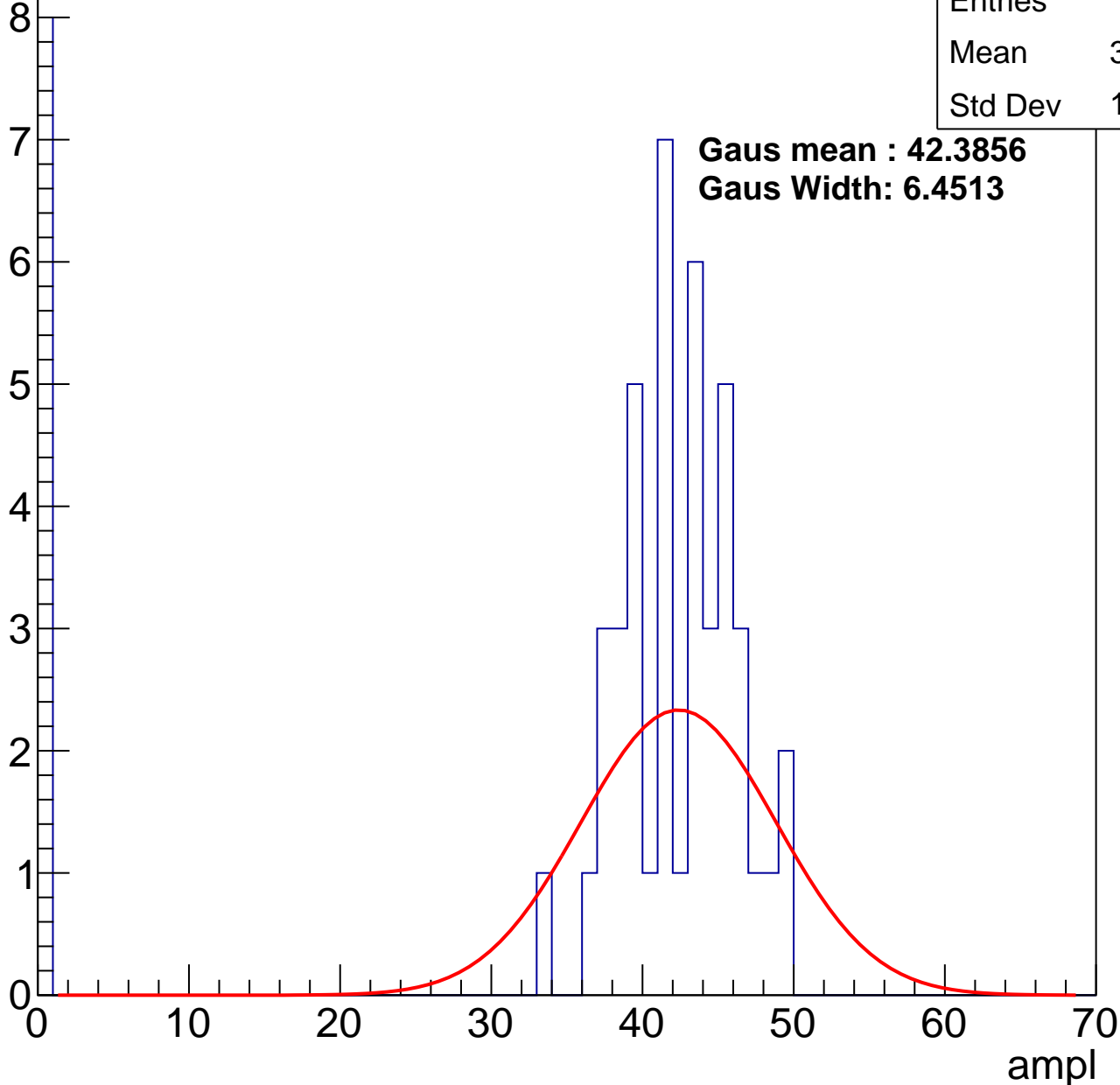
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	35.37
Std Dev	15.62

Gaus mean : 42.3856

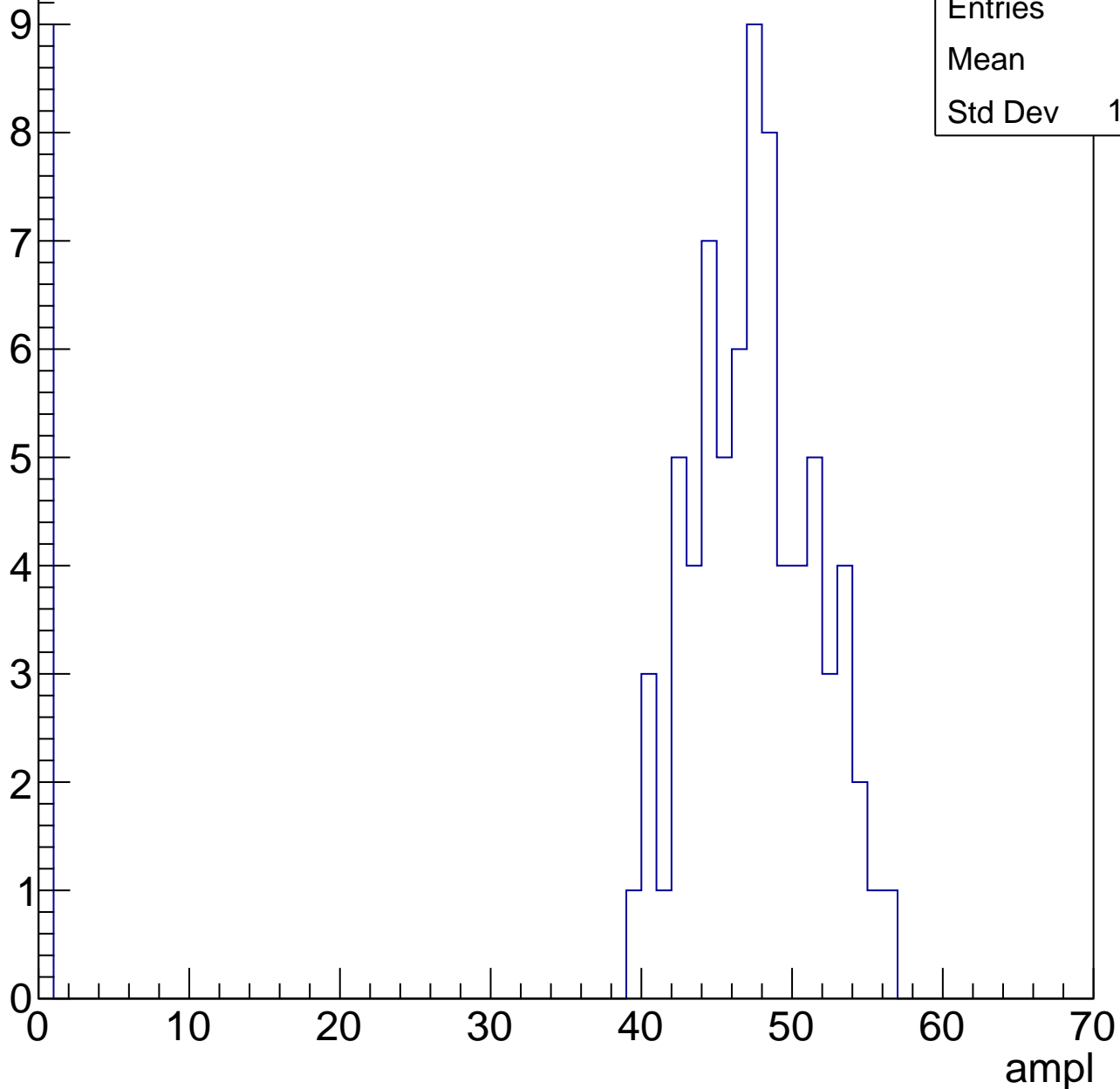
Gaus Width: 6.4513



B1L103S, U18-ch30, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

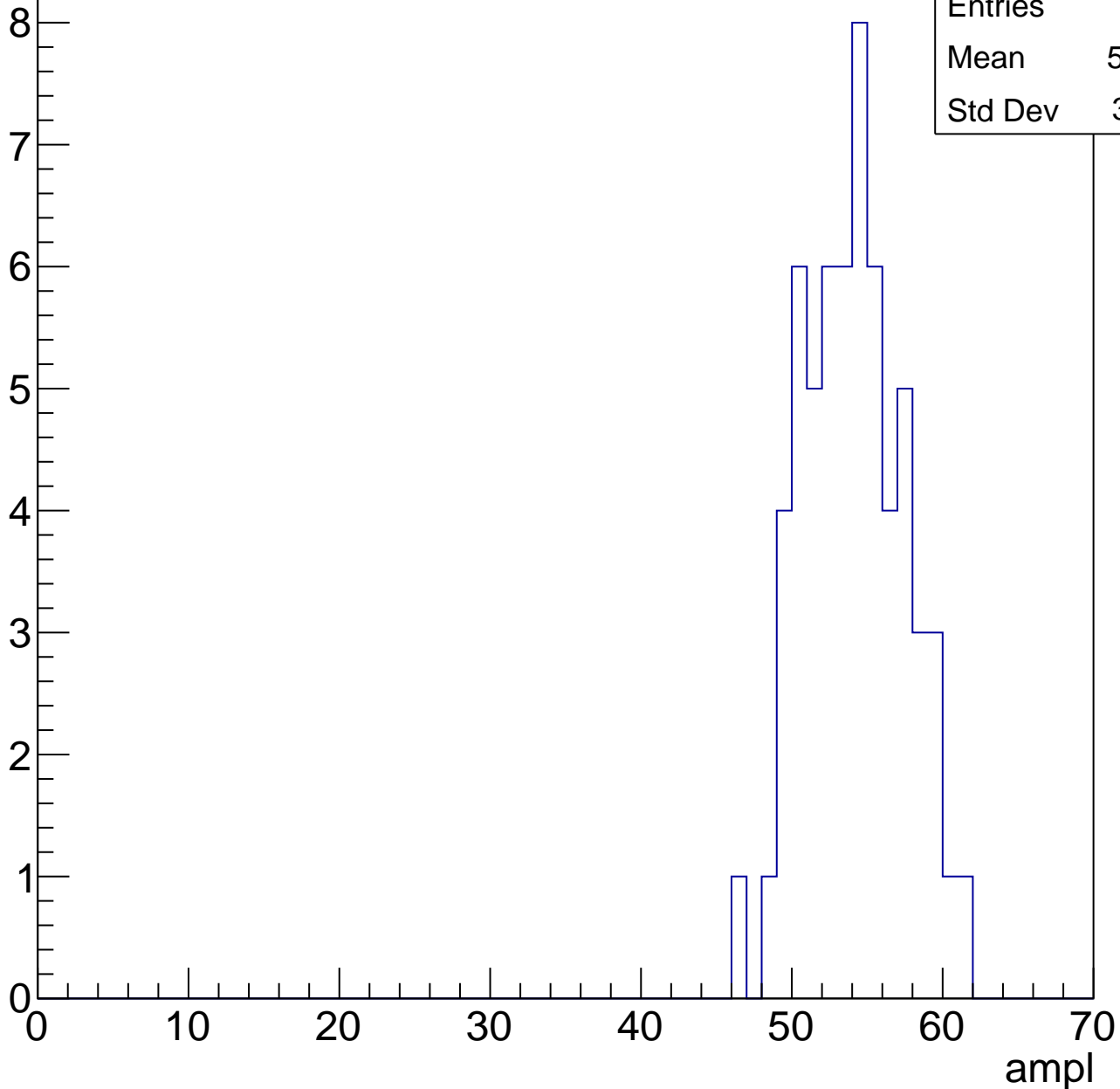


B1L103S, U18-ch30, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	53.63
Std Dev	3.261

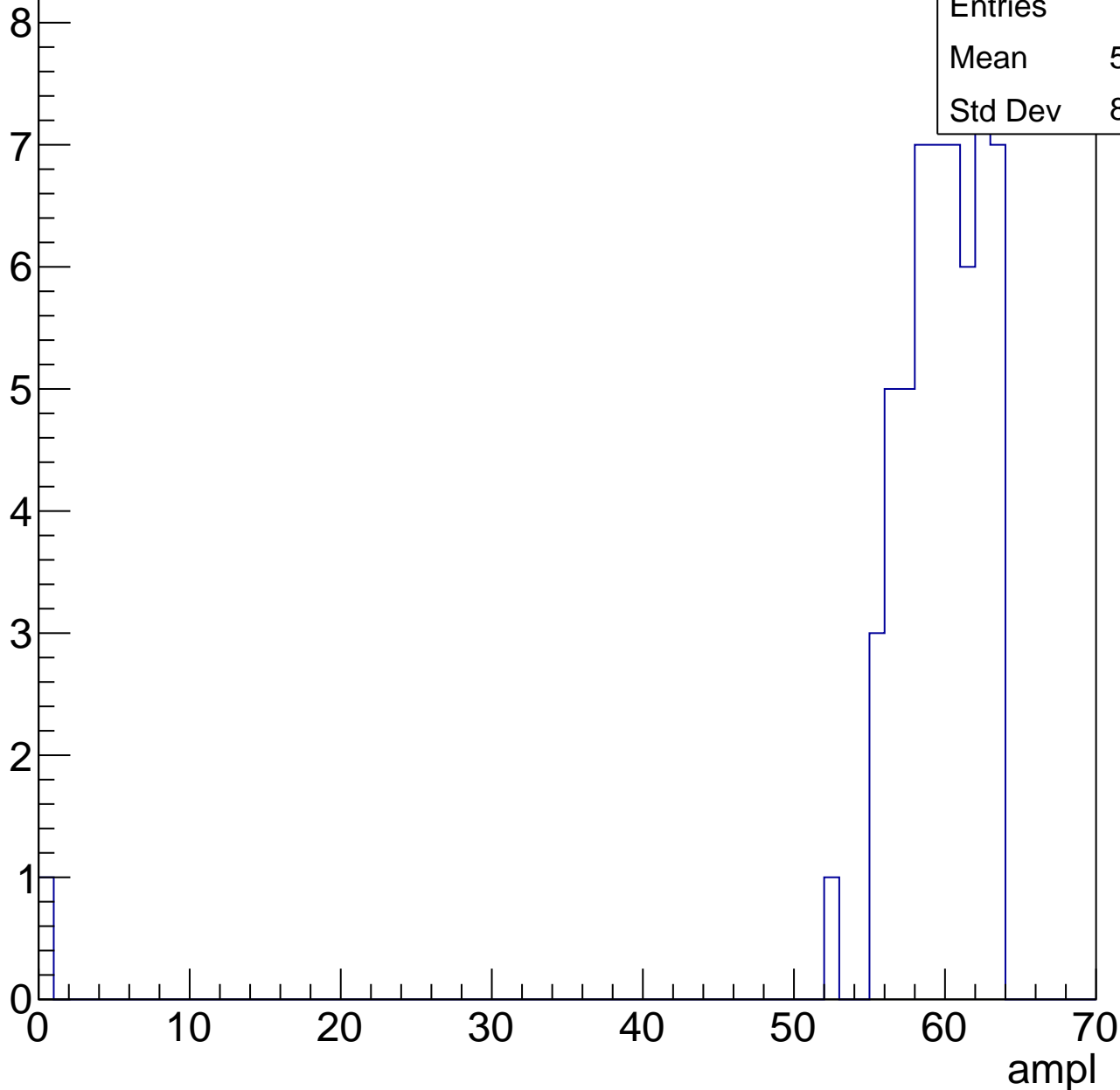


B1L103S, U18-ch30, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

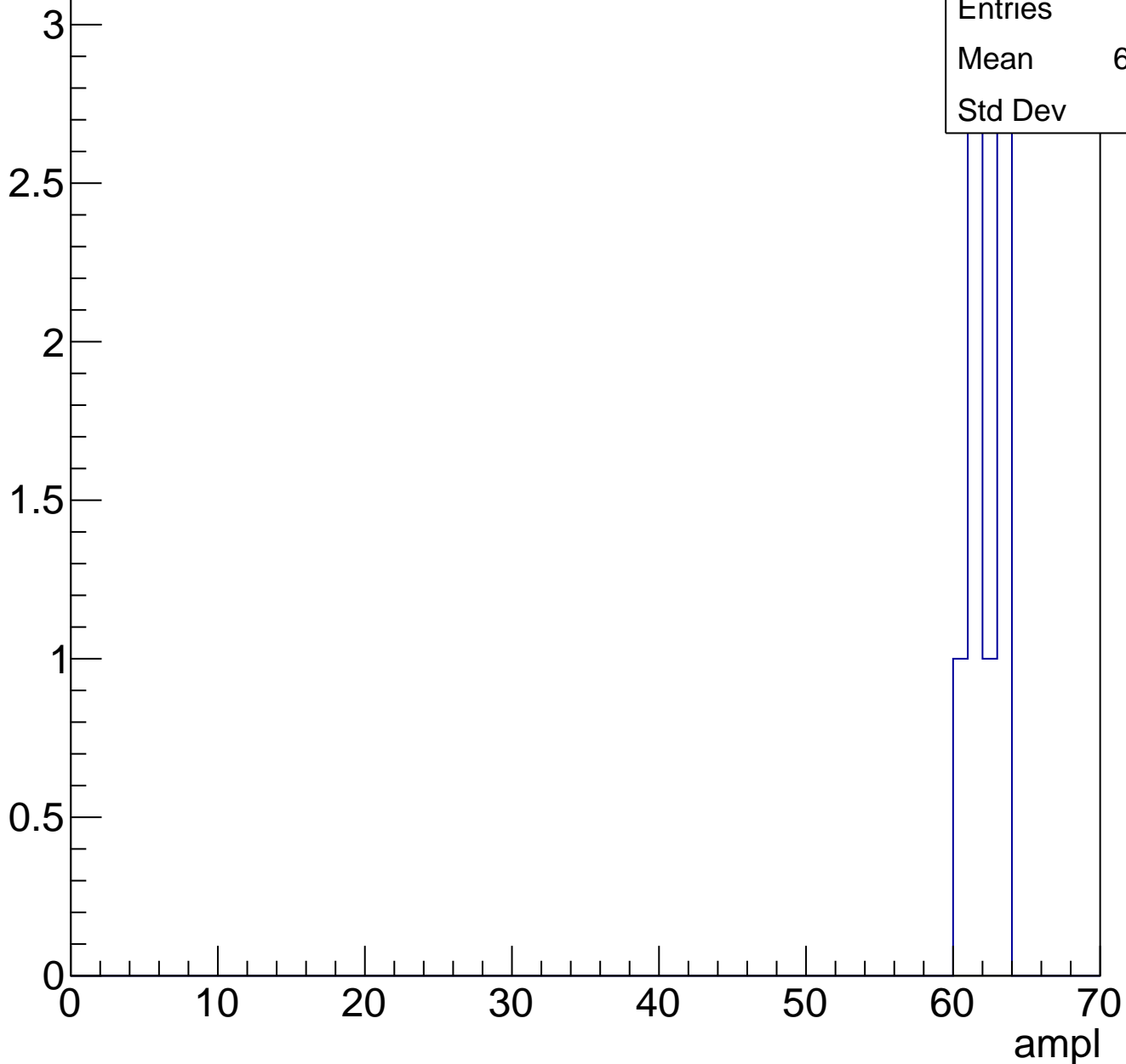
Entries	57
Mean	58.32
Std Dev	8.206



B1L103S, U18-ch30, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

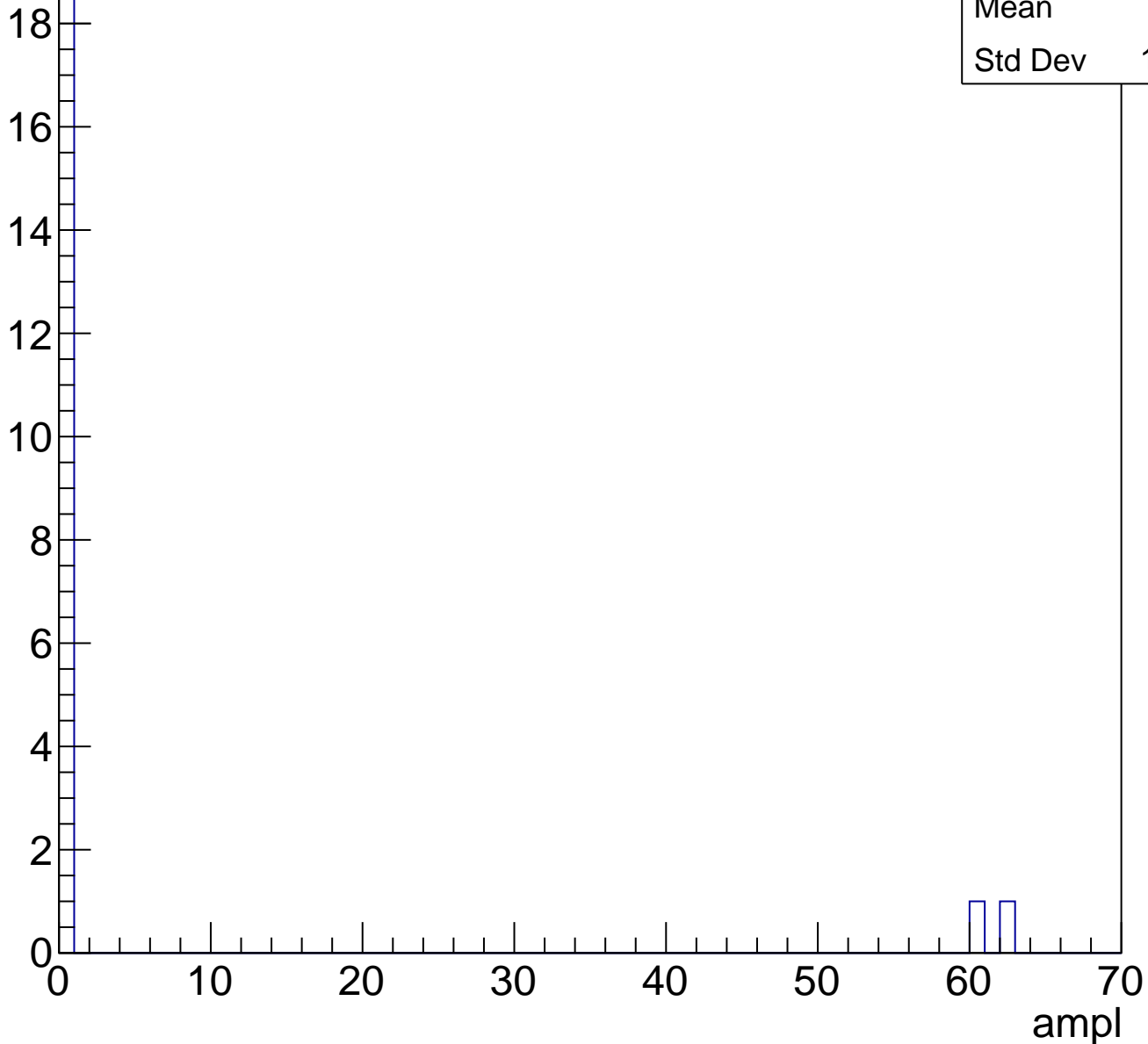


B1L103S, U18-ch30, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.91

Entry



B1L103S, U18-ch31, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	23.59
Std Dev	12.08

Gaus mean : 29.8439

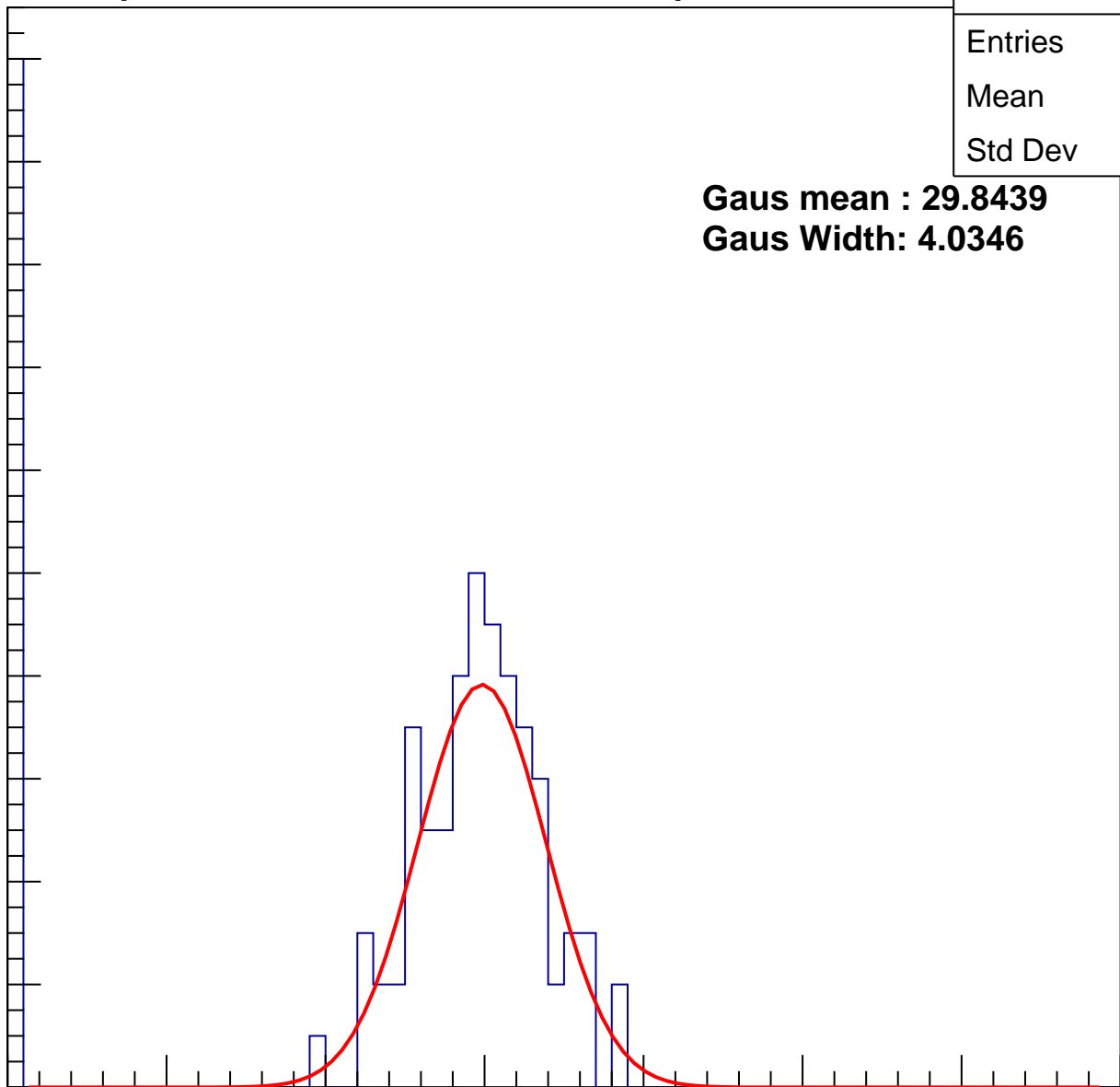
Gaus Width: 4.0346

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch31, adc1

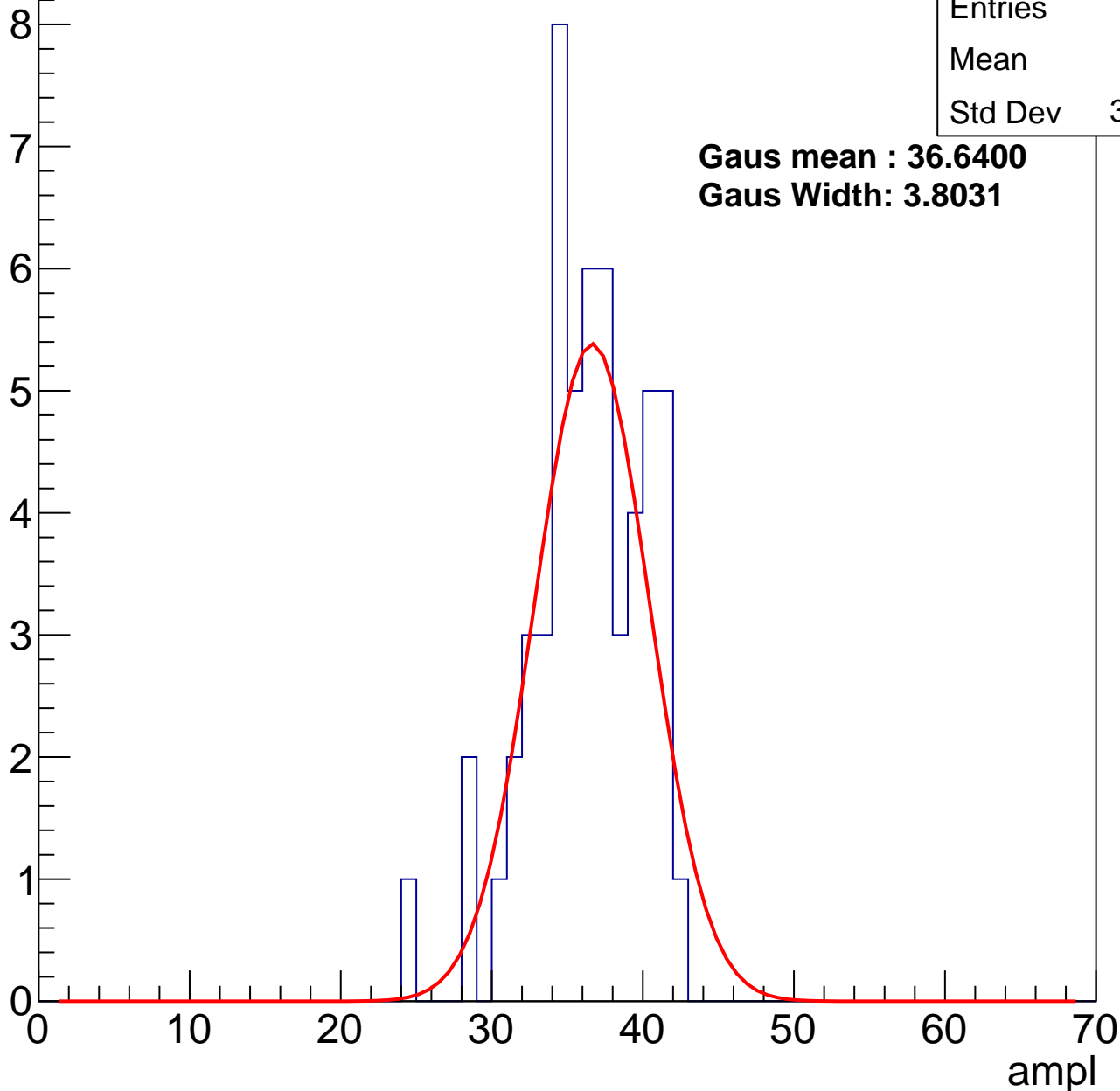
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	35.8
Std Dev	3.724

Gaus mean : 36.6400

Gaus Width: 3.8031



B1L103S, U18-ch31, adc2

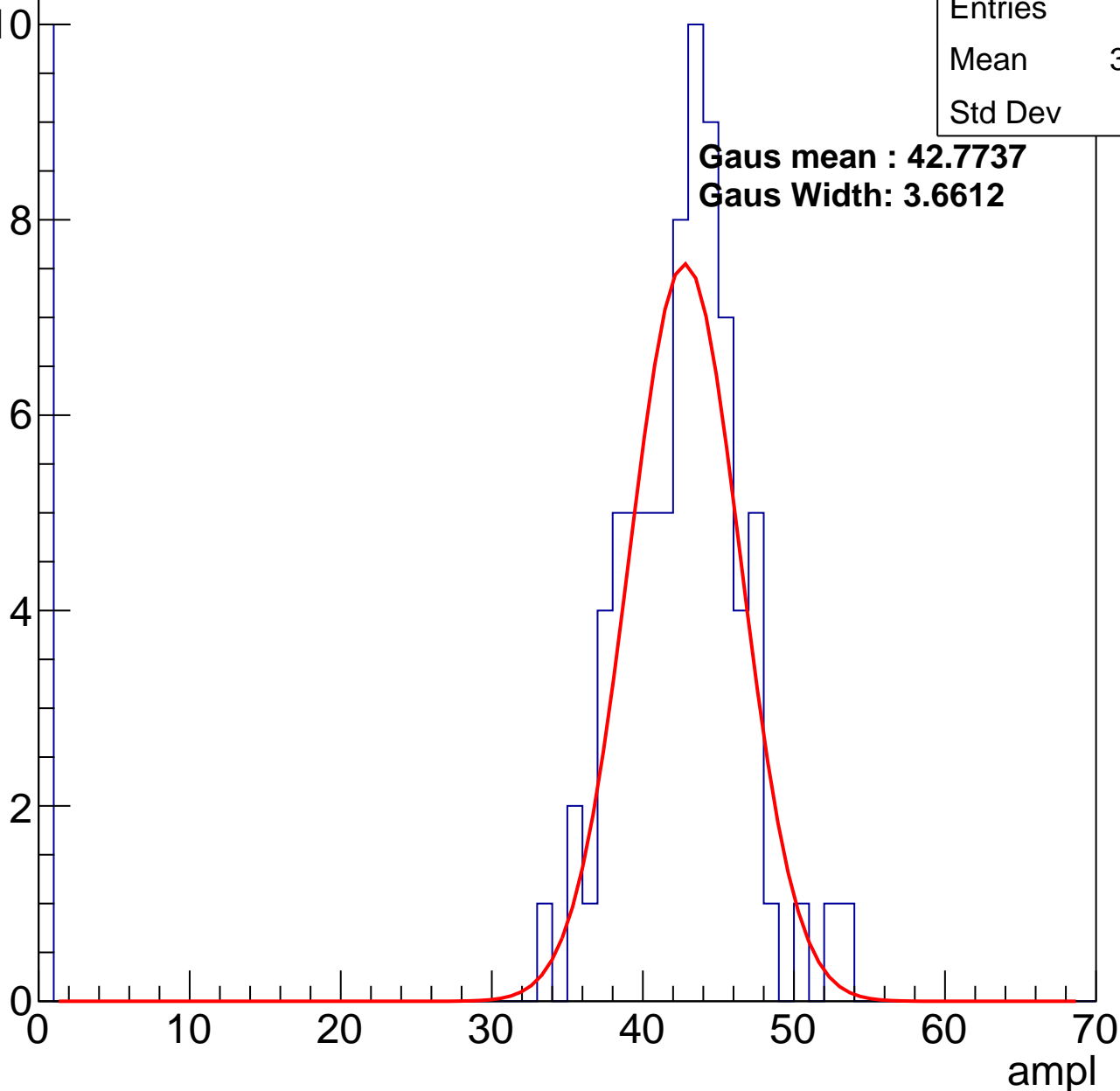
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	37.36
Std Dev	14.1

Gaus mean : 42.7737

Gaus Width: 3.6612



B1L103S, U18-ch31, adc3

calib_packv5_041523_1651.root, FC#0, port C2

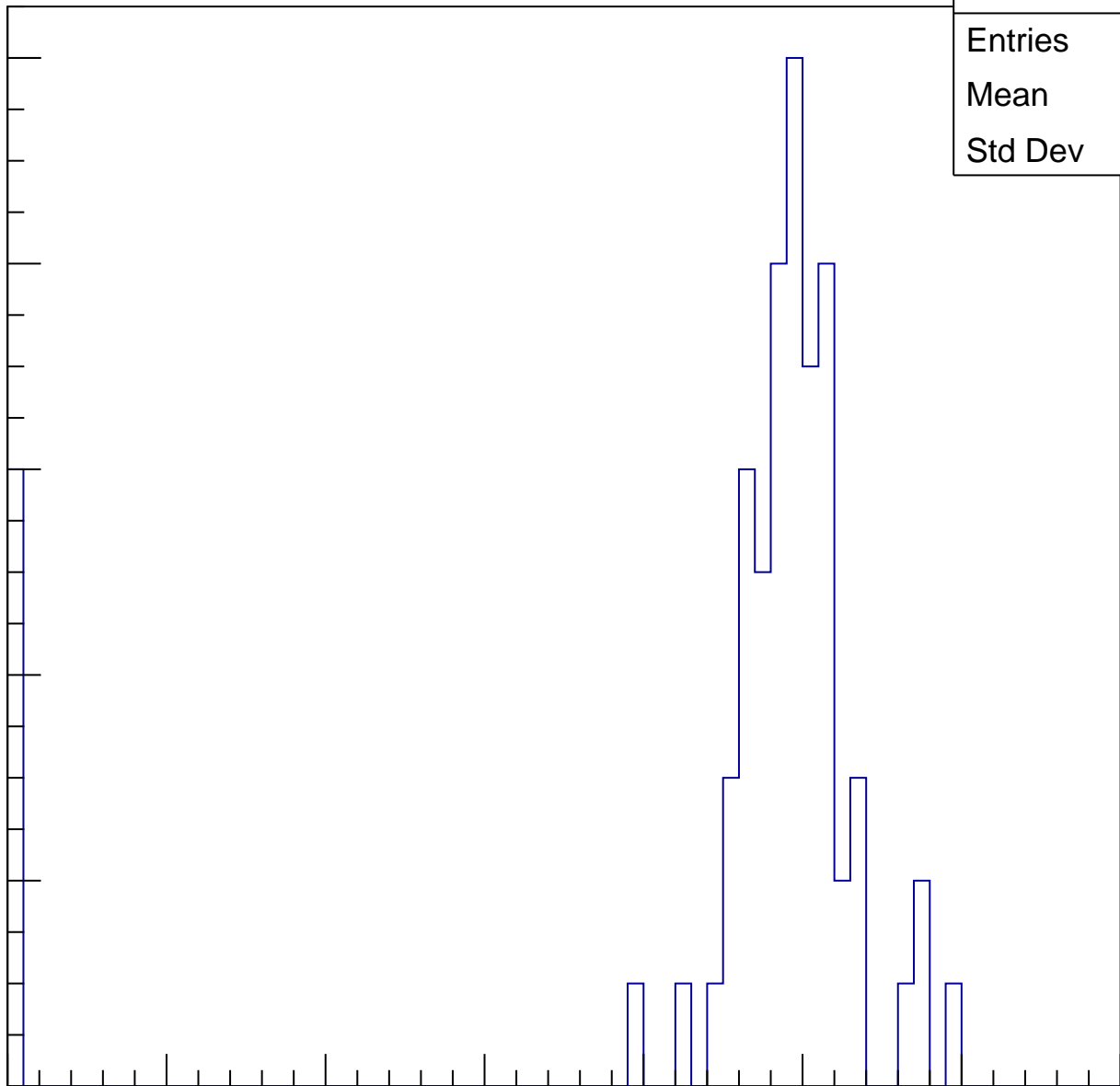
Entries	65
Mean	44.54
Std Dev	14.57

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

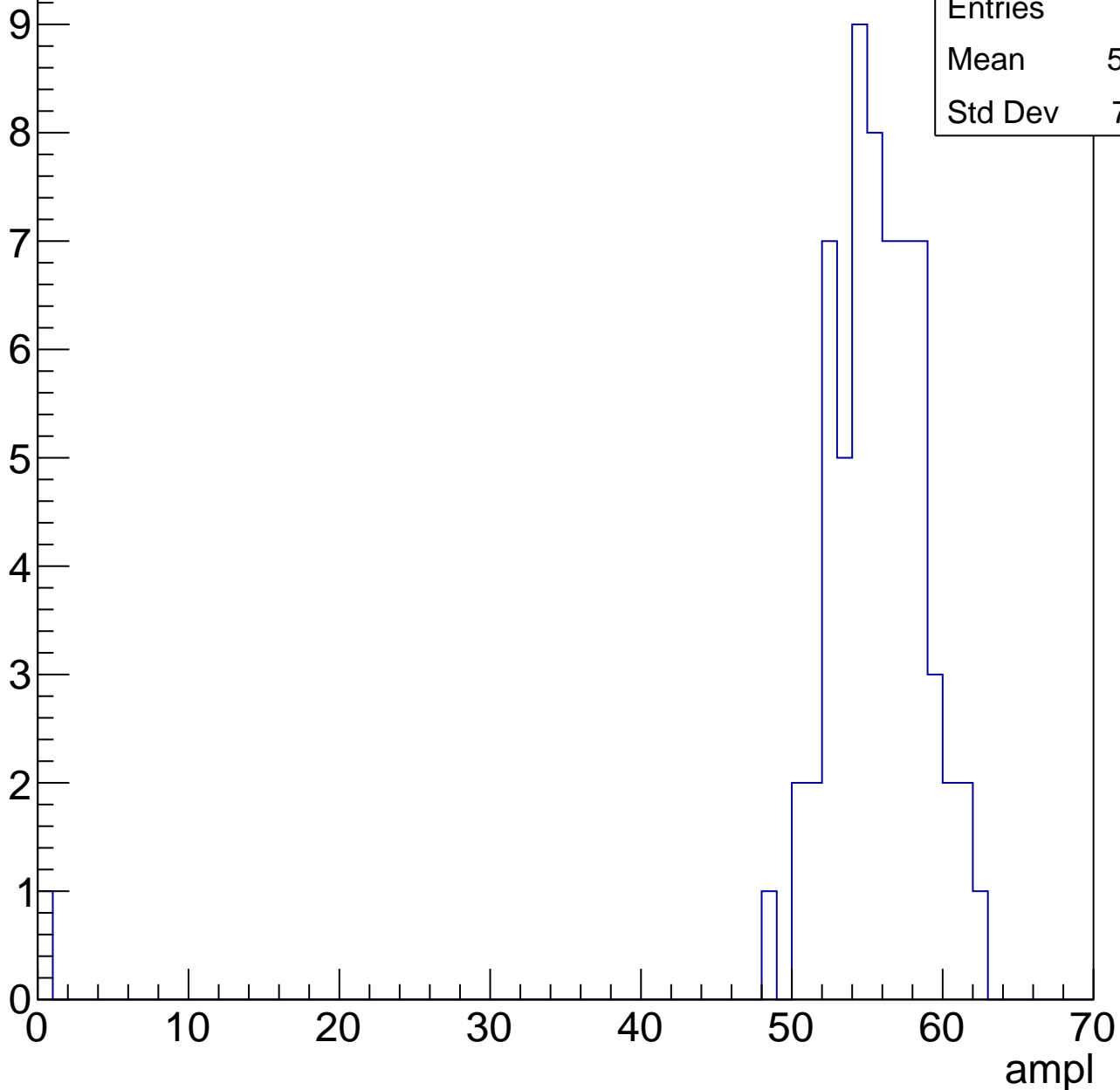


B1L103S, U18-ch31, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.42
Std Dev	7.441

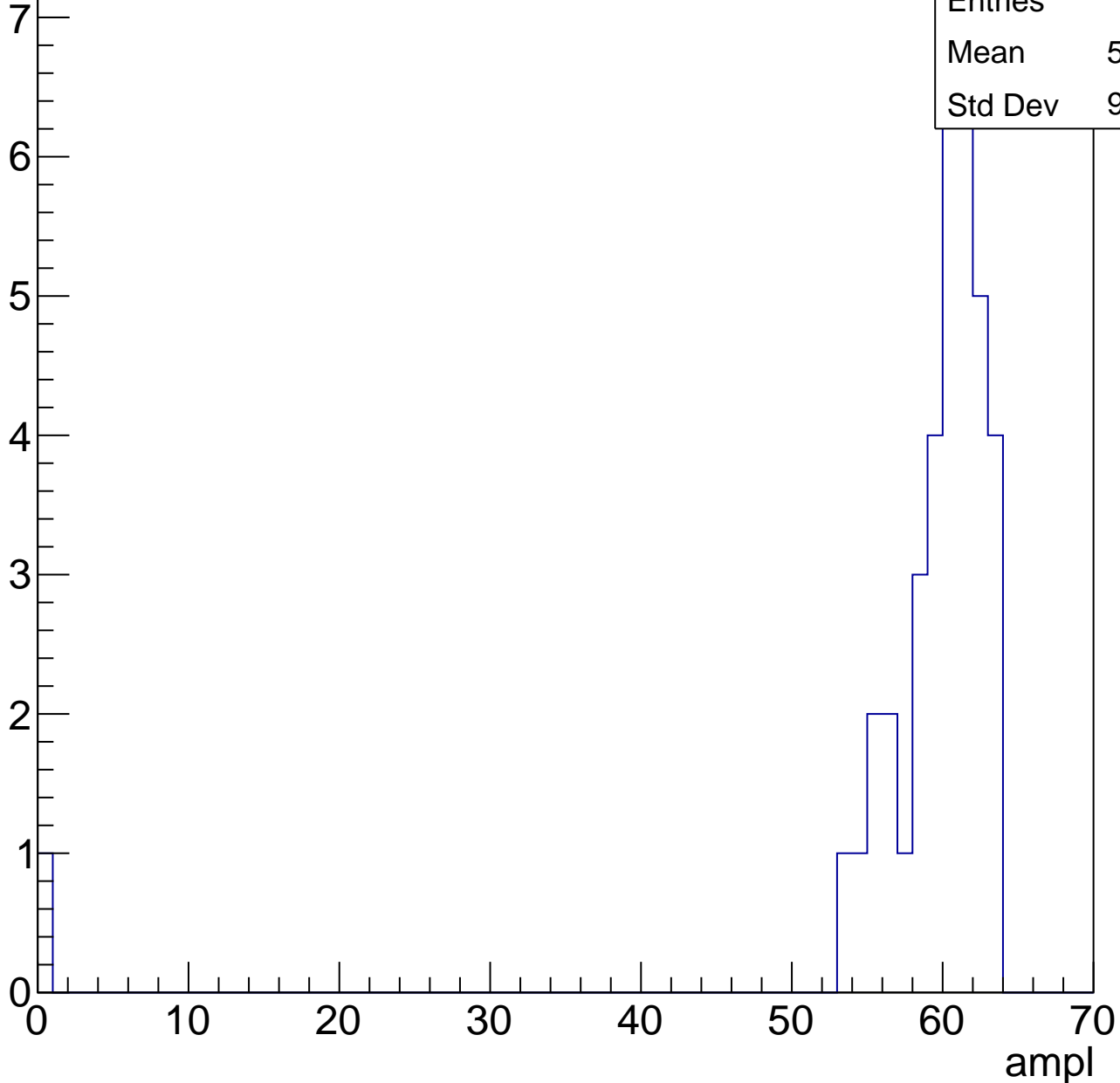


B1L103S, U18-ch31, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.03
Std Dev	9.877

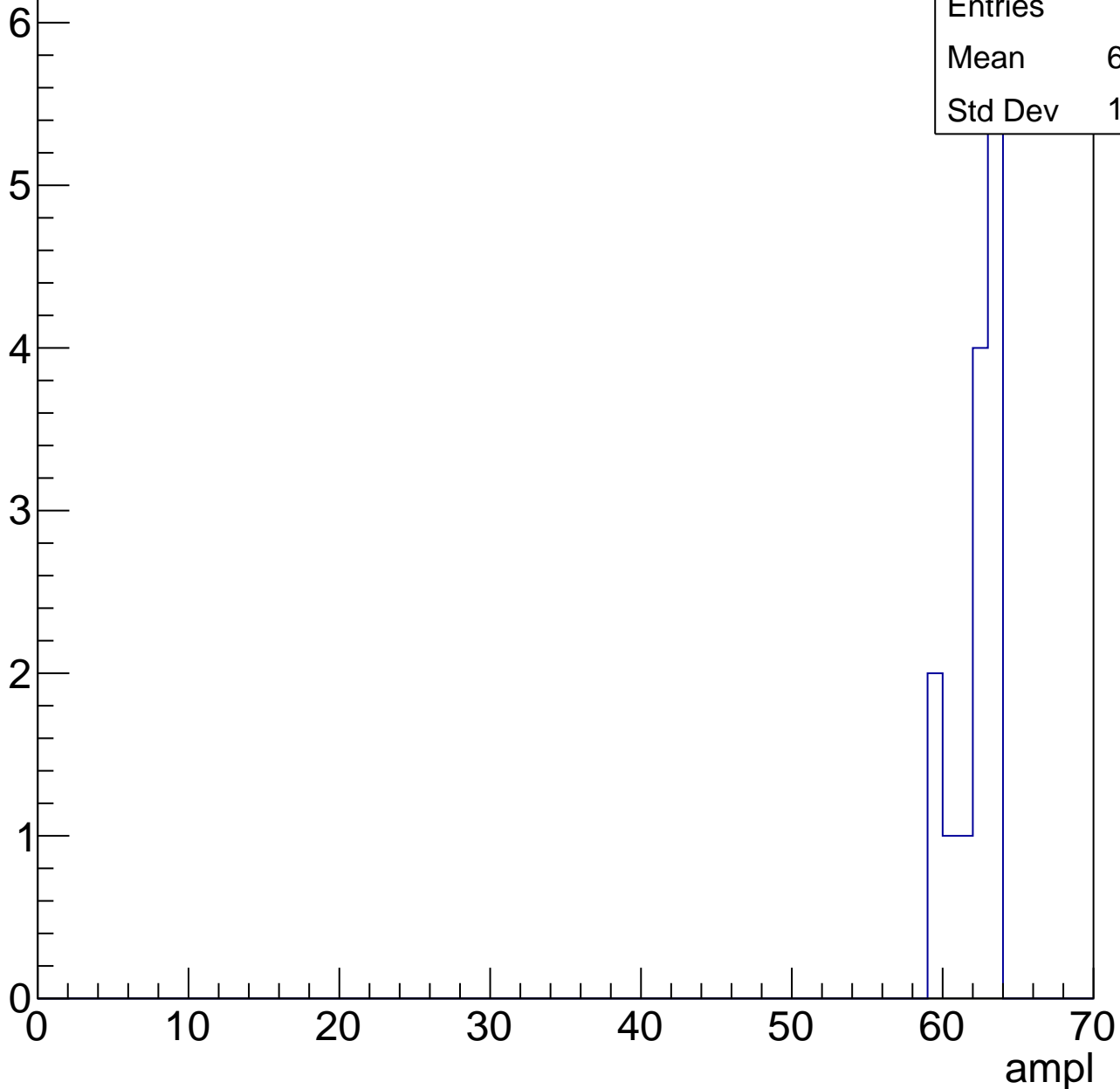


B1L103S, U18-ch31, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.79
Std Dev	1.423



B1L103S, U18-ch31, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



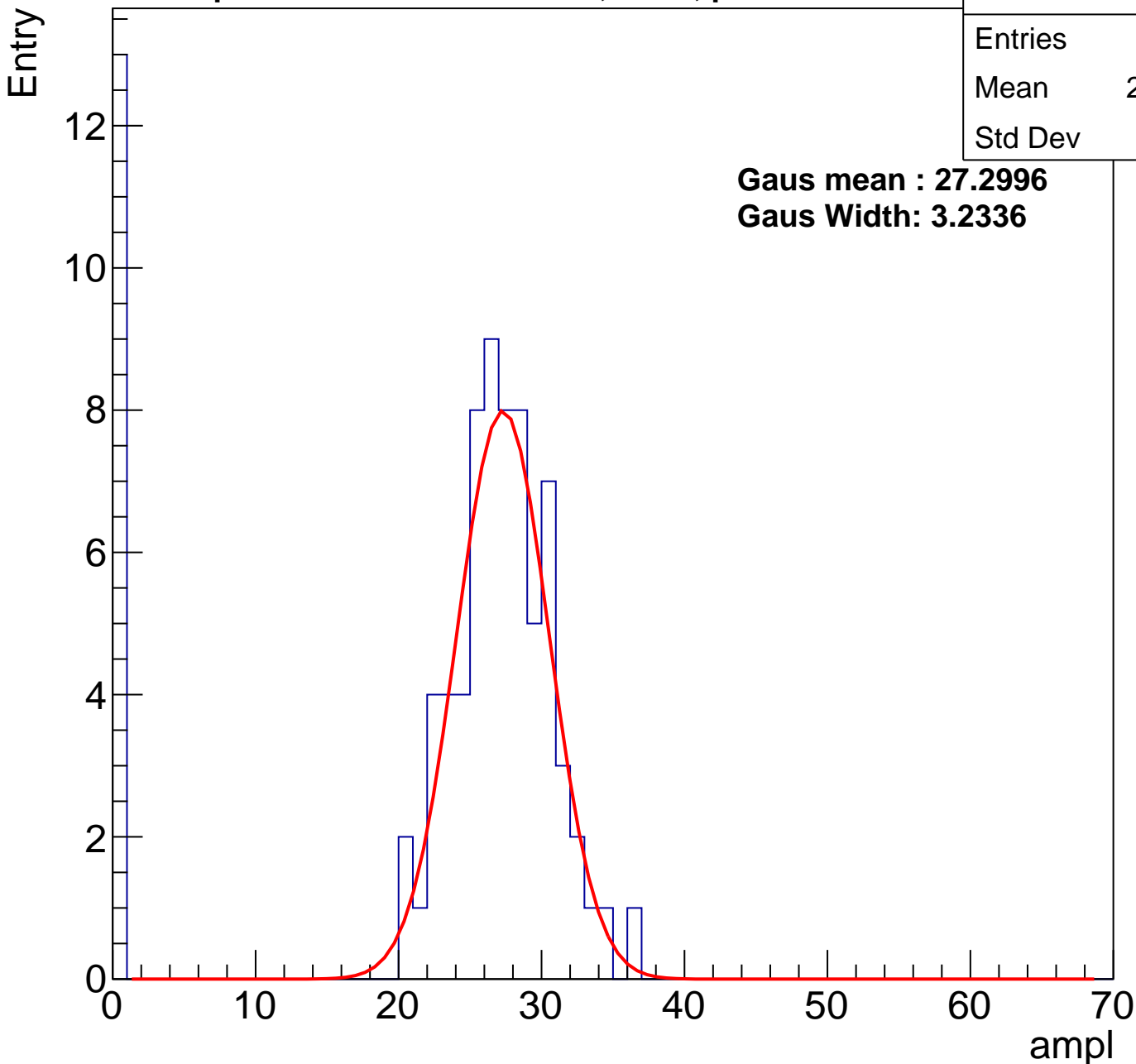
B1L103S, U18-ch32, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	22.54
Std Dev	10.3

Gaus mean : 27.2996

Gaus Width: 3.2336



B1L103S, U18-ch32, adc1

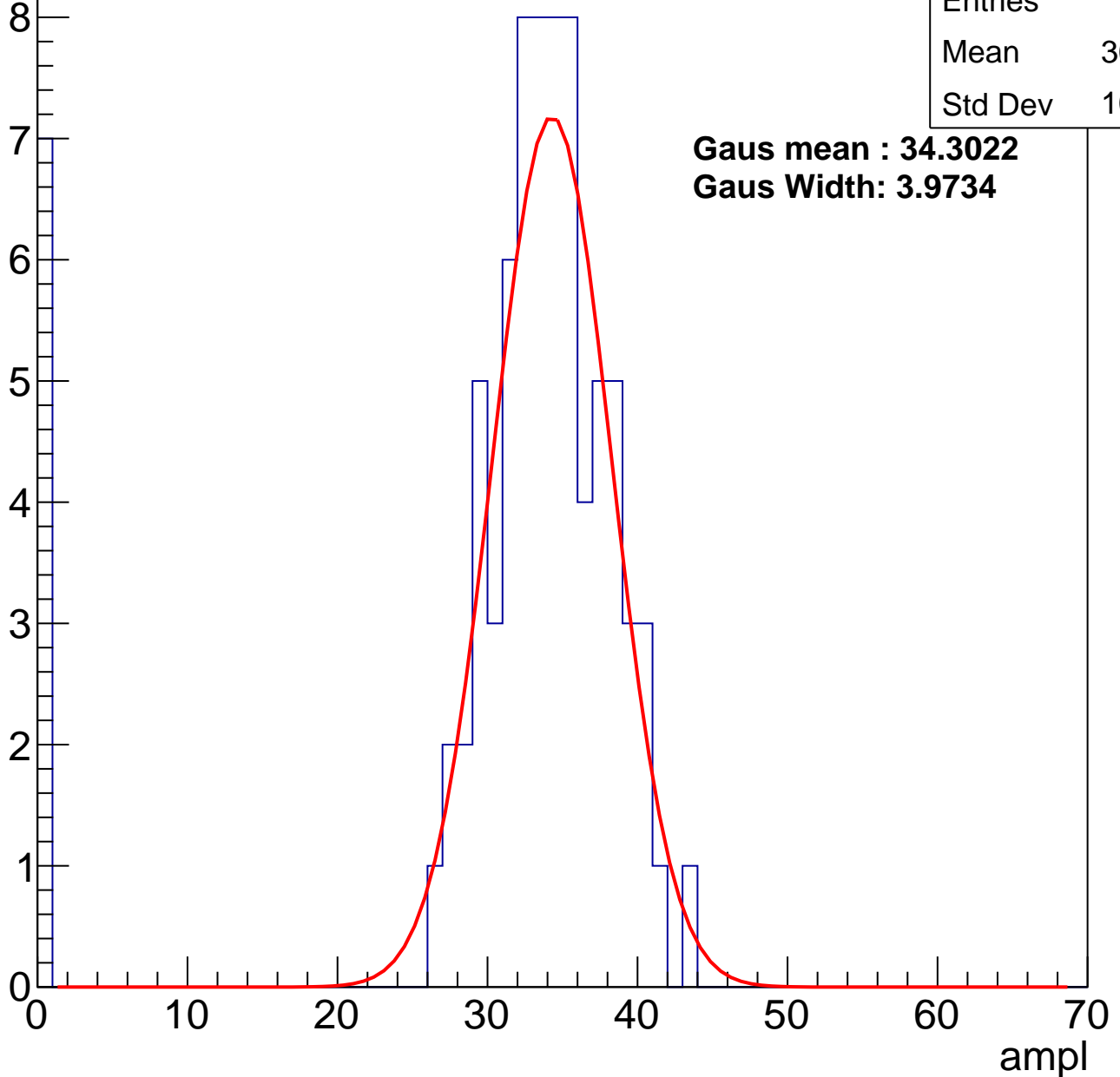
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	30.86
Std Dev	10.17

Gaus mean : 34.3022

Gaus Width: 3.9734



B1L103S, U18-ch32, adc2

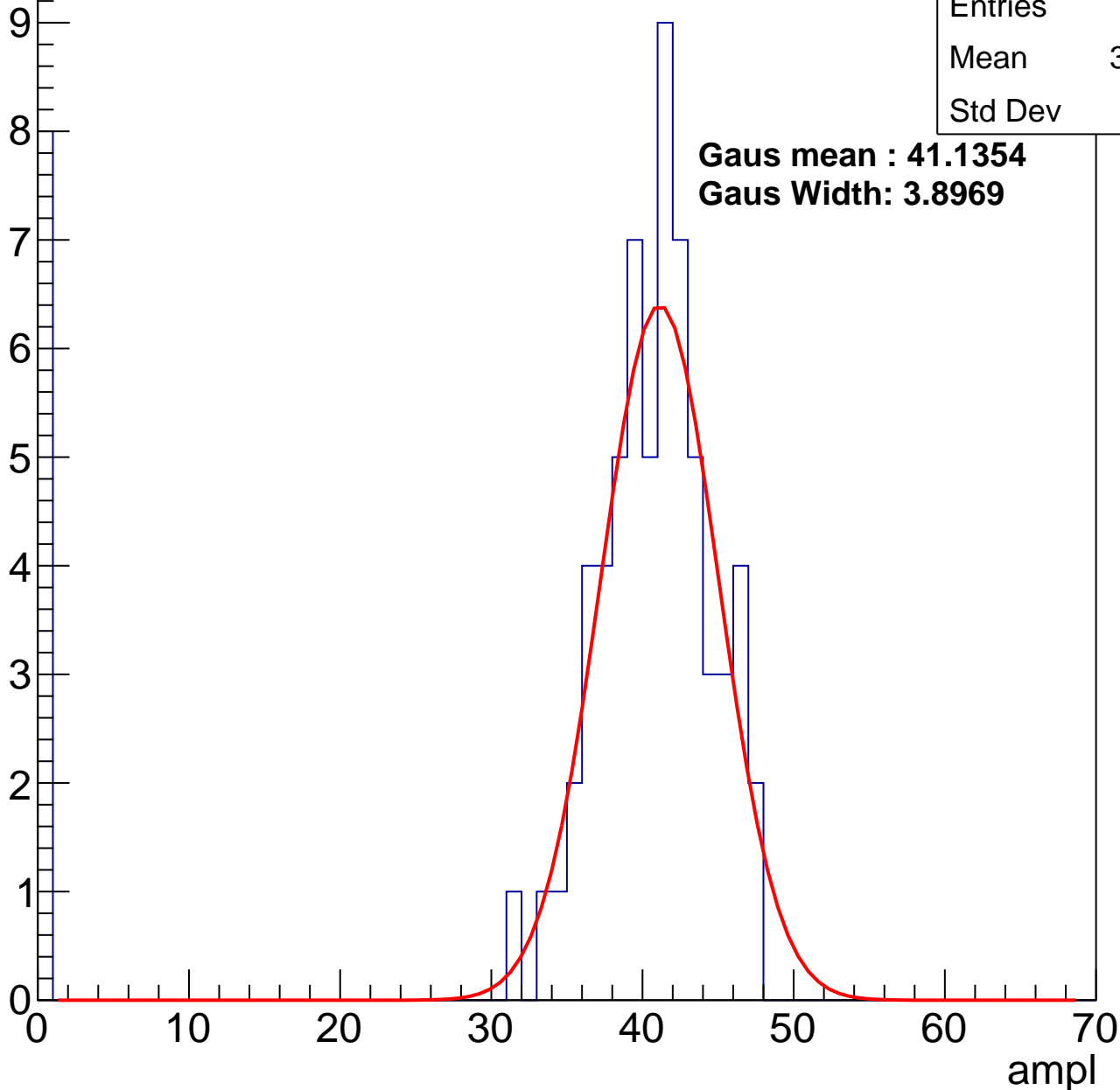
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	35.86
Std Dev	13.2

Gaus mean : 41.1354

Gaus Width: 3.8969

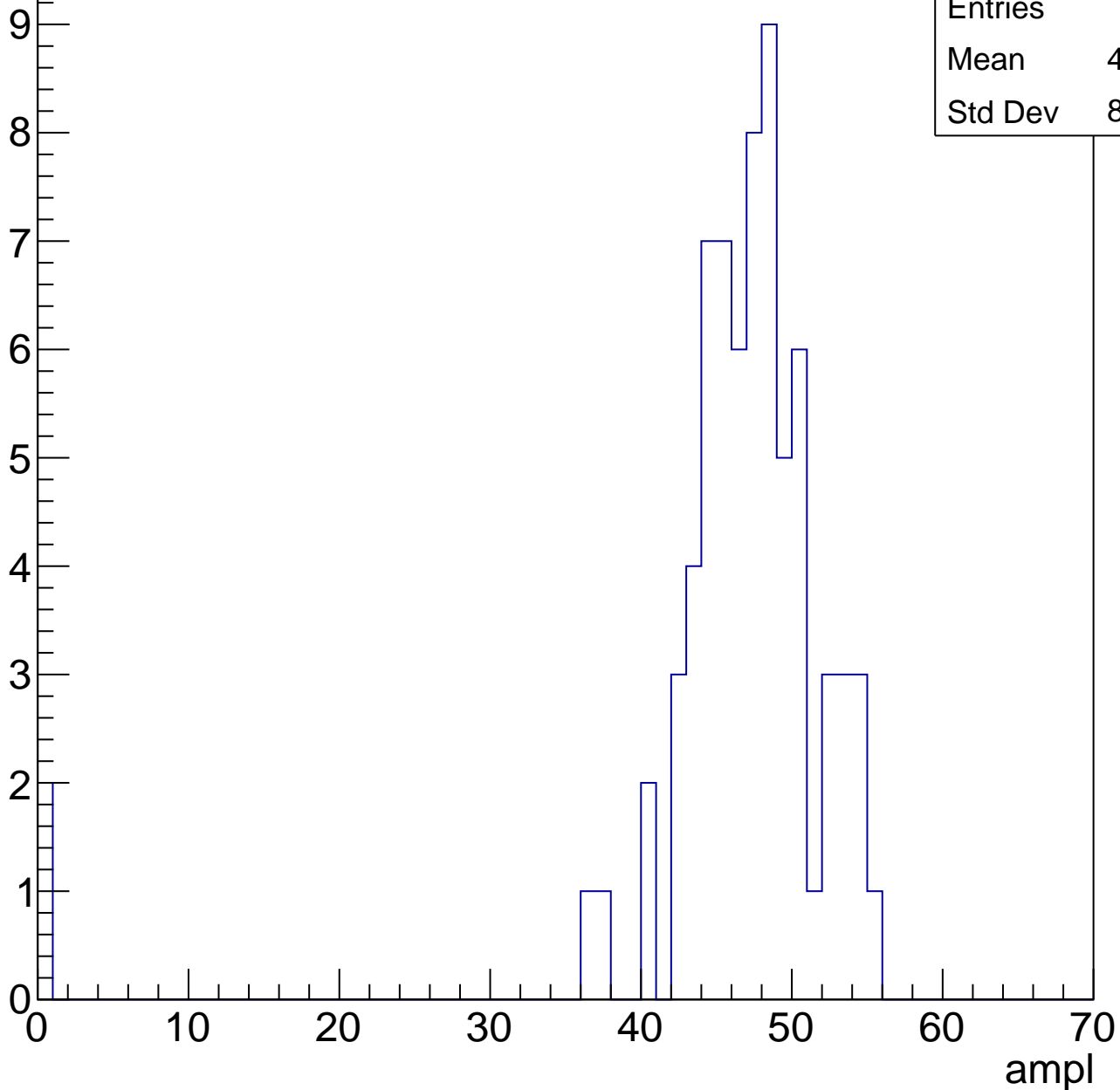


B1L103S, U18-ch32, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	45.64
Std Dev	8.606

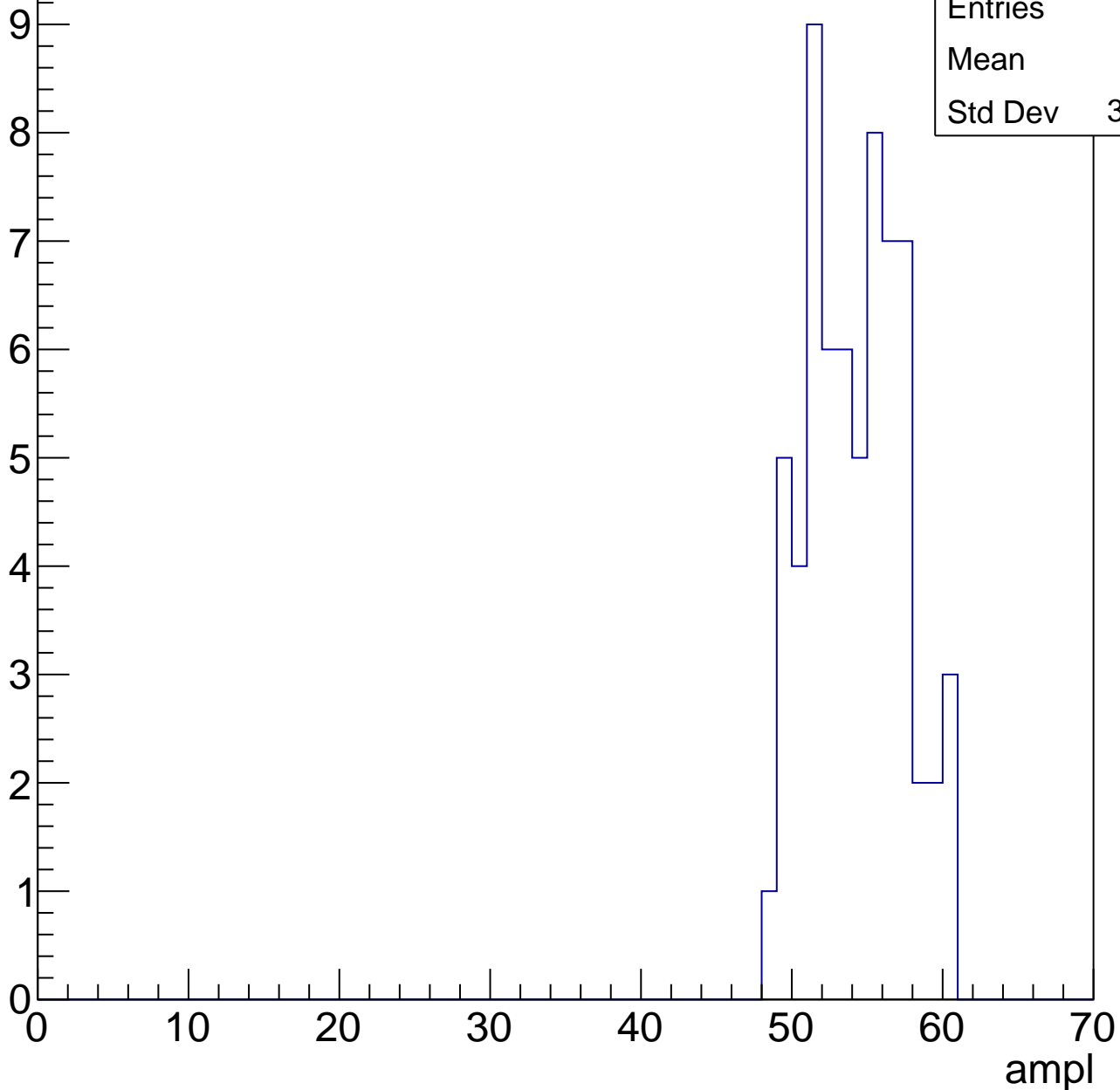


B1L103S, U18-ch32, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	53.8
Std Dev	3.094

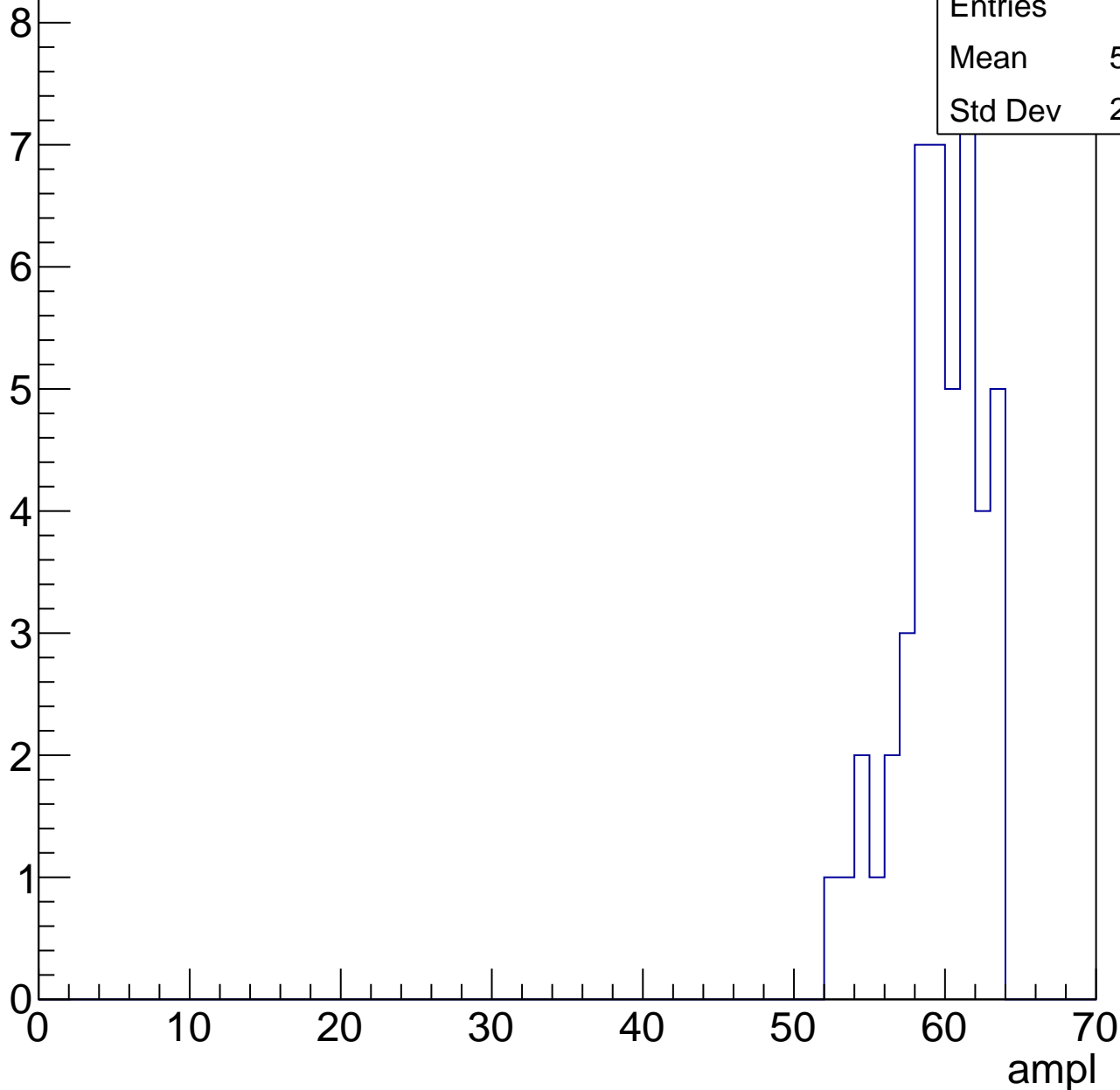


B1L103S, U18-ch32, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	59.15
Std Dev	2.718

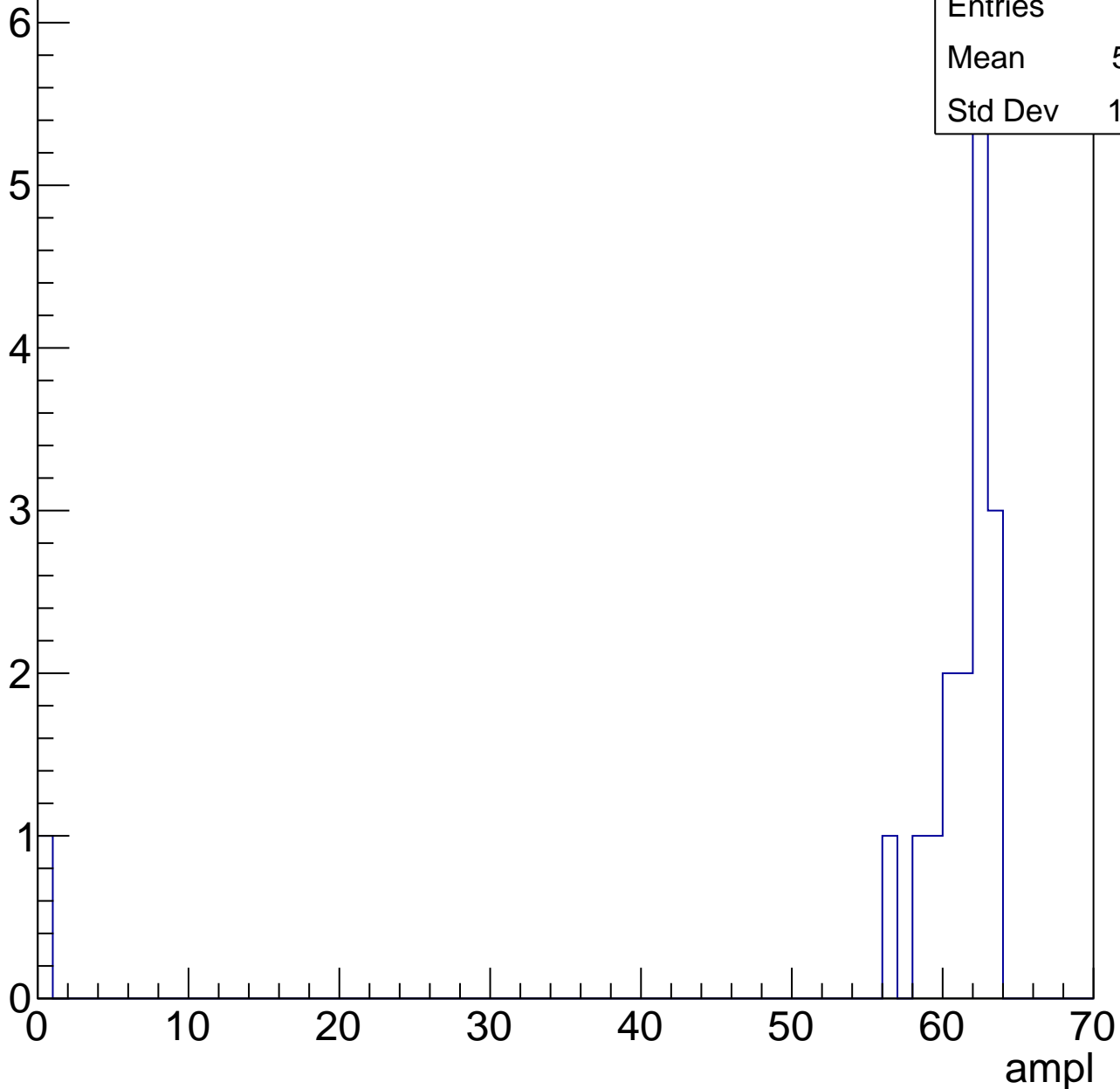


B1L103S, U18-ch32, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.41
Std Dev	14.47



B1L103S, U18-ch32, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



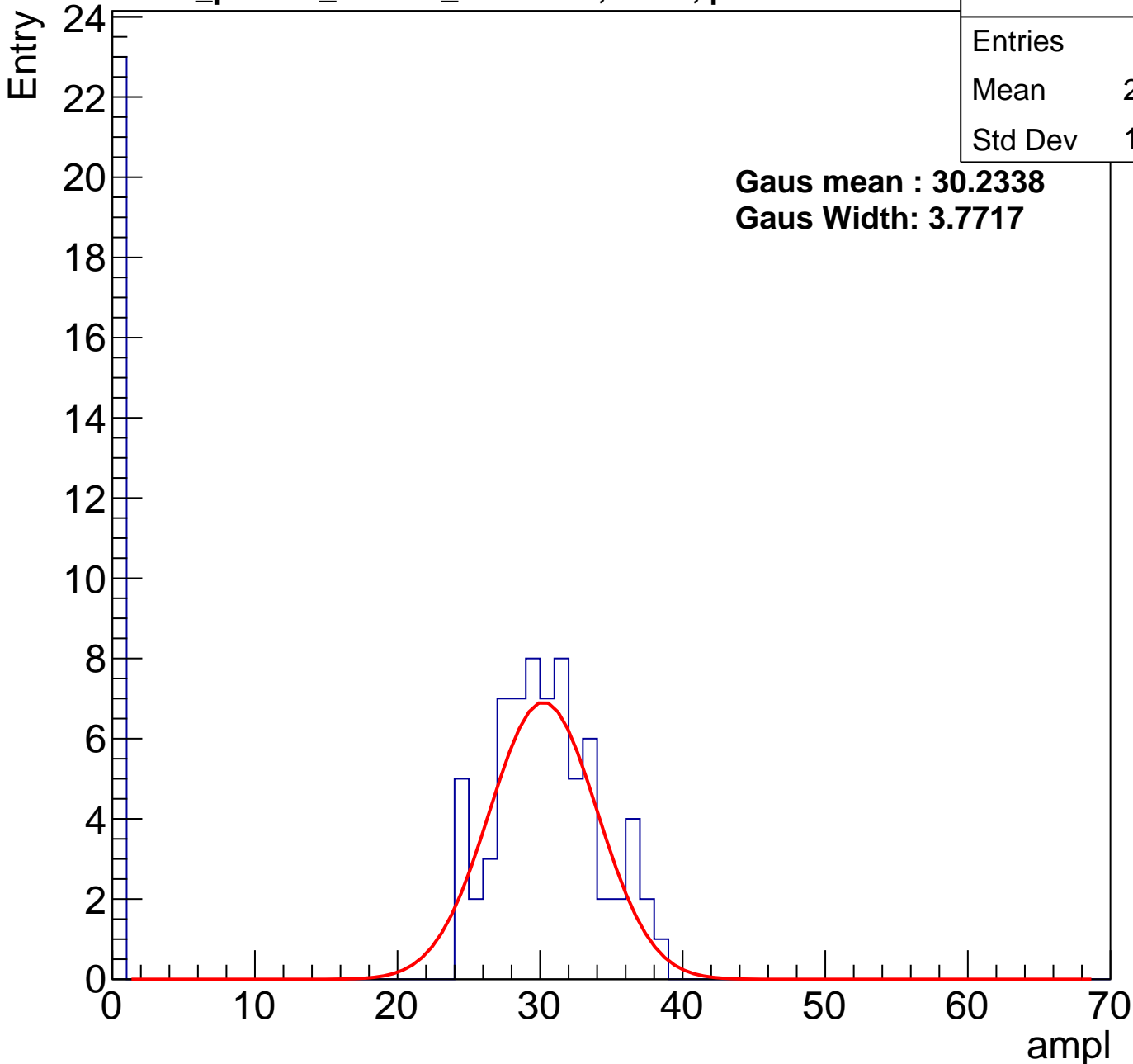
B1L103S, U18-ch33, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	22.55
Std Dev	13.37

Gaus mean : 30.2338

Gaus Width: 3.7717



B1L103S, U18-ch33, adc1

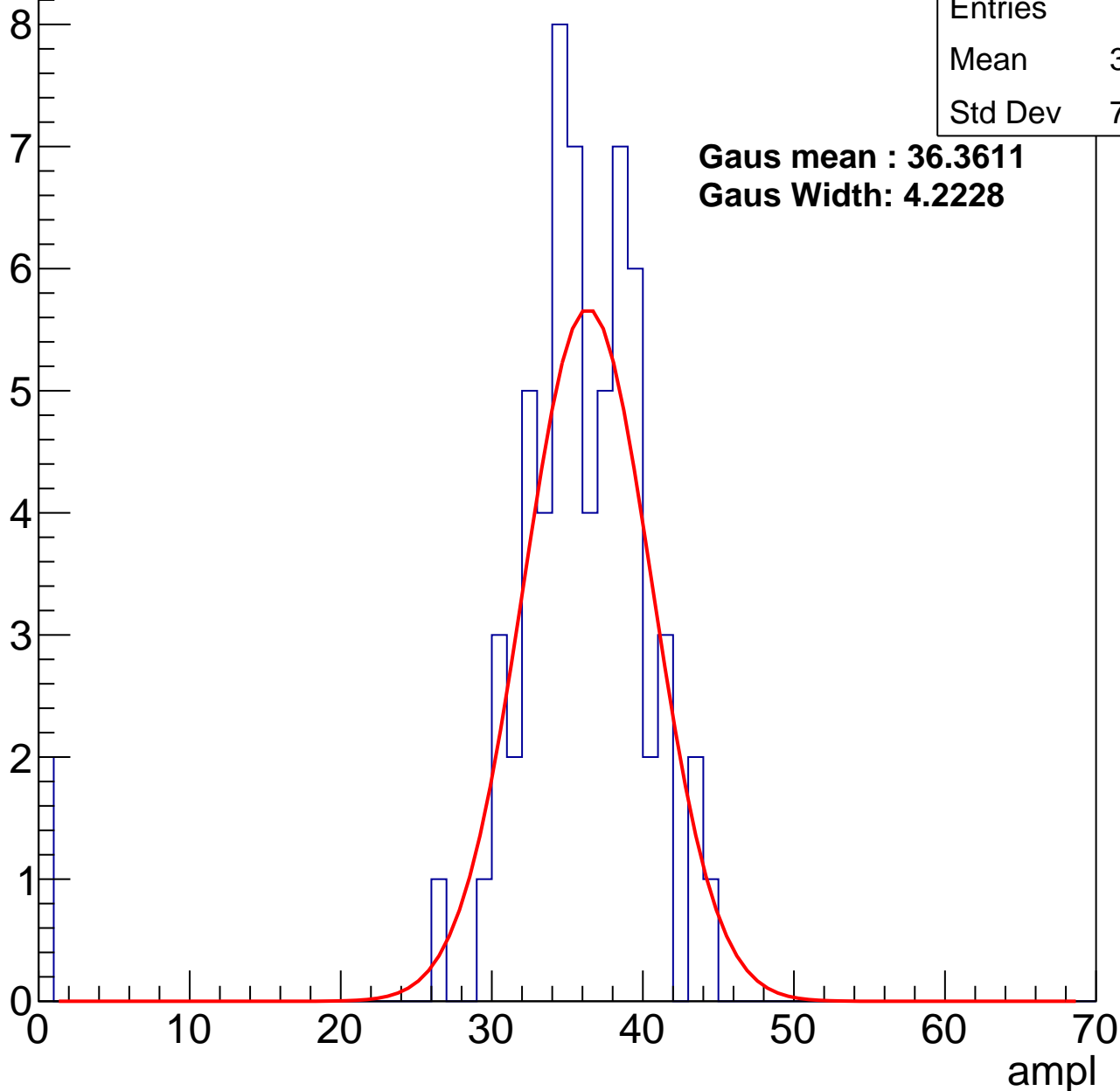
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.57
Std Dev	7.217

Gaus mean : 36.3611

Gaus Width: 4.2228



B1L103S, U18-ch33, adc2

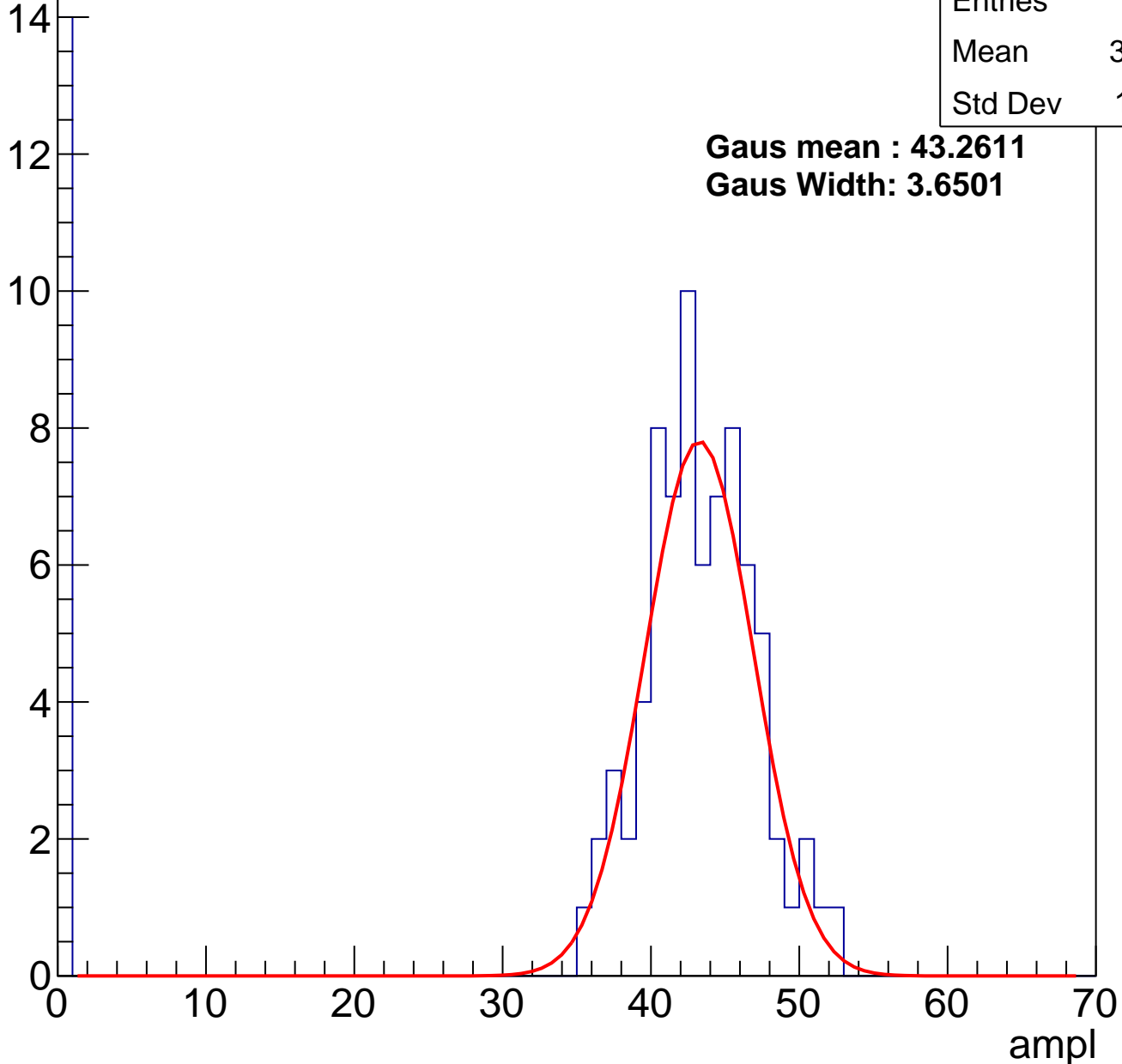
calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	36.24
Std Dev	15.91

Gaus mean : 43.2611

Gaus Width: 3.6501

Entry

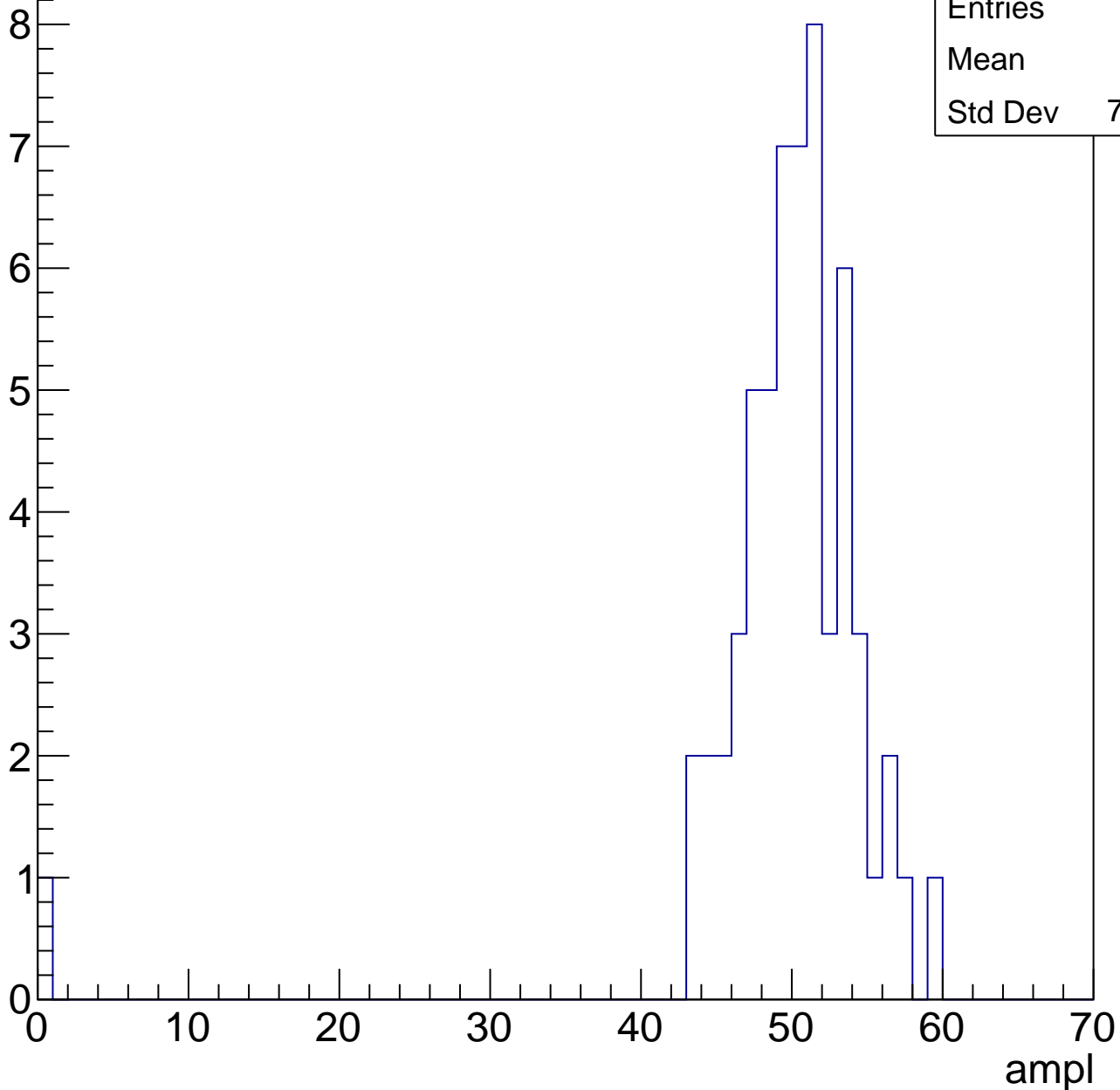


B1L103S, U18-ch33, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	49.1
Std Dev	7.297

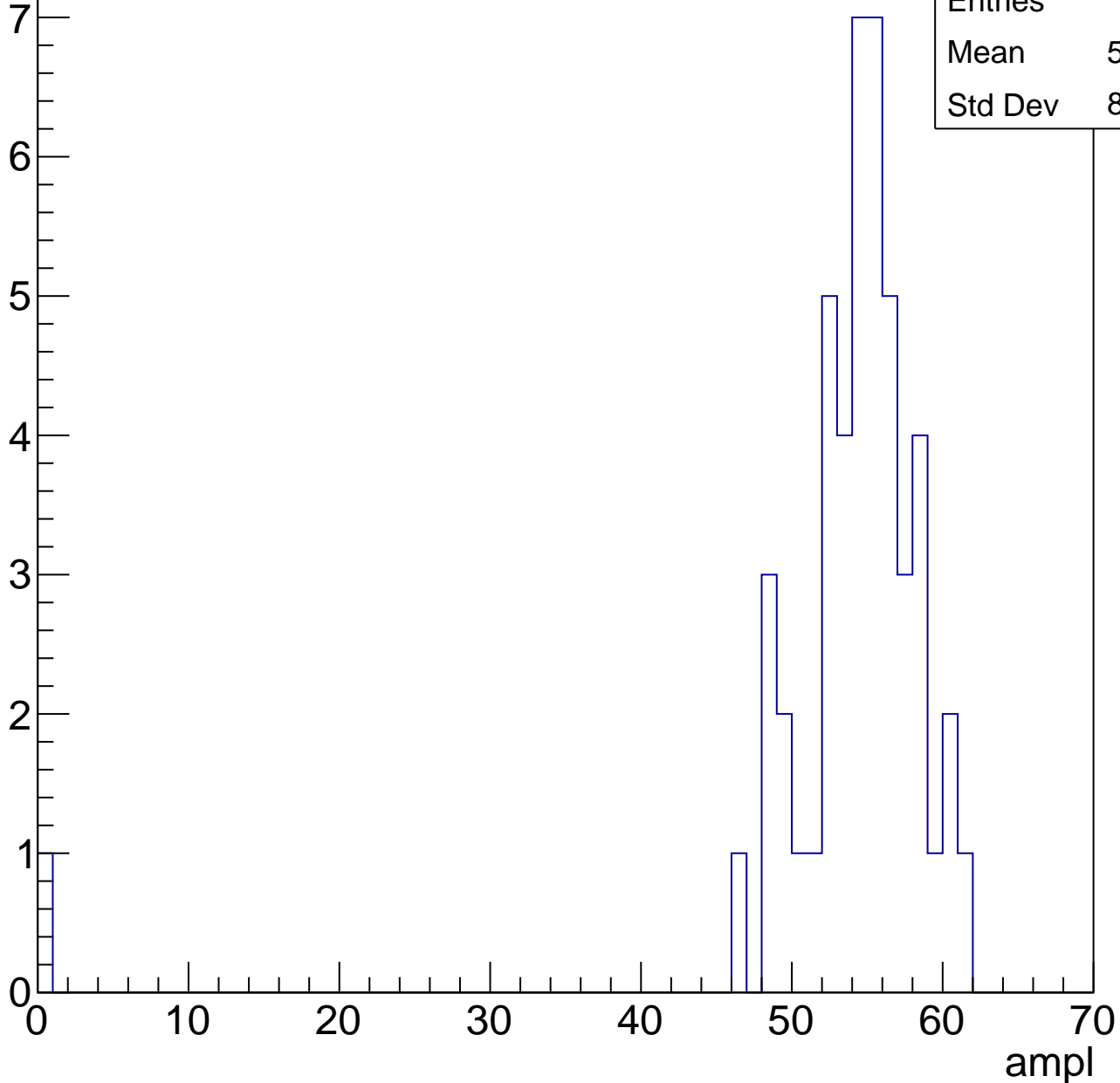


B1L103S, U18-ch33, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

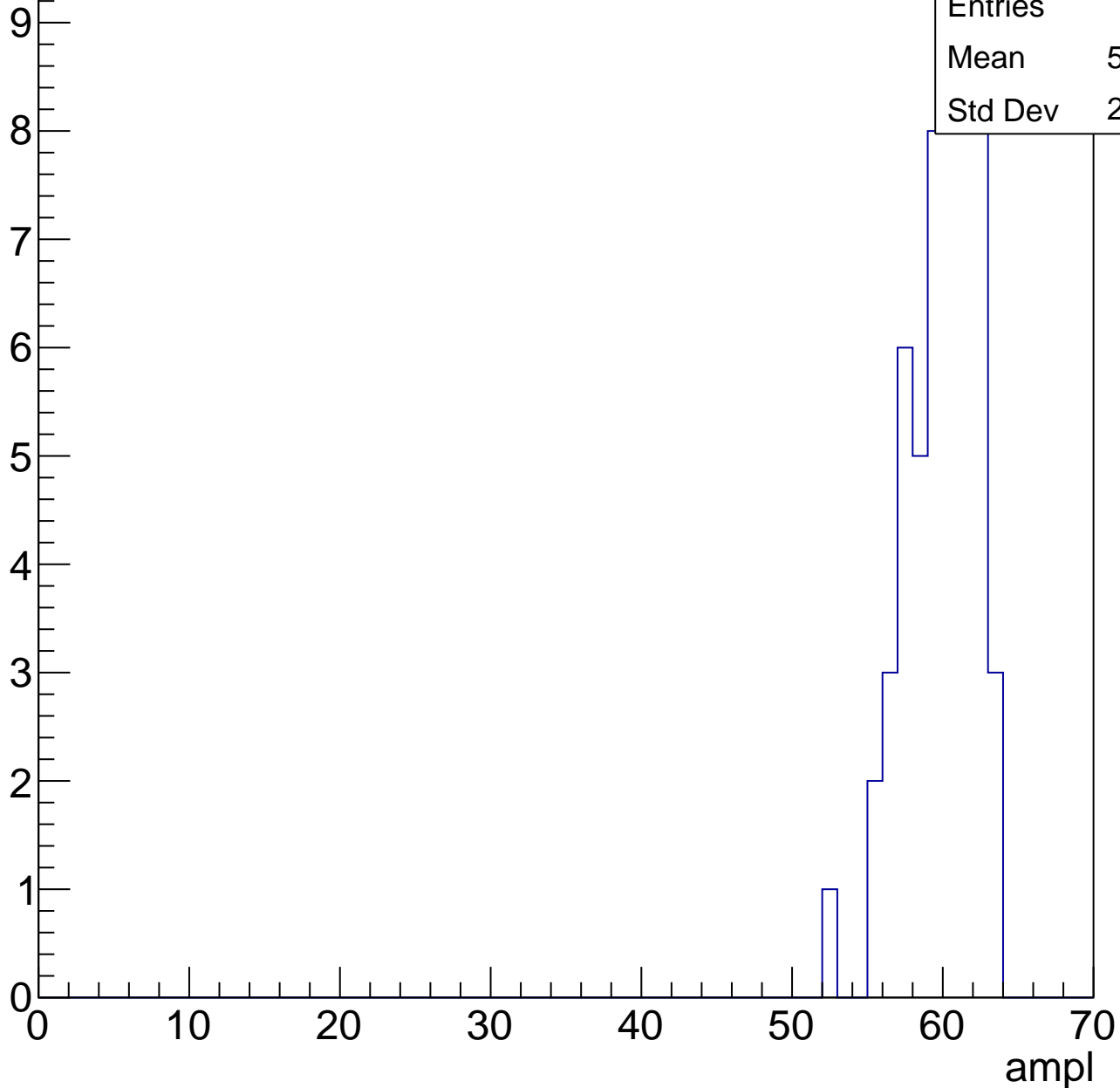
Entries	48
Mean	53.06
Std Dev	8.427



B1L103S, U18-ch33, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

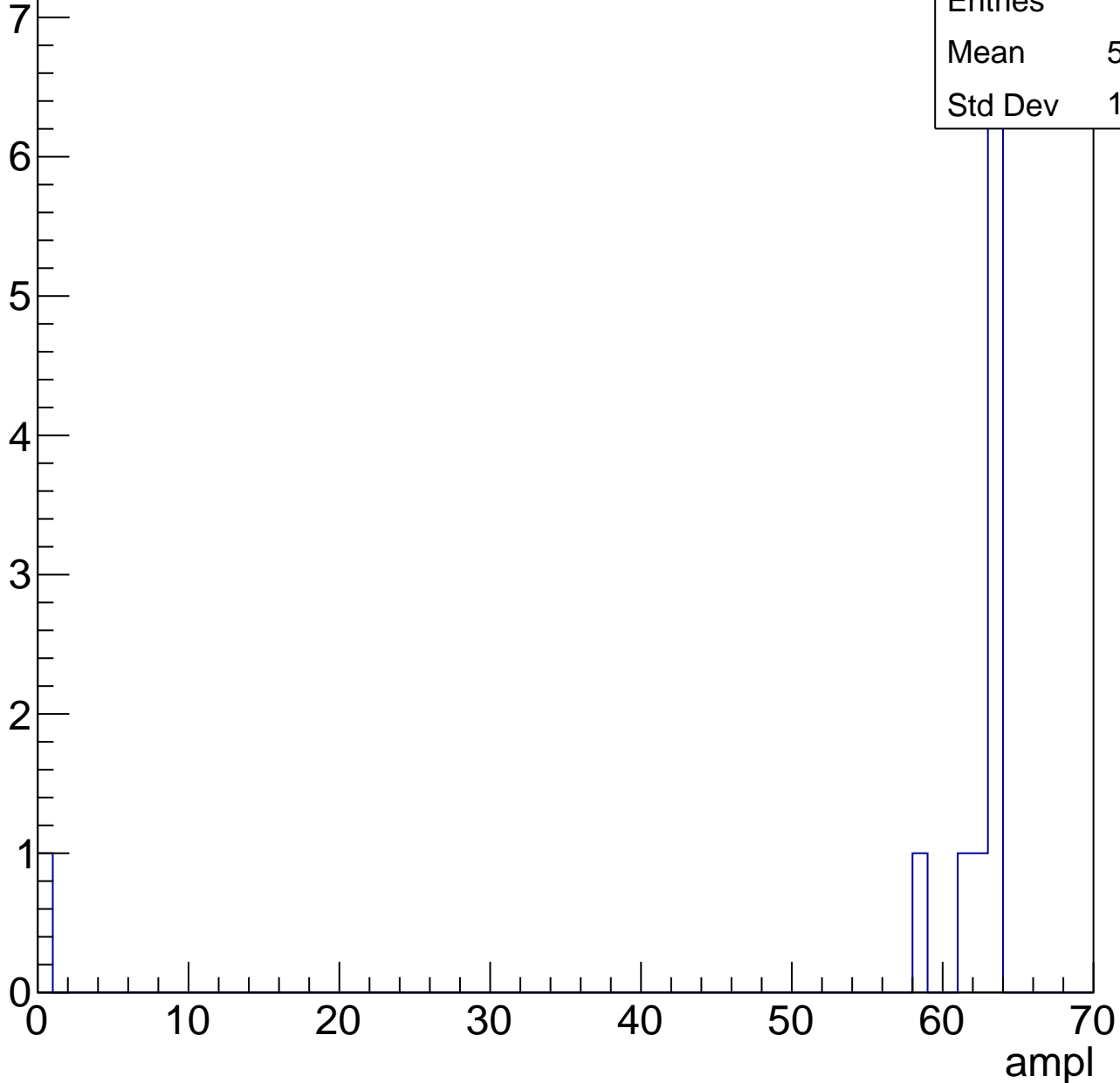


B1L103S, U18-ch33, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	56.55
Std Dev	17.94



B1L103S, U18-ch33, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U18-ch34, adc0

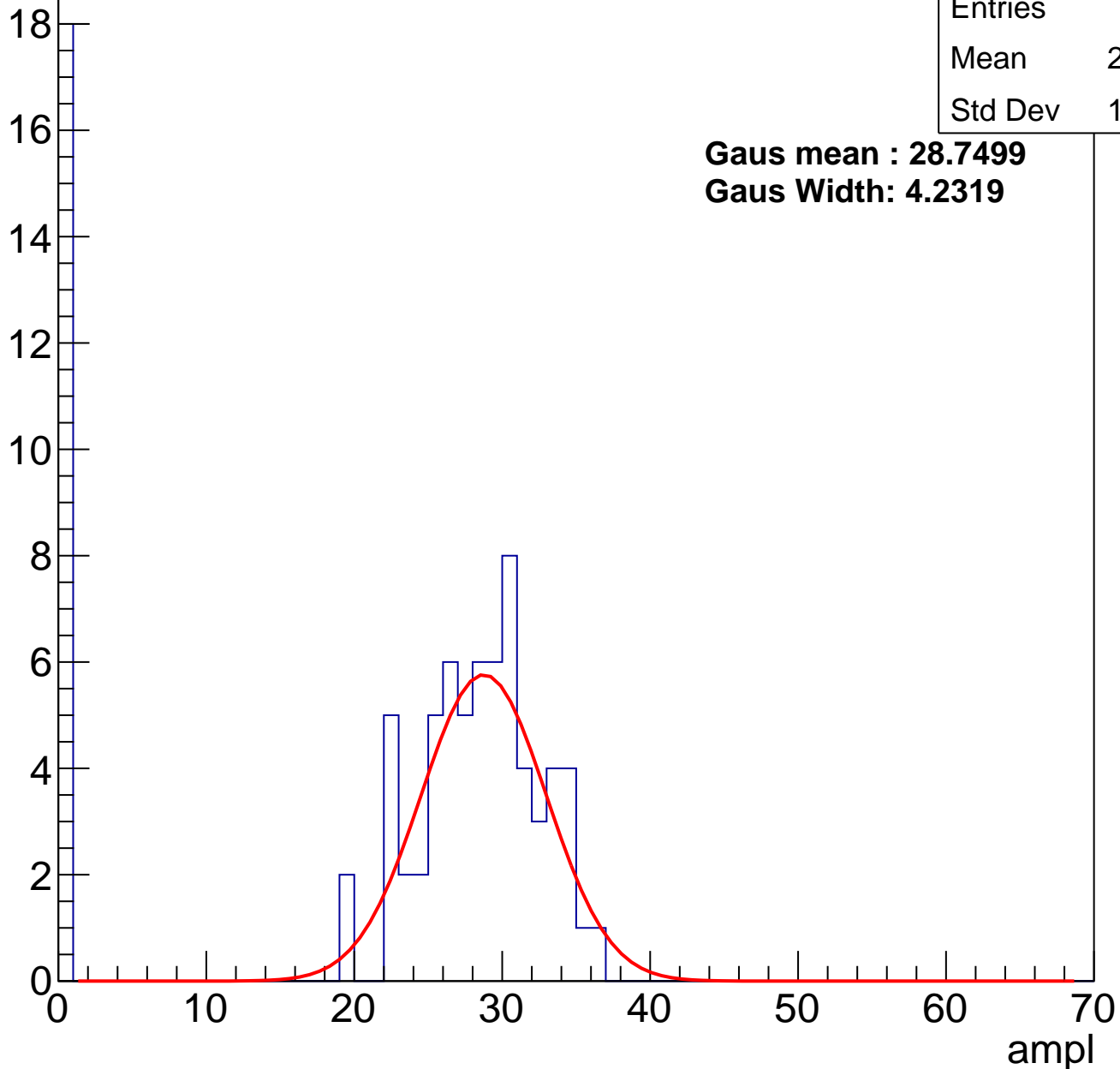
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	21.94
Std Dev	12.14

Gaus mean : 28.7499

Gaus Width: 4.2319

Entry



B1L103S, U18-ch34, adc1

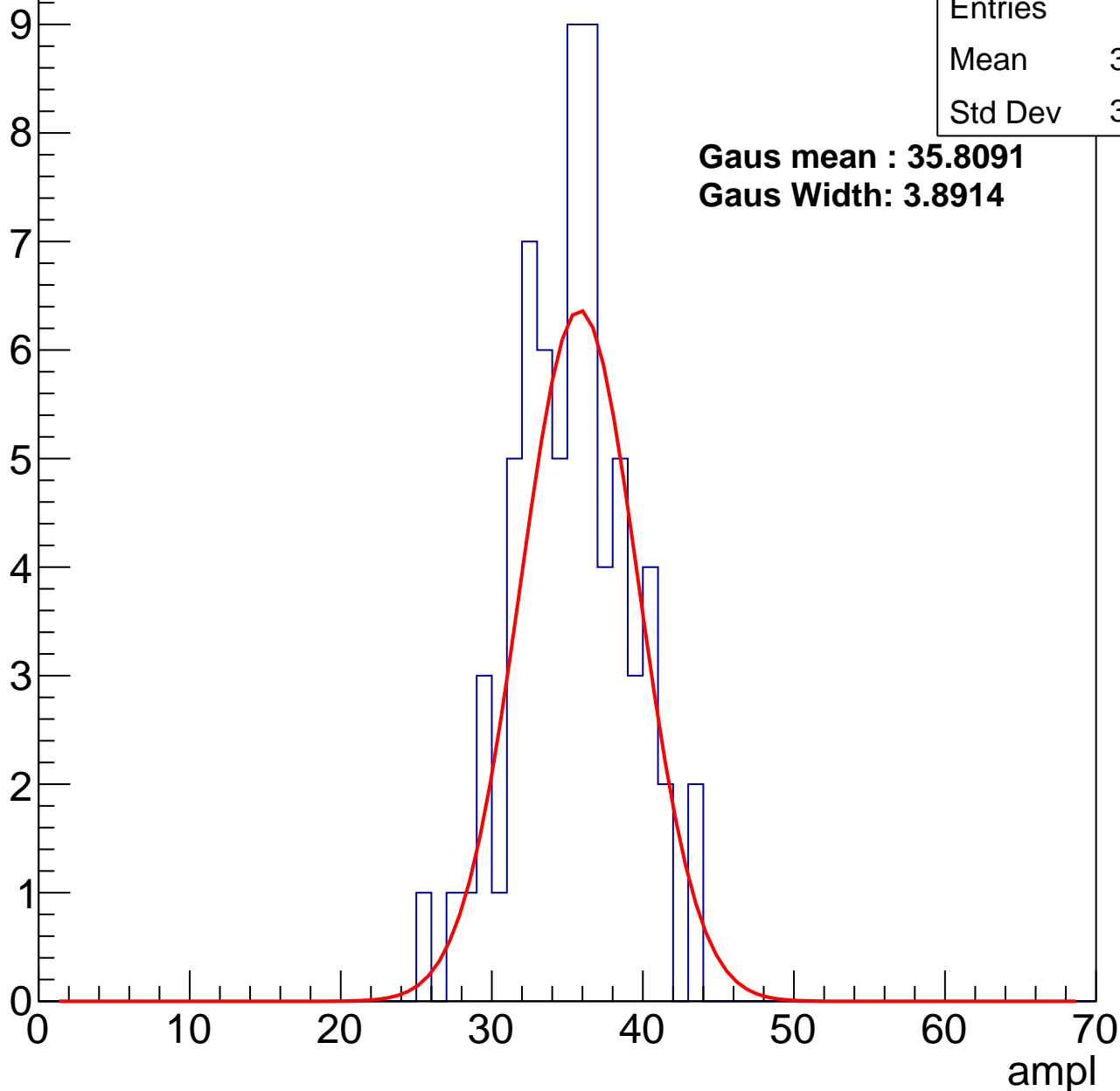
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34.79
Std Dev	3.704

Gaus mean : 35.8091

Gaus Width: 3.8914



B1L103S, U18-ch34, adc2

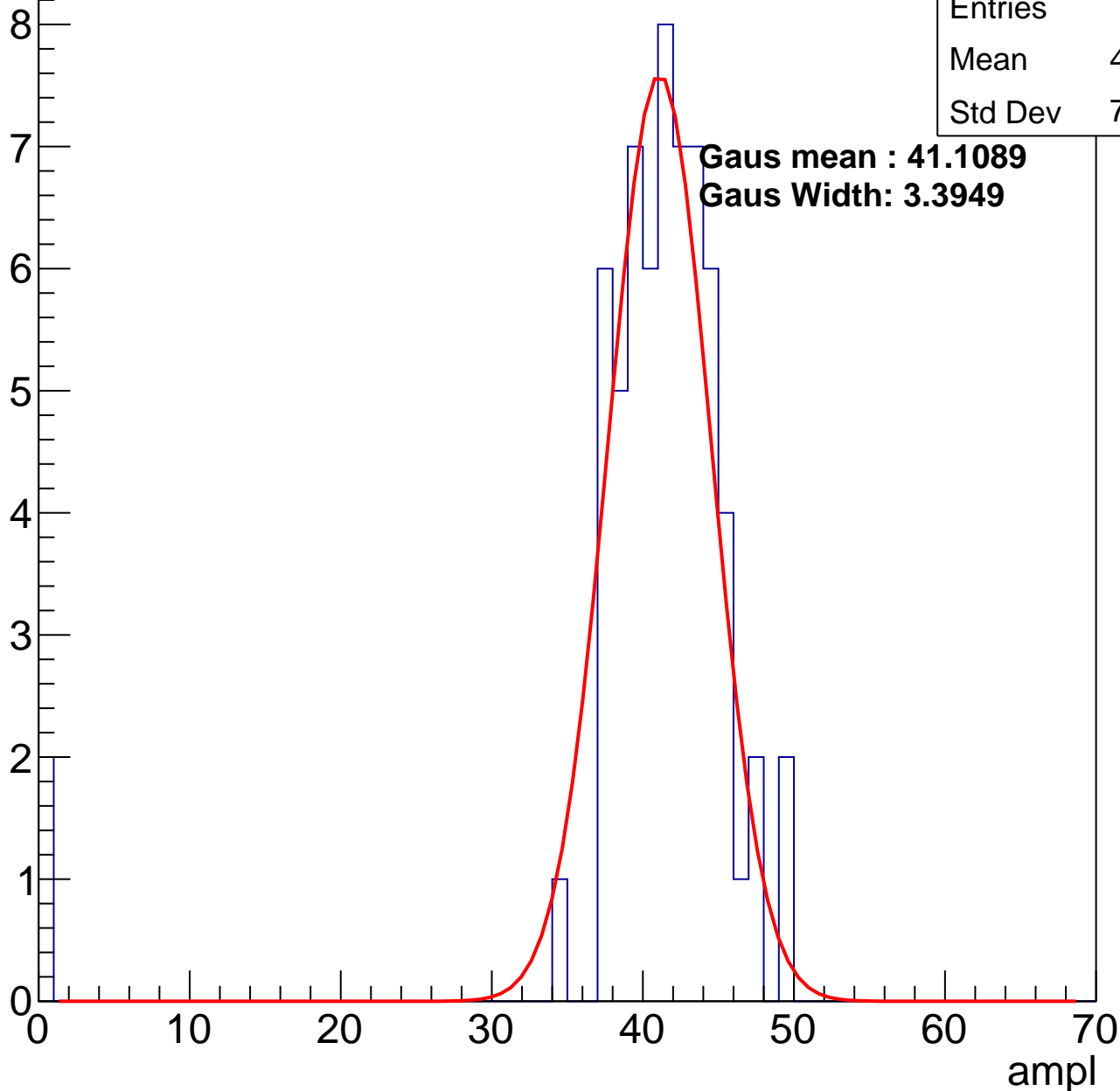
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	40.06
Std Dev	7.814

Gaus mean : 41.1089

Gaus Width: 3.3949

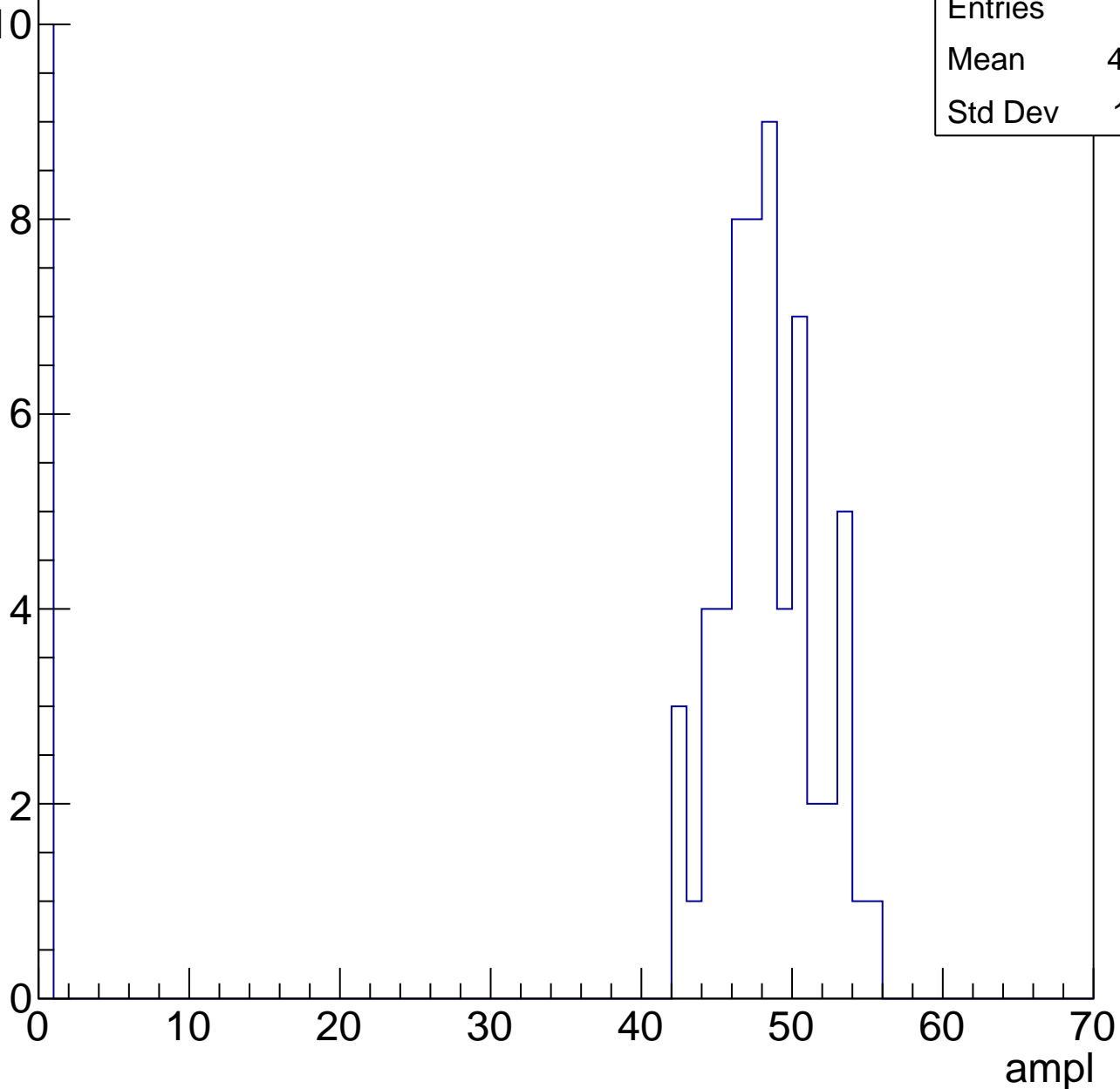


B1L103S, U18-ch34, adc3

calib_packv5_041523_1651.root, FC#0, port C2

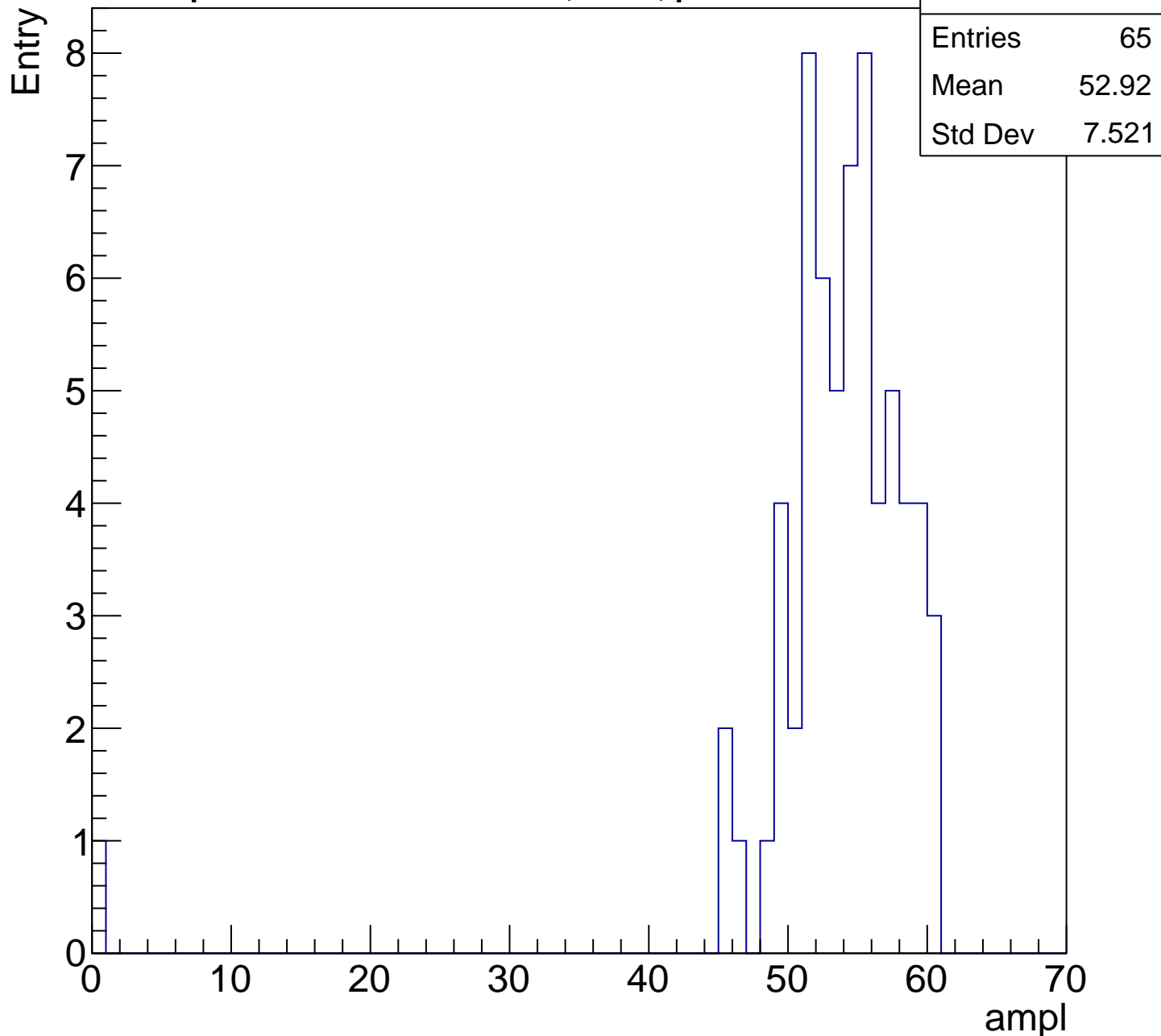
Entry

Entries	69
Mean	40.97
Std Dev	17.11



B1L103S, U18-ch34, adc4

calib_packv5_041523_1651.root, FC#0, port C2

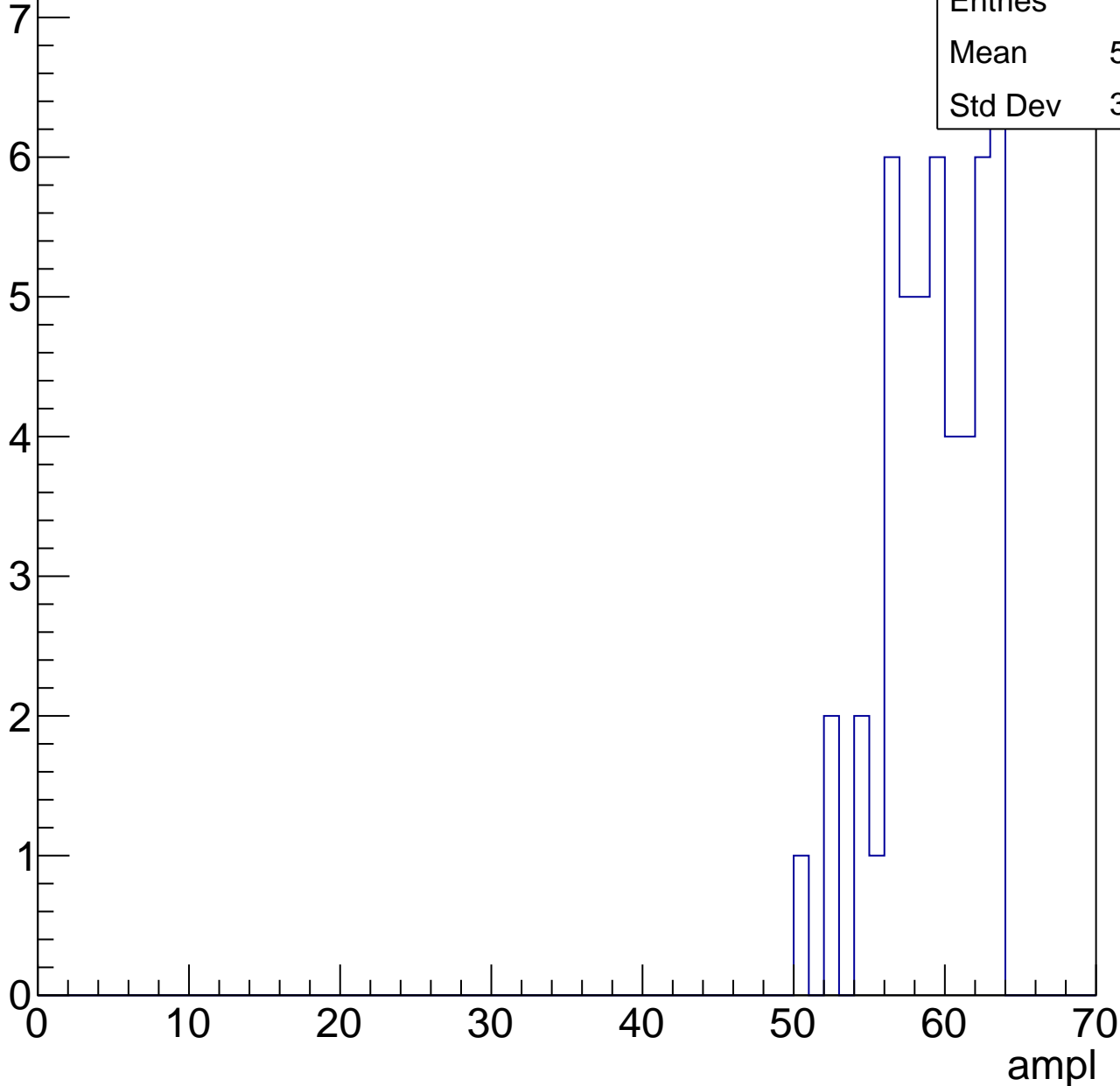


B1L103S, U18-ch34, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.76
Std Dev	3.217

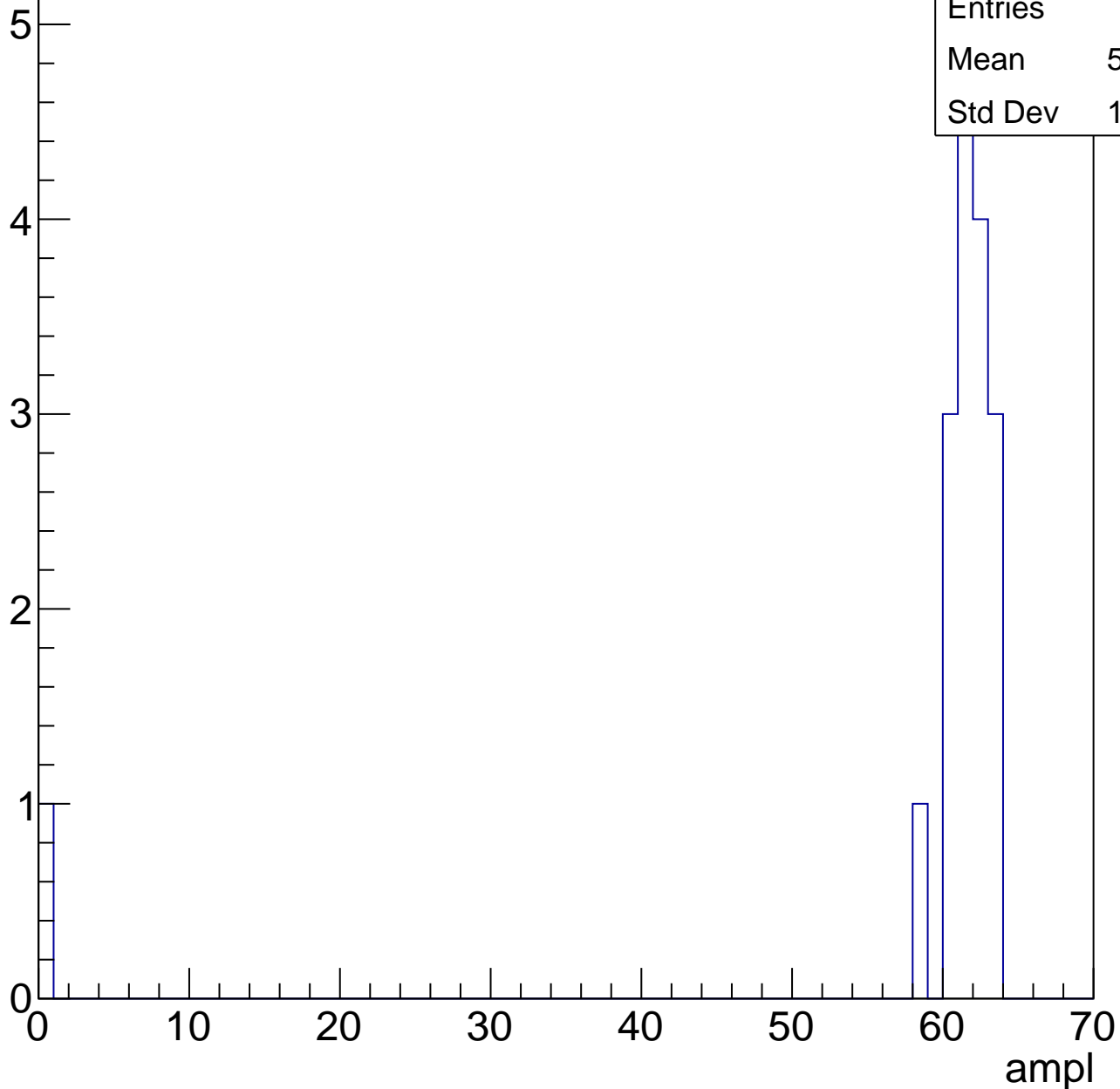


B1L103S, U18-ch34, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.65
Std Dev	14.47



B1L103S, U18-ch34, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U18-ch35, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	26.03
Std Dev	12.09

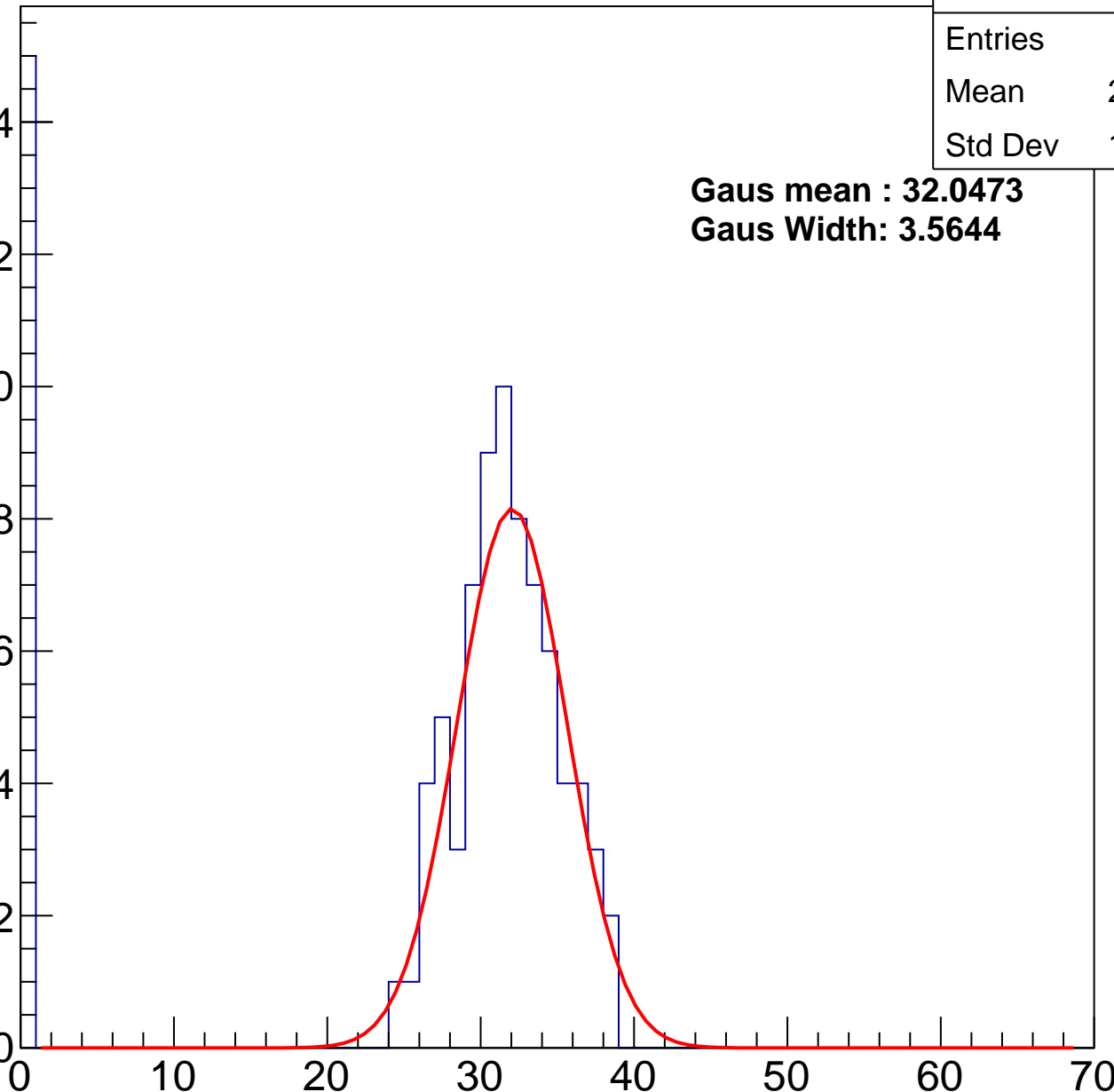
Gaus mean : 32.0473

Gaus Width: 3.5644

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U18-ch35, adc1

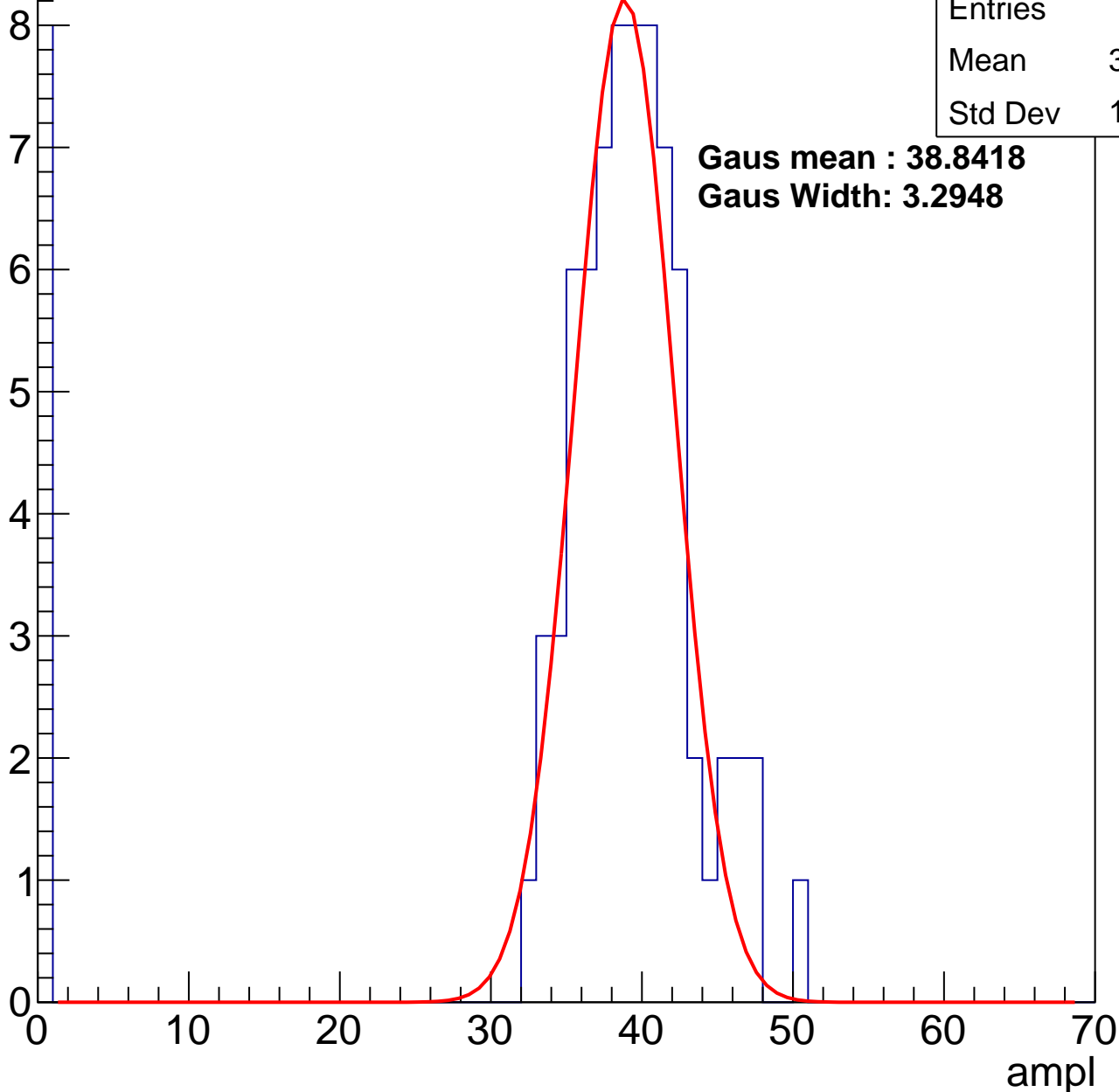
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	35.17
Std Dev	12.15

Gaus mean : 38.8418

Gaus Width: 3.2948



B1L103S, U18-ch35, adc2

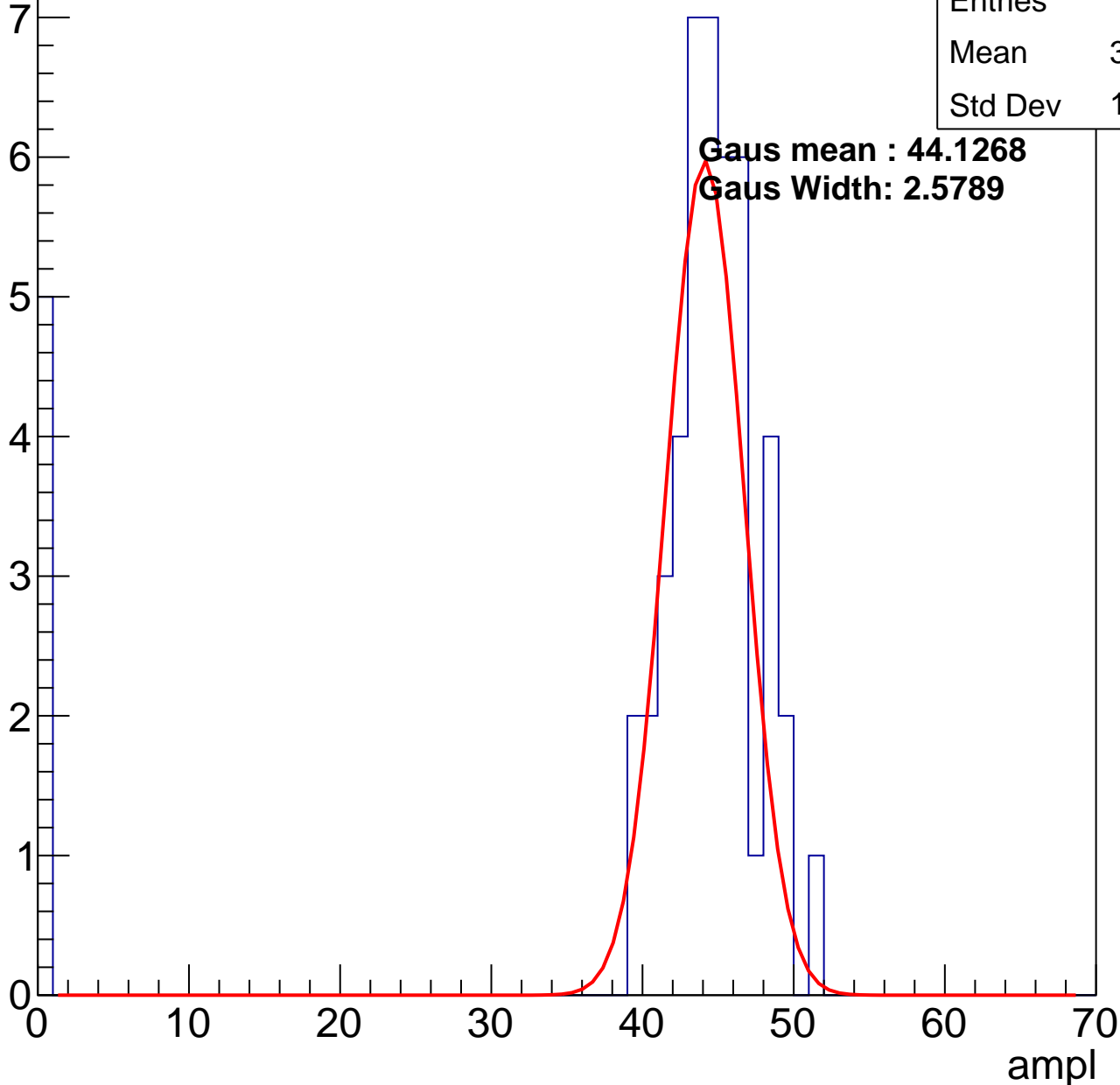
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	39.84
Std Dev	13.53

Gaus mean : 44.1268

Gaus Width: 2.5789

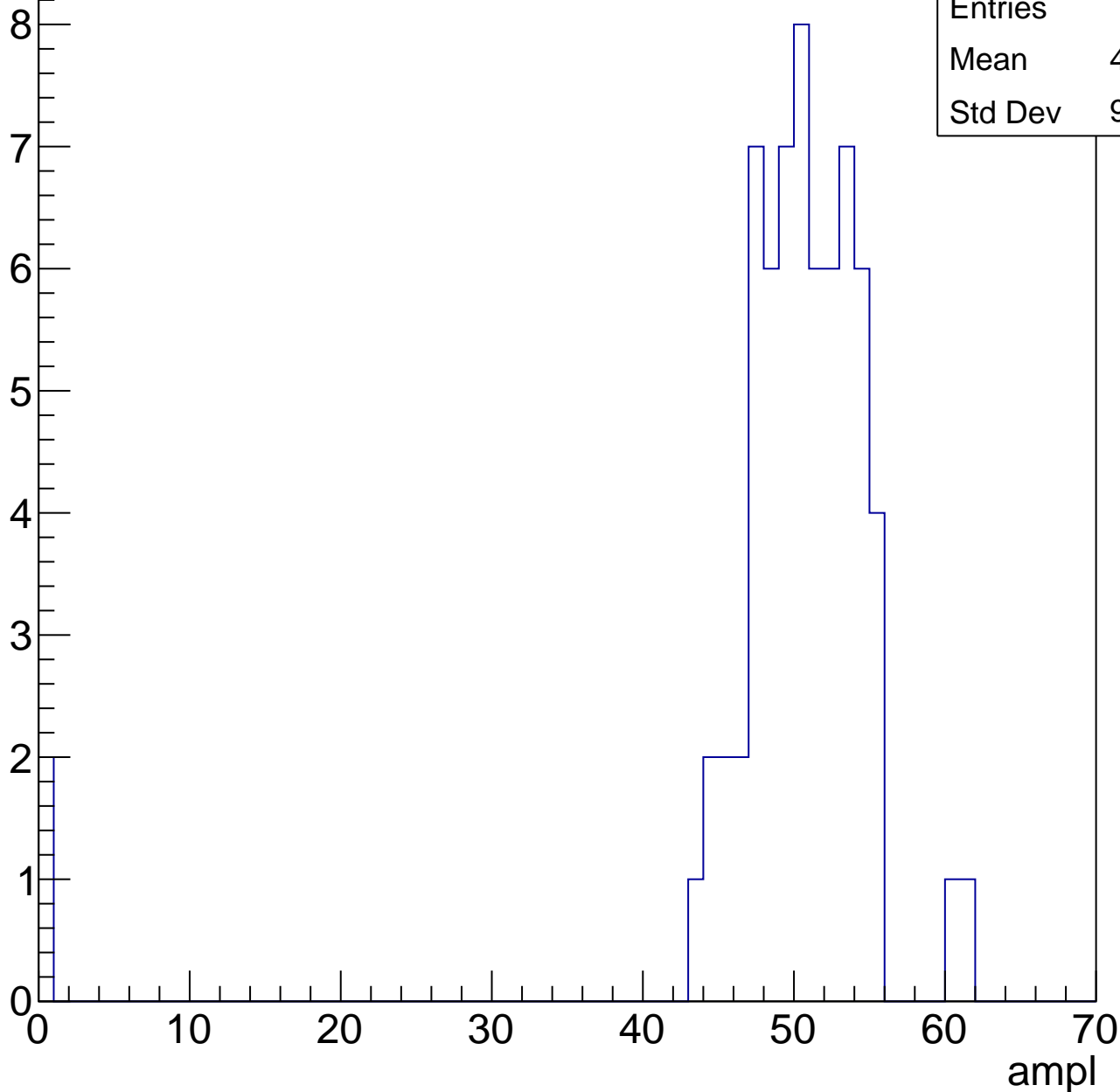


B1L103S, U18-ch35, adc3

calib_packv5_041523_1651.root, FC#0, port C2

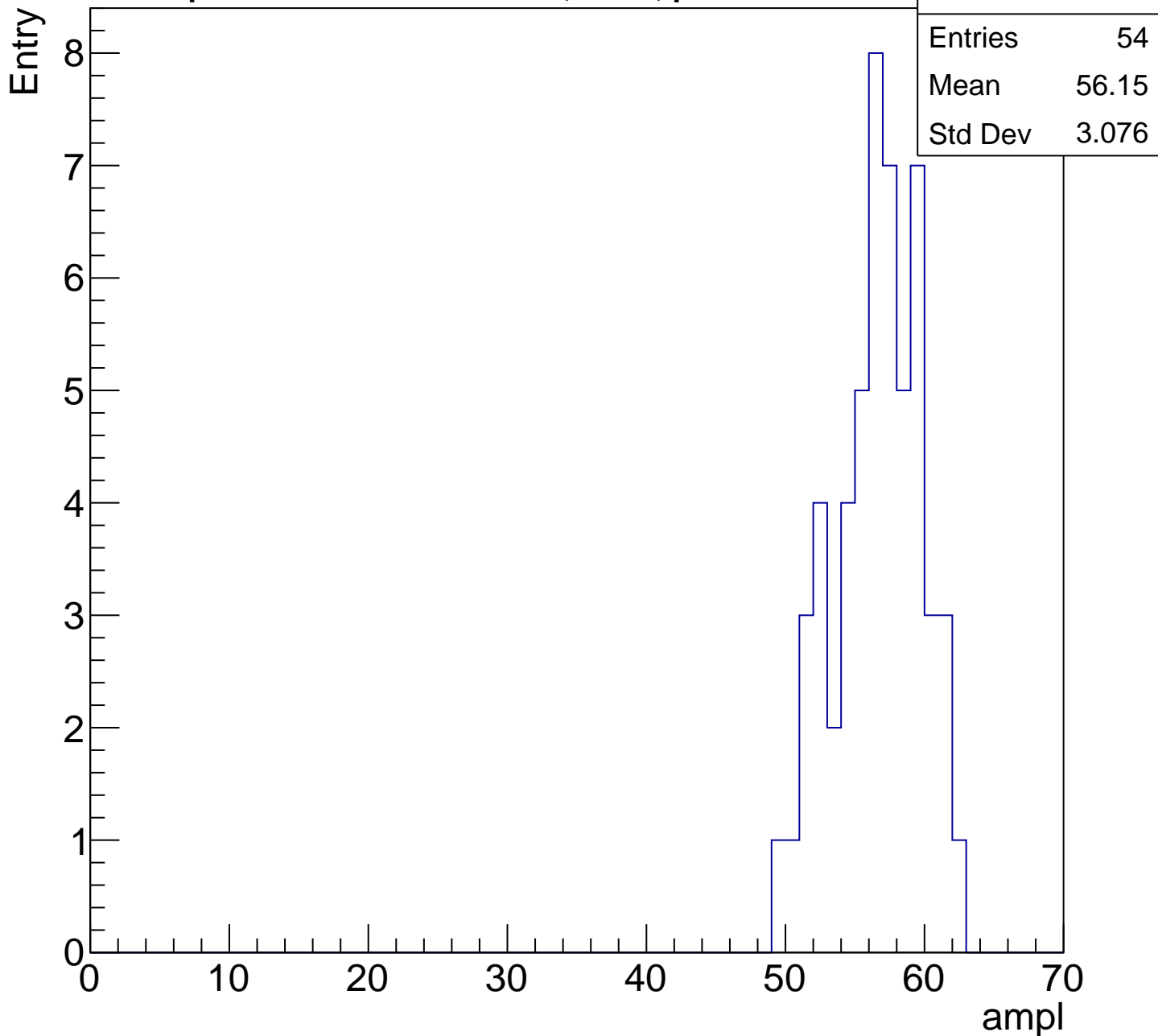
Entry

Entries	68
Mean	48.93
Std Dev	9.178



B1L103S, U18-ch35, adc4

calib_packv5_041523_1651.root, FC#0, port C2

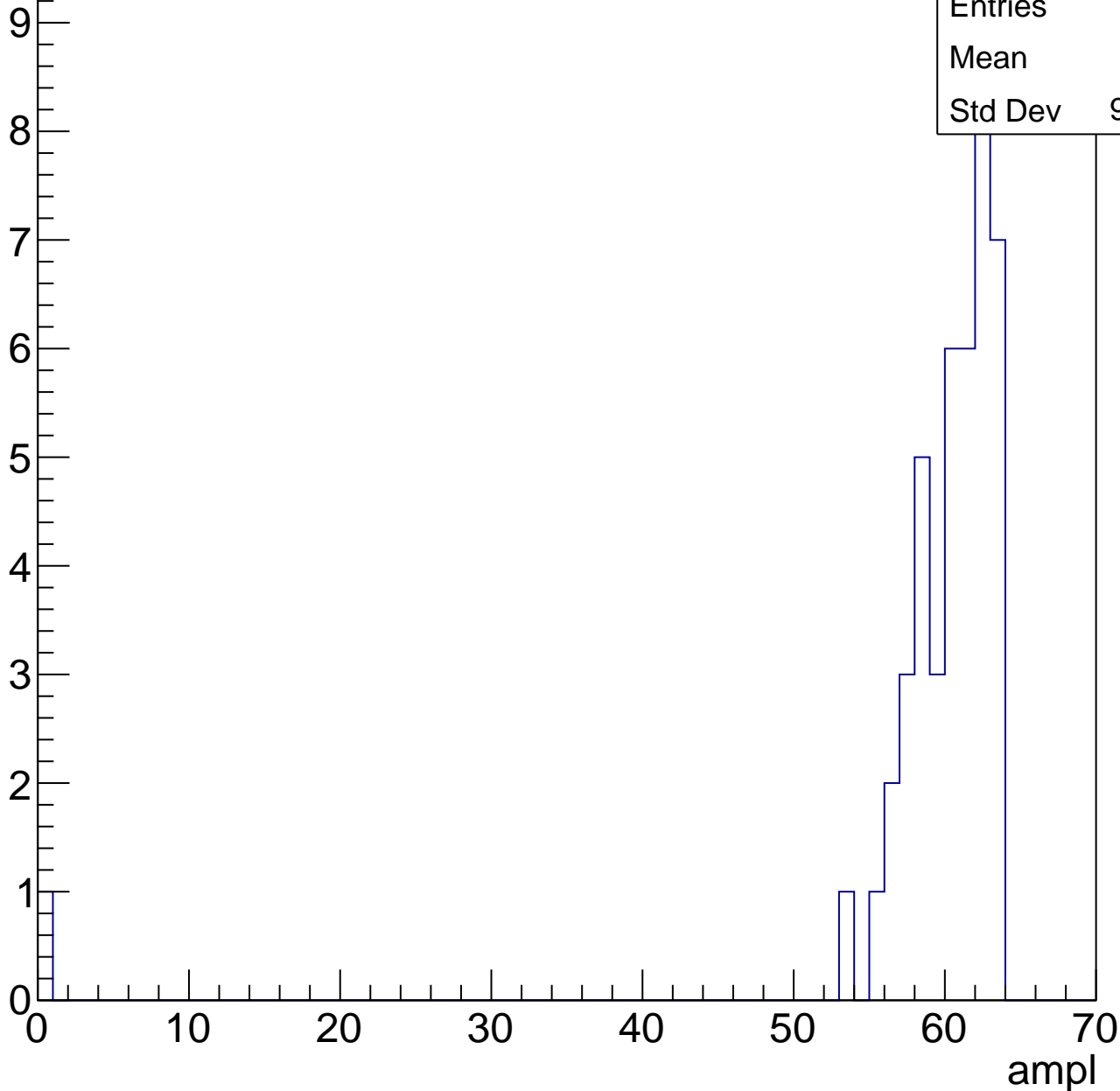


B1L103S, U18-ch35, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	58.7
Std Dev	9.277



B1L103S, U18-ch35, adc6

calib_packv5_041523_1651.root, FC#0, port C2

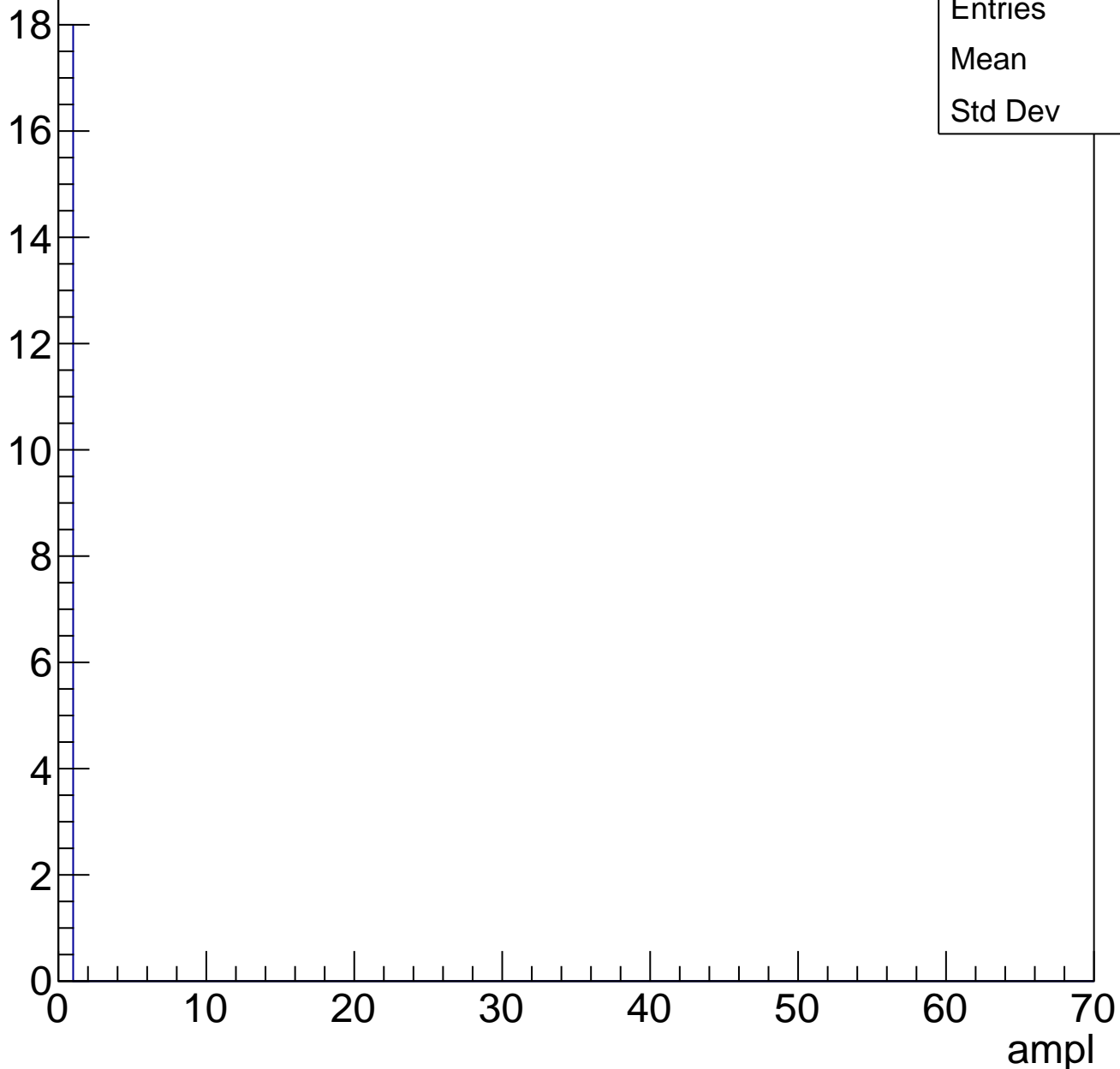
Entry



B1L103S, U18-ch35, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch36, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	23.16
Std Dev	12.22

Gaus mean : 29.7474

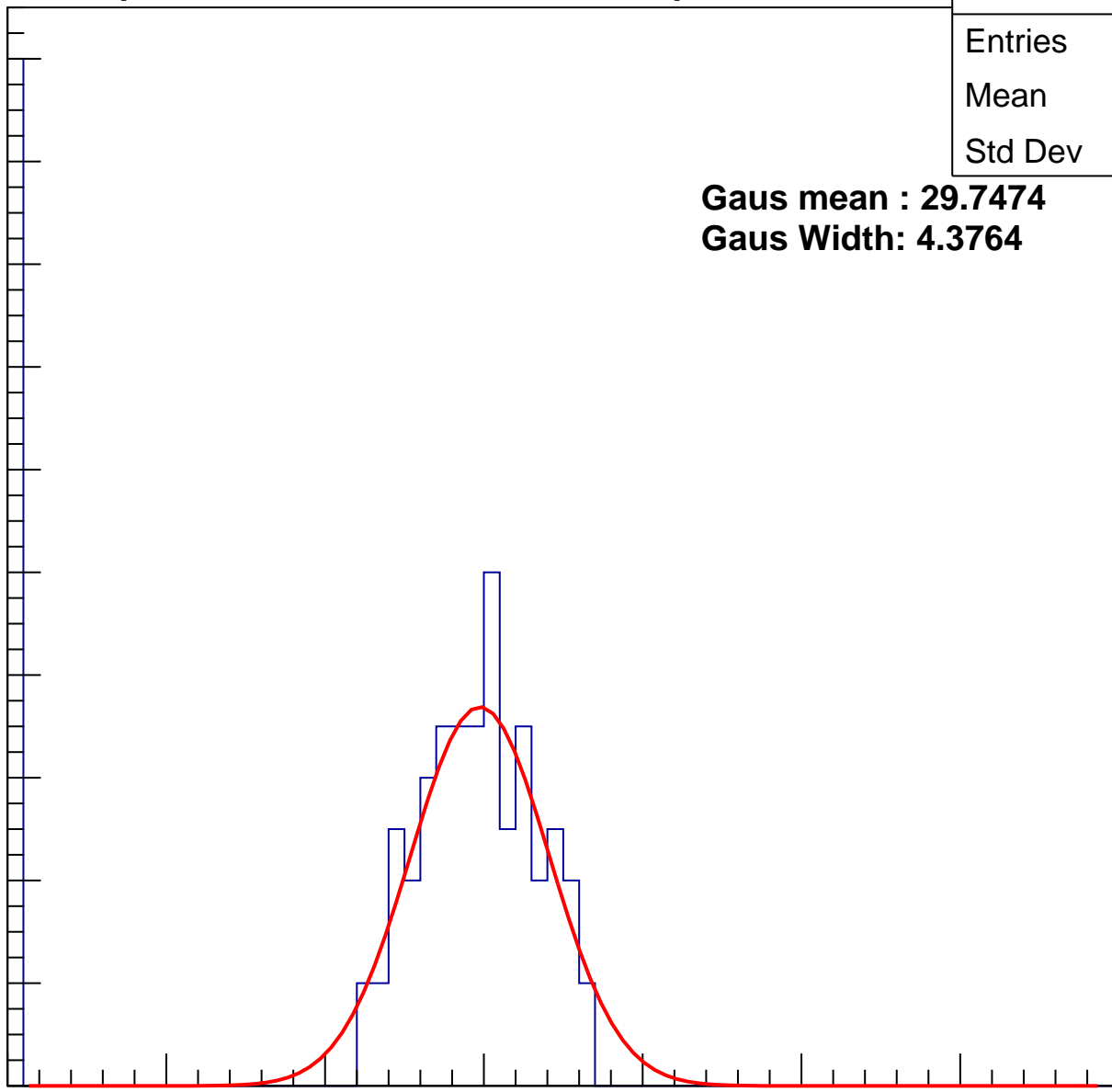
Gaus Width: 4.3764

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U18-ch36, adc1

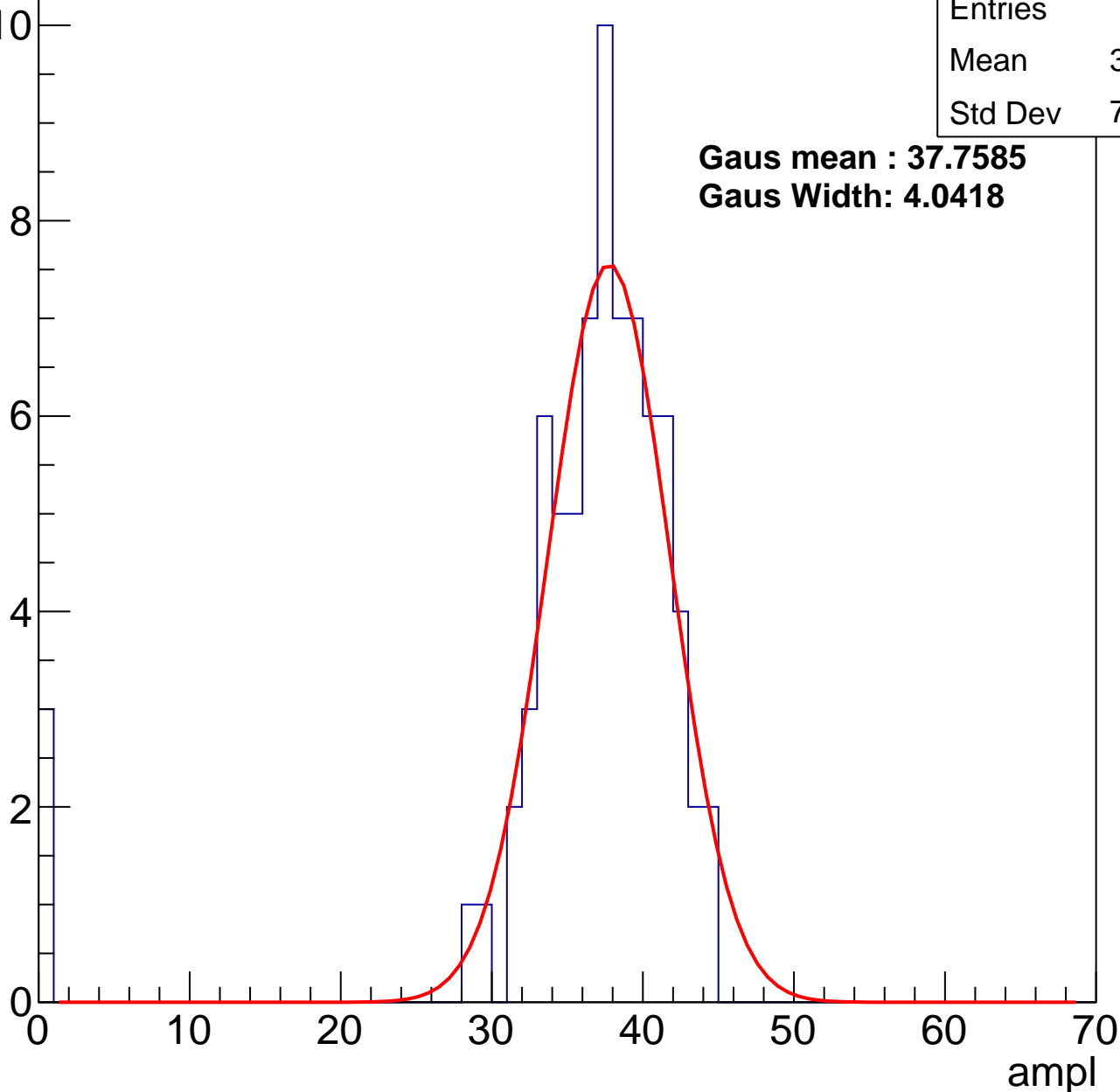
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	35.68
Std Dev	7.967

Gaus mean : 37.7585

Gaus Width: 4.0418



B1L103S, U18-ch36, adc2

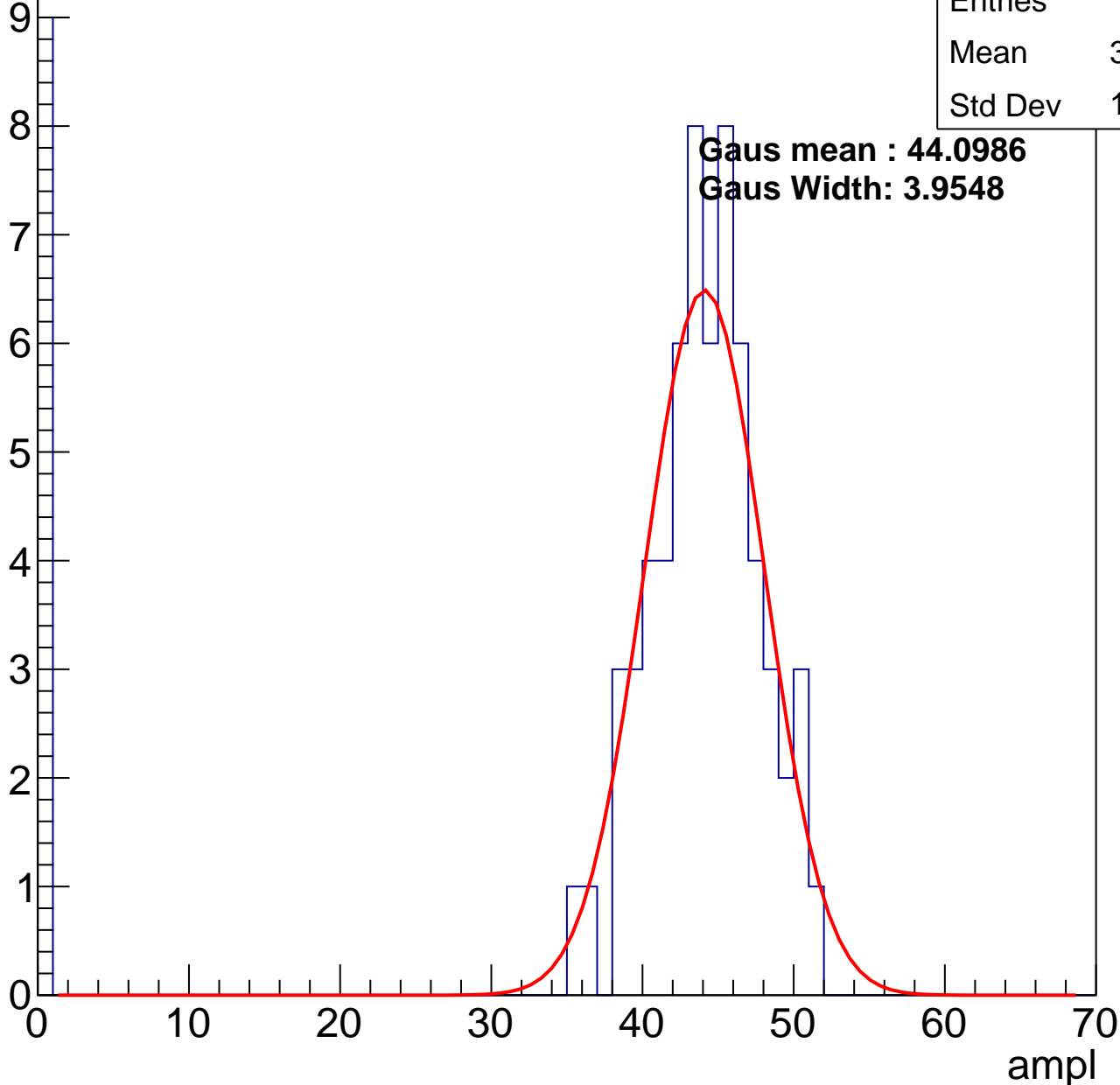
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	38.24
Std Dev	14.82

Gaus mean : 44.0986

Gaus Width: 3.9548

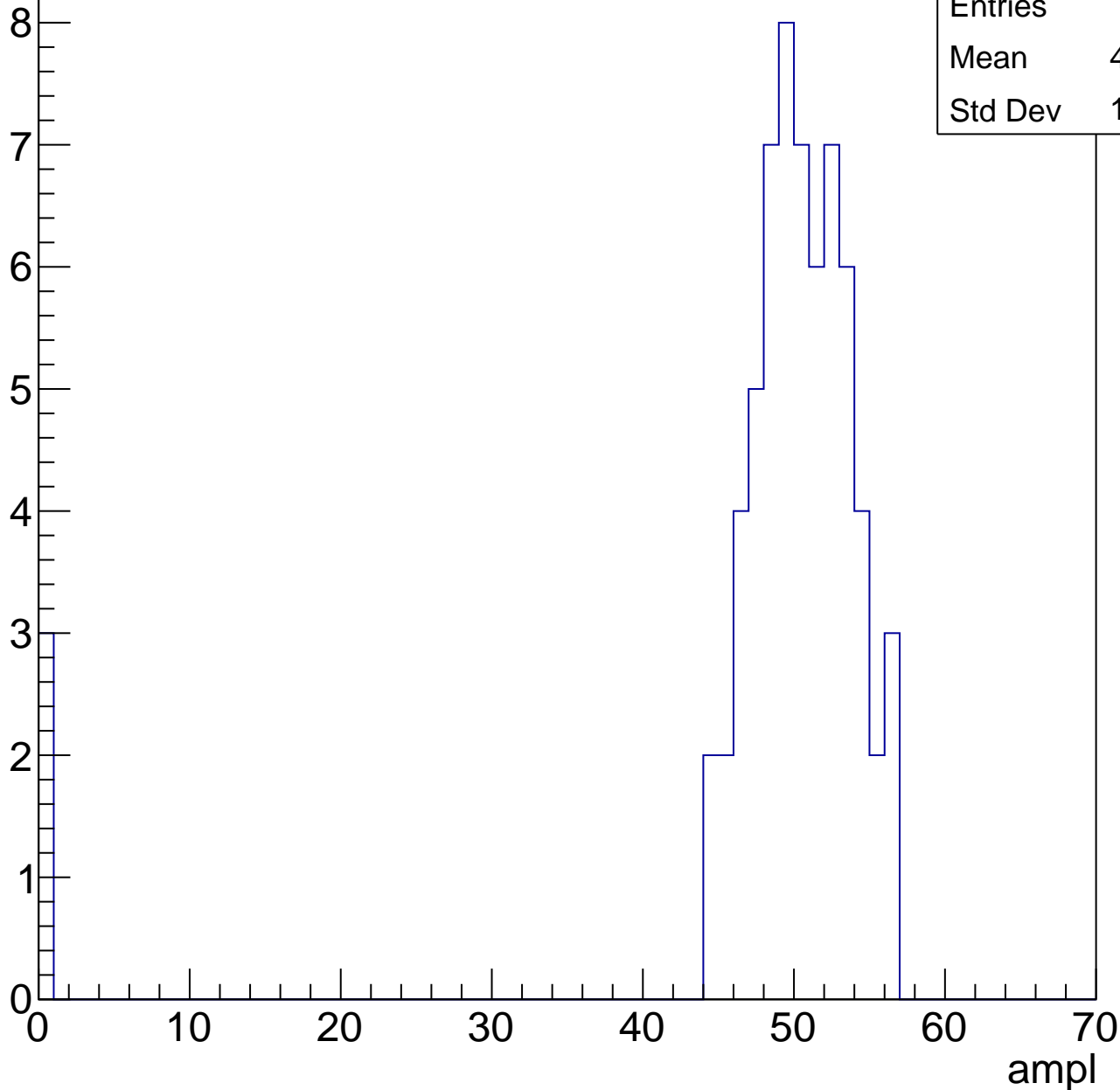


B1L103S, U18-ch36, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	47.83
Std Dev	10.85

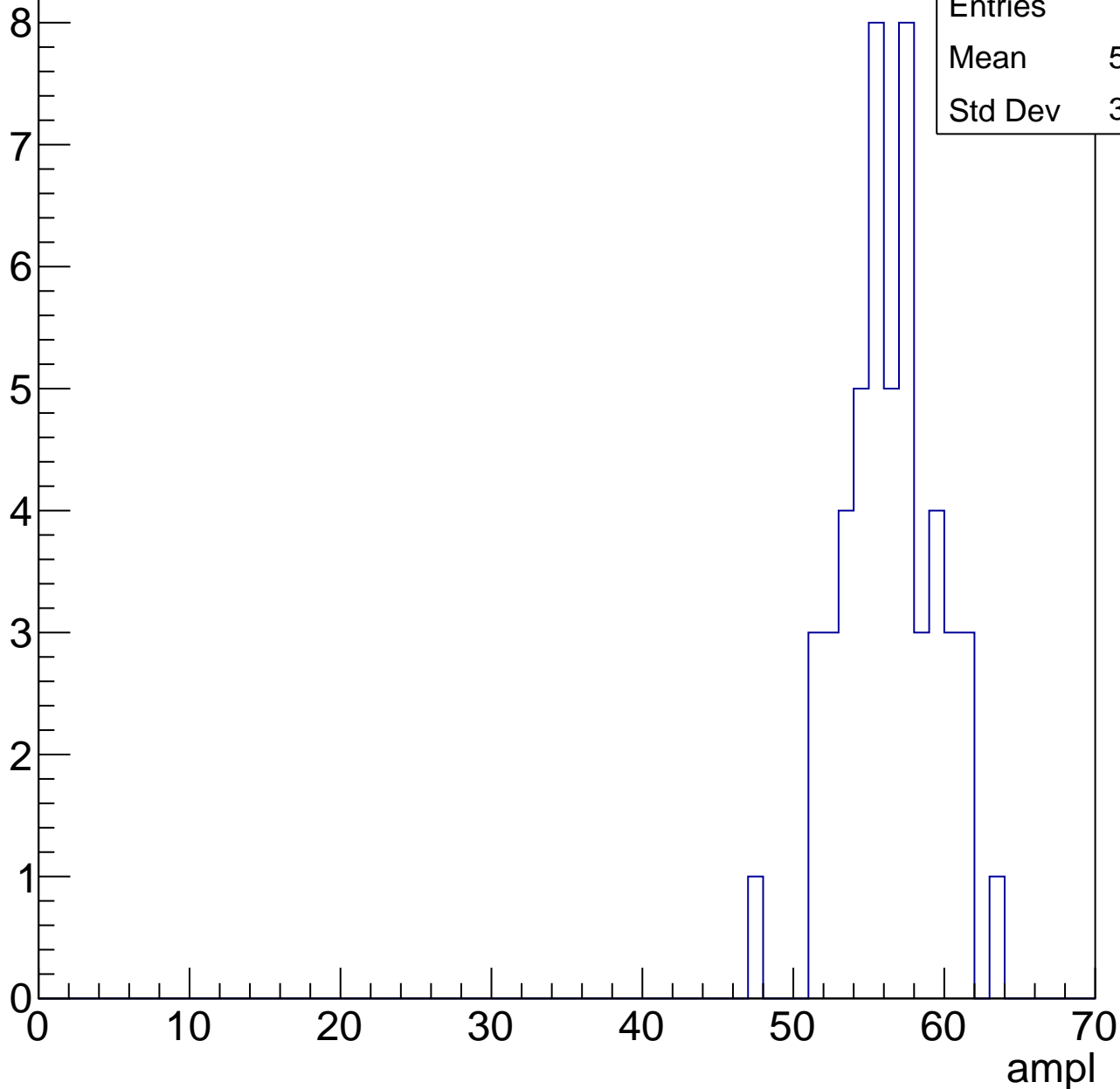


B1L103S, U18-ch36, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	55.88
Std Dev	3.116

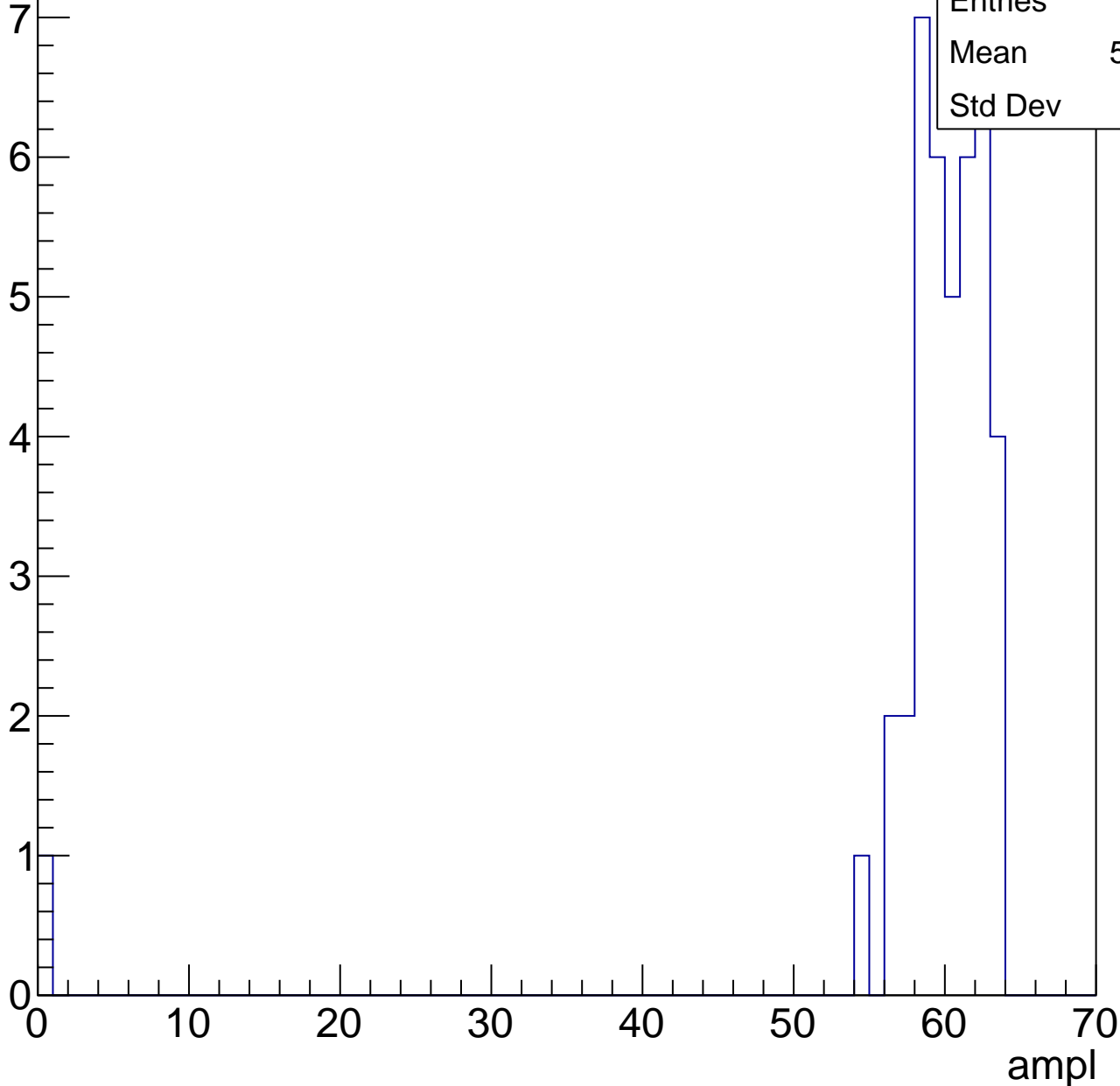


B1L103S, U18-ch36, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	58.34
Std Dev	9.47

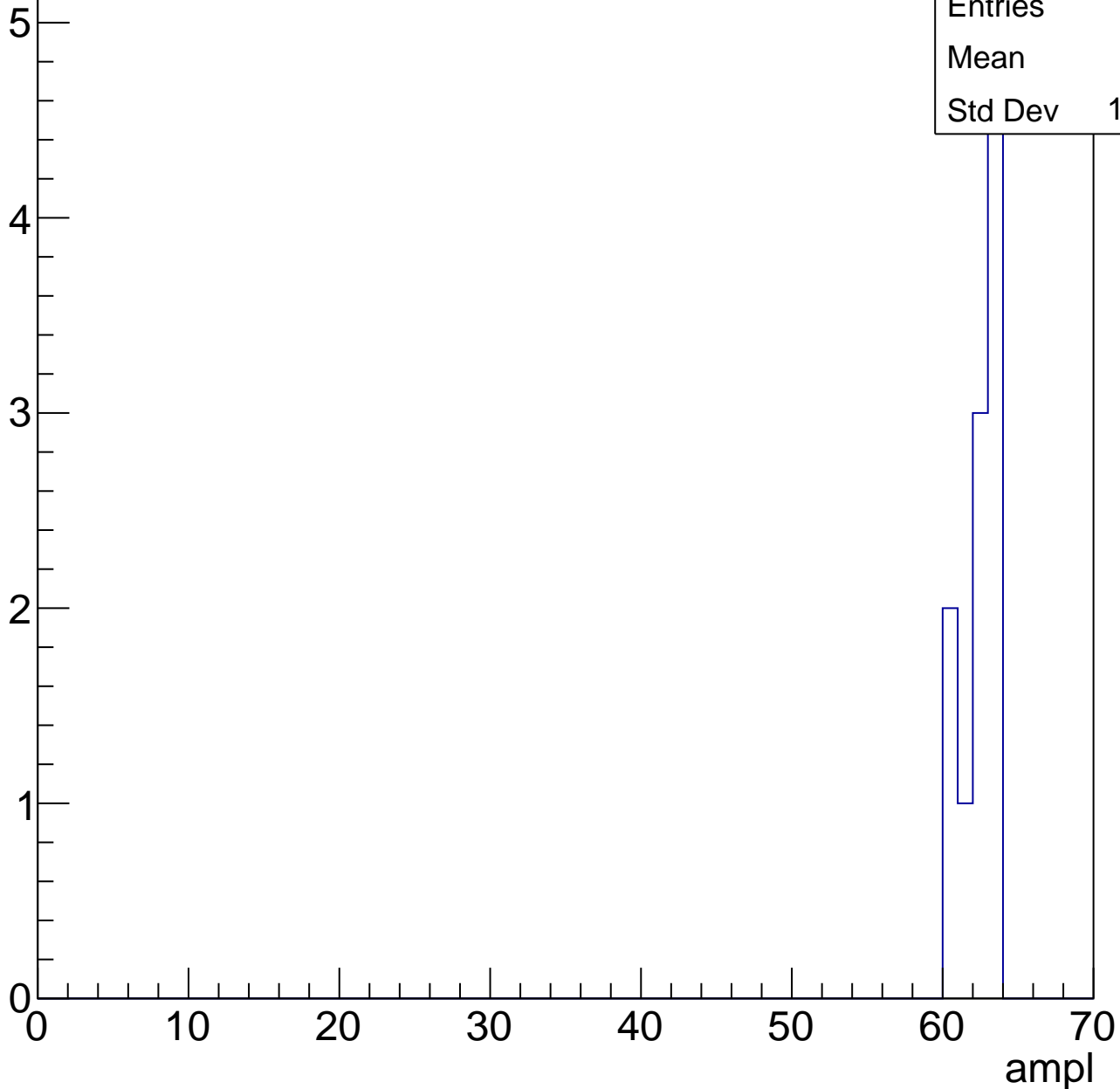


B1L103S, U18-ch36, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	62
Std Dev	1.128



B1L103S, U18-ch36, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch37, adc0

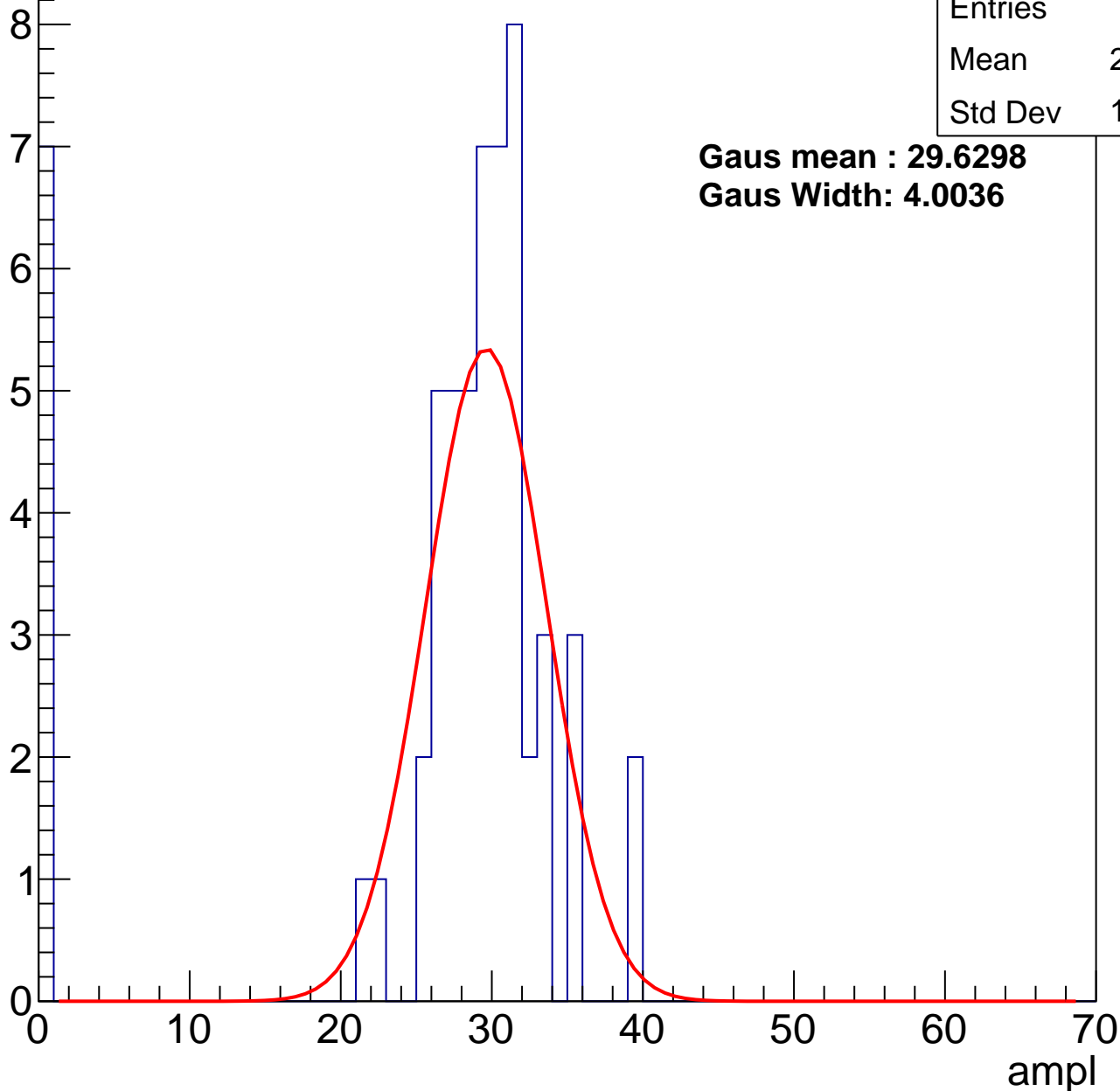
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	25.95
Std Dev	10.15

Gaus mean : 29.6298

Gaus Width: 4.0036



B1L103S, U18-ch37, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	35.64
Std Dev	5.321

Gaus mean : 36.5703

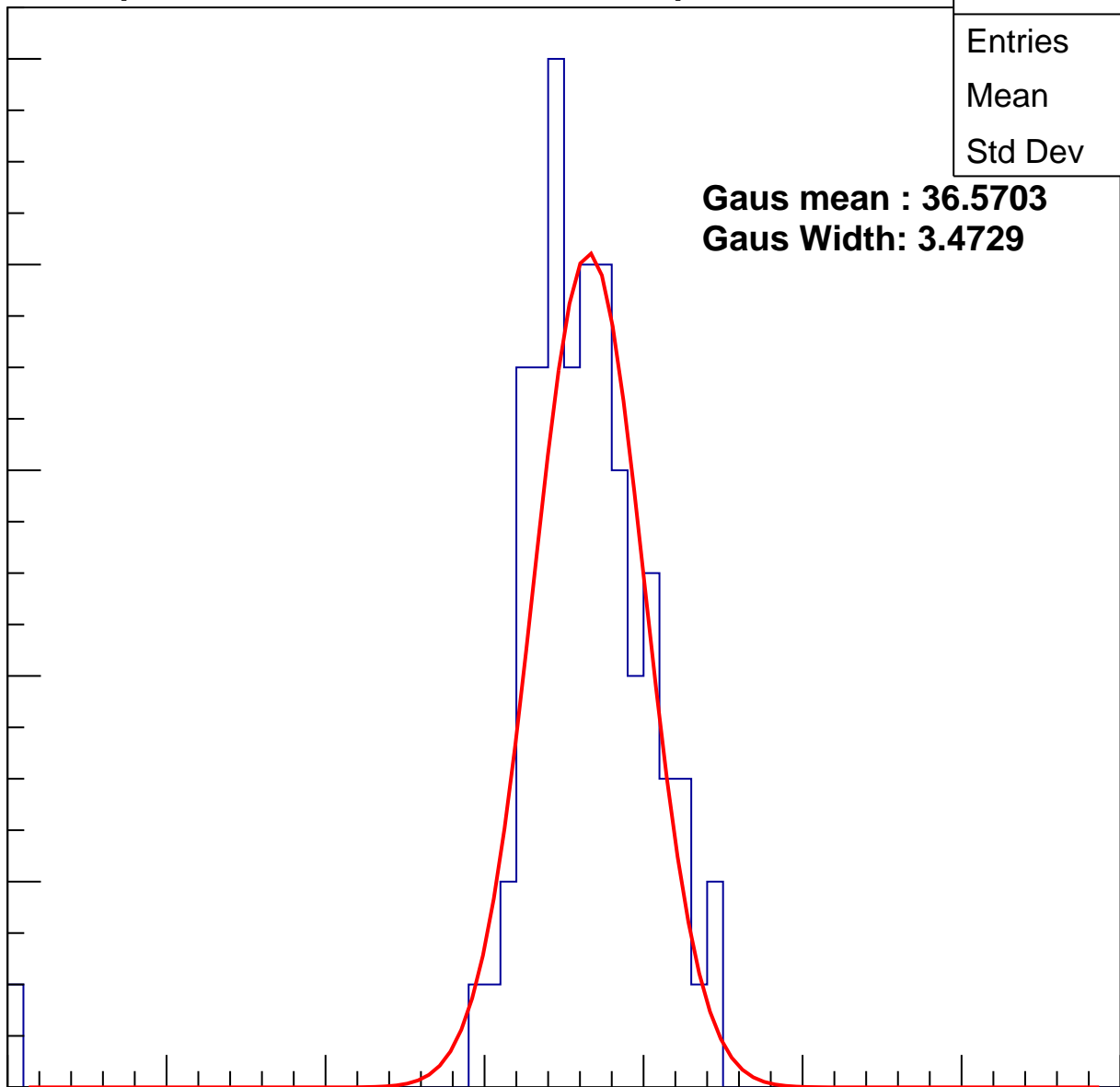
Gaus Width: 3.4729

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch37, adc2

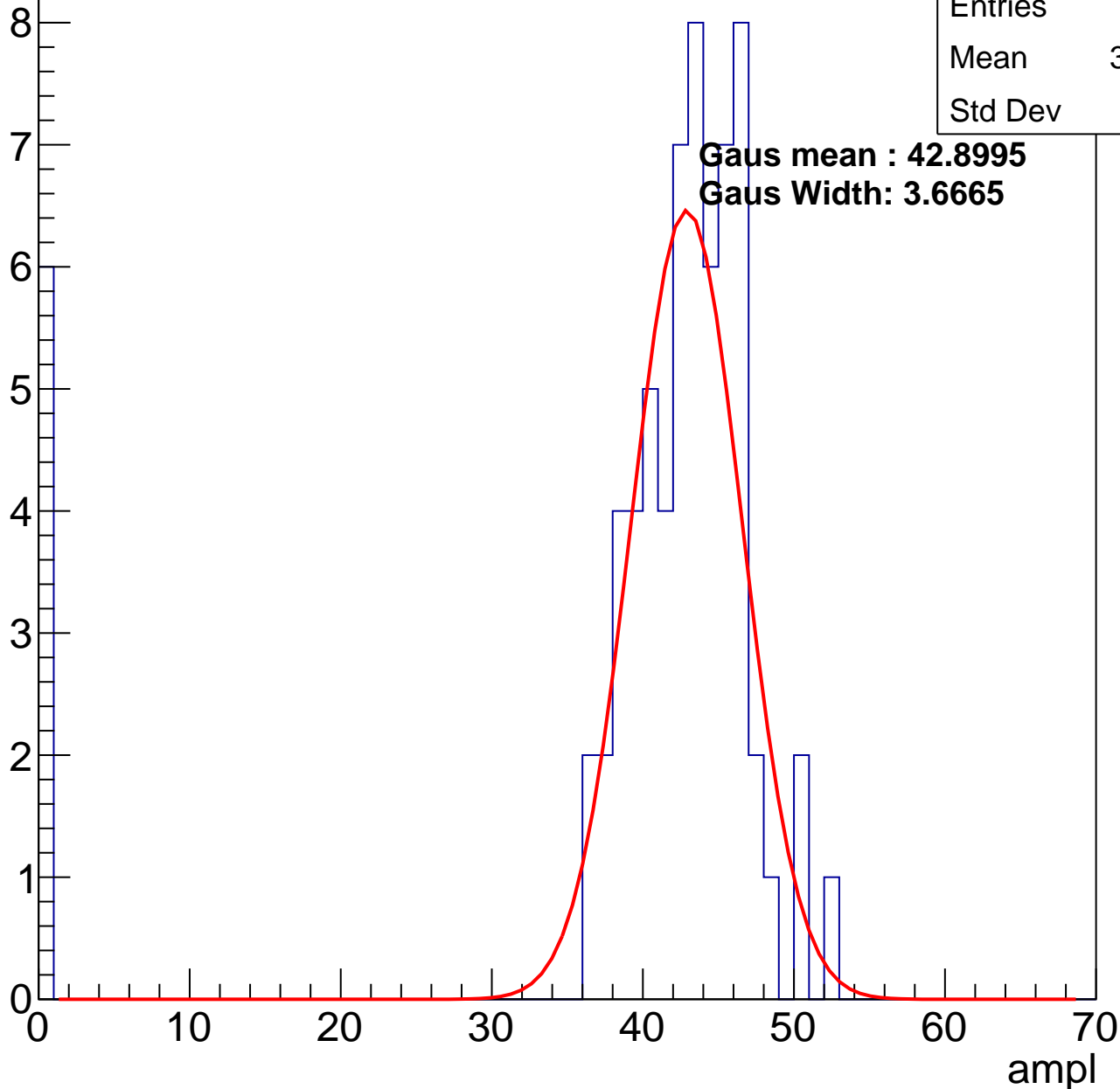
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	39.09
Std Dev	12.5

Gaus mean : 42.8995

Gaus Width: 3.6665

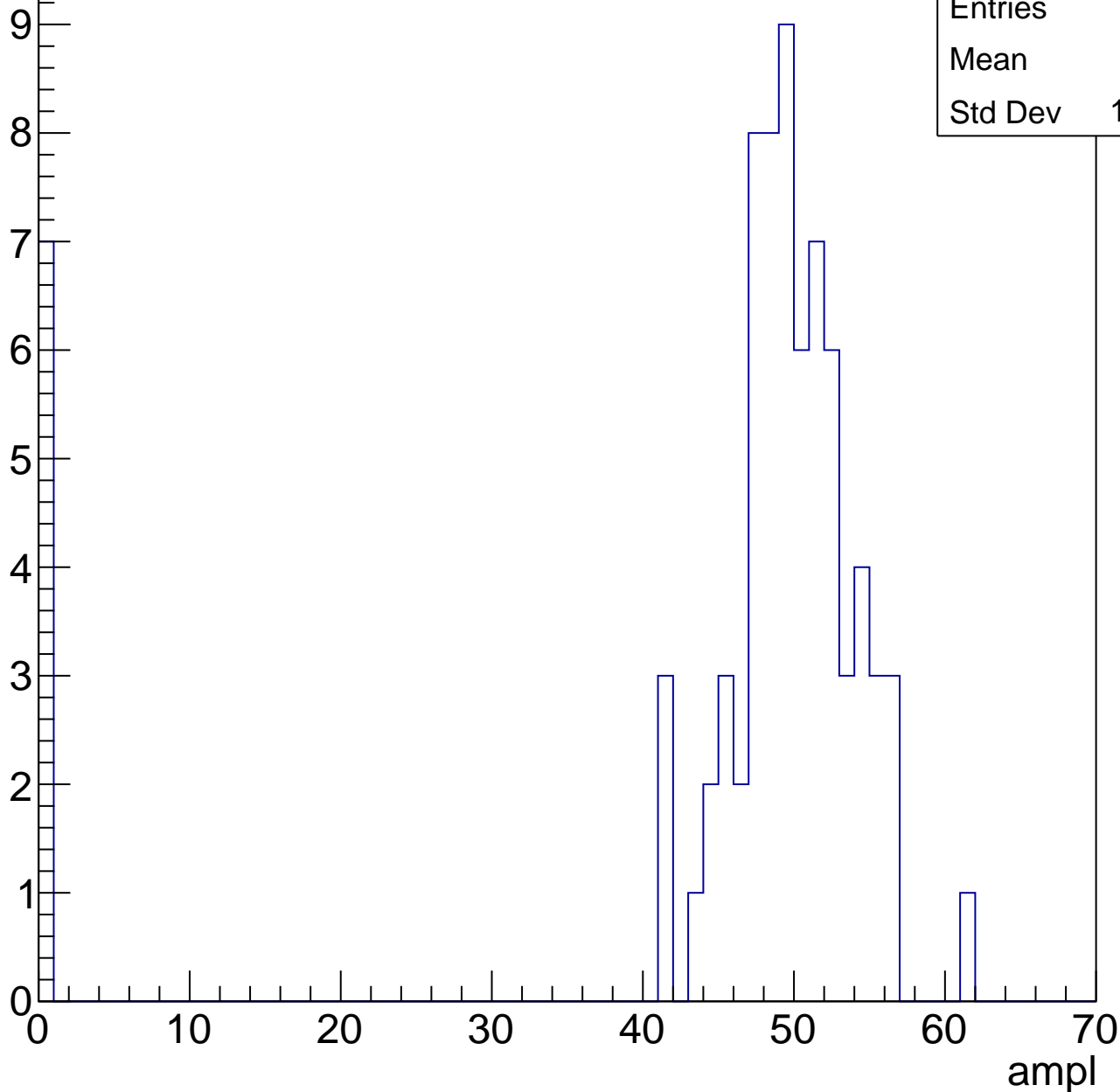


B1L103S, U18-ch37, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	45
Std Dev	14.78

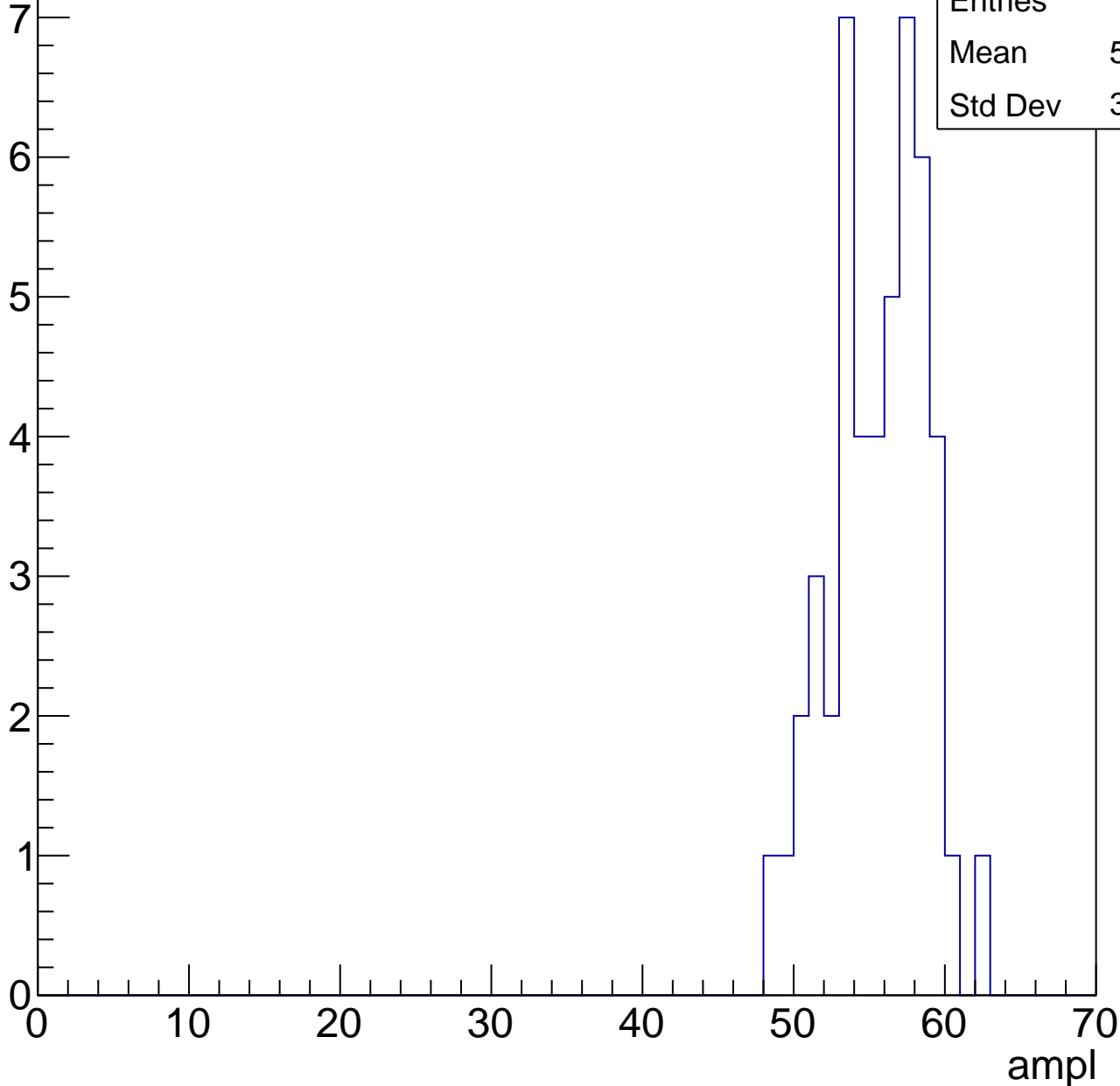


B1L103S, U18-ch37, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	55.12
Std Dev	3.086

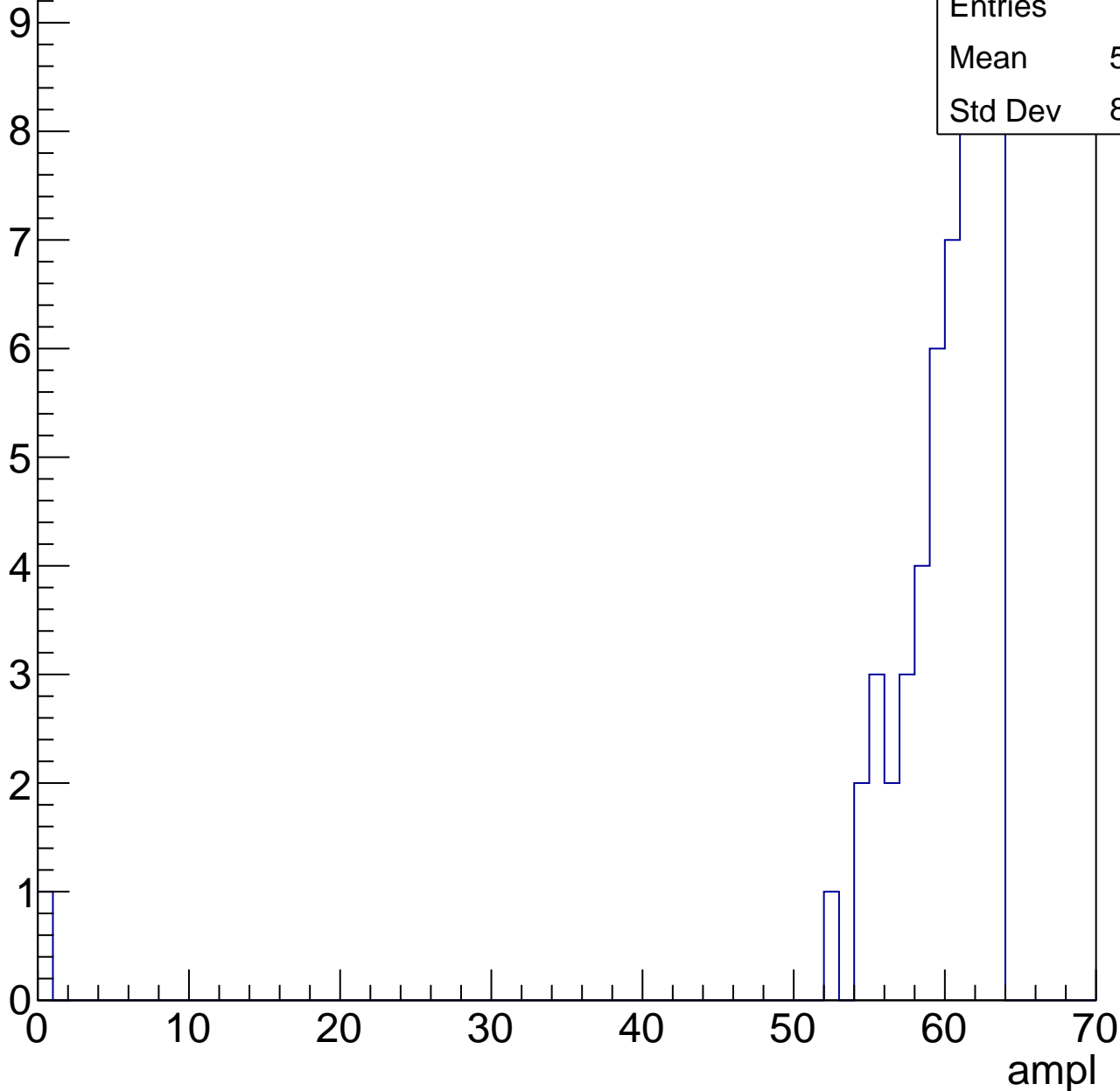


B1L103S, U18-ch37, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	54
Mean	58.57
Std Dev	8.493



B1L103S, U18-ch37, adc6

calib_packv5_041523_1651.root, FC#0, port C2

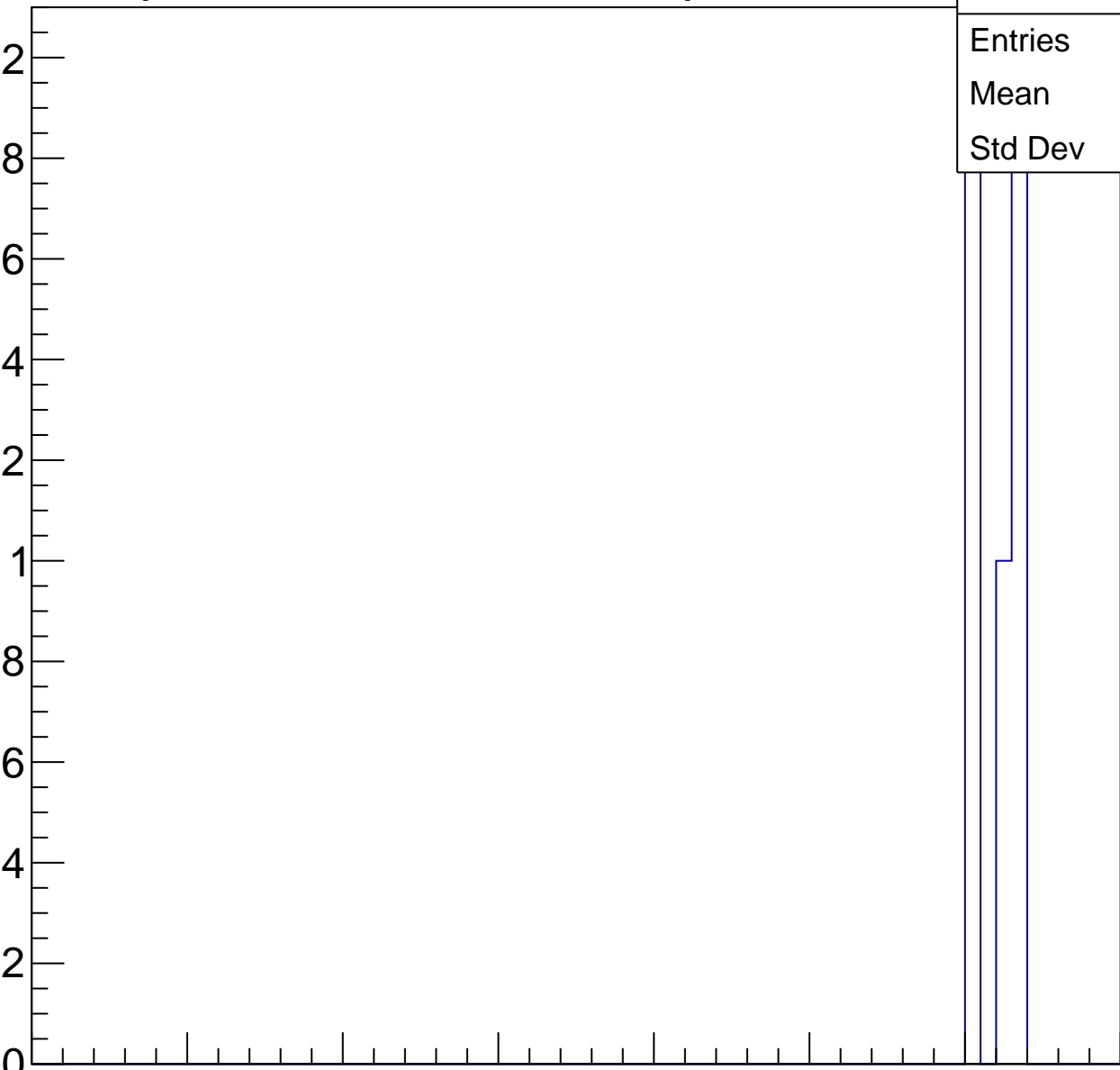
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.356

0 10 20 30 40 50 60 70

ampl

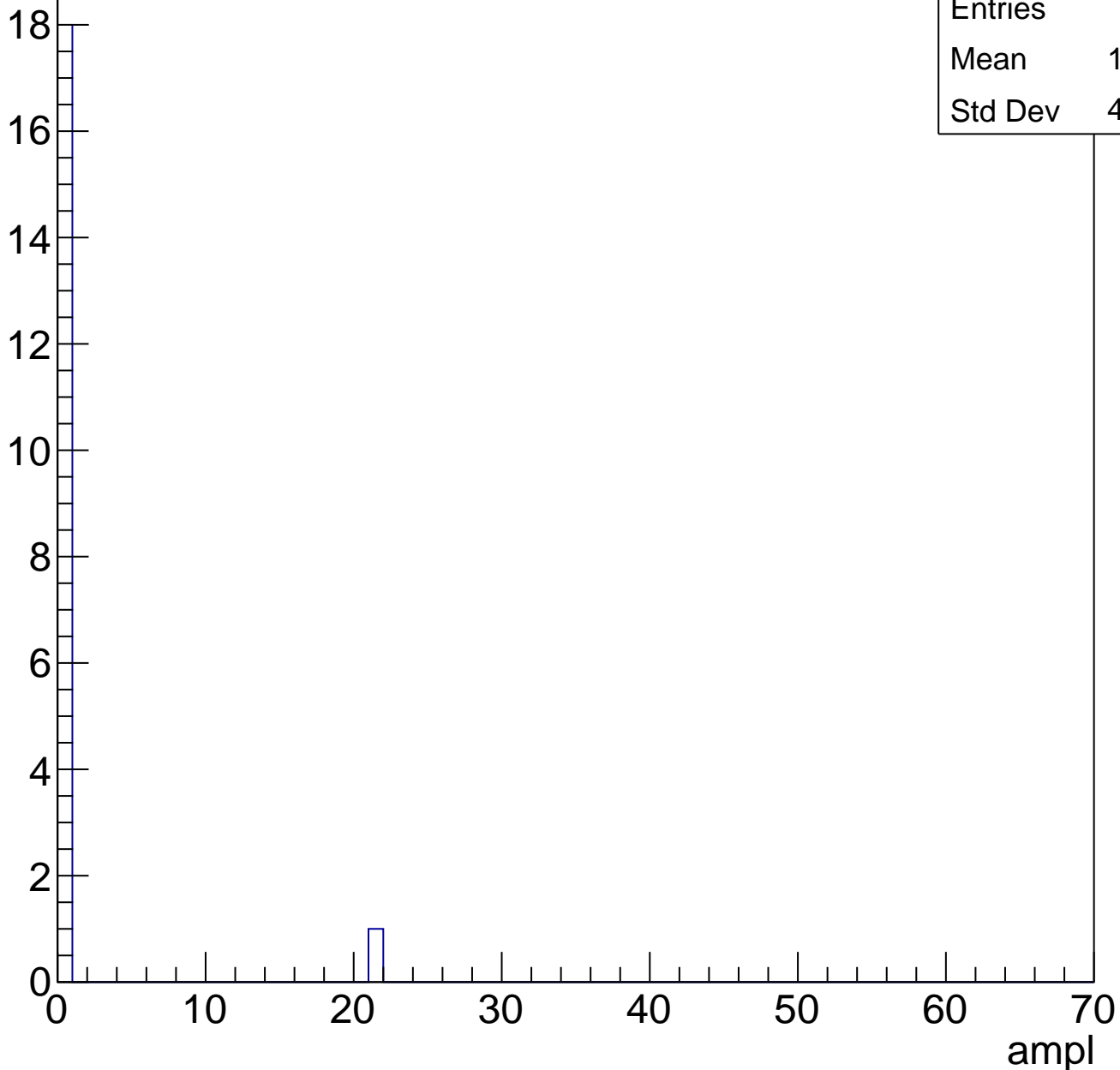


B1L103S, U18-ch37, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.105
Std Dev	4.689

Entry



B1L103S, U18-ch38, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	98
Mean	22.47
Std Dev	10.83

Gaus mean : 27.3282

Gaus Width: 3.5440

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

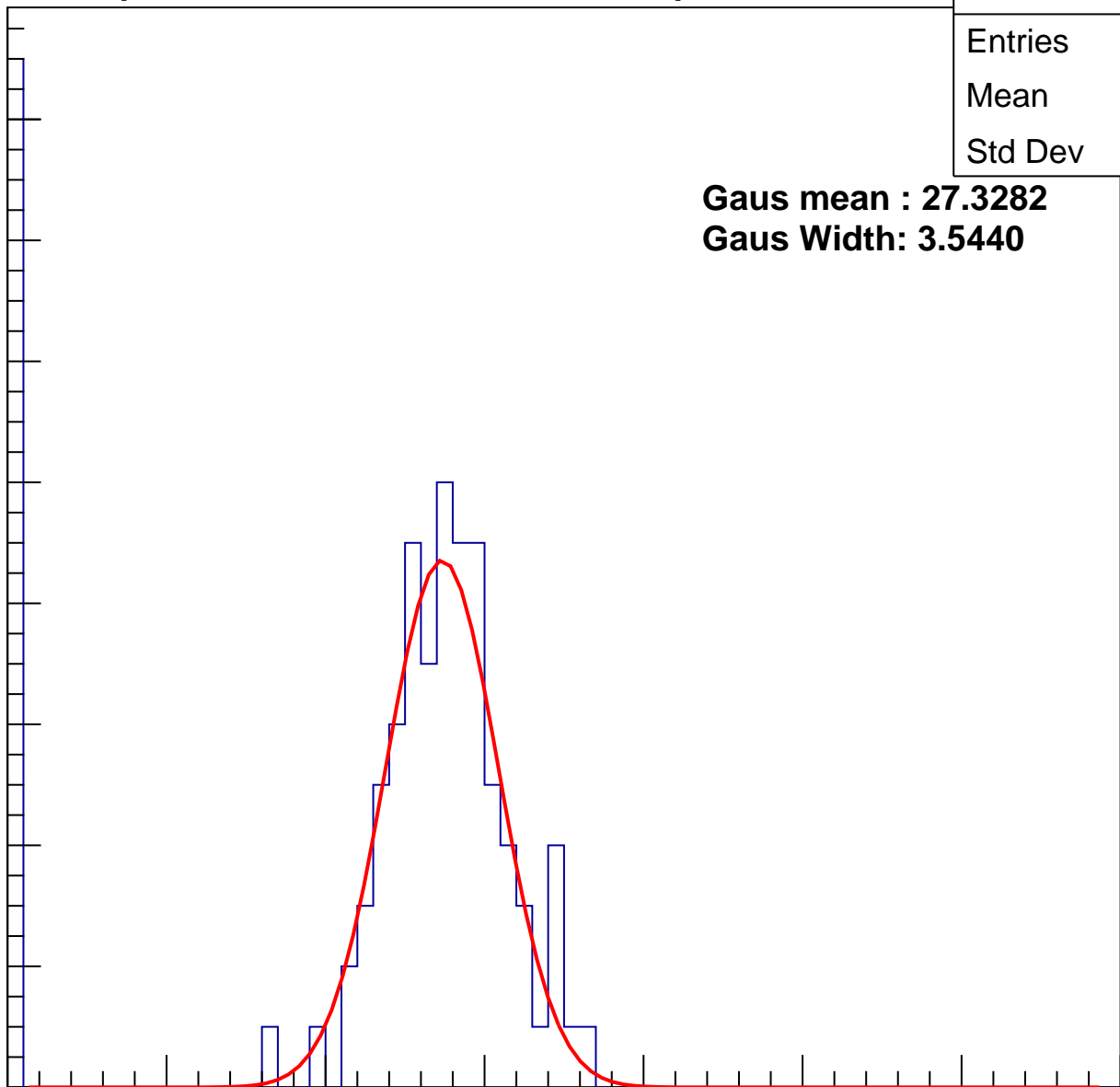
40

50

60

70

ampl



B1L103S, U18-ch38, adc1

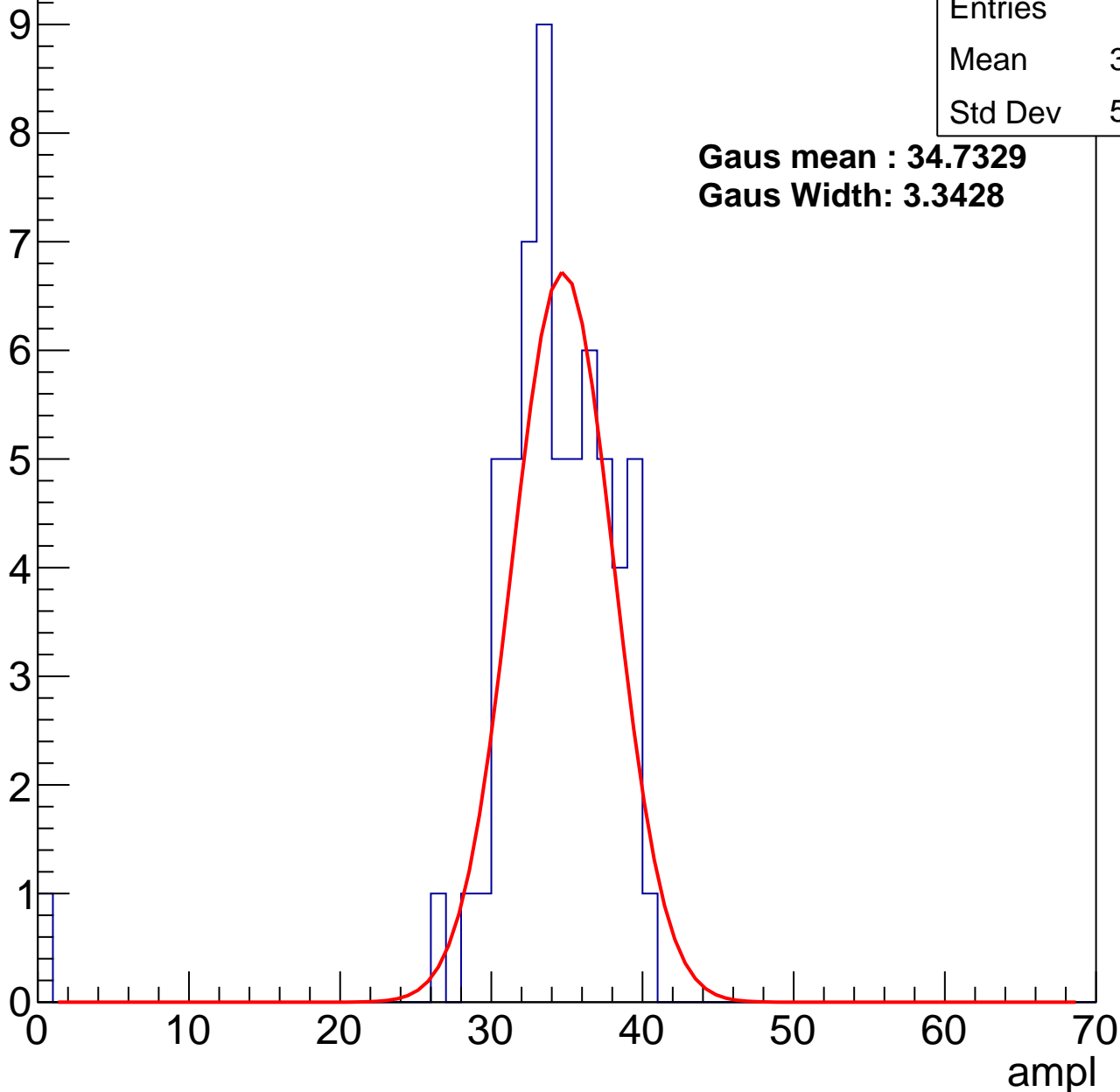
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	33.48
Std Dev	5.318

Gaus mean : 34.7329

Gaus Width: 3.3428



B1L103S, U18-ch38, adc2

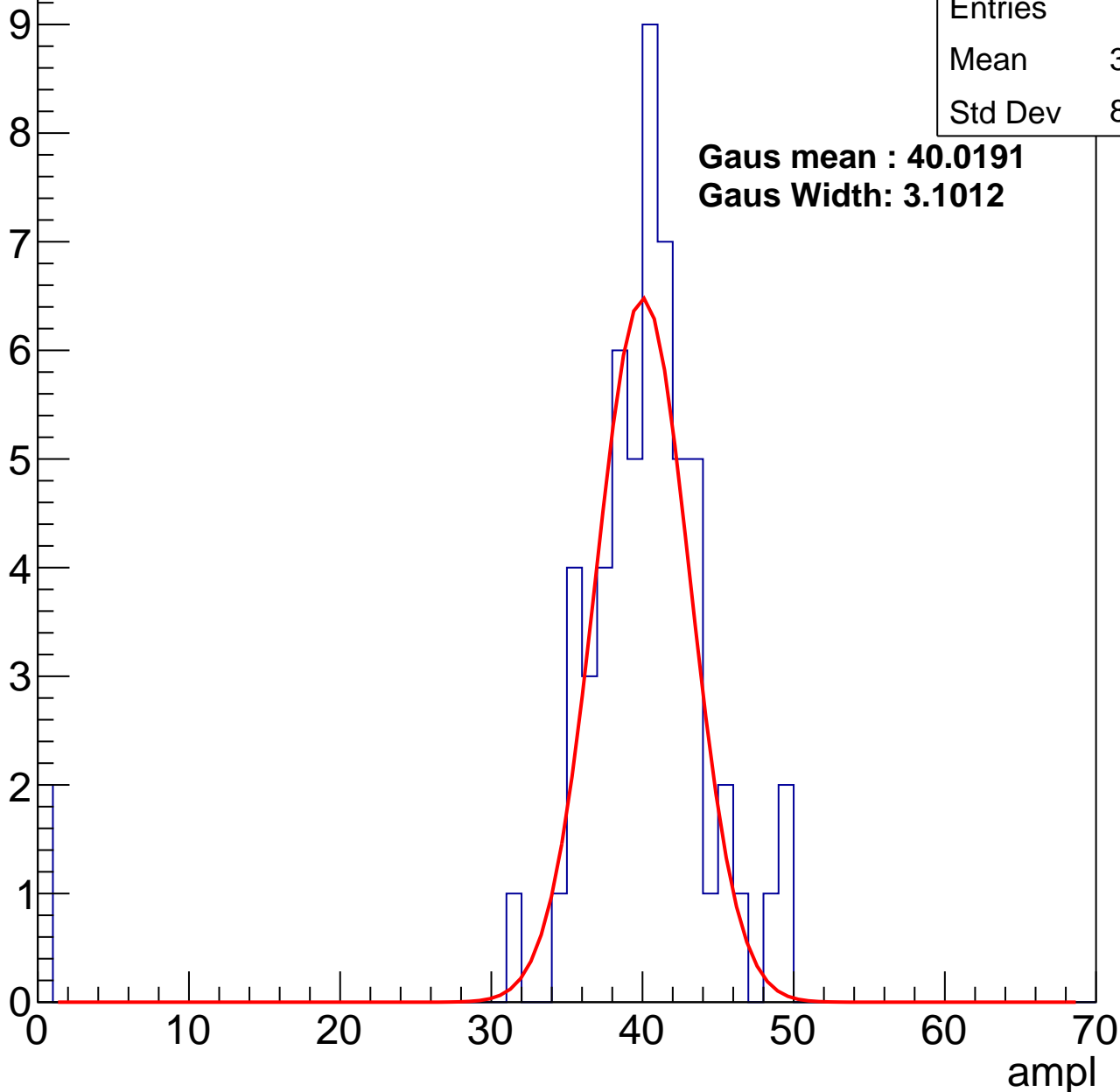
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	38.68
Std Dev	8.054

Gaus mean : 40.0191

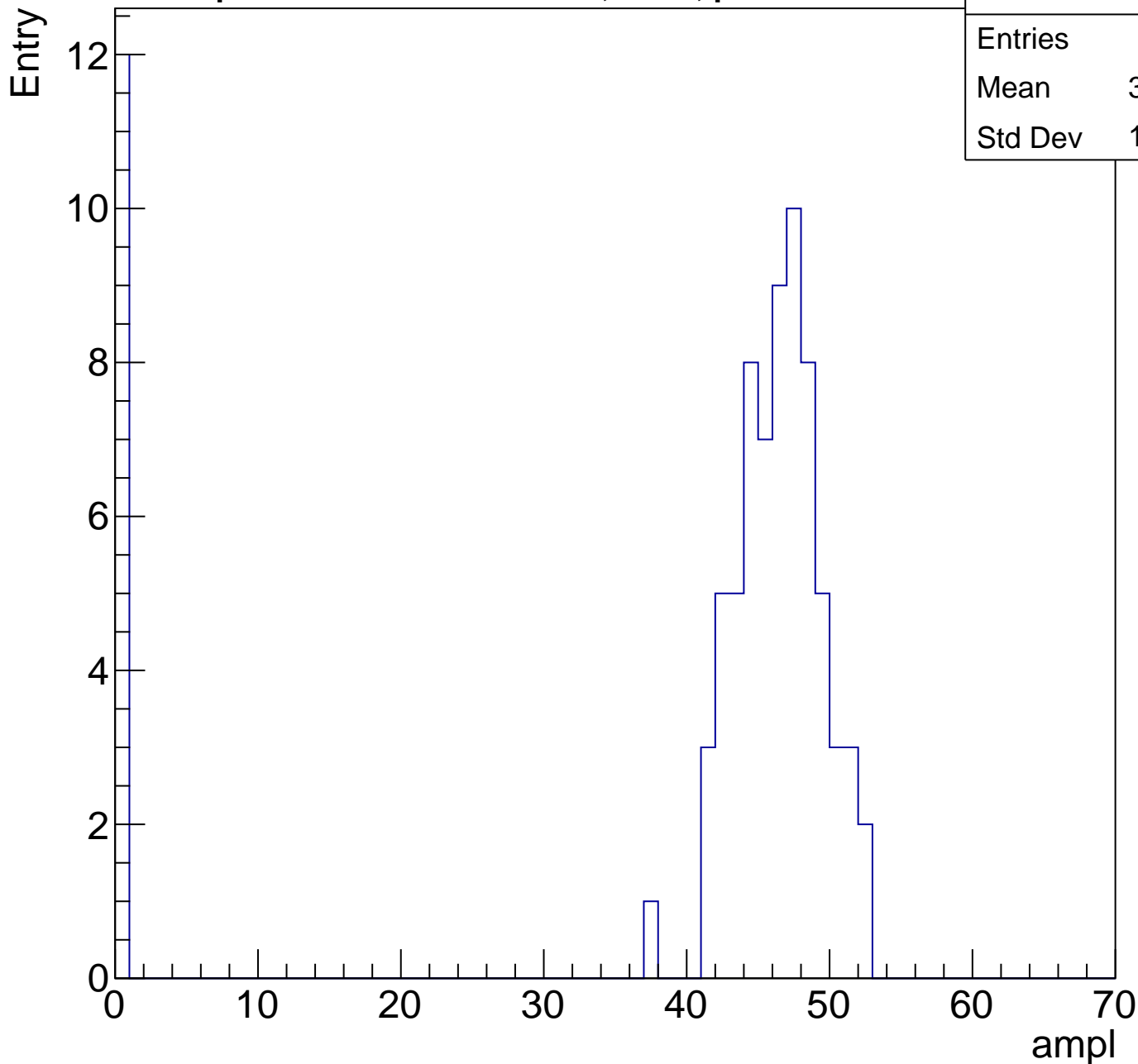
Gaus Width: 3.1012



B1L103S, U18-ch38, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	39.16
Std Dev	16.56

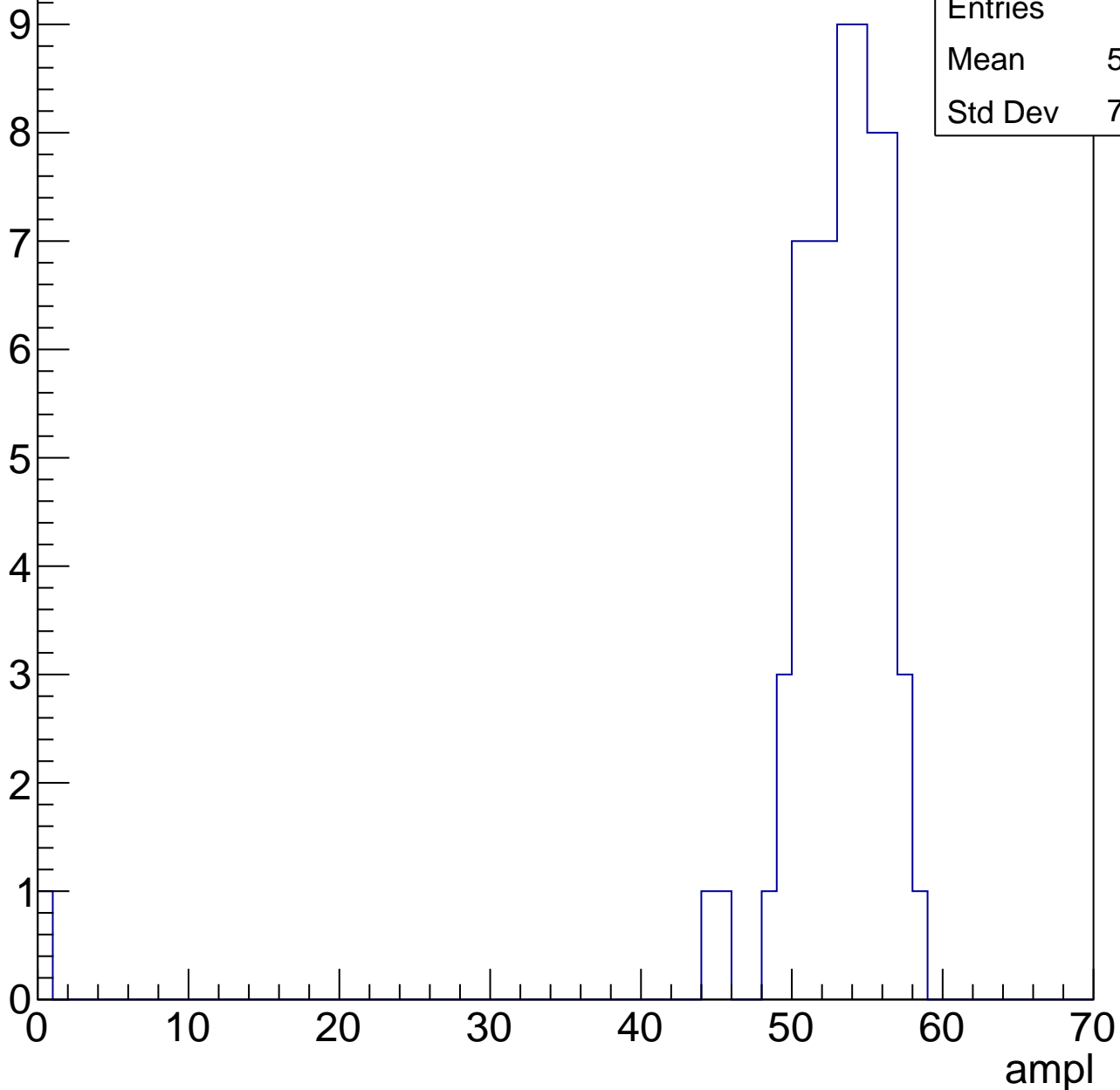


B1L103S, U18-ch38, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

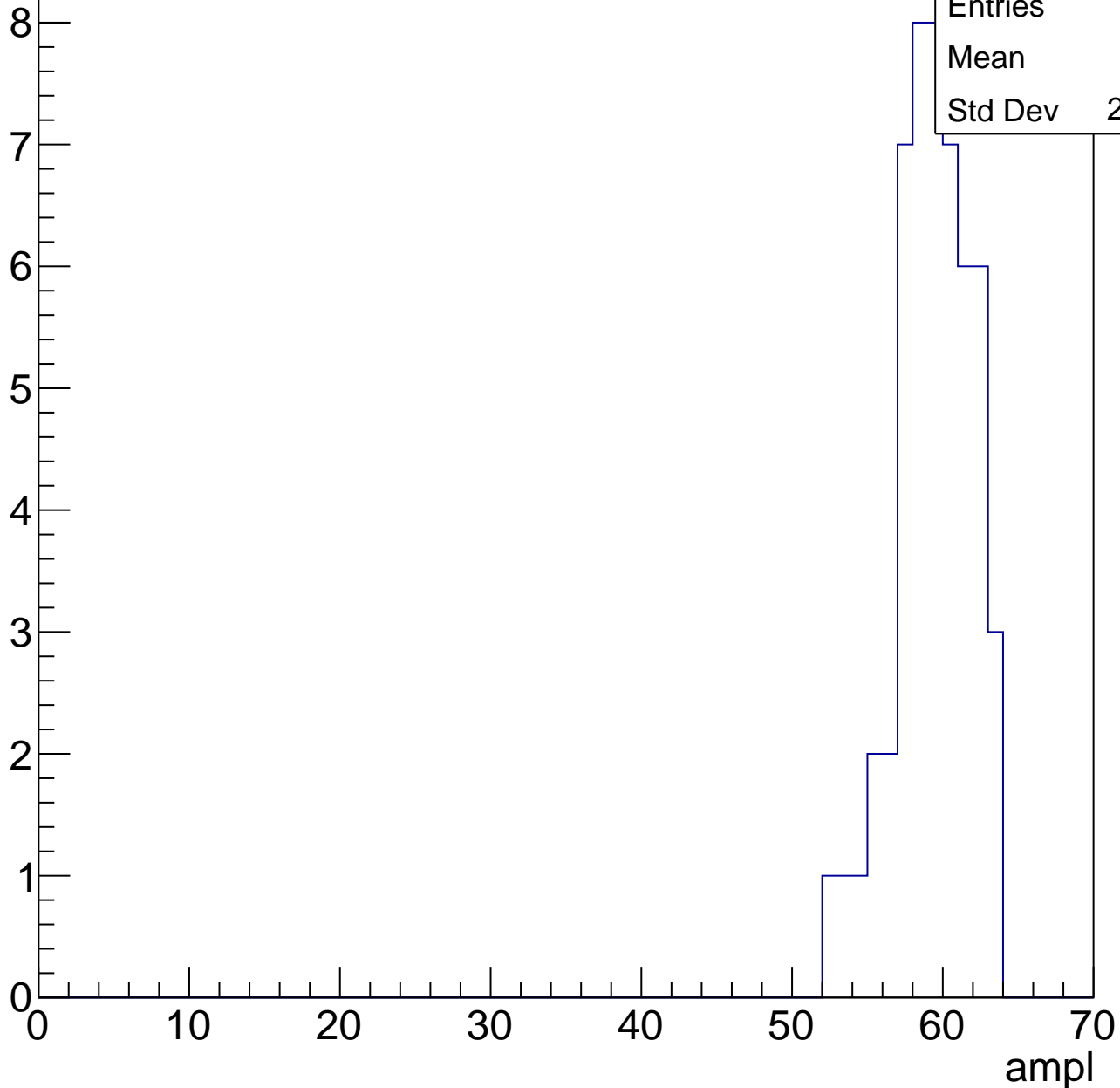
Entries	66
Mean	52.05
Std Dev	7.018



B1L103S, U18-ch38, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



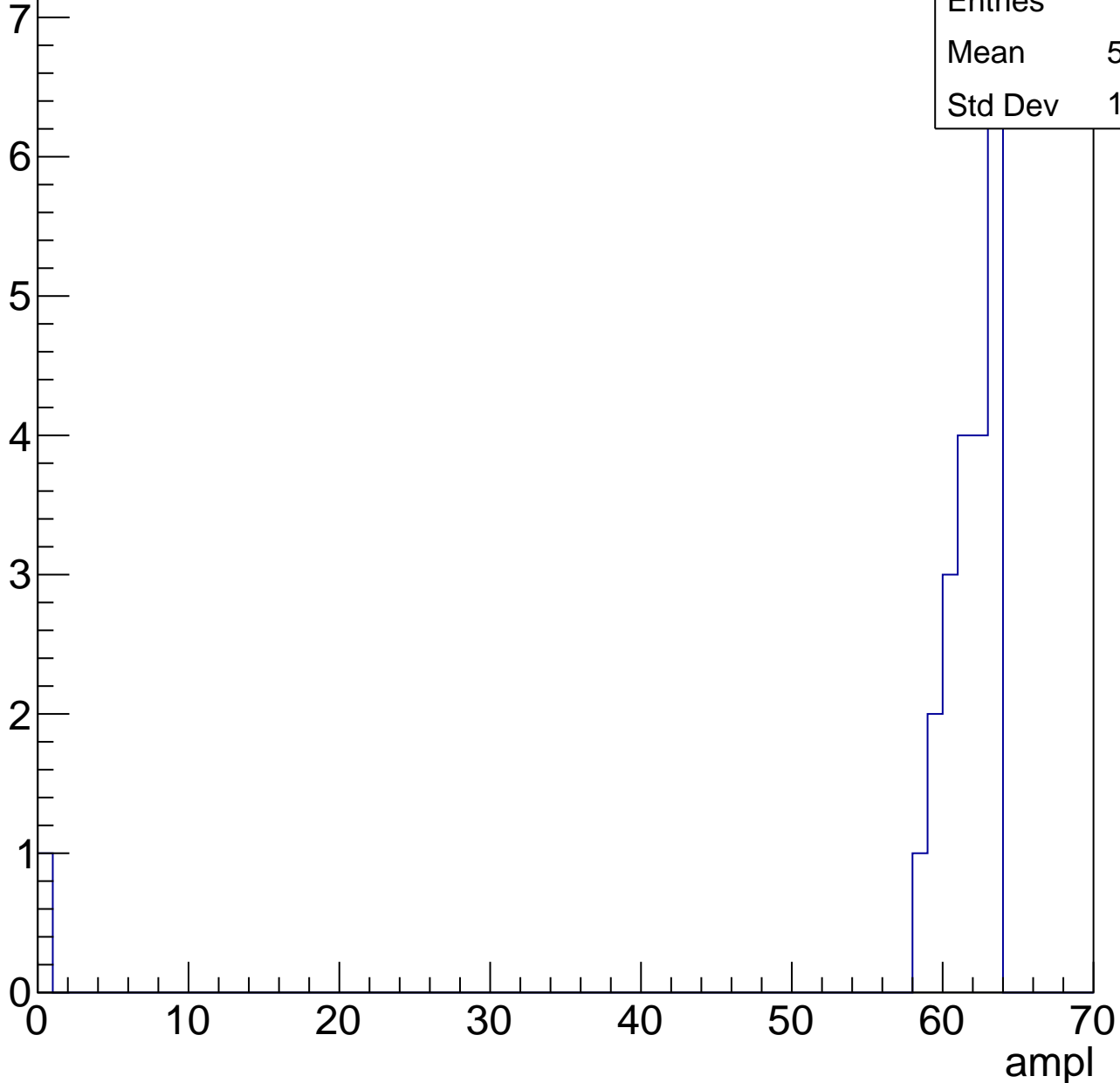
Entries	52
Mean	58.9
Std Dev	2.513

B1L103S, U18-ch38, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	58.59
Std Dev	12.87



B1L103S, U18-ch38, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch39, adc0

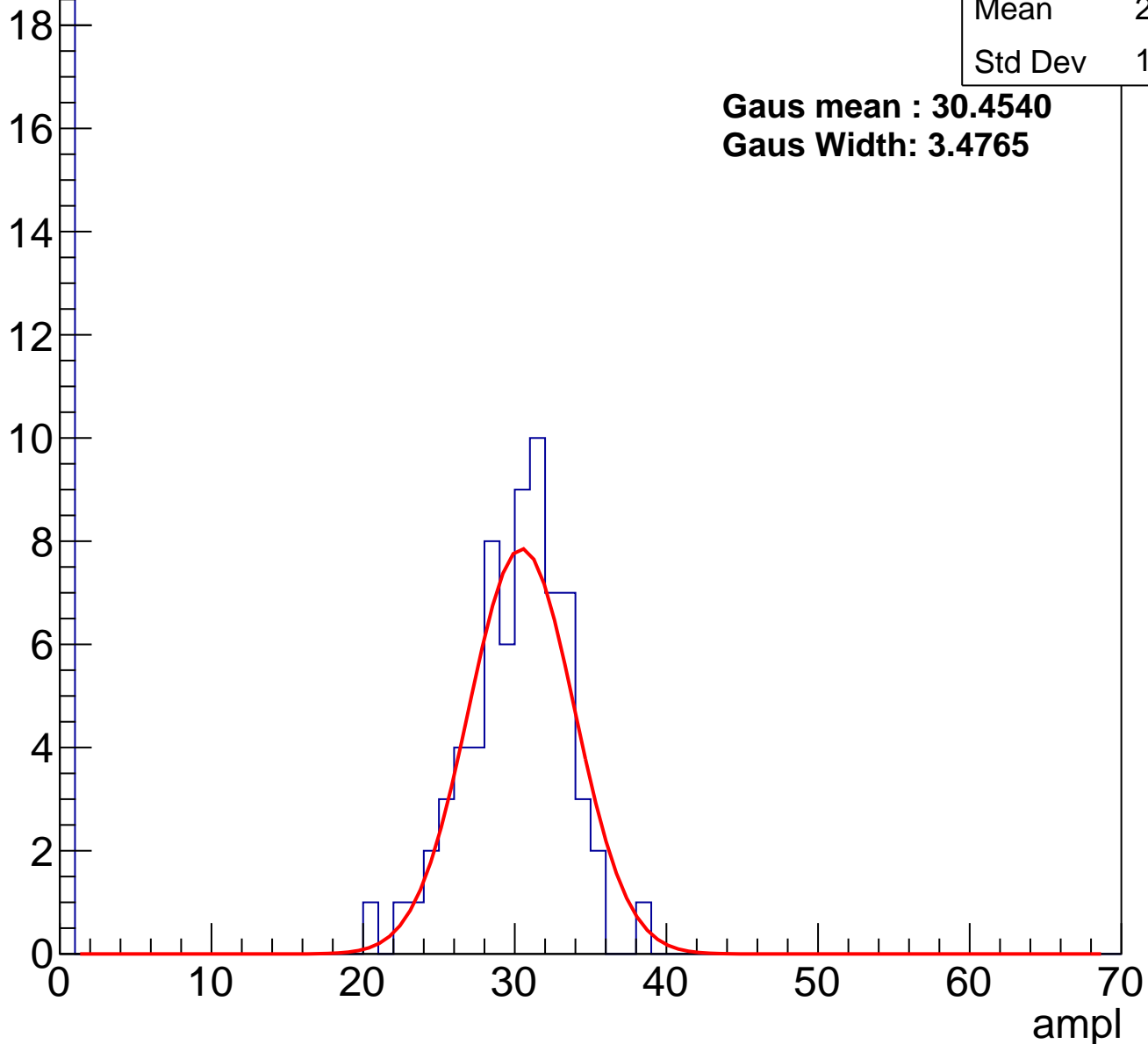
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	23.22
Std Dev	12.53

Gaus mean : 30.4540

Gaus Width: 3.4765

Entry



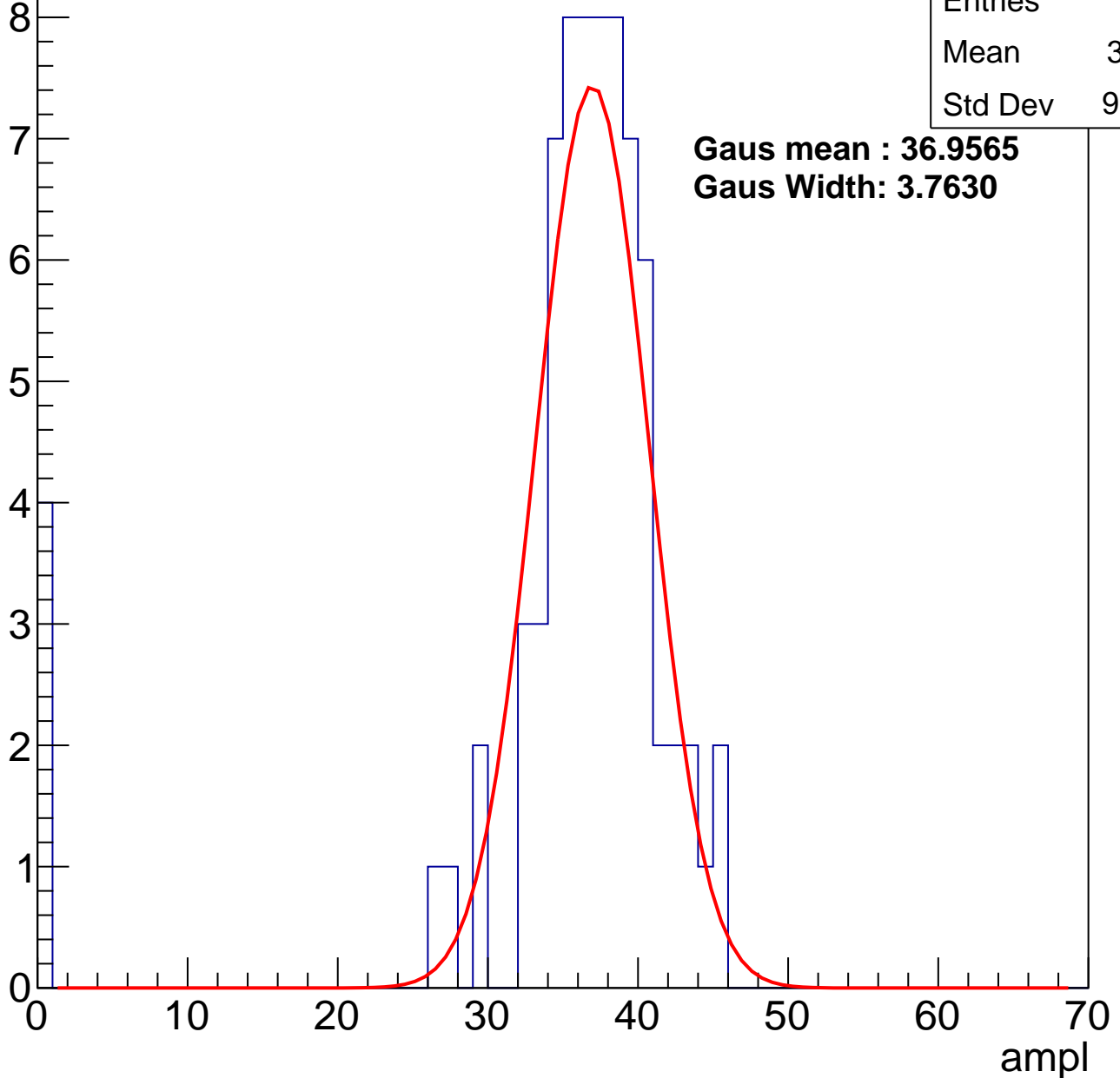
B1L103S, U18-ch39, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	34.81
Std Dev	9.028

Gaus mean : 36.9565
Gaus Width: 3.7630



B1L103S, U18-ch39, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	37.74
Std Dev	15.19

Gaus mean : 44.4136

Gaus Width: 3.6236

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

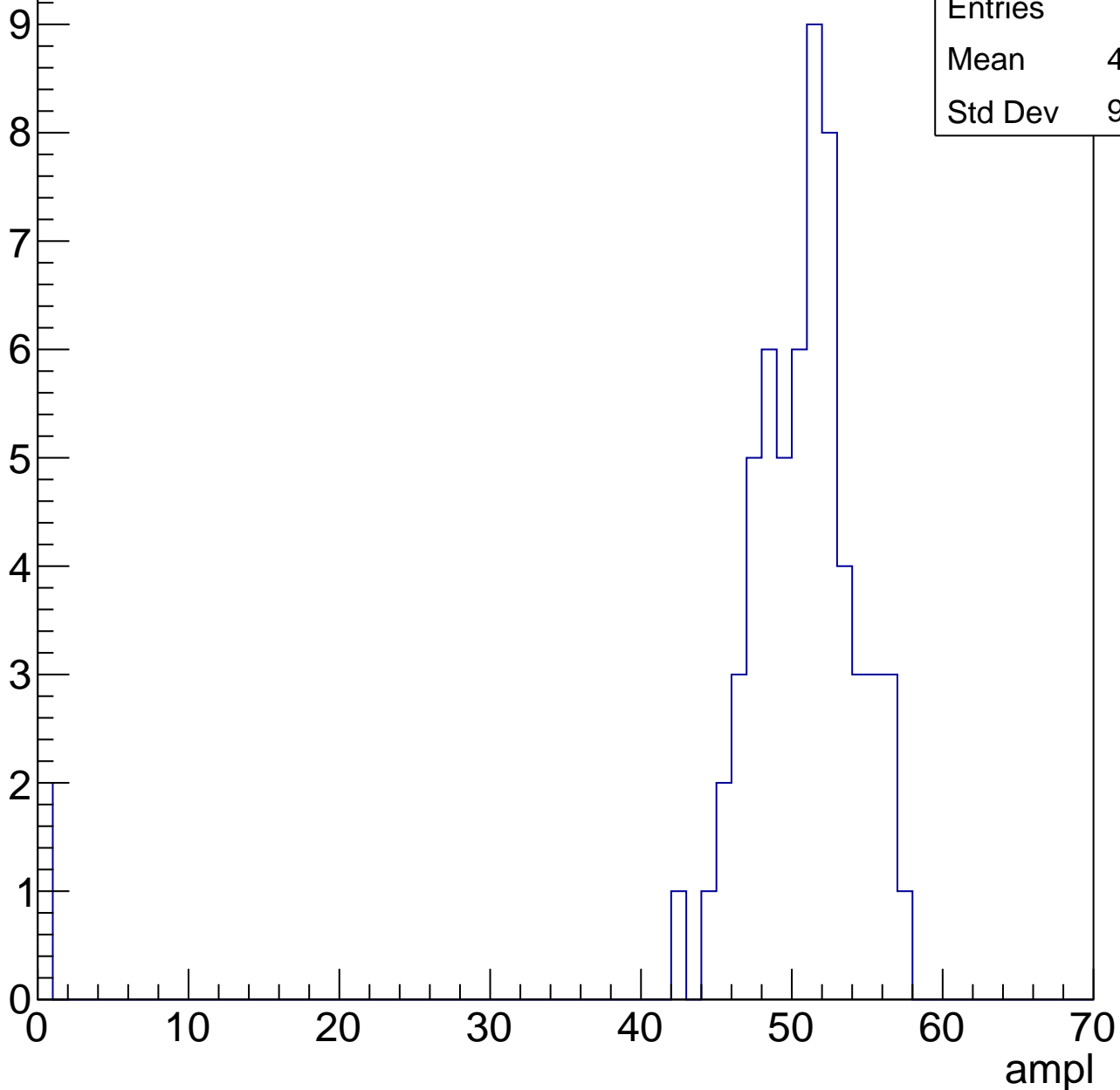
70

B1L103S, U18-ch39, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48.73
Std Dev	9.443

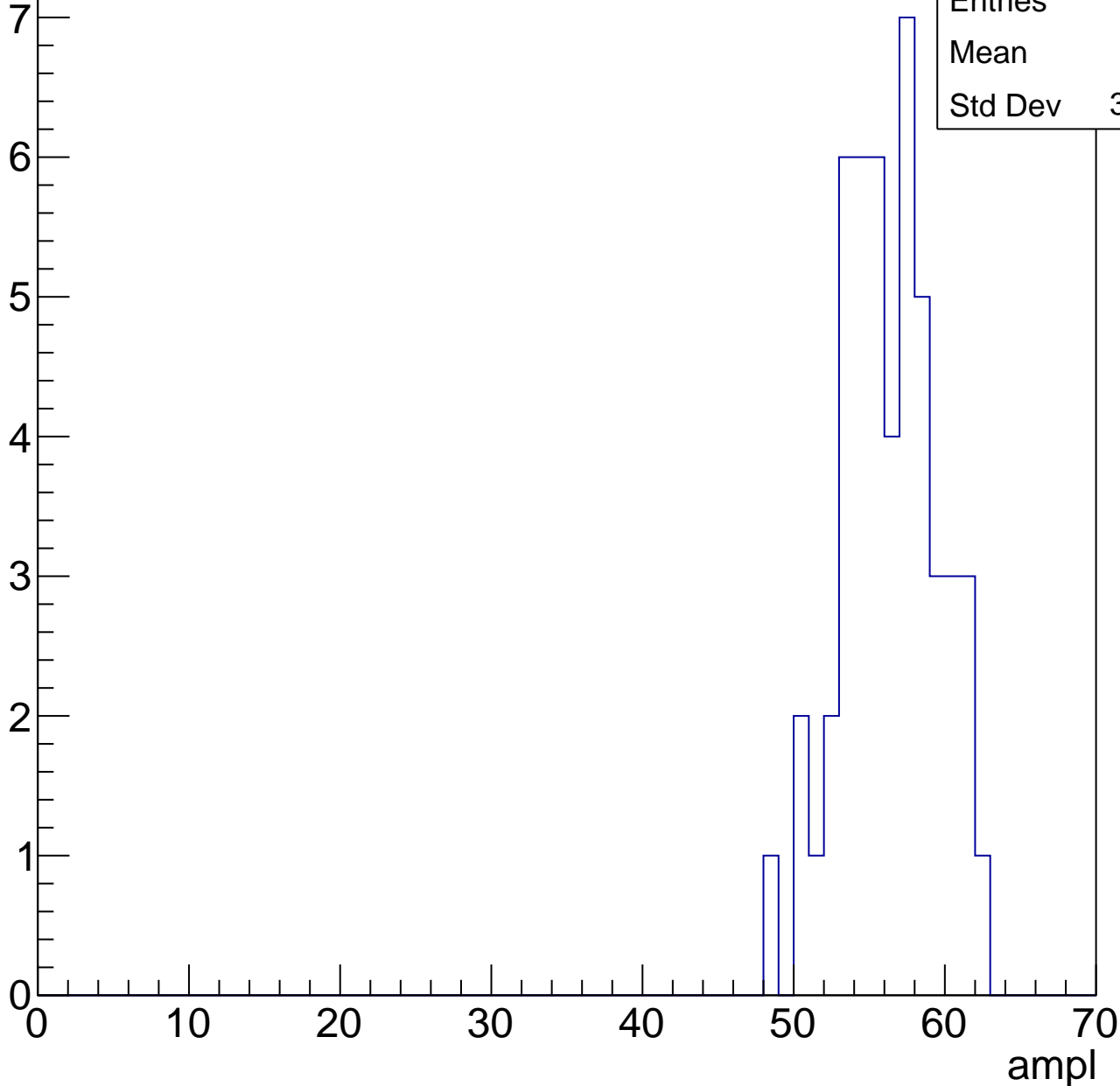


B1L103S, U18-ch39, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.8
Std Dev	3.124

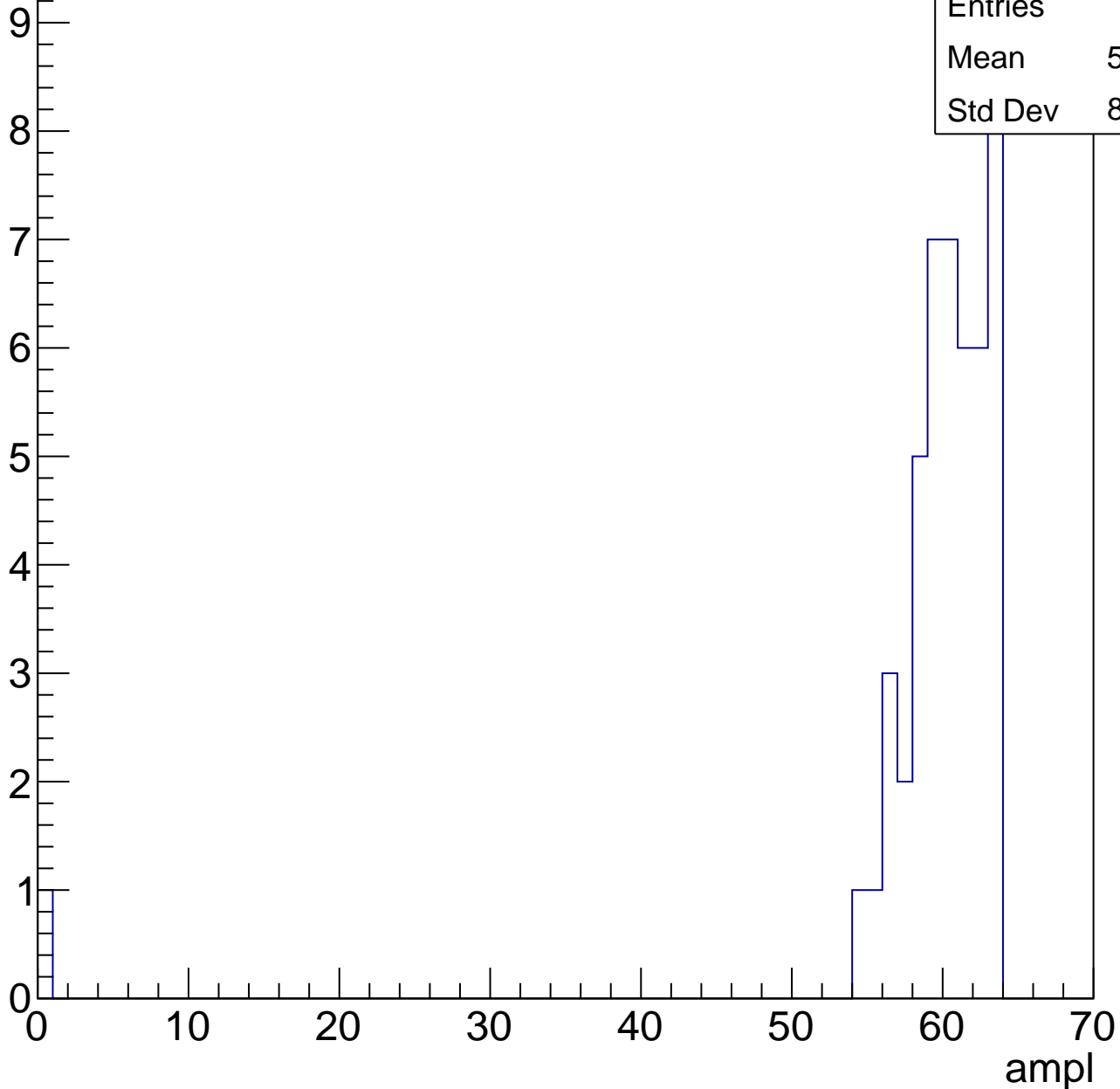


B1L103S, U18-ch39, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	58.73
Std Dev	8.883



B1L103S, U18-ch39, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch39, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch40, adc0

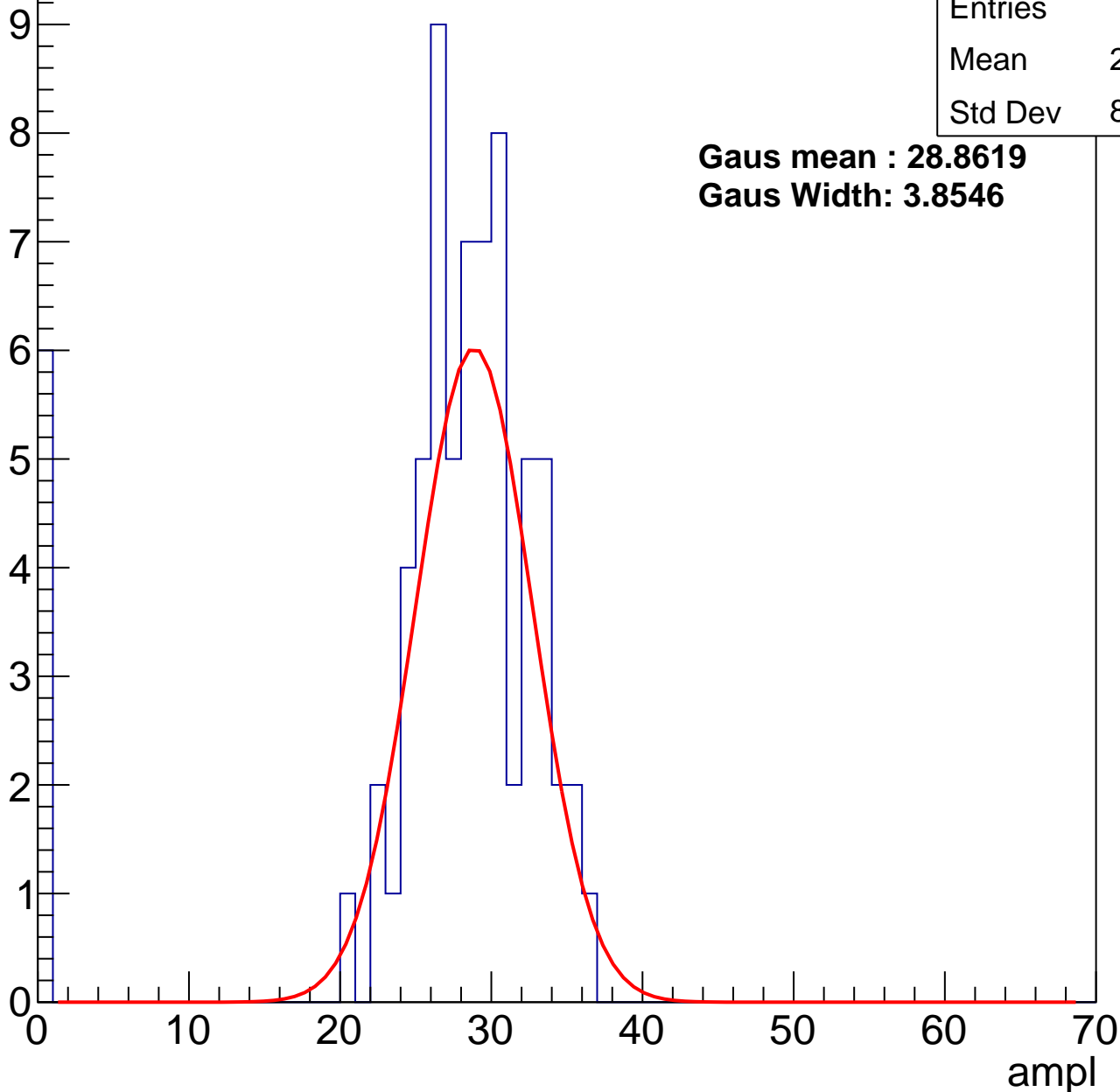
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	26.07
Std Dev	8.533

Gaus mean : 28.8619

Gaus Width: 3.8546



B1L103S, U18-ch40, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	35.93
Std Dev	5.871

Gaus mean : 36.9414

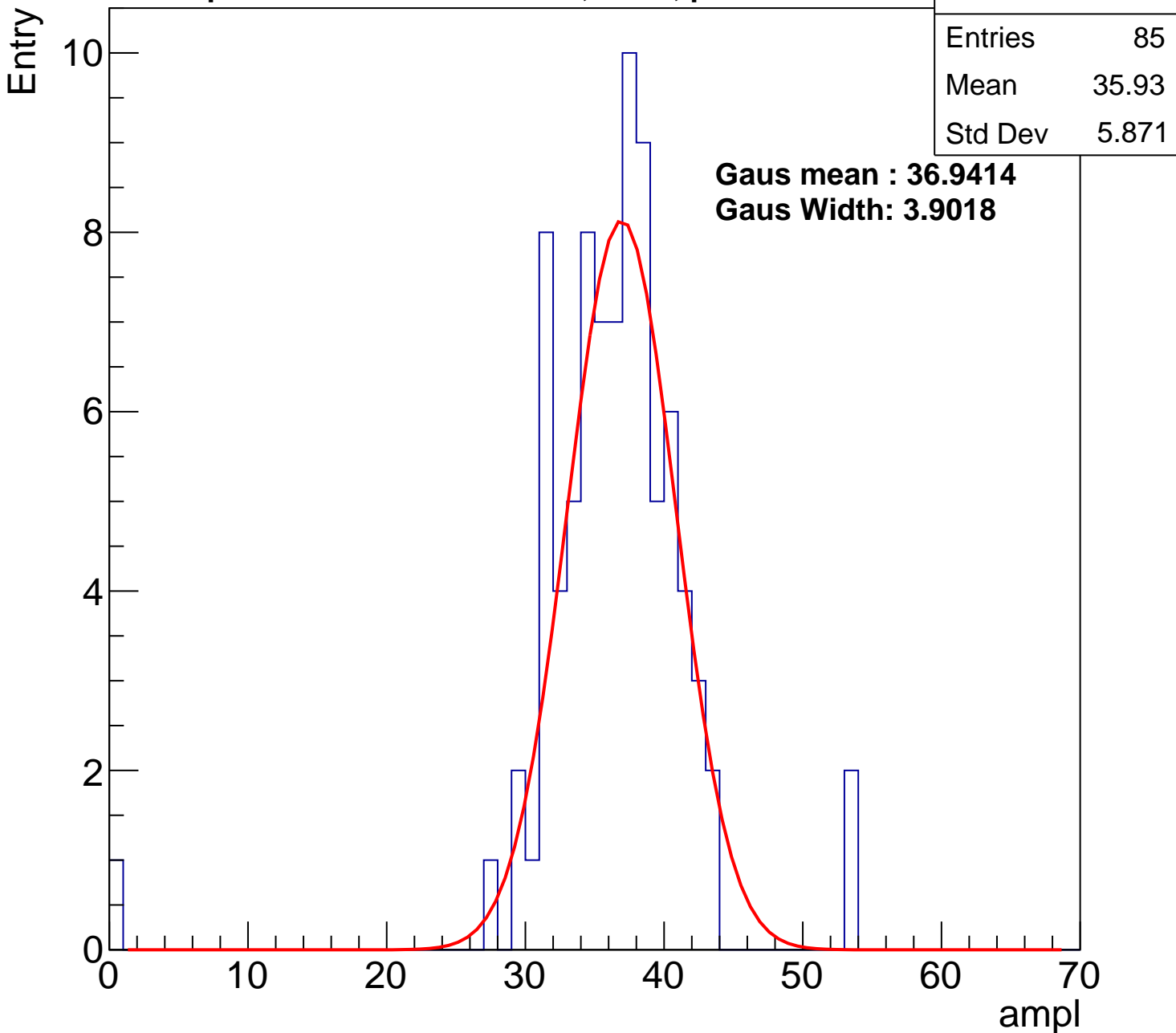
Gaus Width: 3.9018

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch40, adc2

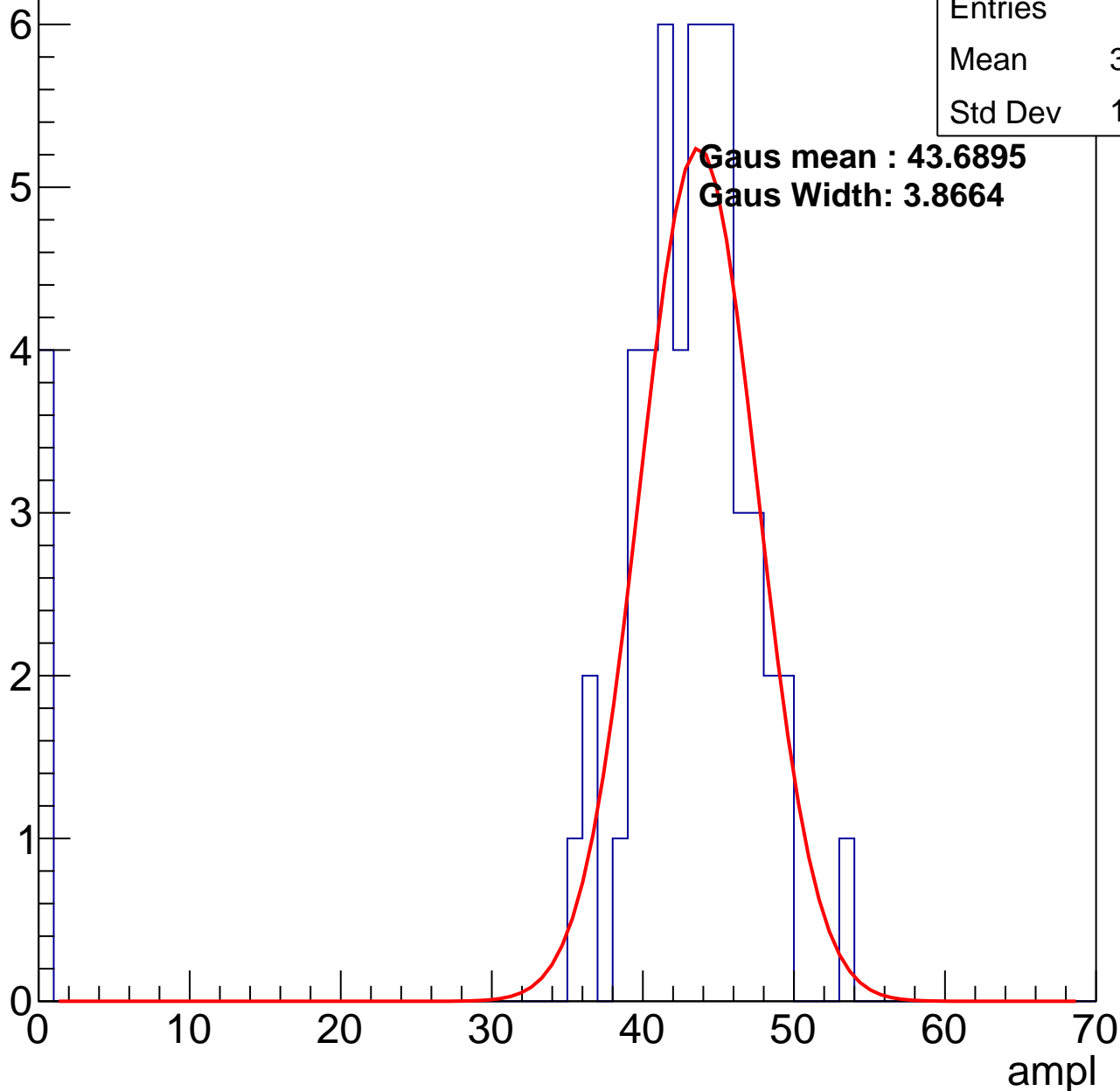
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	39.87
Std Dev	11.68

Gaus mean : 43.6895

Gaus Width: 3.8664

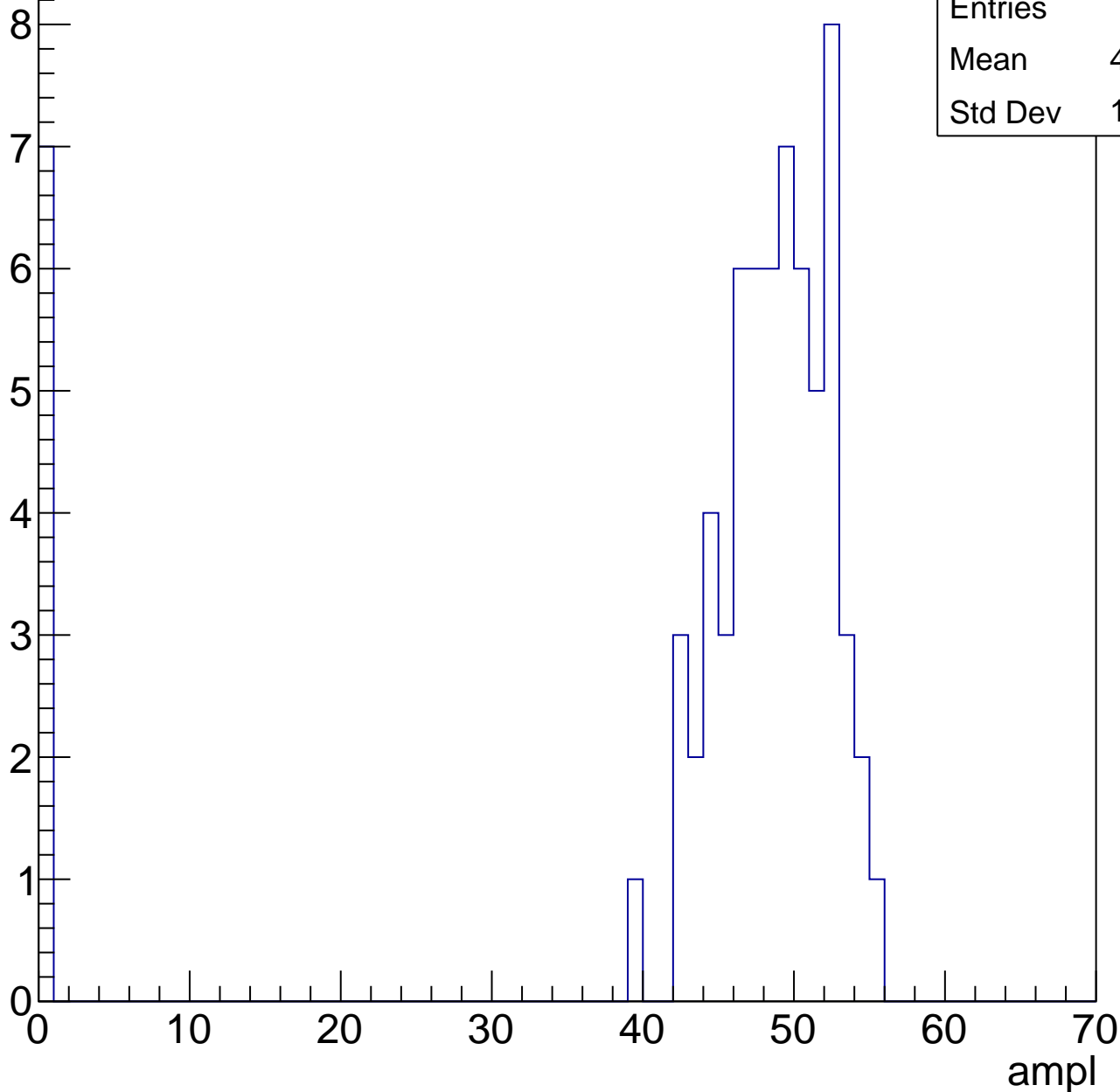


B1L103S, U18-ch40, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	43.49
Std Dev	14.86

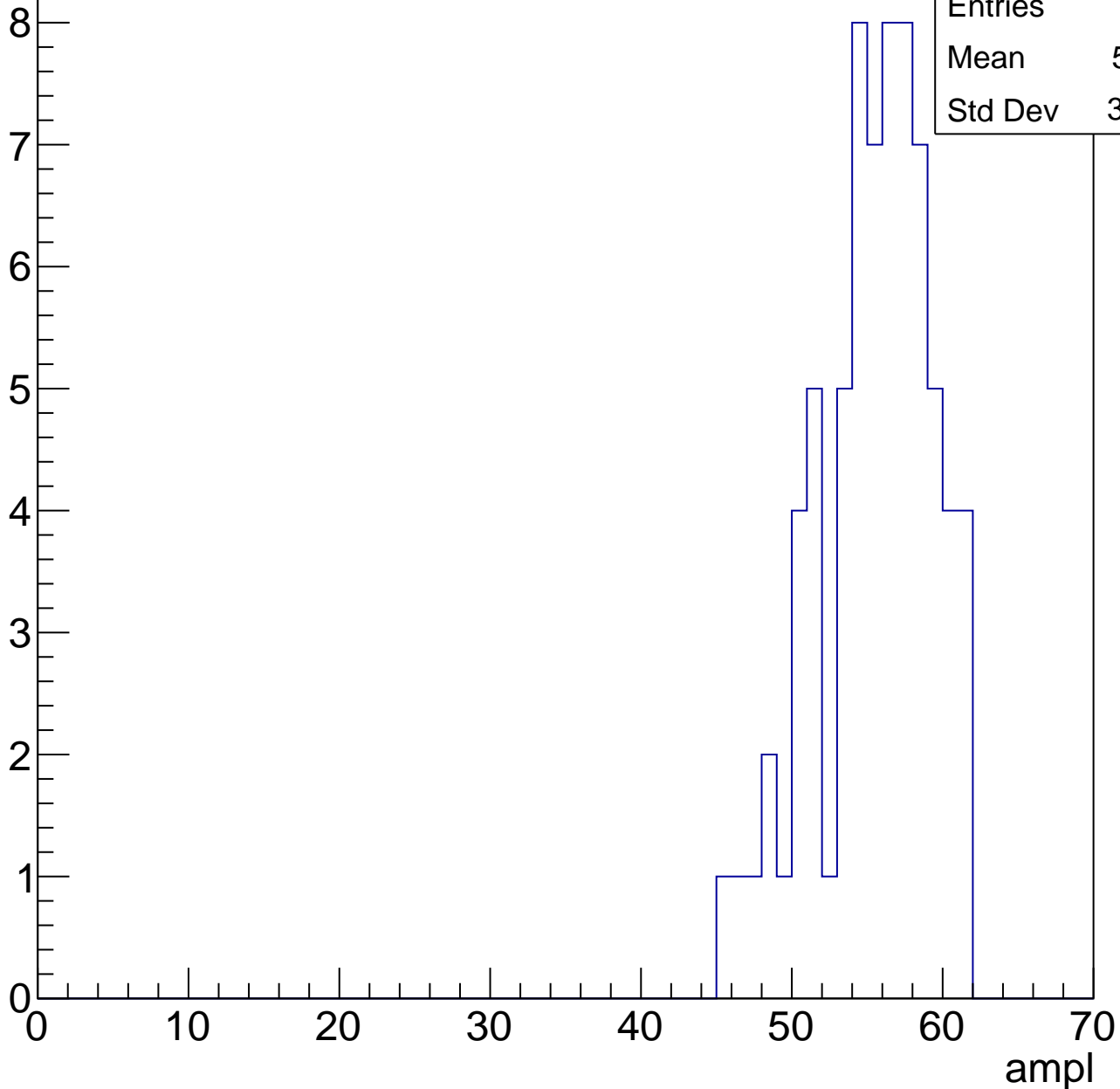


B1L103S, U18-ch40, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	55.01
Std Dev	3.769

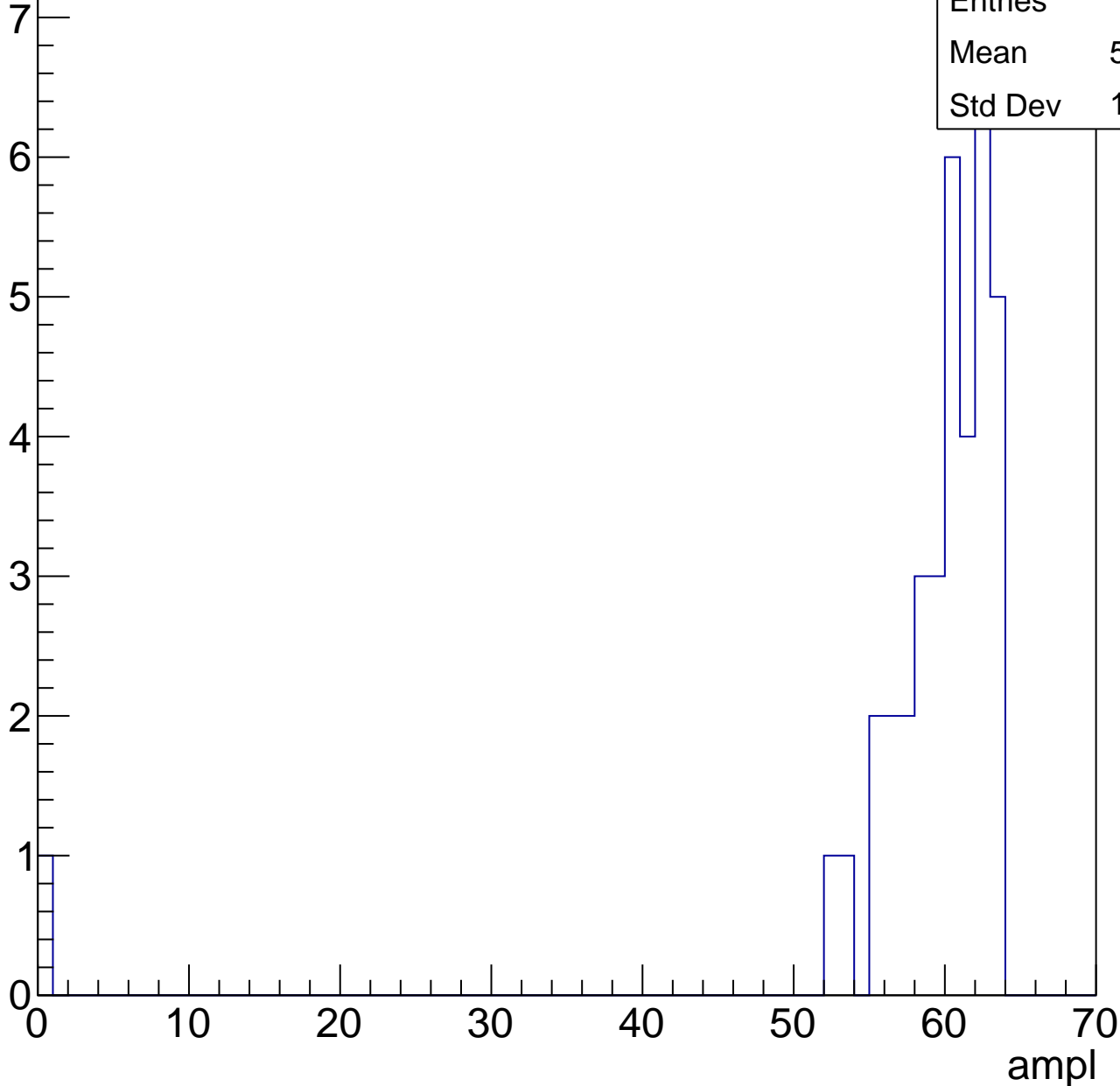


B1L103S, U18-ch40, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

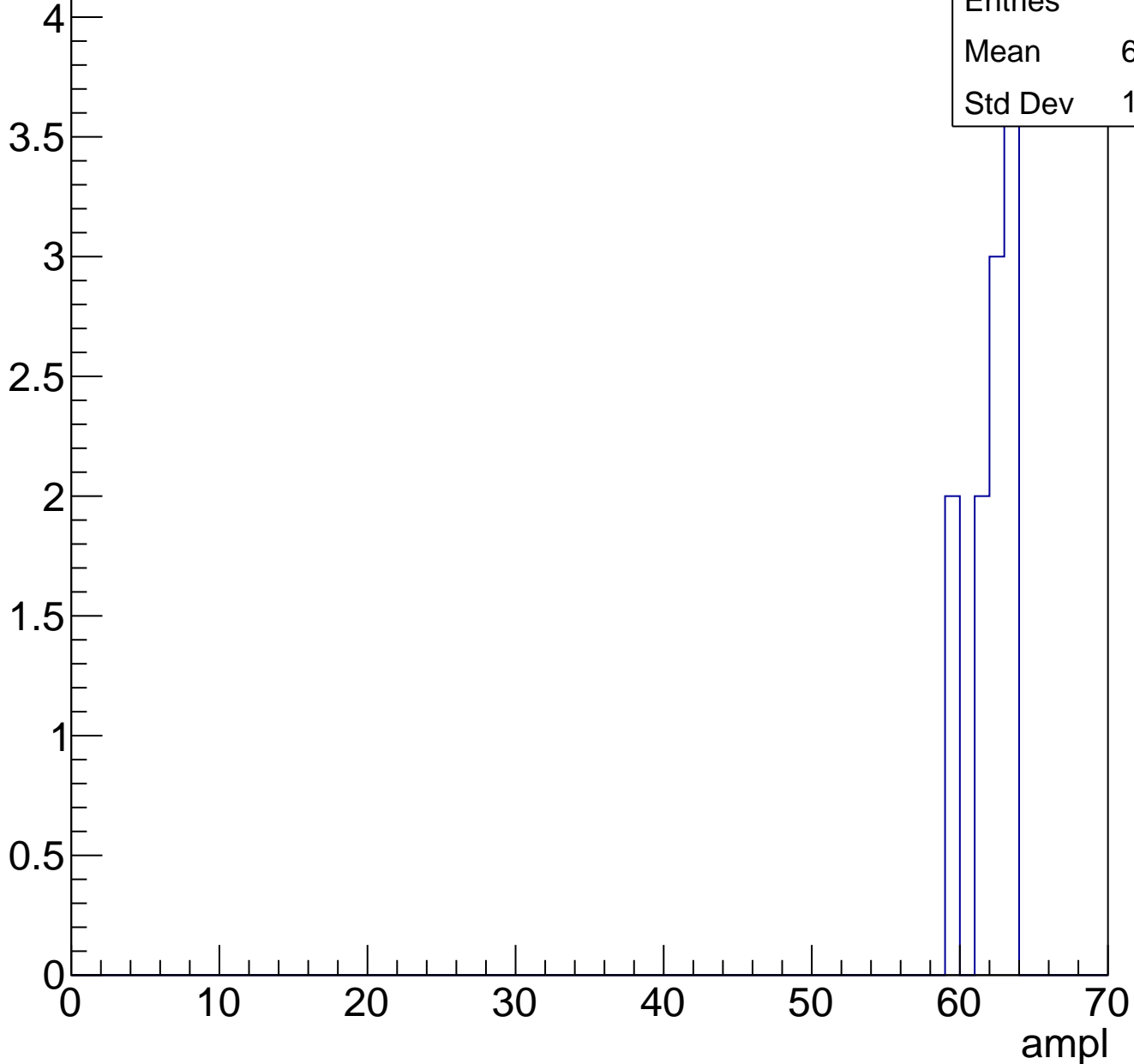
Entries	37
Mean	57.97
Std Dev	10.07



B1L103S, U18-ch40, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	11
Mean	61.64
Std Dev	1.432

B1L103S, U18-ch40, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U18-ch41, adc0

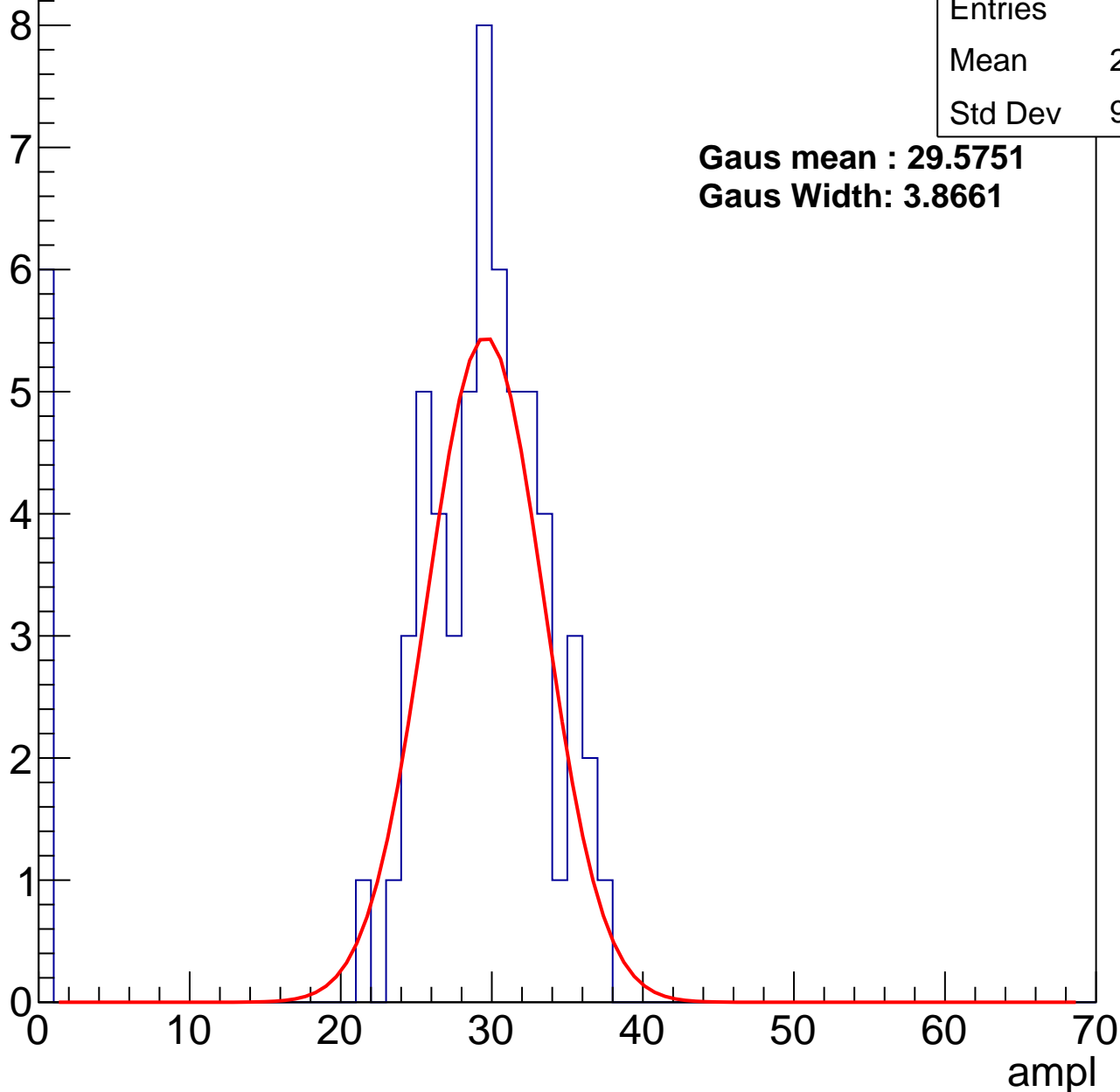
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	26.56
Std Dev	9.265

Gaus mean : 29.5751

Gaus Width: 3.8661



B1L103S, U18-ch41, adc1

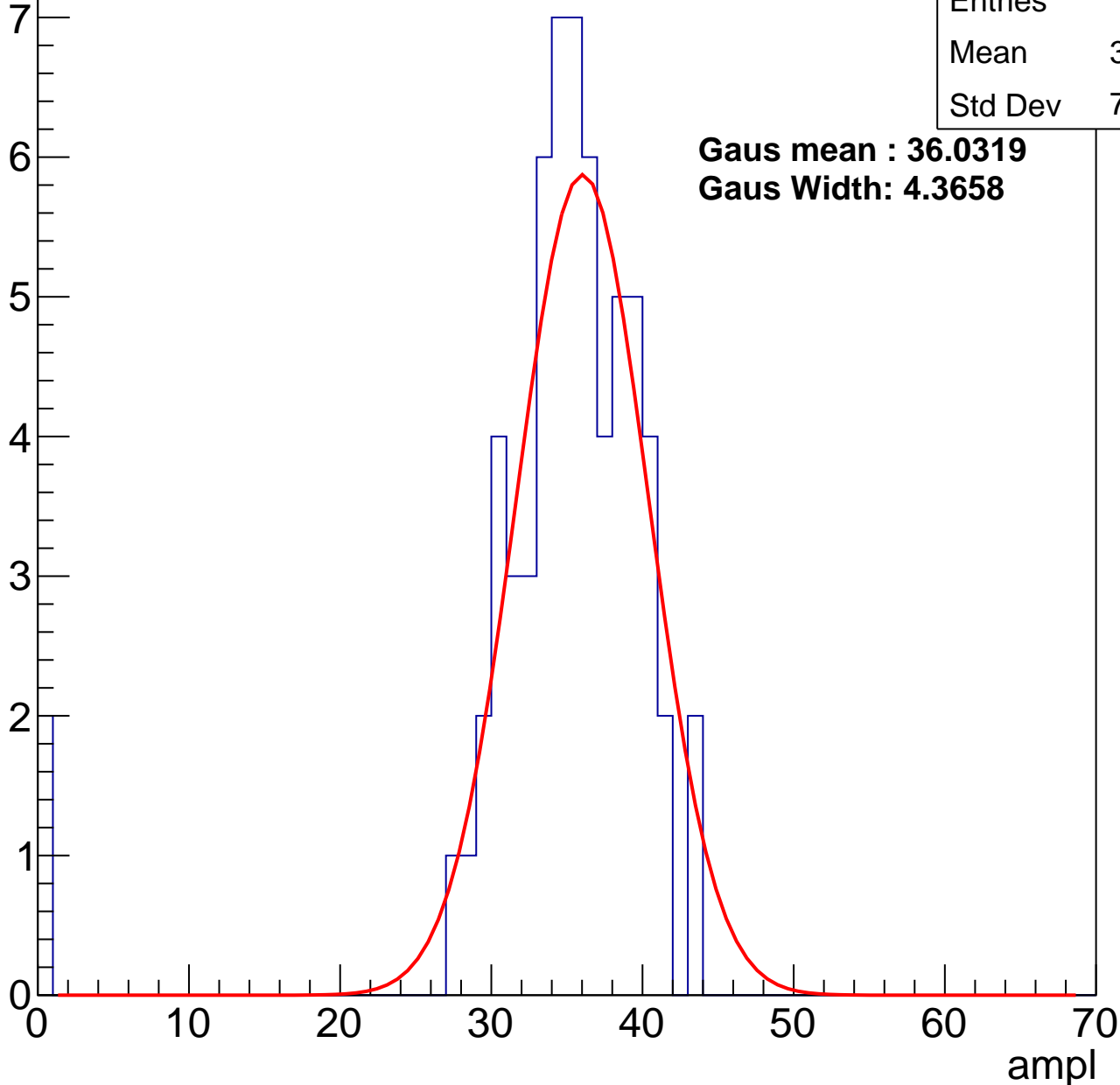
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	34.06
Std Dev	7.106

Gaus mean : 36.0319

Gaus Width: 4.3658



B1L103S, U18-ch41, adc2

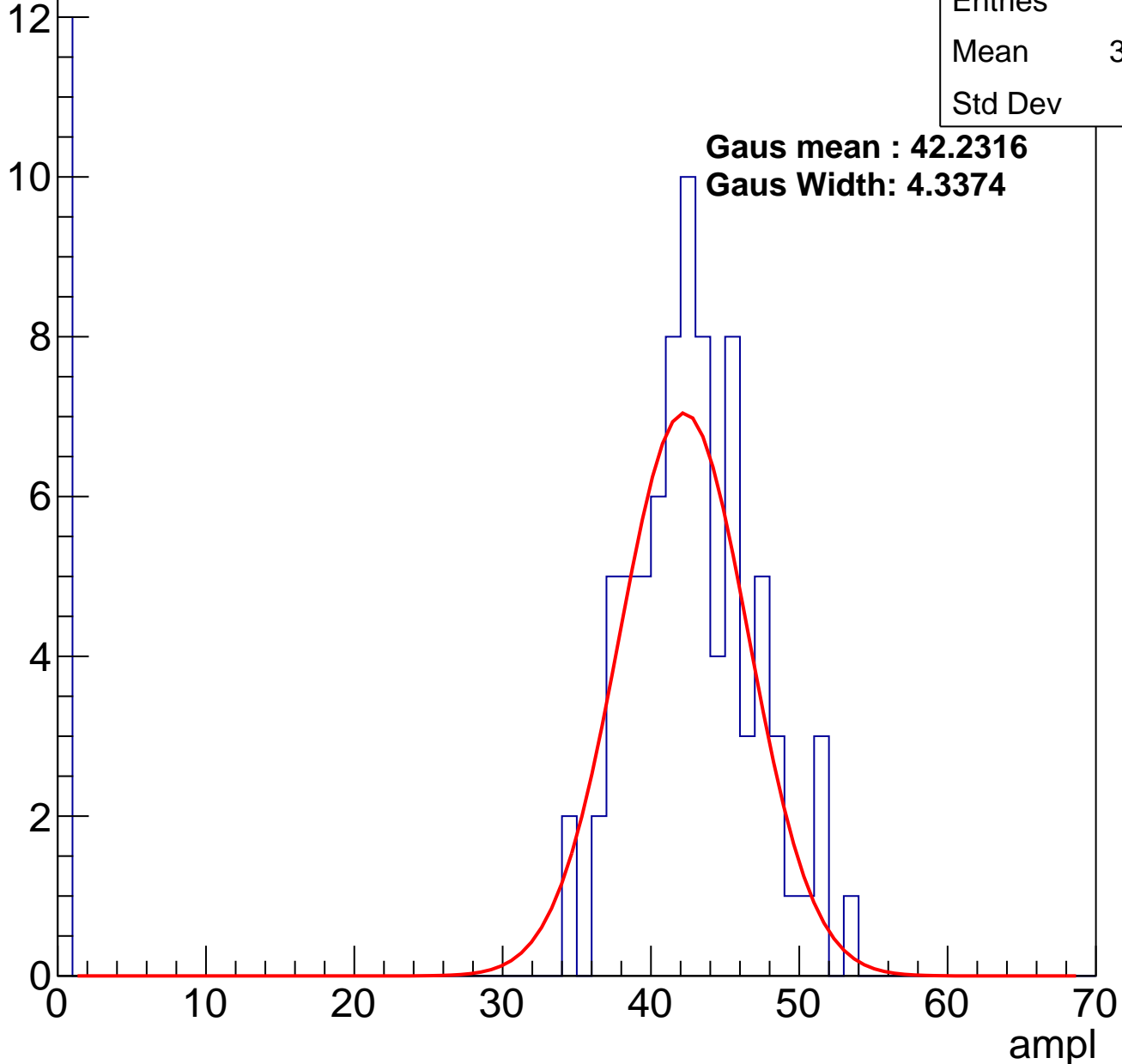
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	36.96
Std Dev	14.8

Gaus mean : 42.2316

Gaus Width: 4.3374

Entry

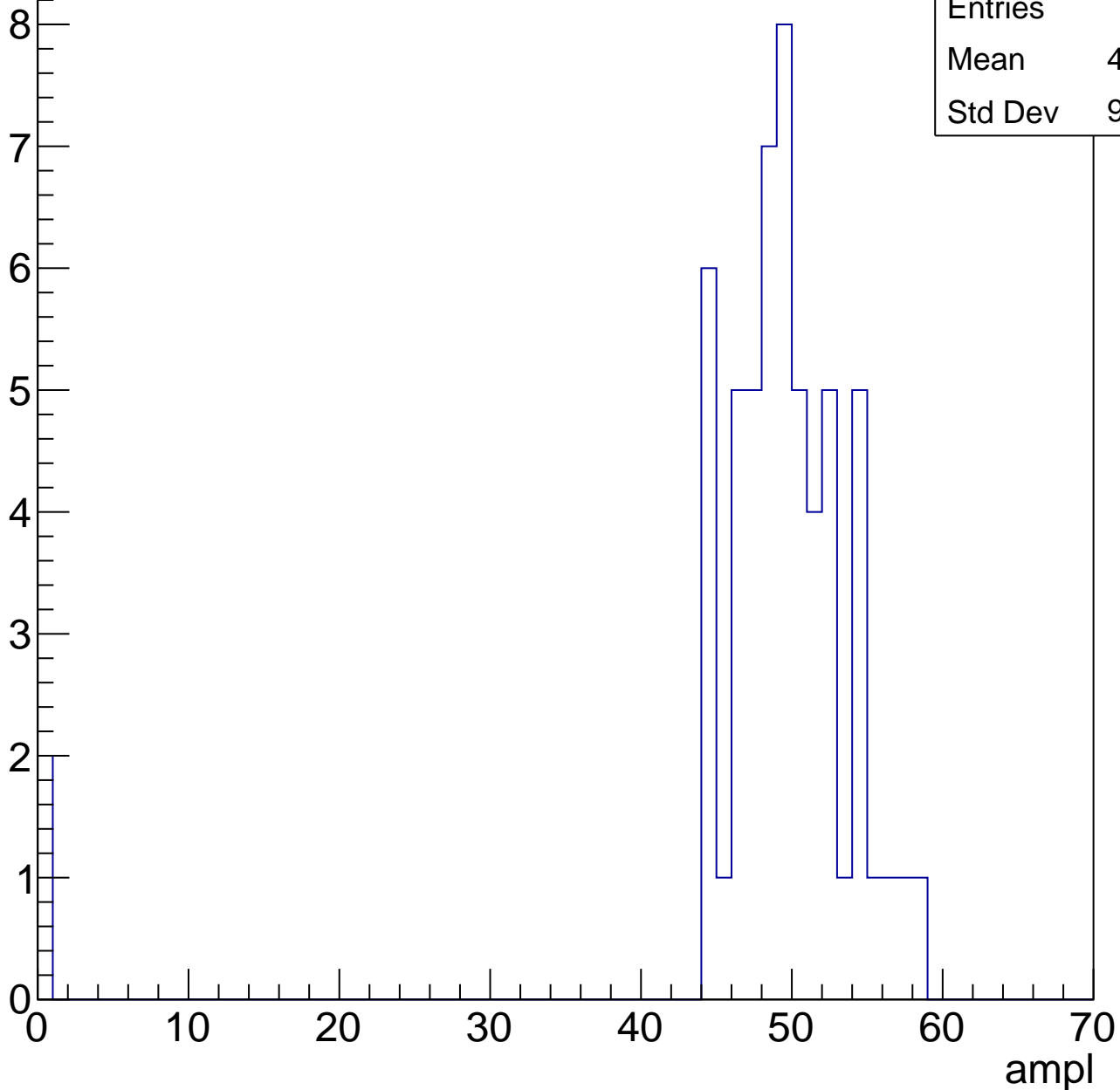


B1L103S, U18-ch41, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	47.67
Std Dev	9.626

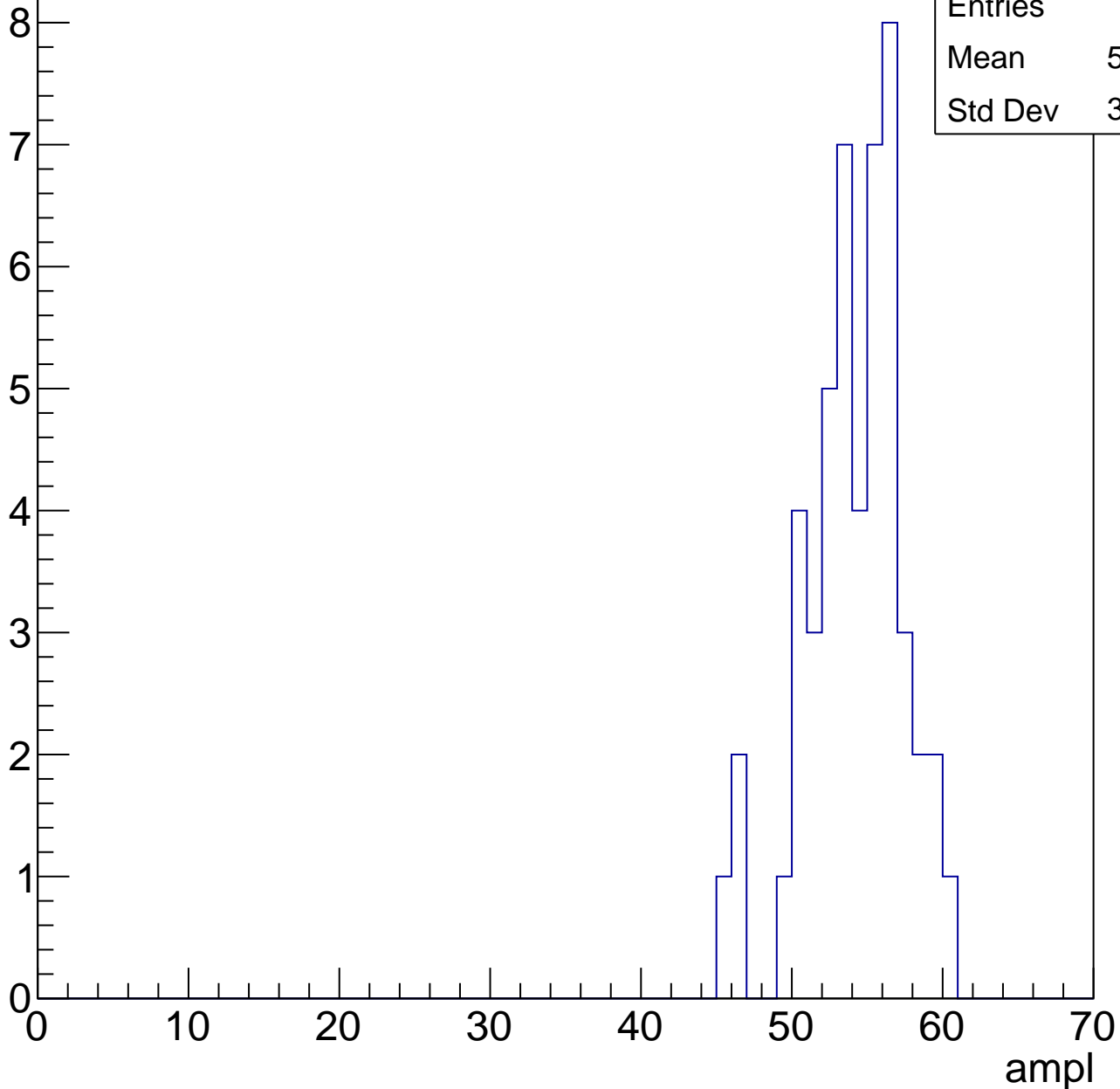


B1L103S, U18-ch41, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

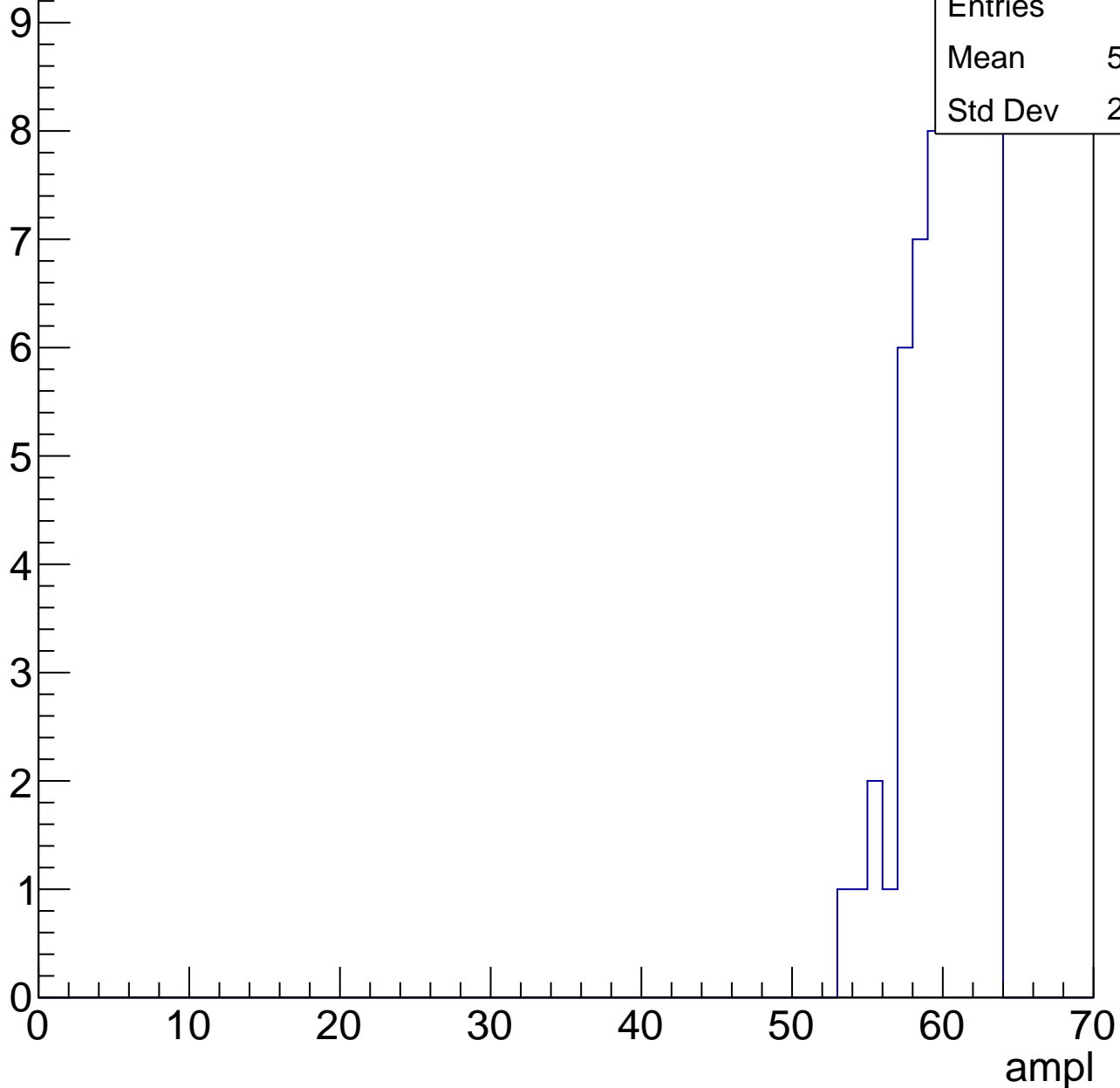
Entries	50
Mean	53.68
Std Dev	3.258



B1L103S, U18-ch41, adc5

calib_packv5_041523_1651.root, FC#0, port C2

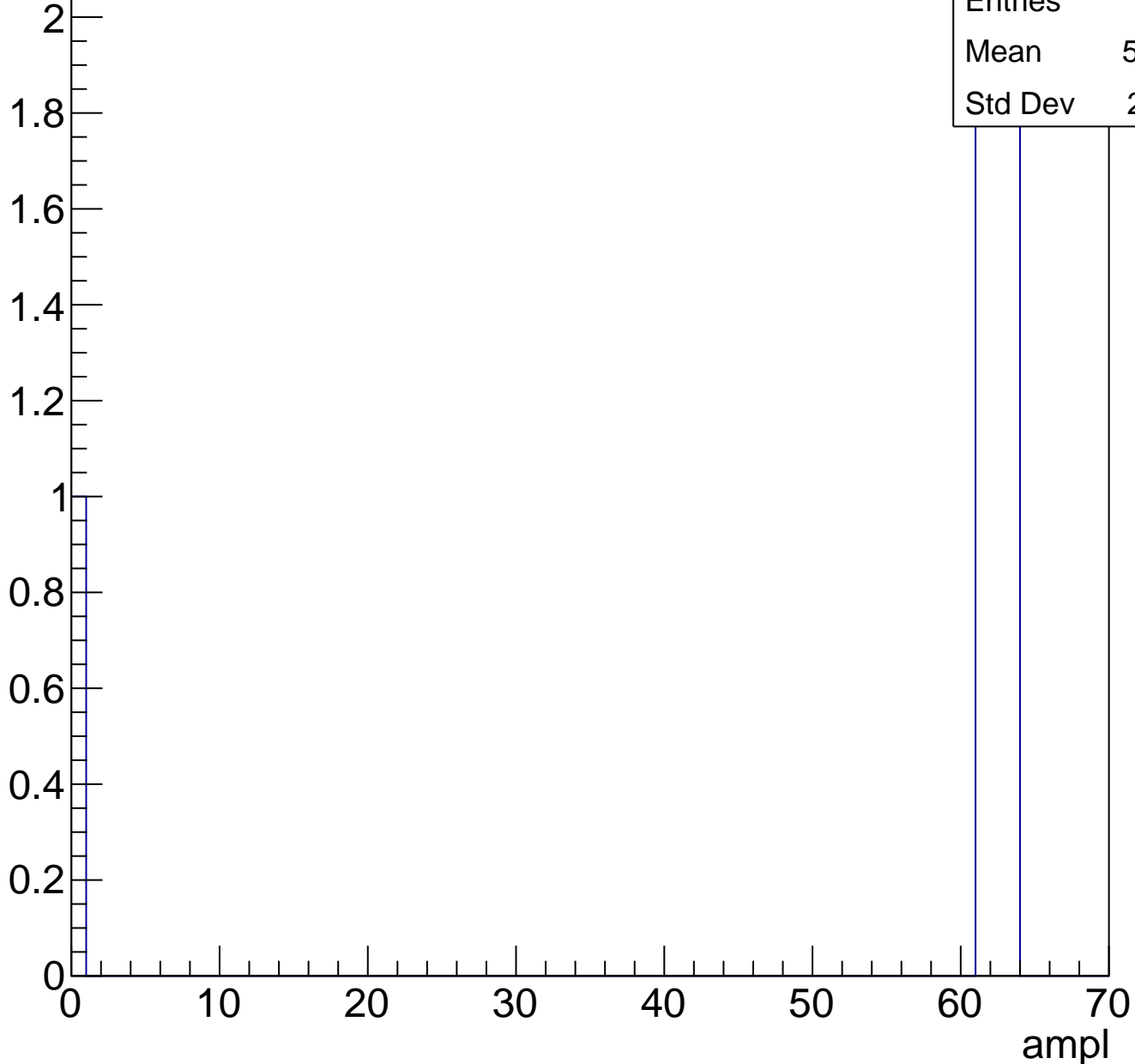
Entry



B1L103S, U18-ch41, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch41, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

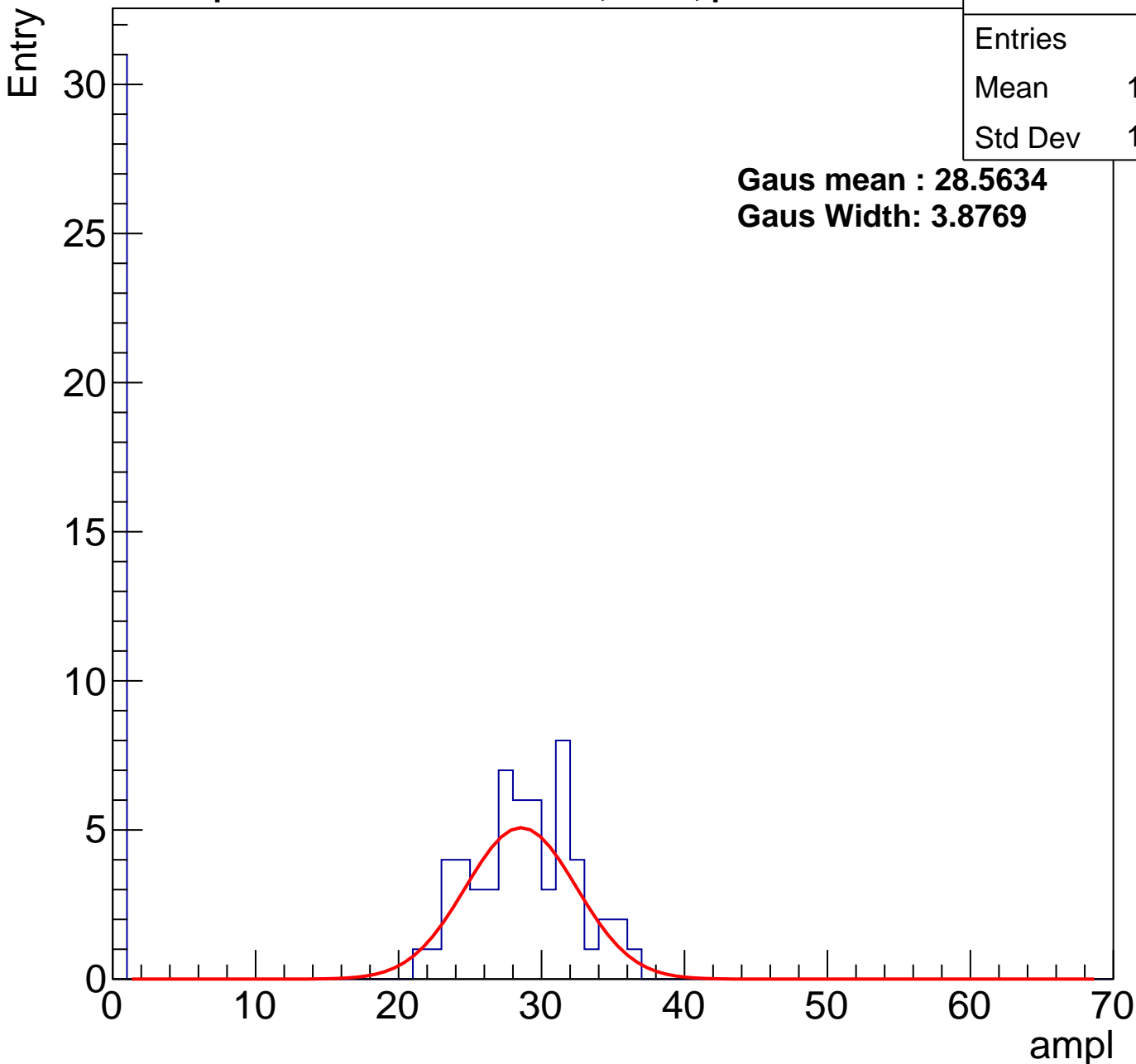
B1L103S, U18-ch42, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	18.25
Std Dev	13.87

Gaus mean : 28.5634

Gaus Width: 3.8769



B1L103S, U18-ch42, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	30.21
Std Dev	12.02

Gaus mean : 35.1604

Gaus Width: 3.4584

Entry

10

8

6

4

2

0

0

10

20

30

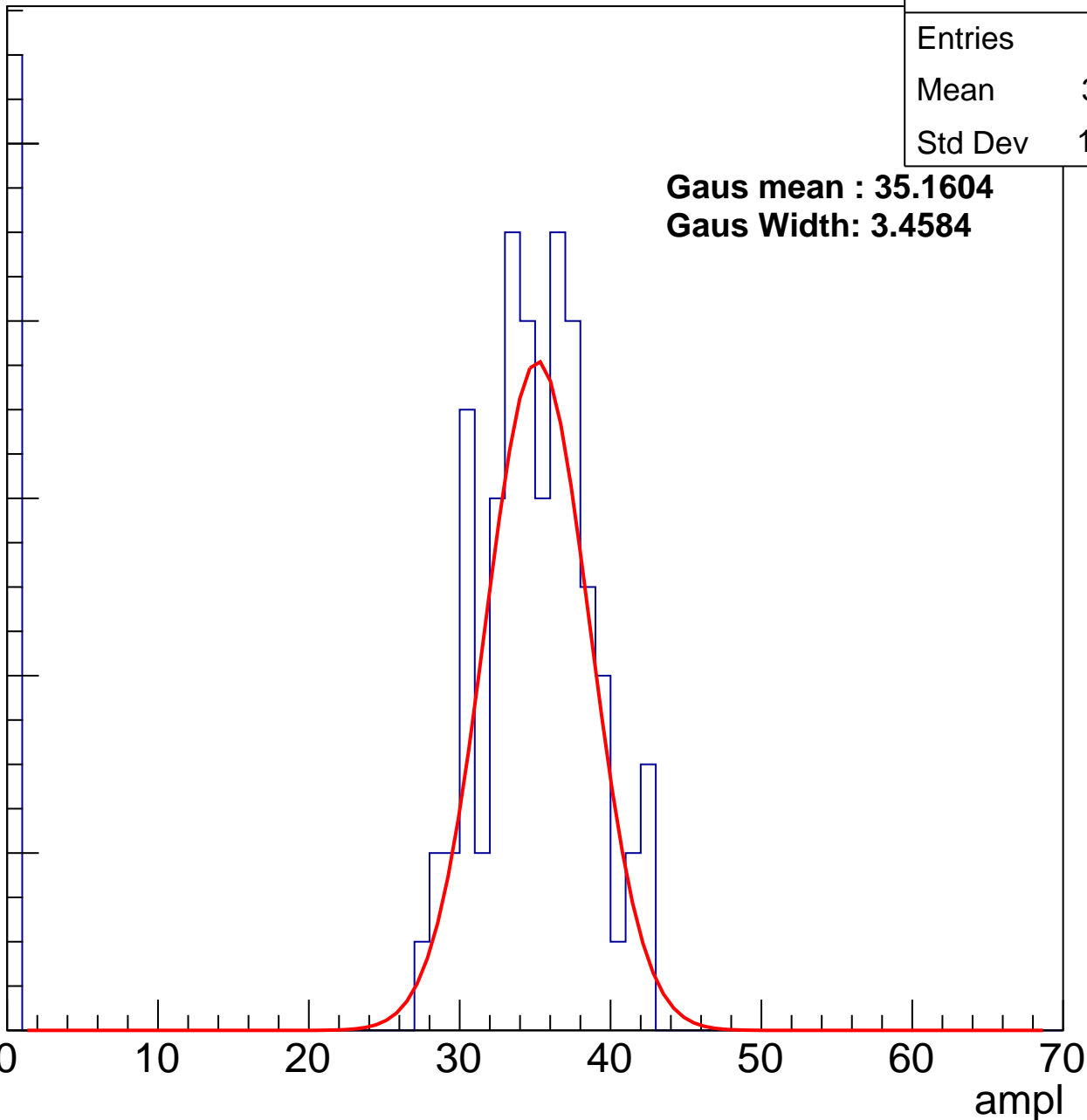
40

50

60

70

ampl



B1L103S, U18-ch42, adc2

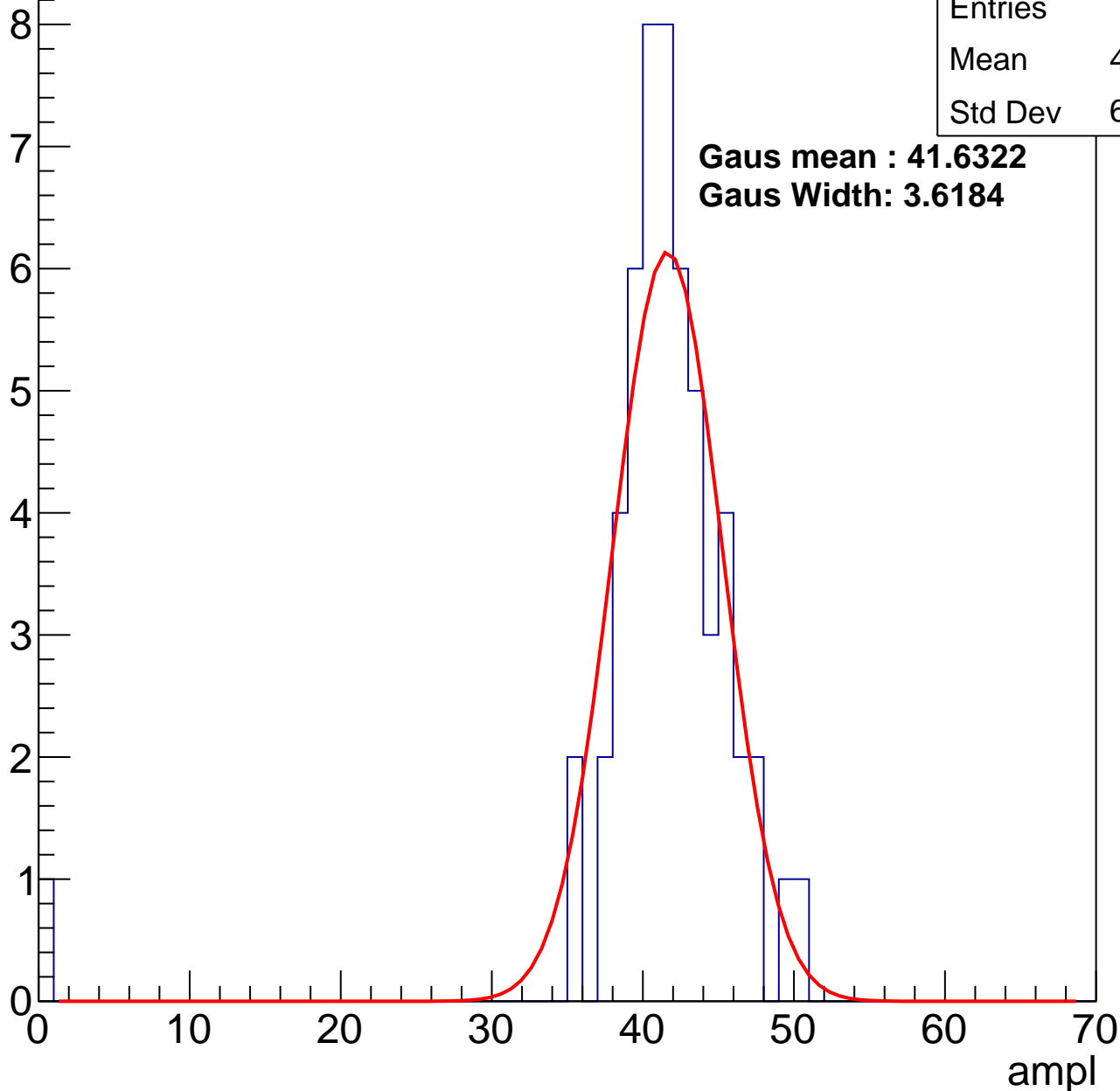
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	40.76
Std Dev	6.373

Gaus mean : 41.6322

Gaus Width: 3.6184

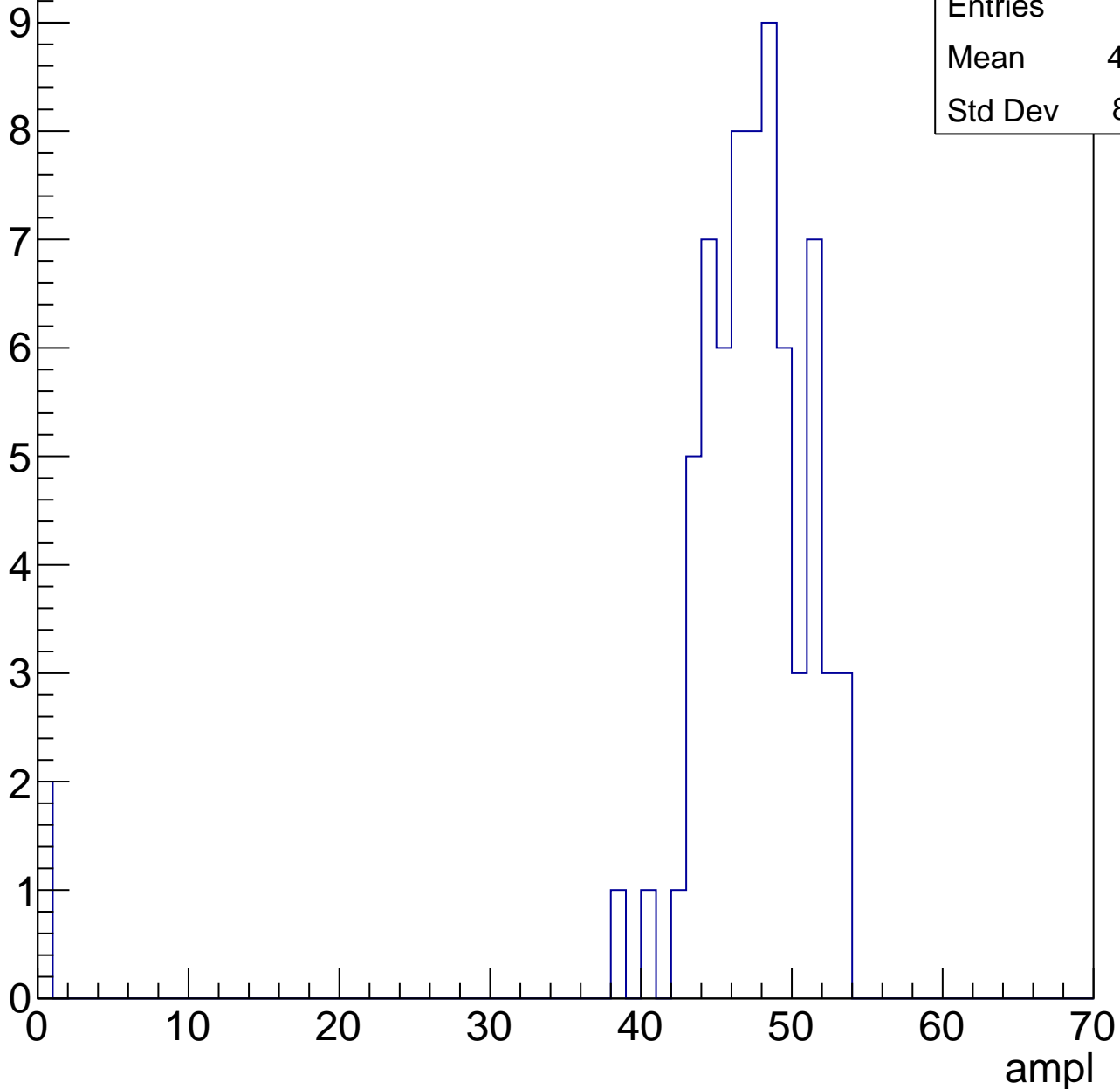


B1L103S, U18-ch42, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	45.79
Std Dev	8.451

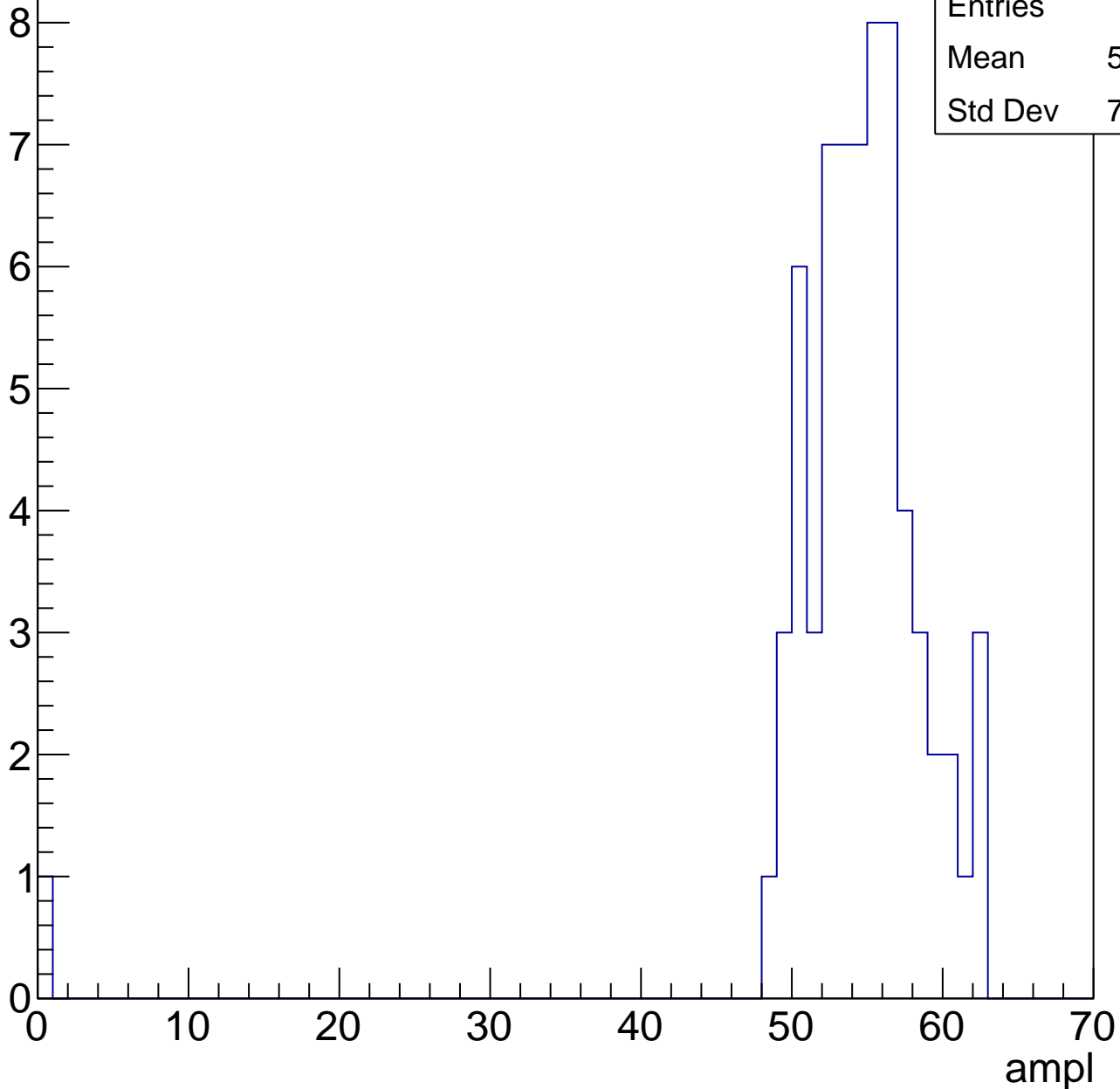


B1L103S, U18-ch42, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	53.58
Std Dev	7.447



B1L103S, U18-ch42, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries	38
Mean	58.71
Std Dev	2.533

ampl

0

10

20

30

40

50

60

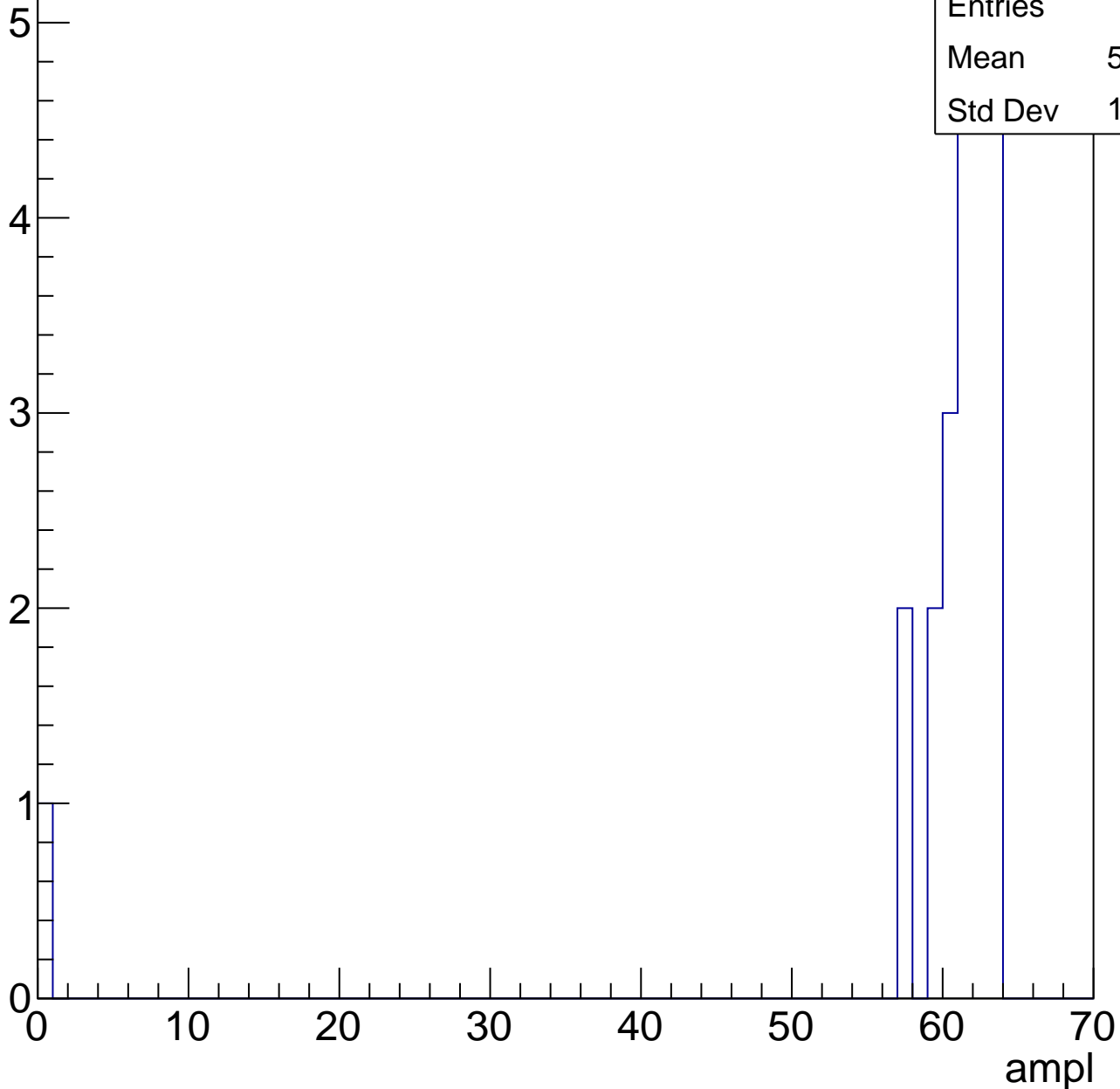
70

B1L103S, U18-ch42, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.35
Std Dev	12.56



B1L103S, U18-ch42, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U18-ch43, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	25.3
Std Dev	10.05

Gaus mean : 29.2208

Gaus Width: 3.8002

Entry

10

8

6

4

2

0

0

10

20

30

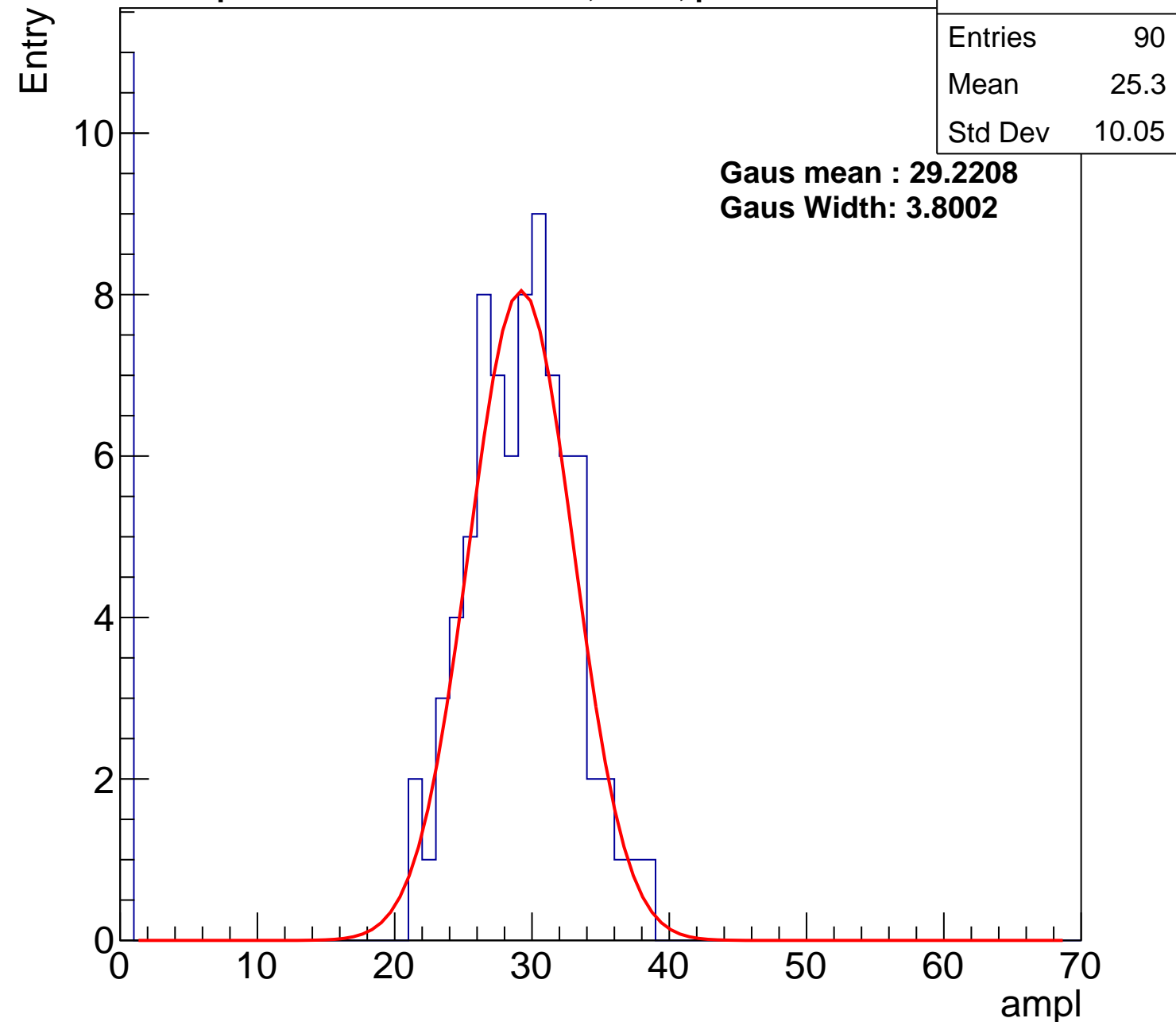
40

50

60

70

ampl



B1L103S, U18-ch43, adc1

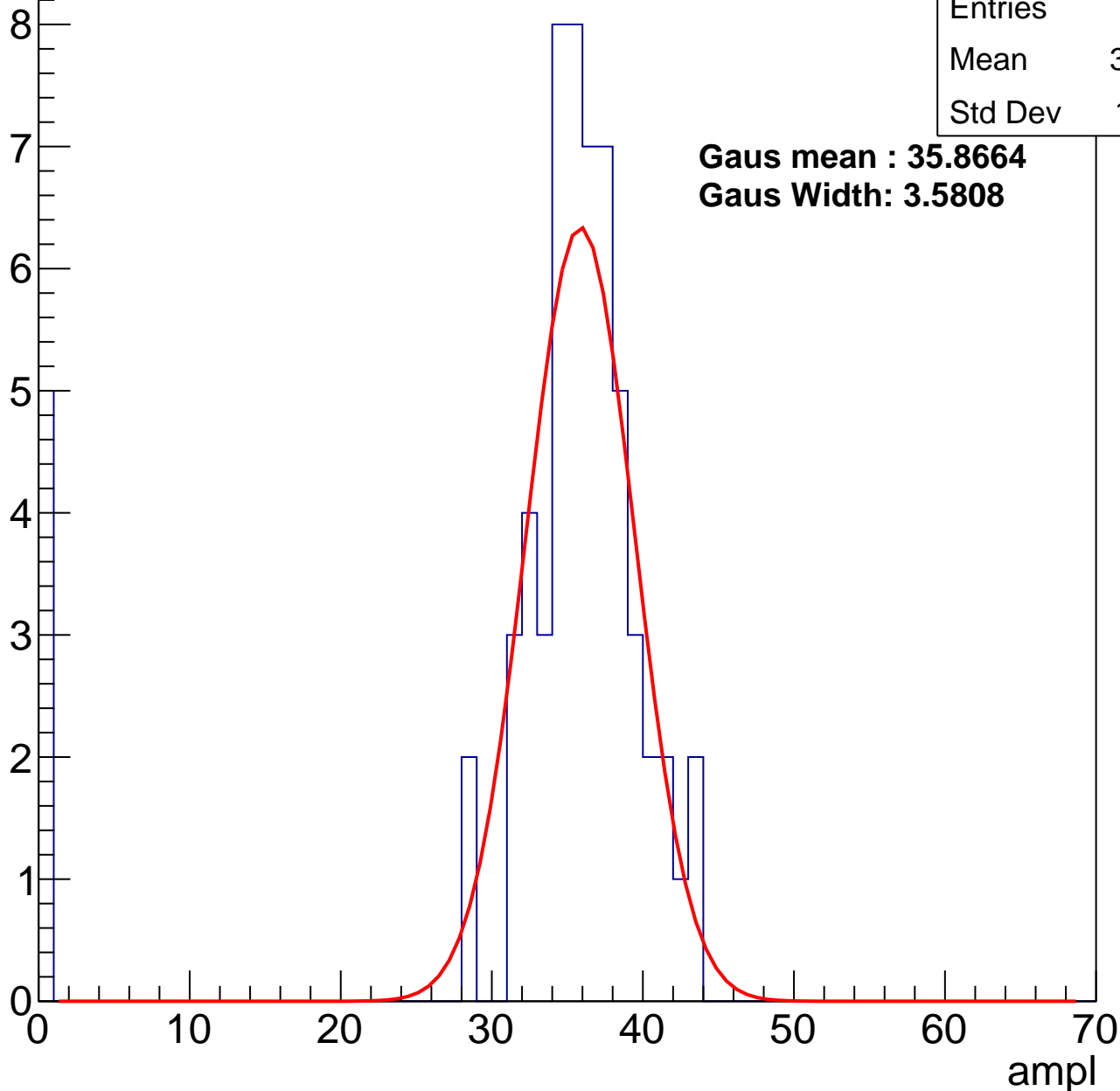
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.84
Std Dev	10.21

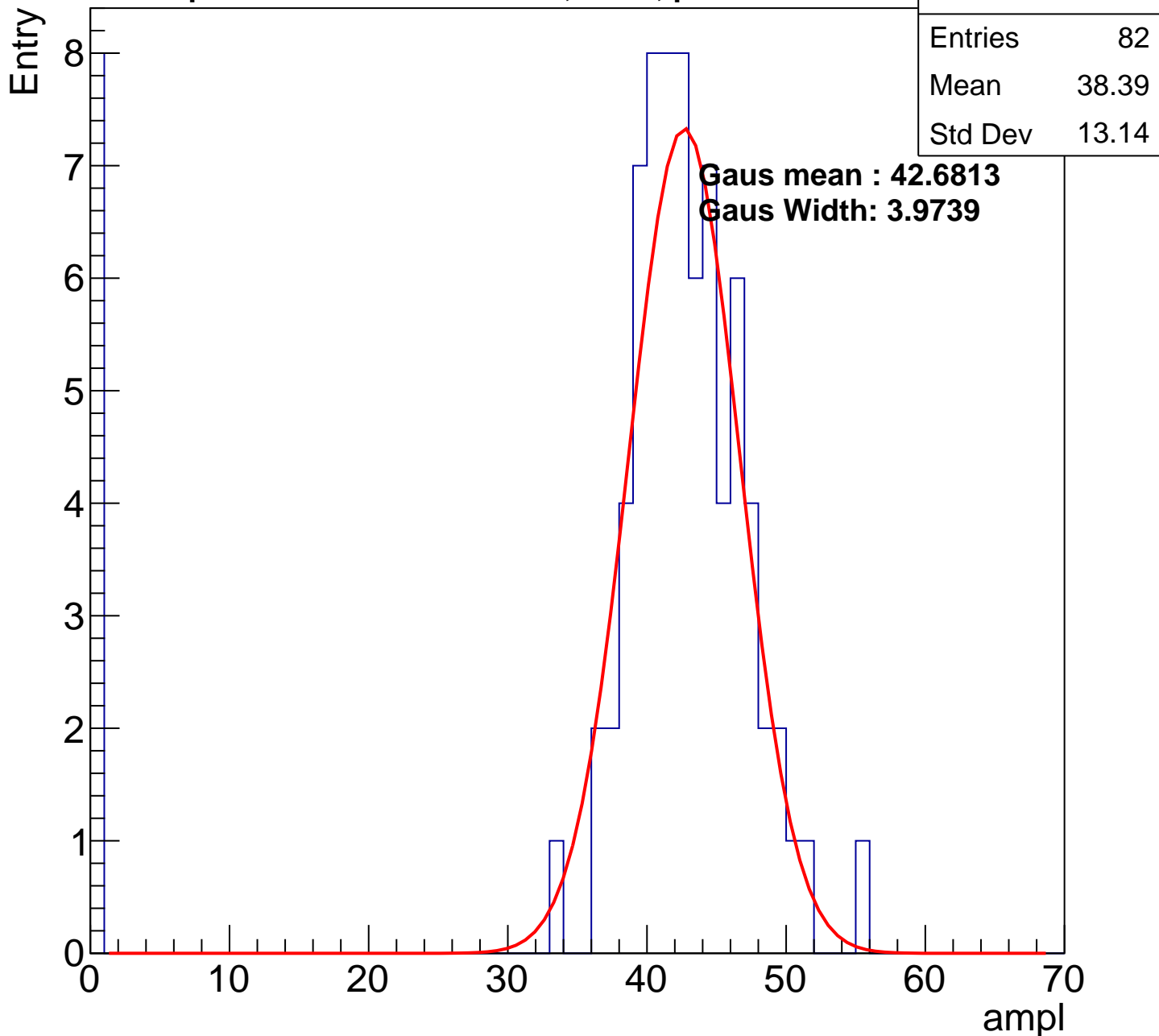
Gaus mean : 35.8664

Gaus Width: 3.5808



B1L103S, U18-ch43, adc2

calib_packv5_041523_1651.root, FC#0, port C2

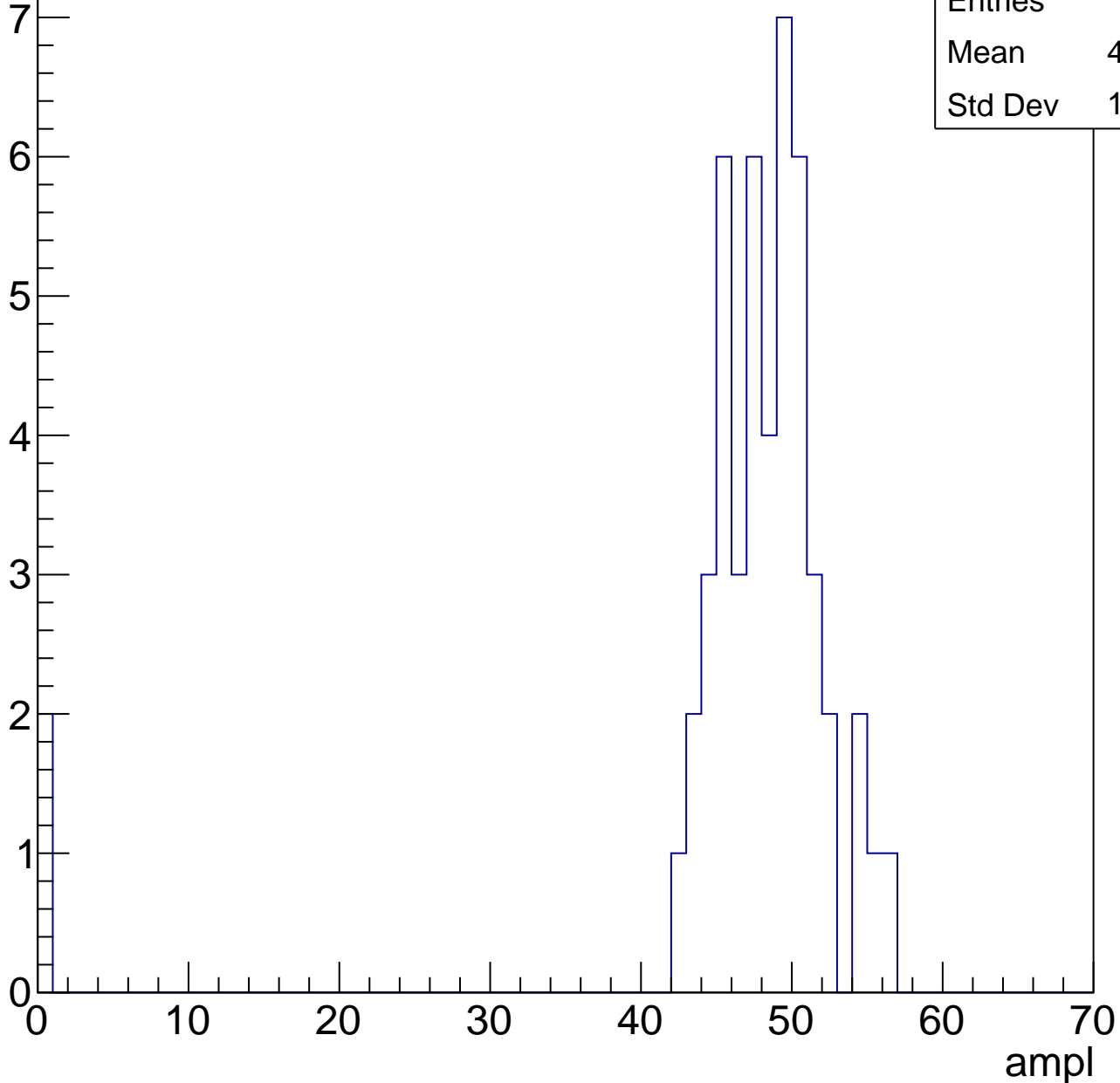


B1L103S, U18-ch43, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	46.14
Std Dev	10.02



B1L103S, U18-ch43, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	54.2
Std Dev	3.454

Entry

10

8

6

4

2

0

0

10

20

30

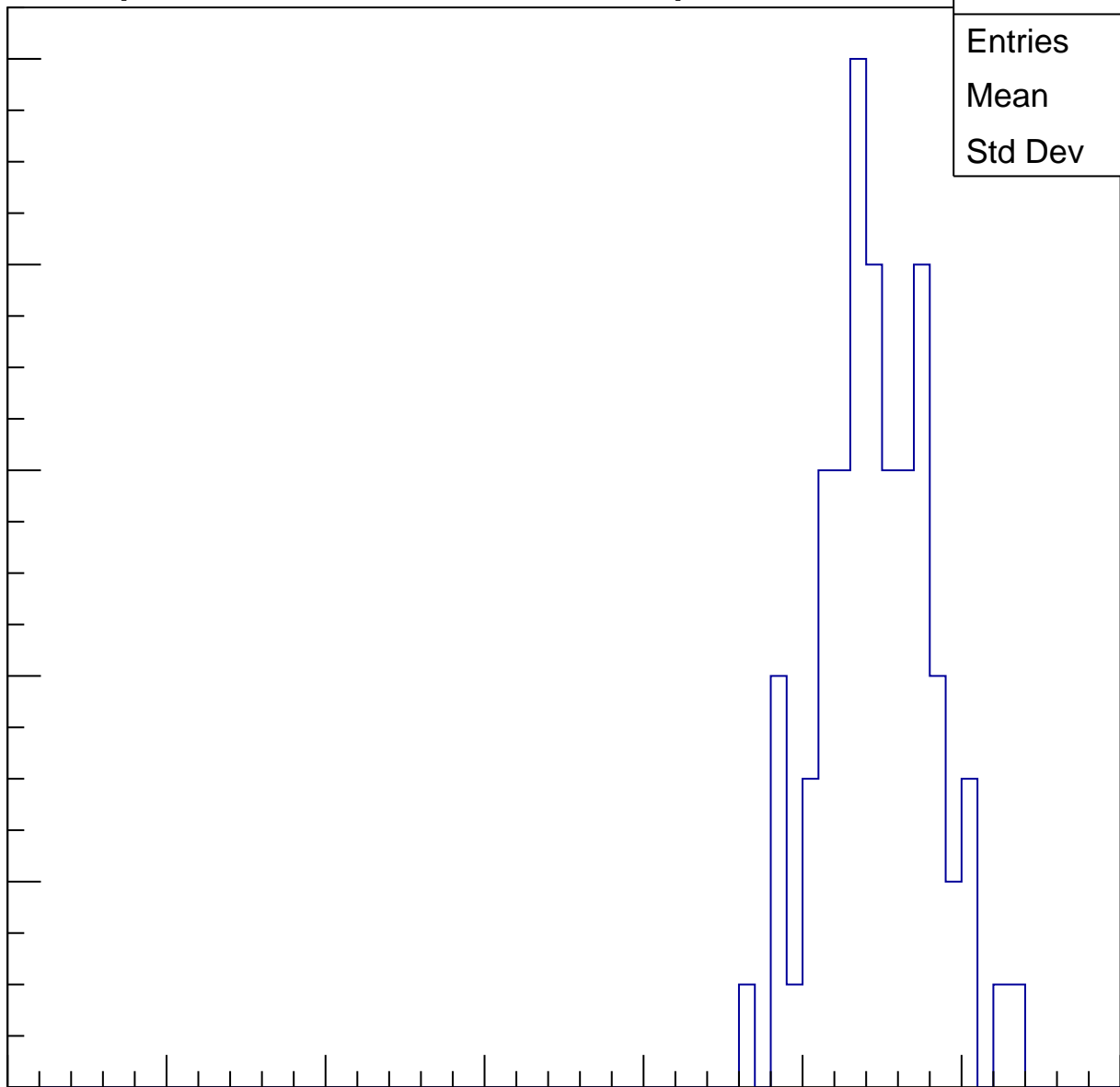
40

50

60

70

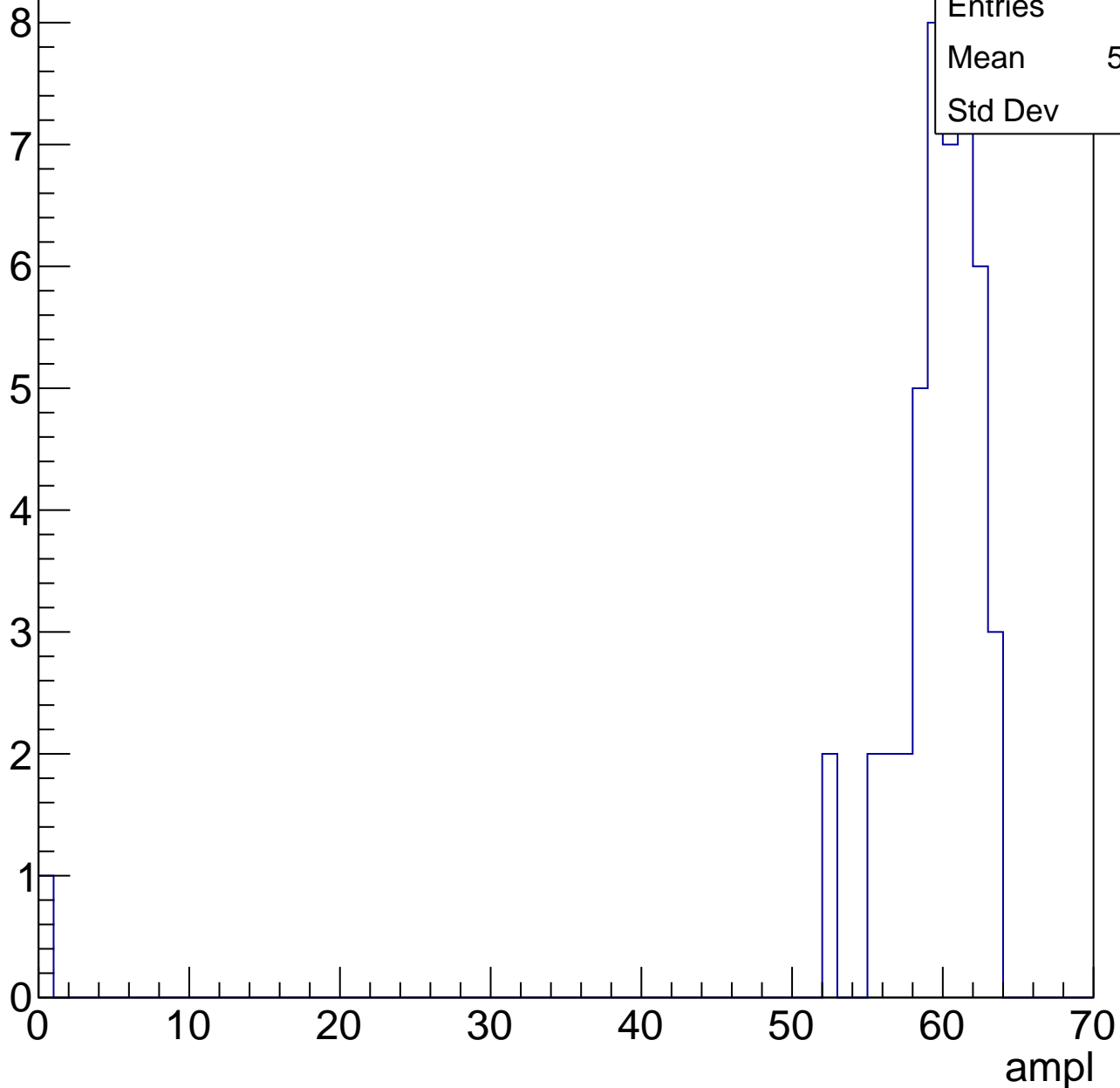
ampl



B1L103S, U18-ch43, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

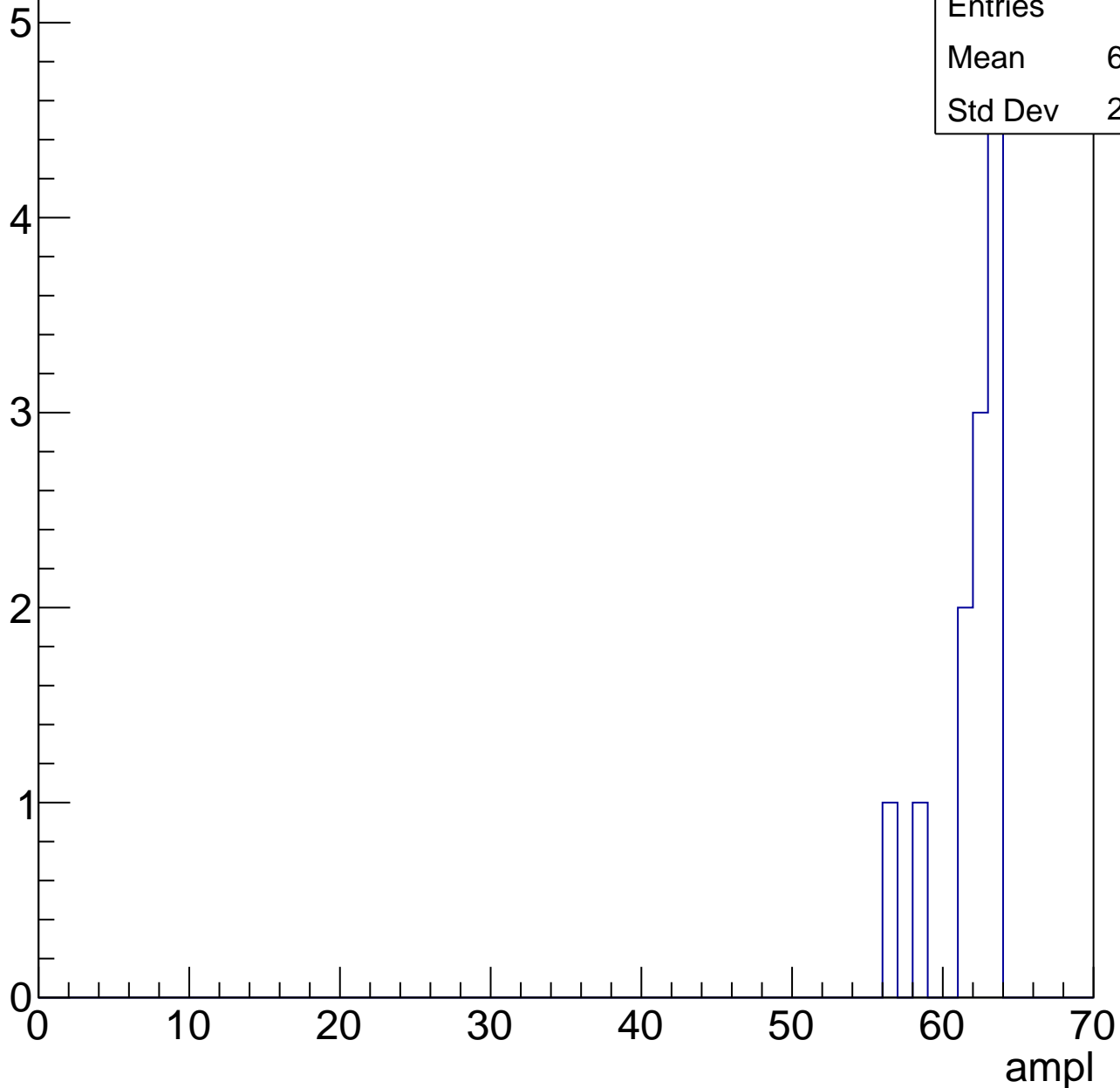


B1L103S, U18-ch43, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.42
Std Dev	2.139



B1L103S, U18-ch43, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U18-ch44, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	24.19
Std Dev	10.49

Gaus mean : 29.0797

Gaus Width: 3.9720

Entry

12

10

8

6

4

2

0

0

10

20

30

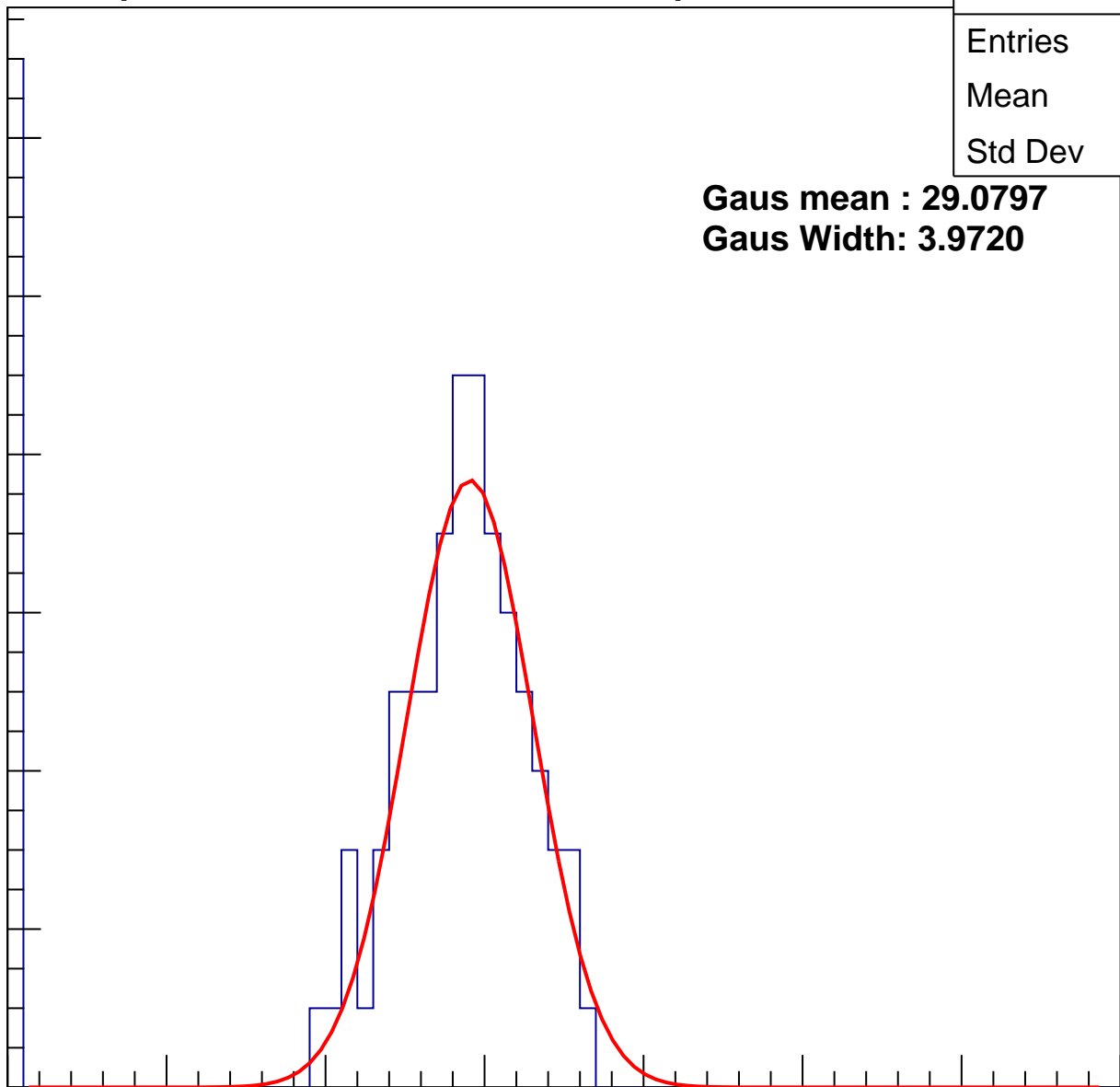
40

50

60

70

ampl



B1L103S, U18-ch44, adc1

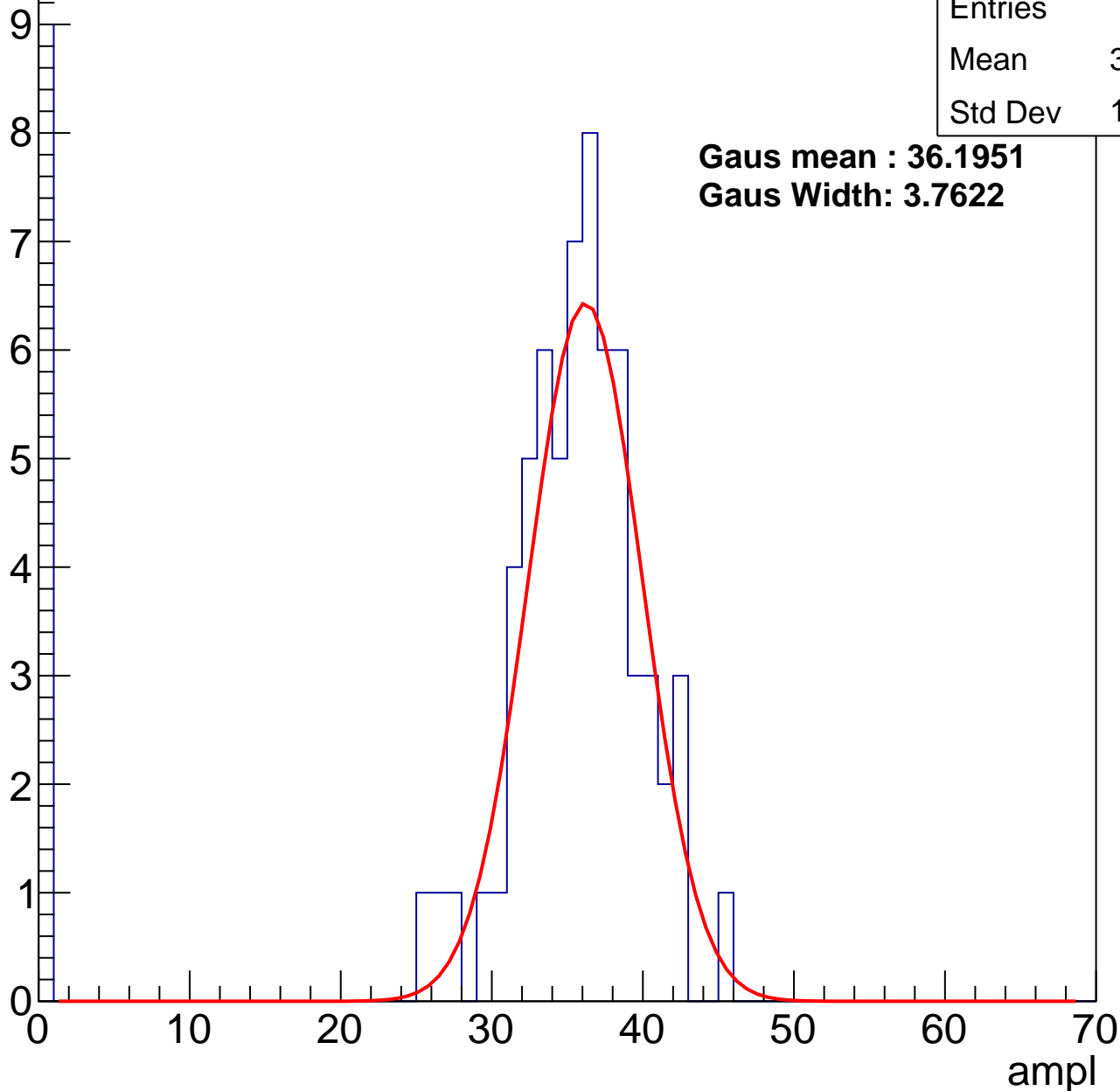
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	30.99
Std Dev	12.18

Gaus mean : 36.1951

Gaus Width: 3.7622



B1L103S, U18-ch44, adc2

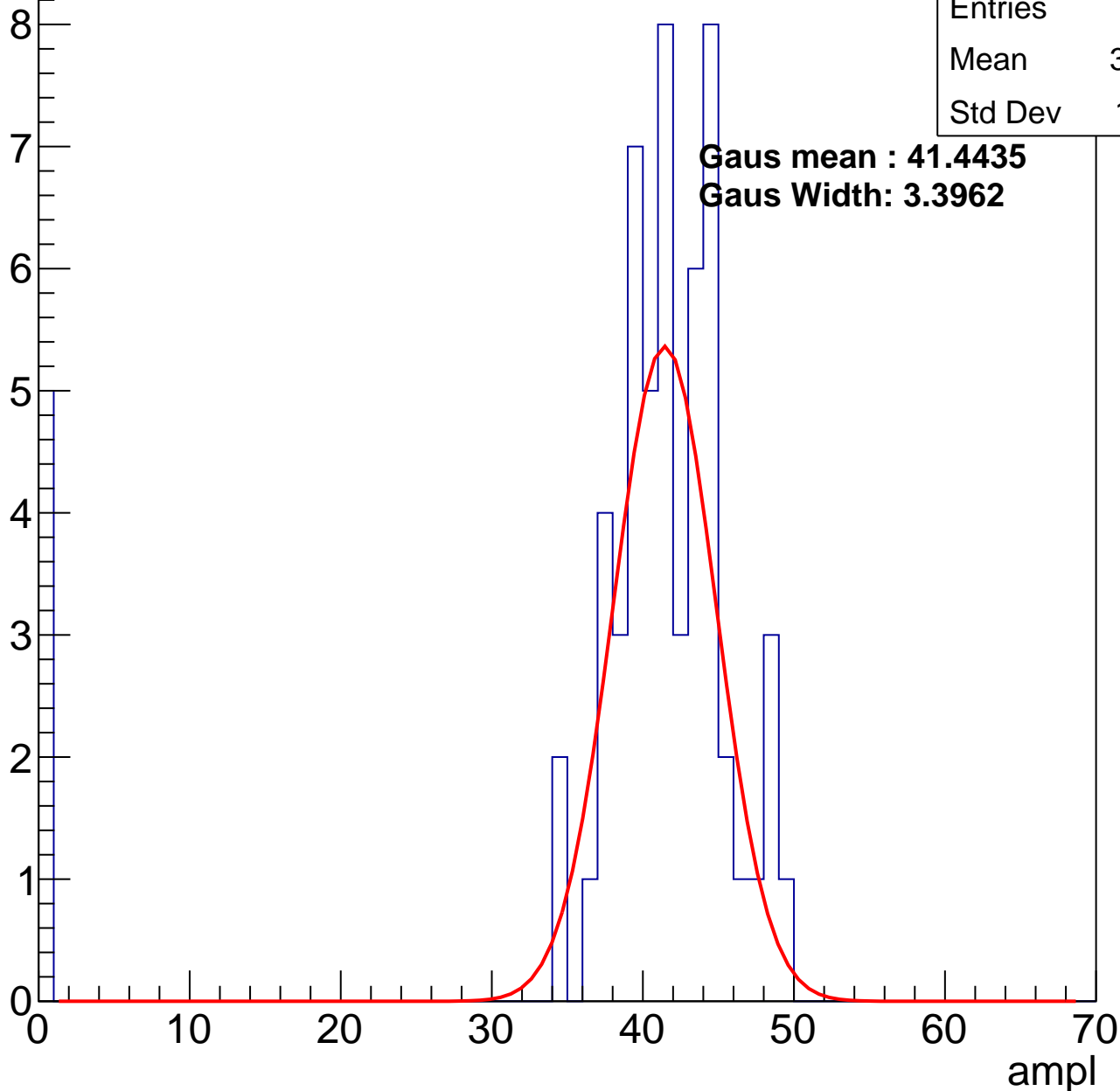
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	37.98
Std Dev	11.91

Gaus mean : 41.4435

Gaus Width: 3.3962

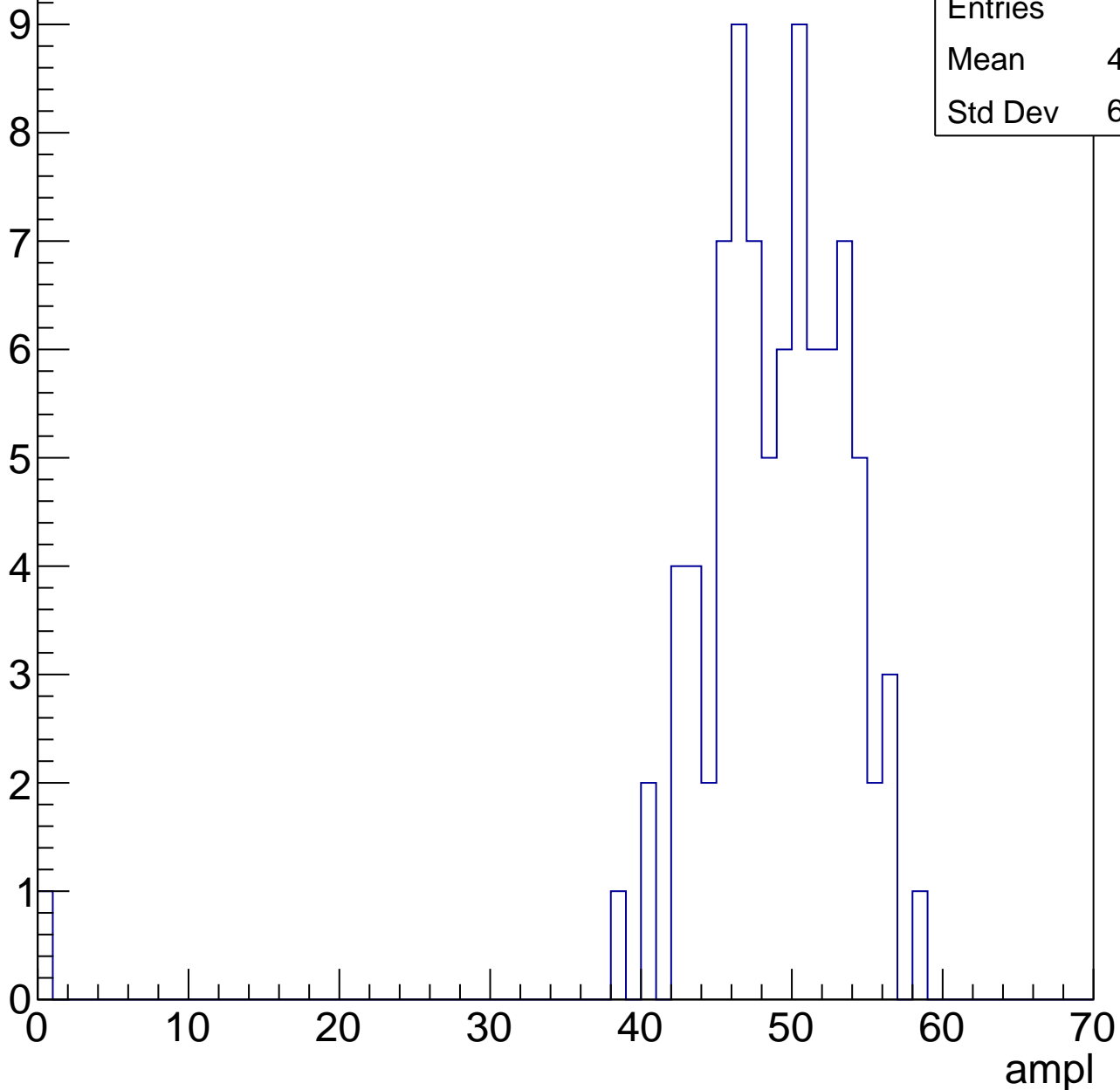


B1L103S, U18-ch44, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	48.08
Std Dev	6.653

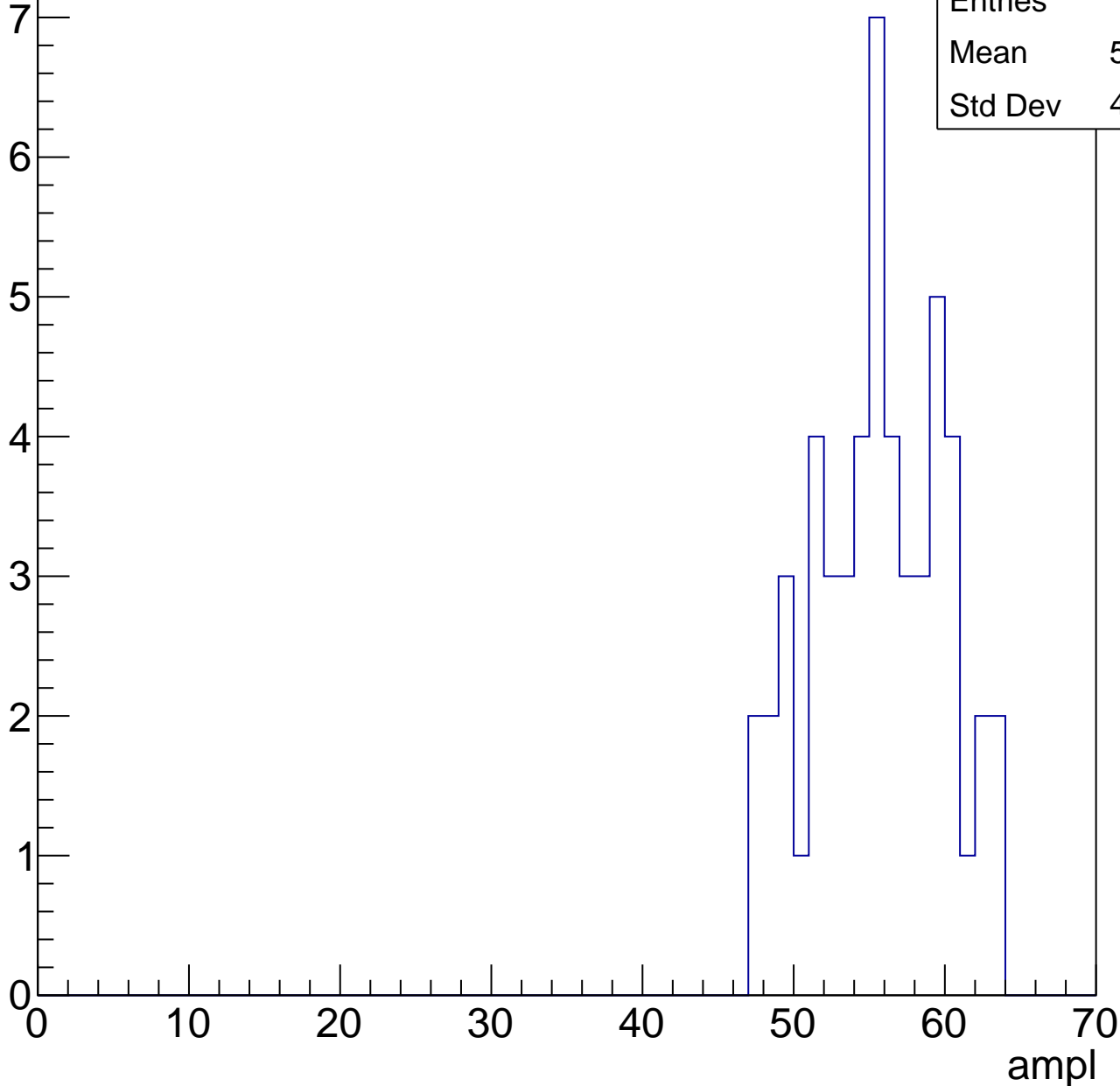


B1L103S, U18-ch44, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	55.13
Std Dev	4.234

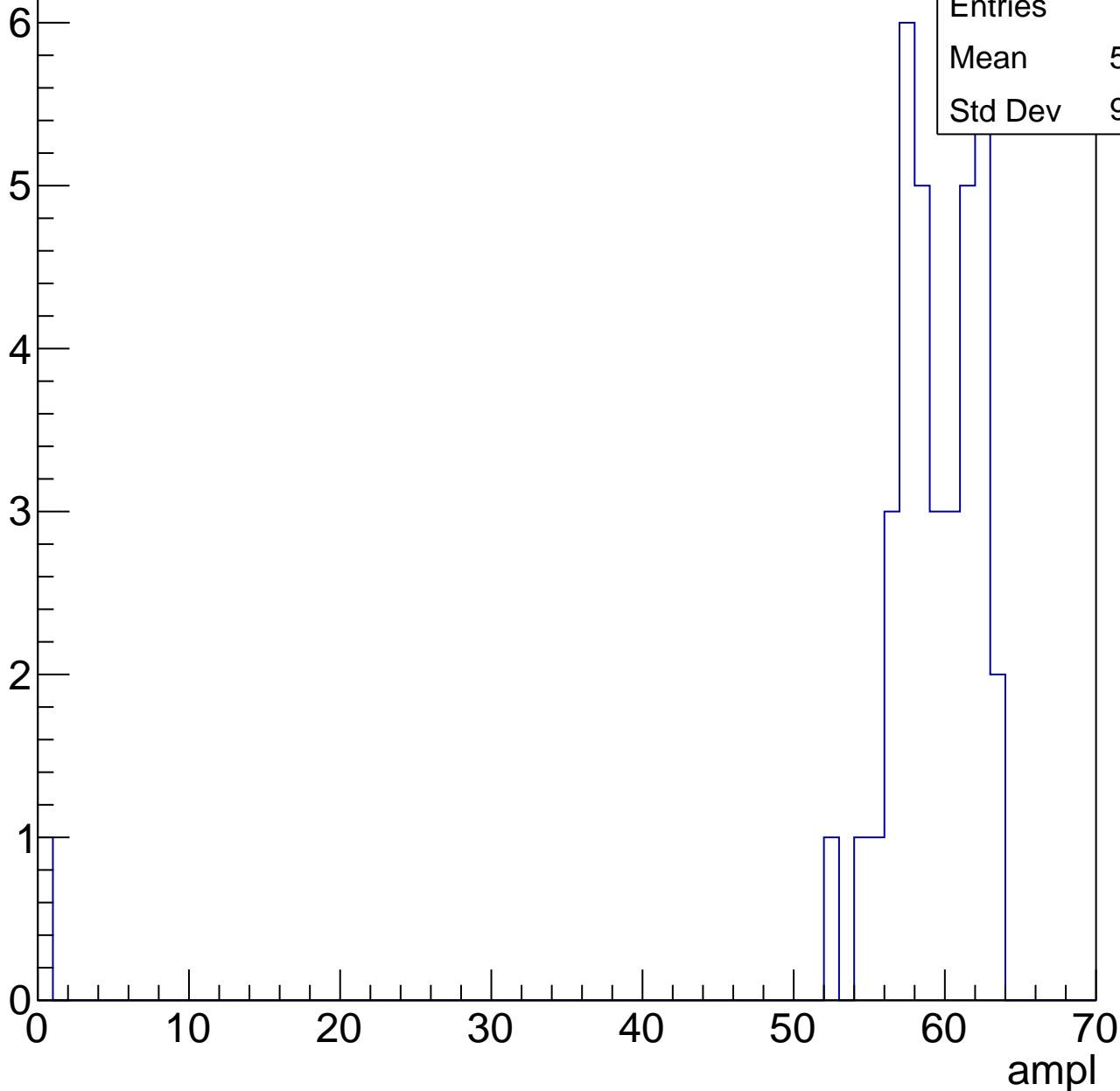


B1L103S, U18-ch44, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	57.32
Std Dev	9.908

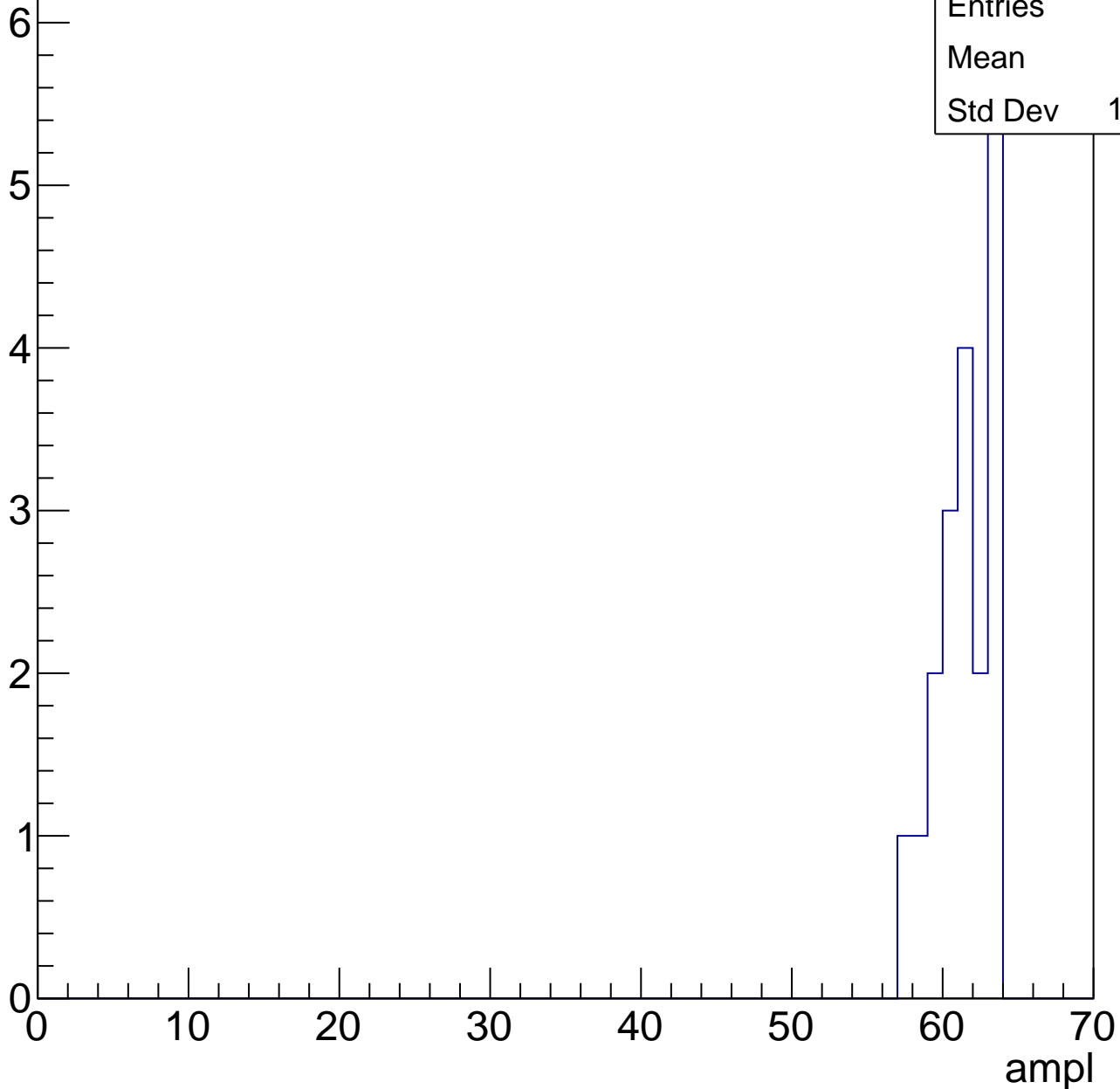


B1L103S, U18-ch44, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	61
Std Dev	1.806



B1L103S, U18-ch44, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch45, adc0

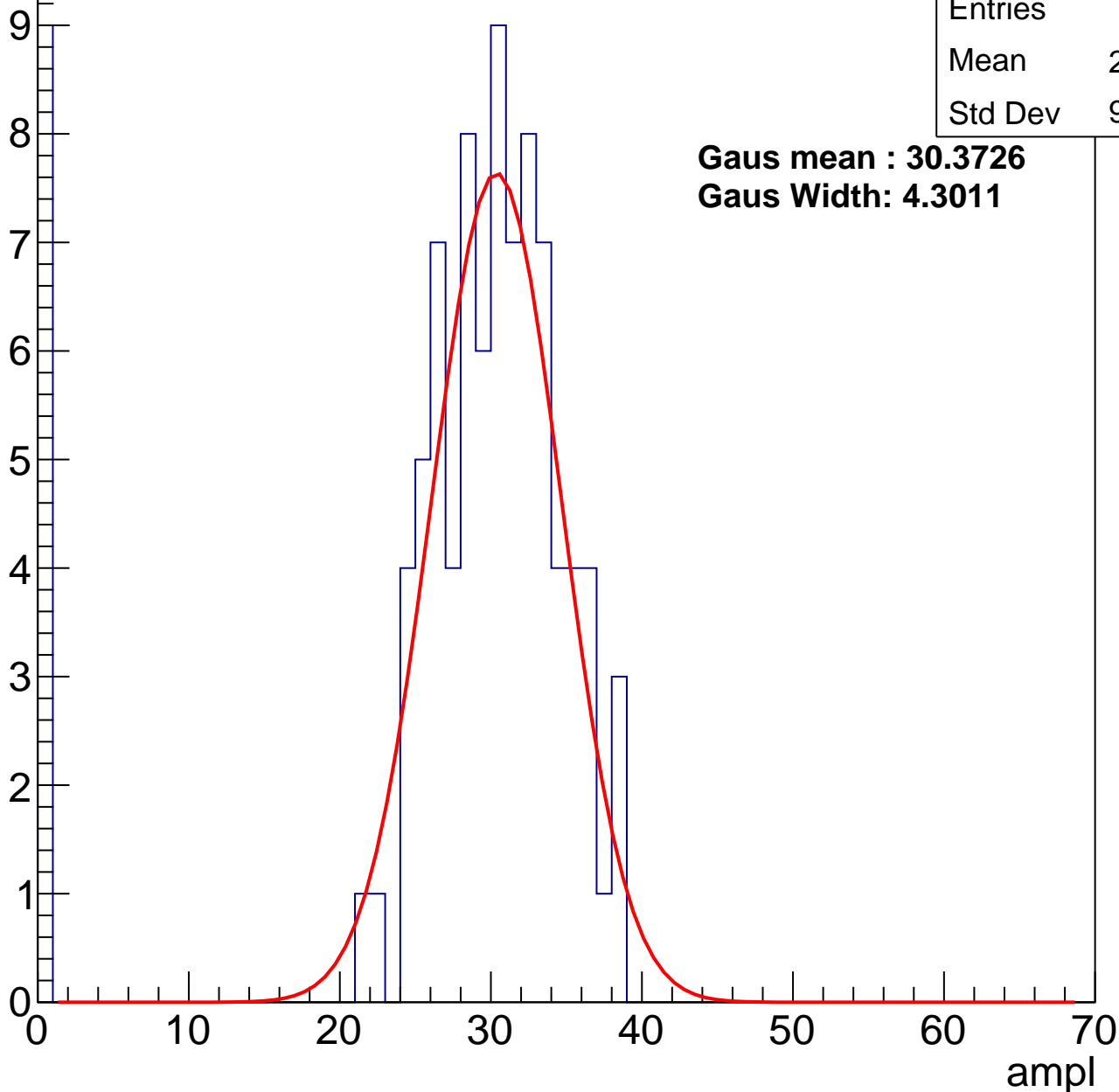
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	92
Mean	27.14
Std Dev	9.668

Gaus mean : 30.3726

Gaus Width: 4.3011



B1L103S, U18-ch45, adc1

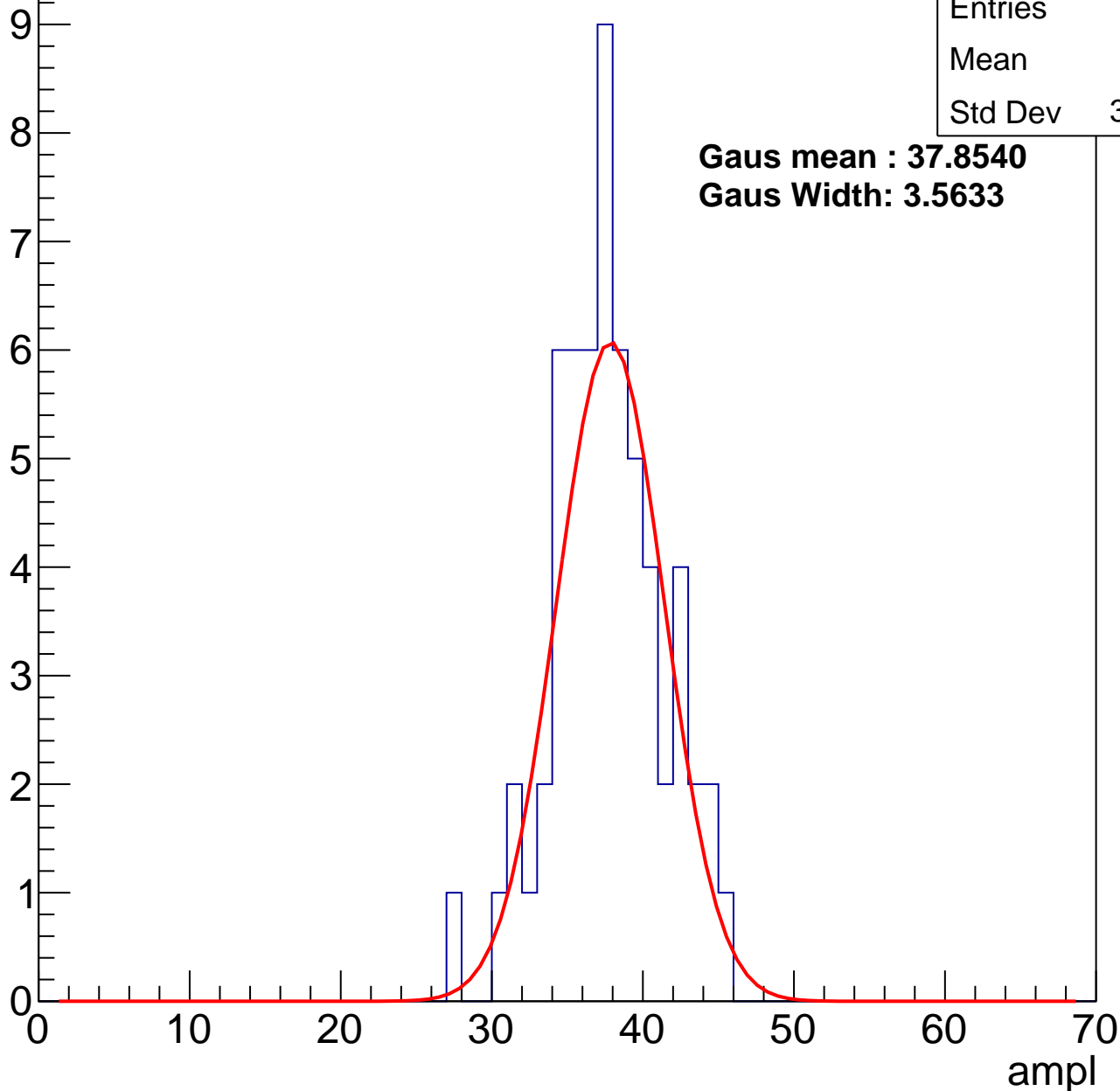
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	37.2
Std Dev	3.623

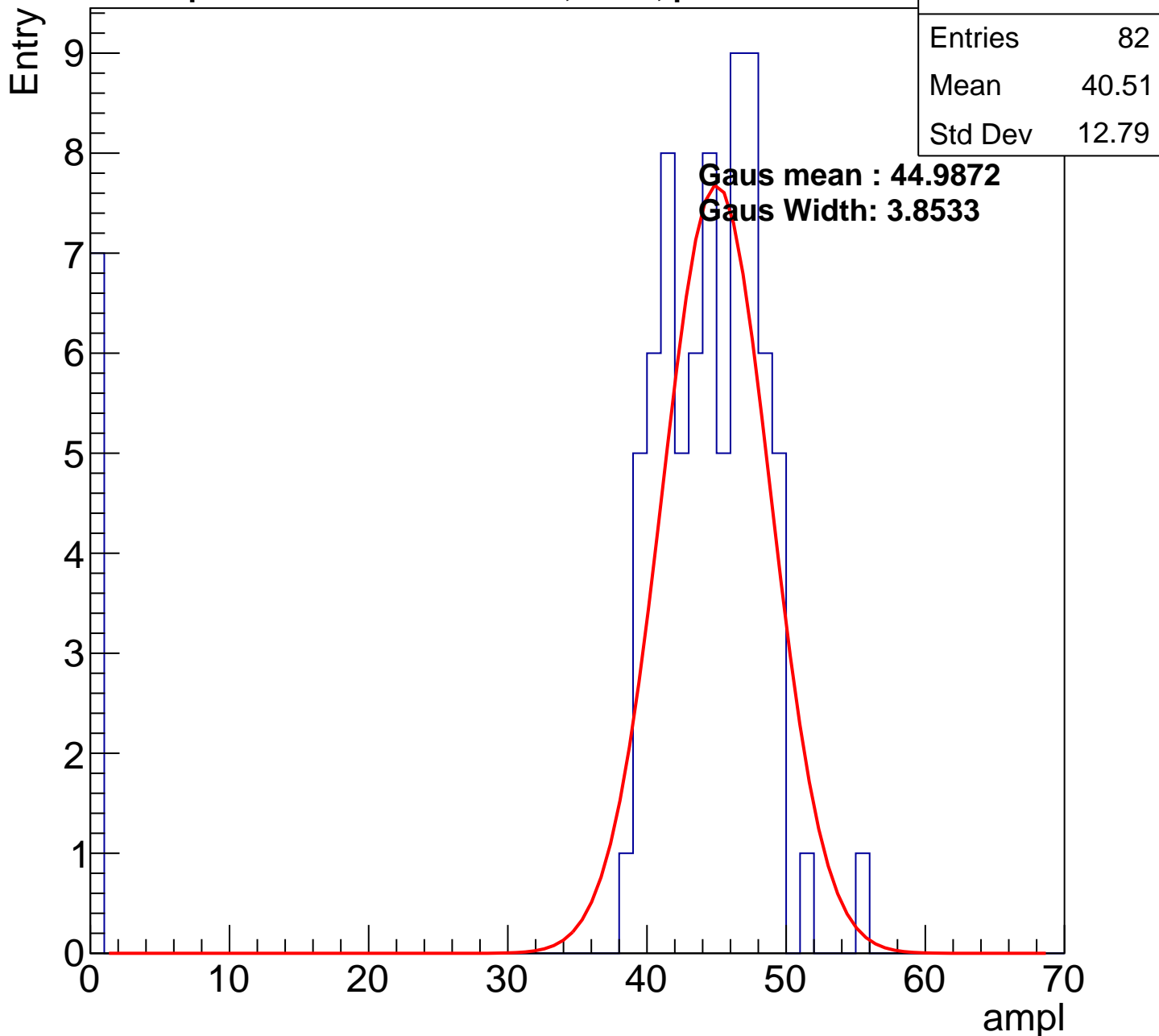
Gaus mean : 37.8540

Gaus Width: 3.5633



B1L103S, U18-ch45, adc2

calib_packv5_041523_1651.root, FC#0, port C2

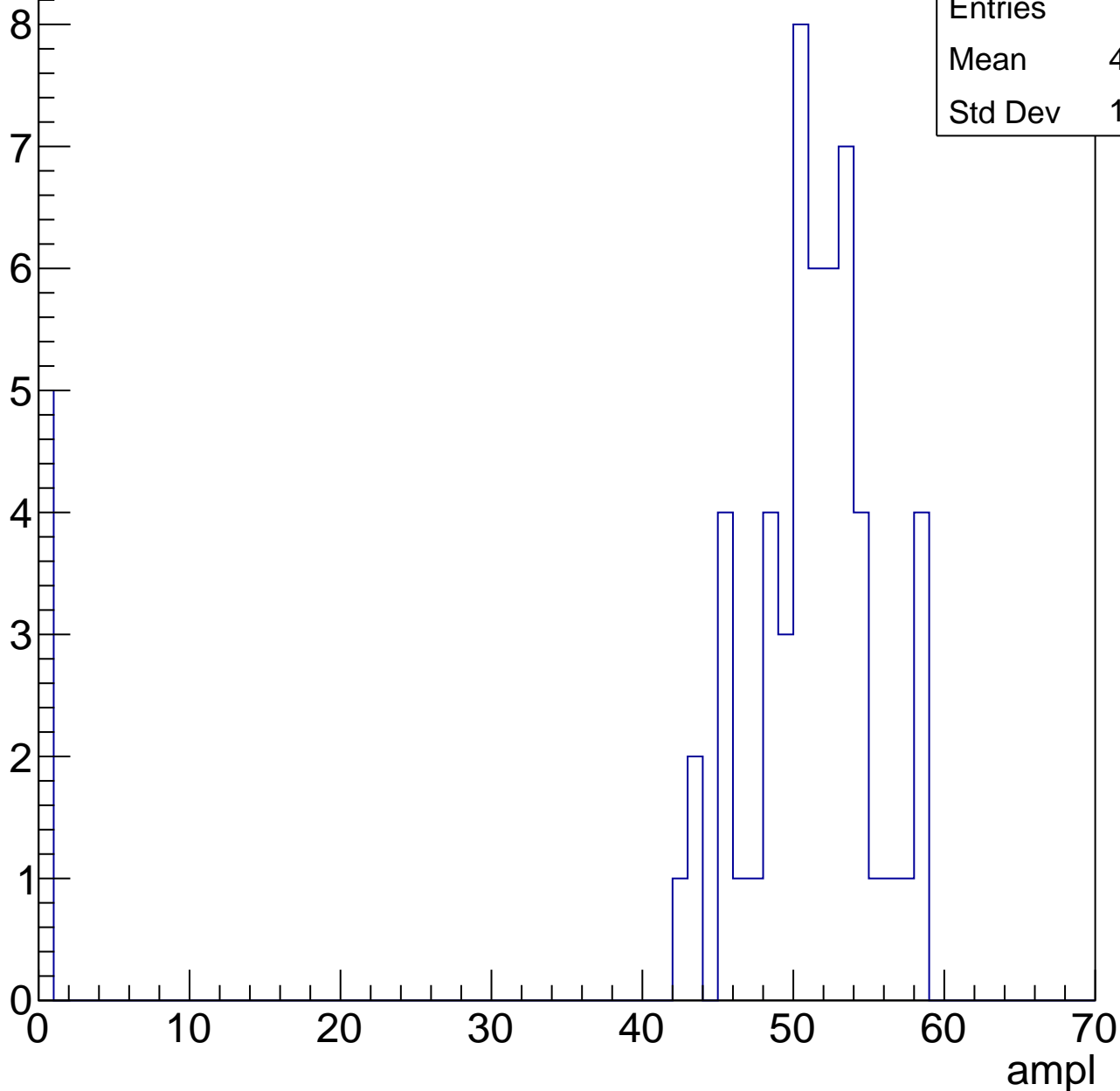


B1L103S, U18-ch45, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	46.53
Std Dev	14.62

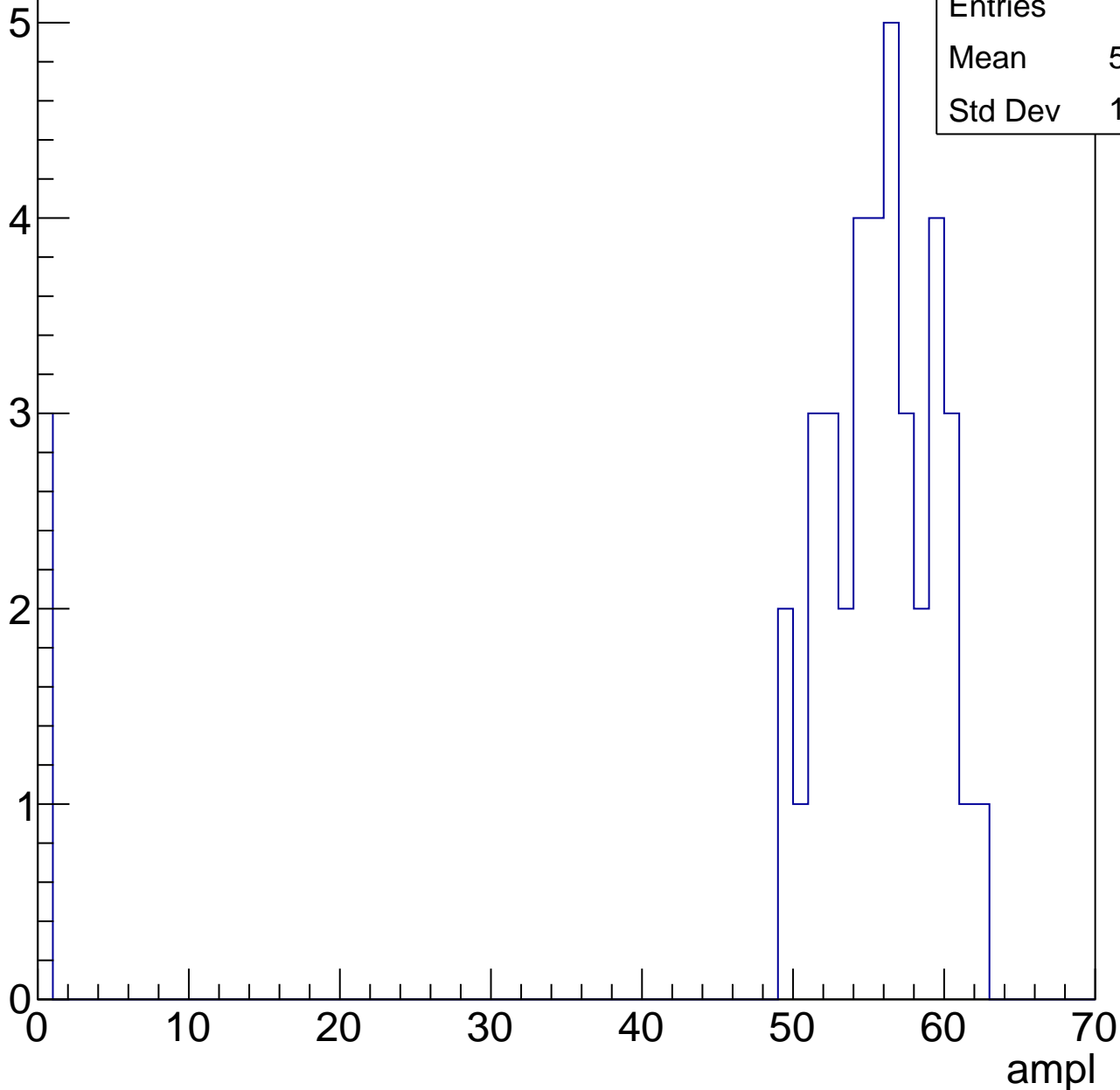


B1L103S, U18-ch45, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	41
Mean	51.34
Std Dev	14.79

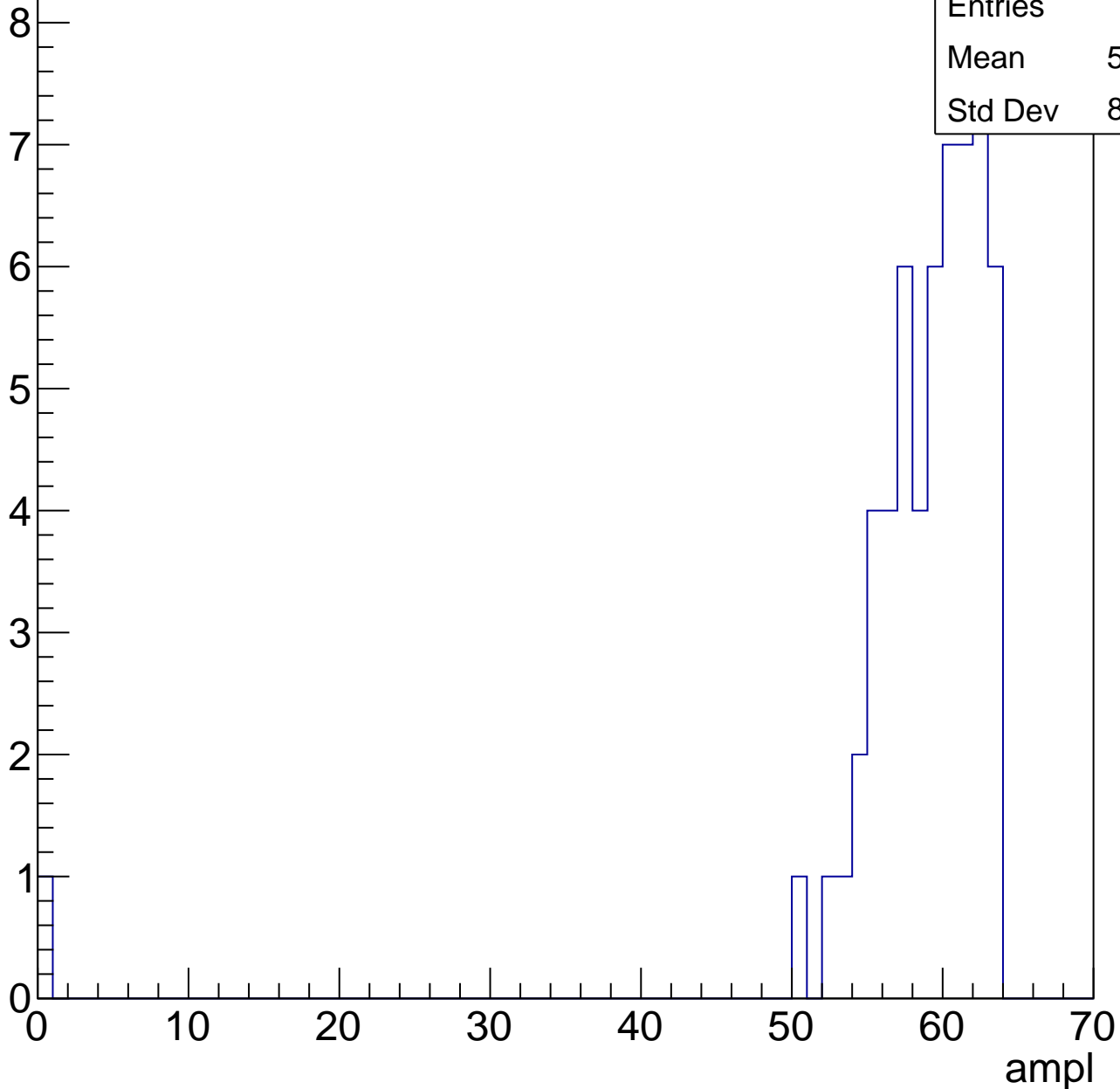


B1L103S, U18-ch45, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

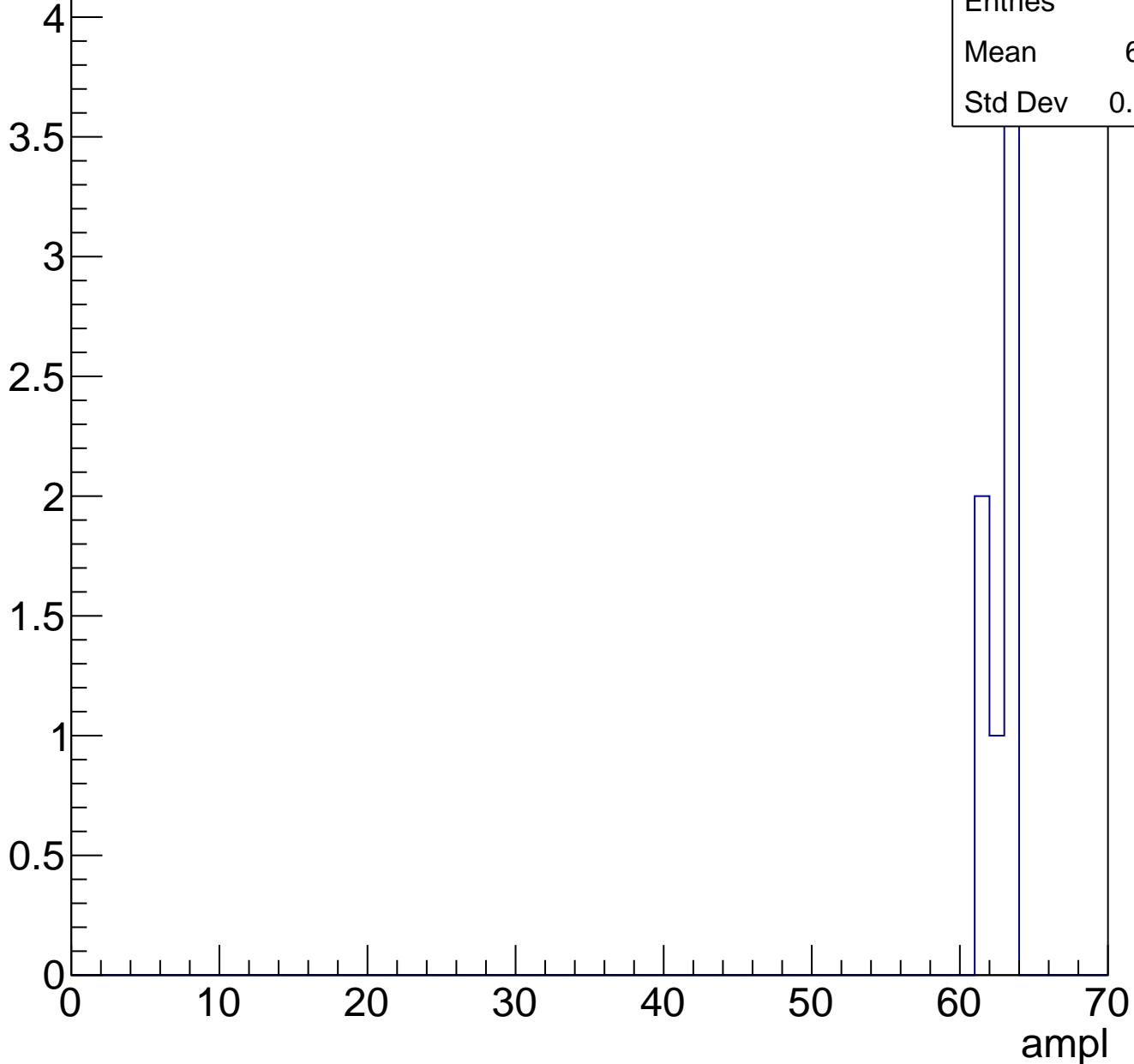
Entries	58
Mean	57.86
Std Dev	8.256



B1L103S, U18-ch45, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch45, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch46, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	22.92
Std Dev	10.91

Gaus mean : 27.7904

Gaus Width: 3.6076

Entry

12

10

8

6

4

2

0

0

10

20

30

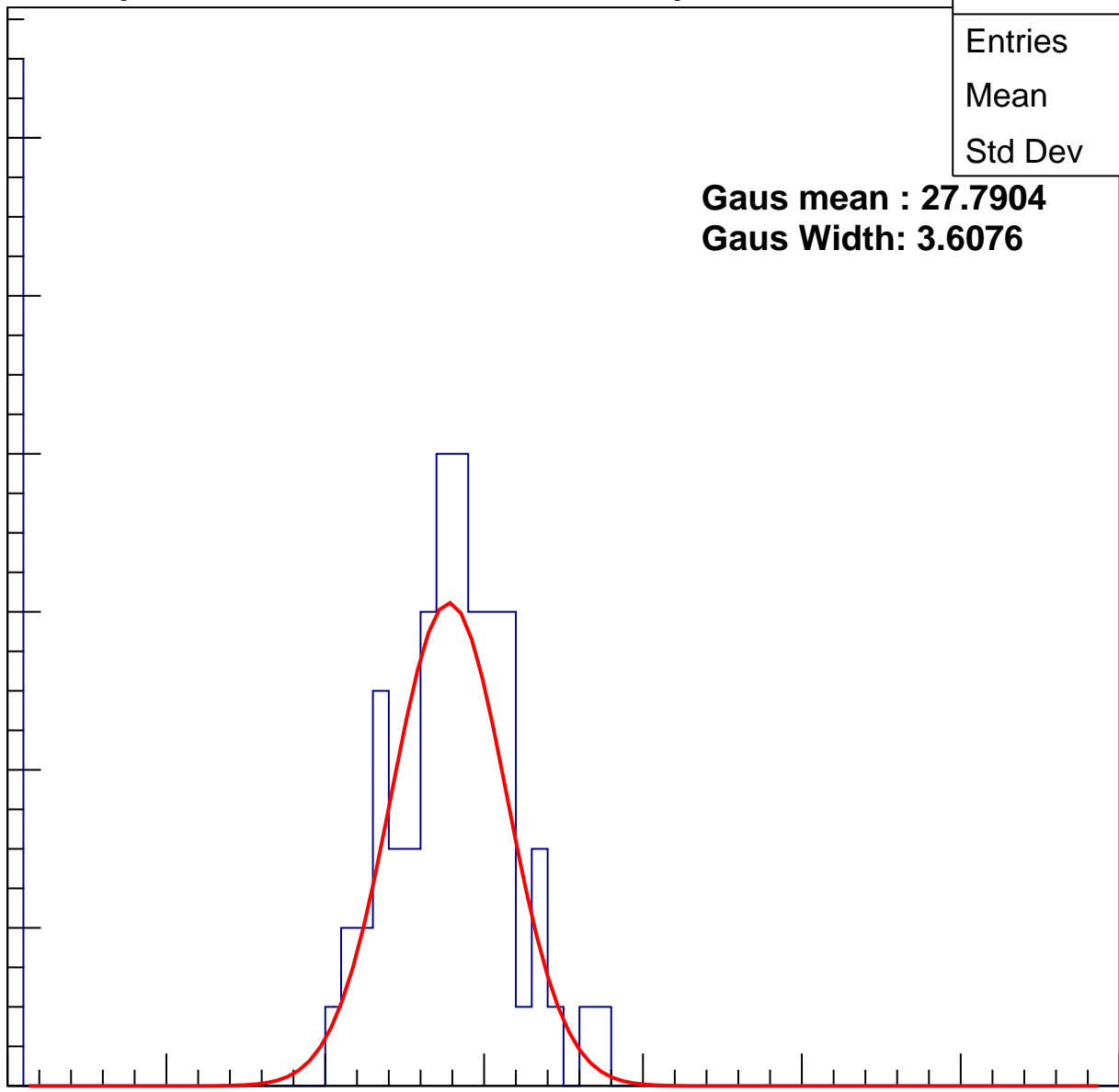
40

50

60

70

ampl



B1L103S, U18-ch46, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	34.25
Std Dev	5.33

Gaus mean : 34.7818

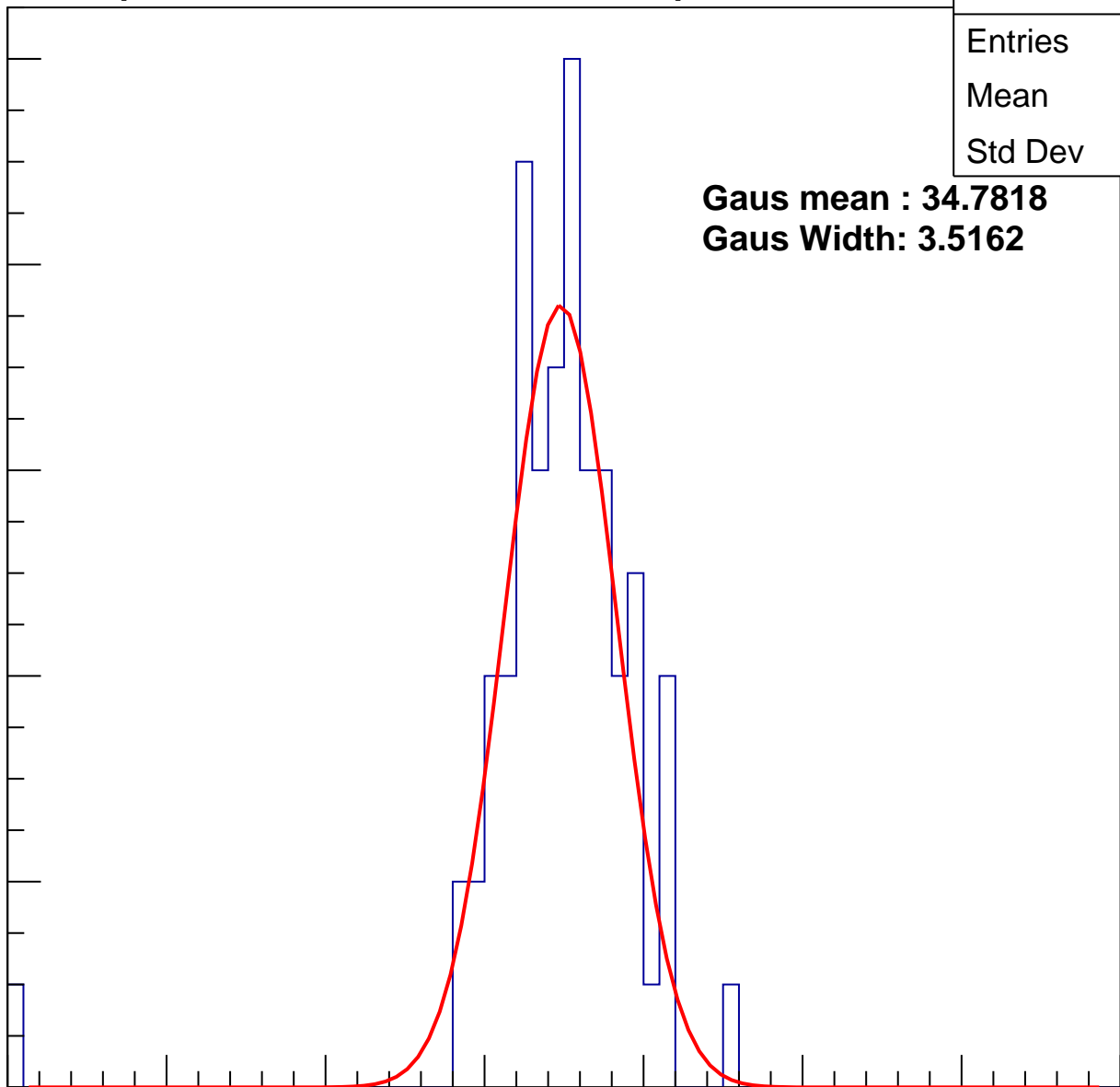
Gaus Width: 3.5162

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch46, adc2

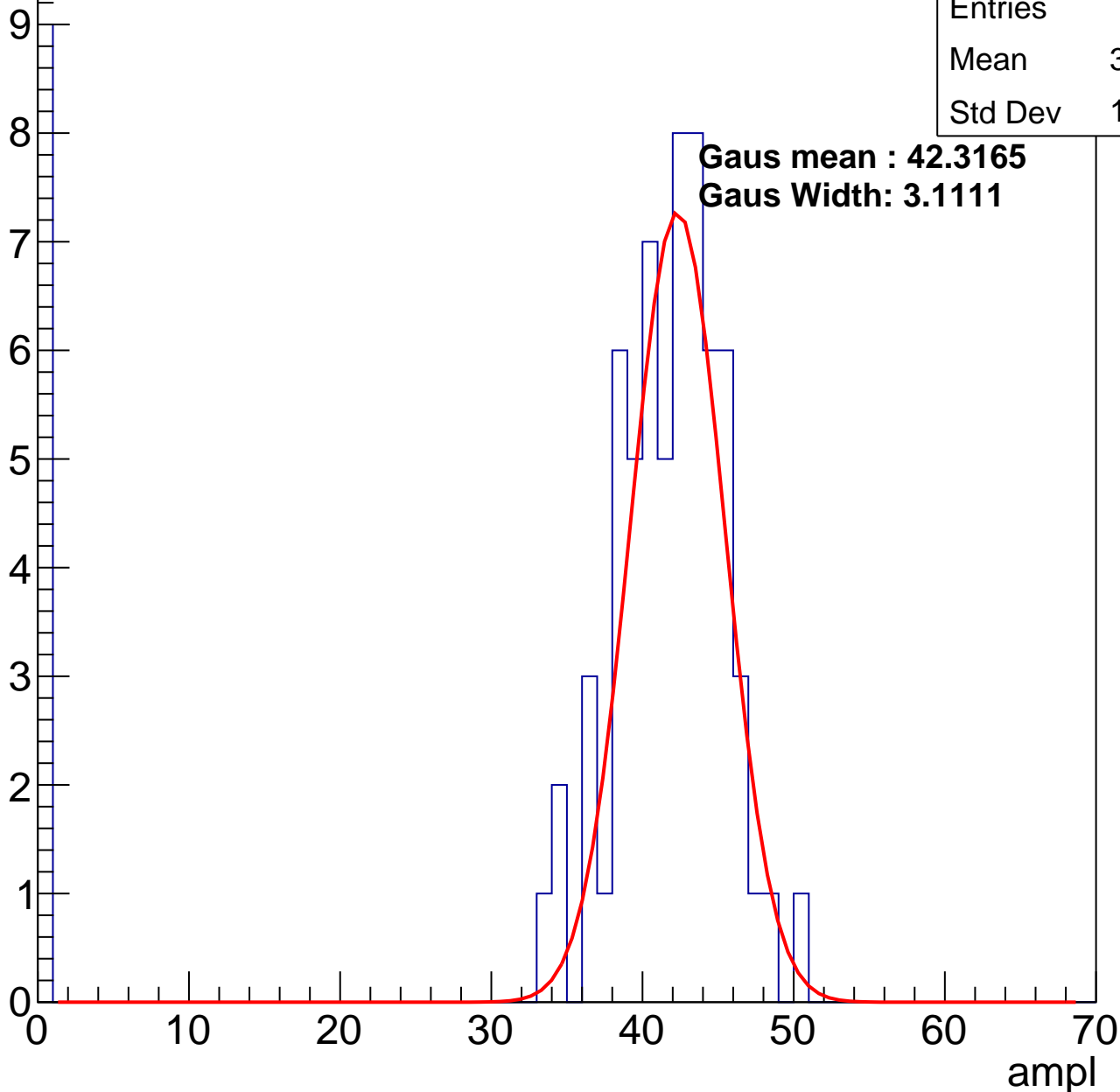
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	36.32
Std Dev	13.99

Gaus mean : 42.3165

Gaus Width: 3.1111

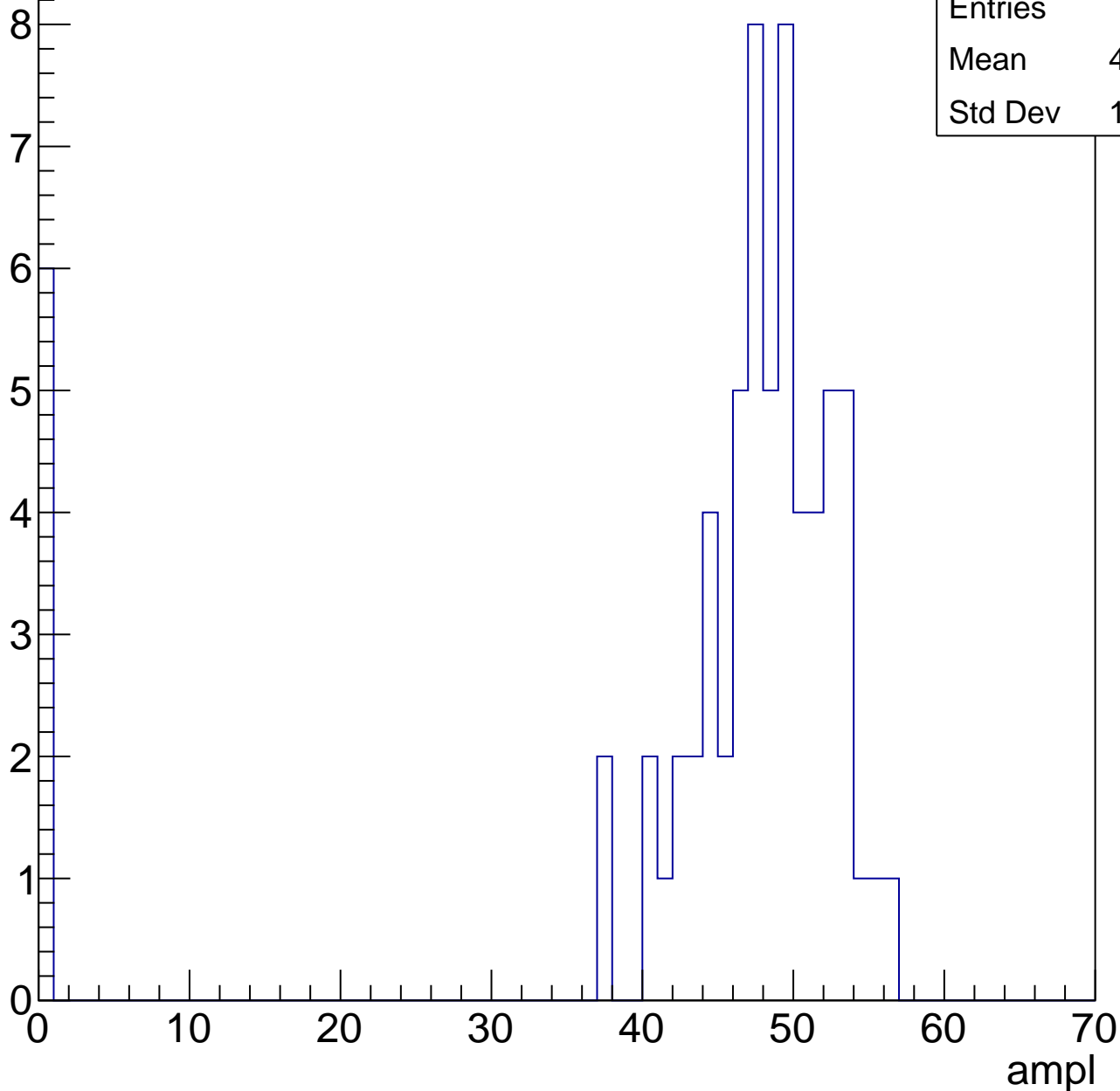


B1L103S, U18-ch46, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	43.57
Std Dev	14.12

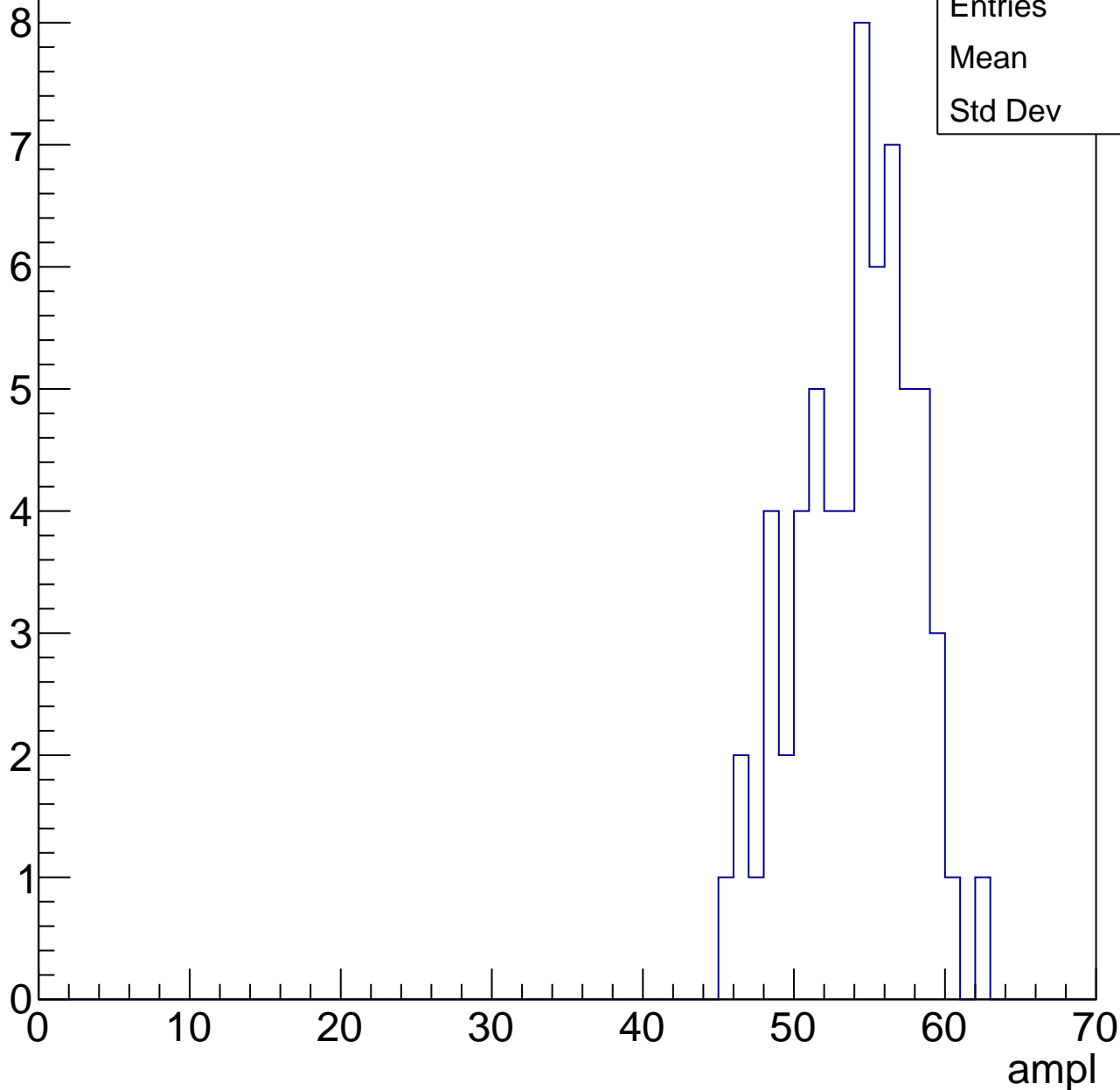


B1L103S, U18-ch46, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	53.6
Std Dev	3.79

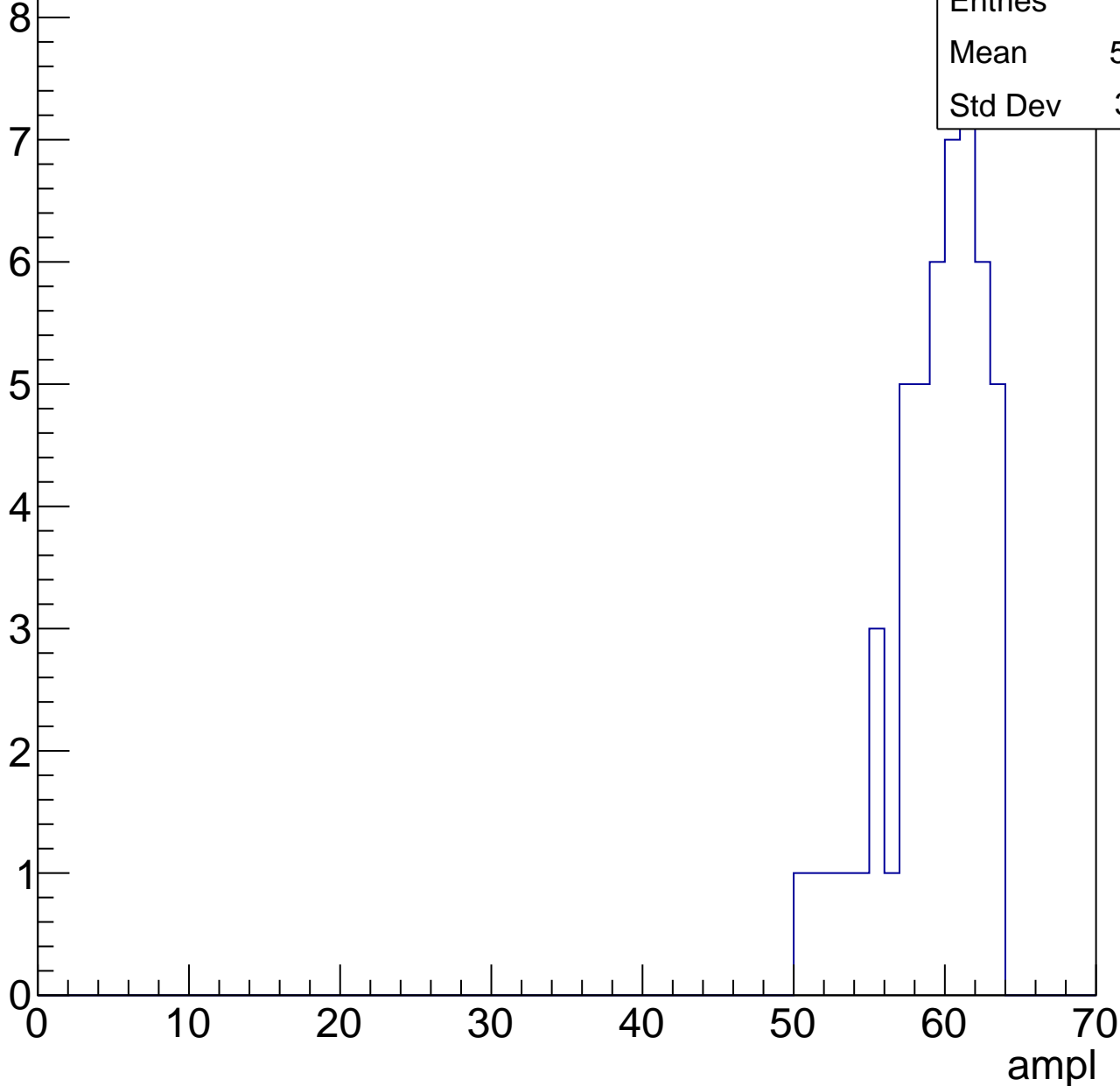


B1L103S, U18-ch46, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

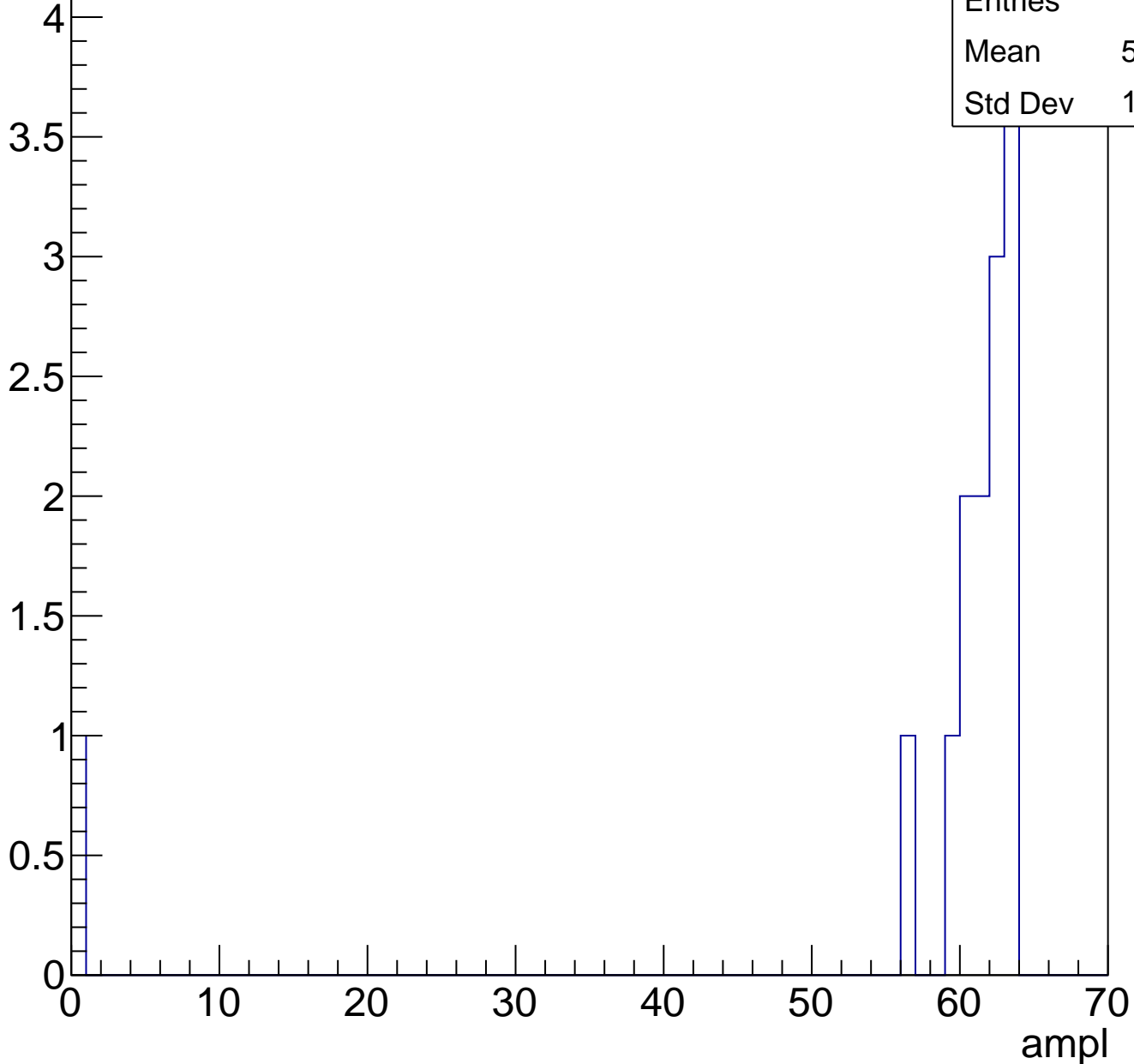
Entries	51
Mean	58.92
Std Dev	3.161



B1L103S, U18-ch46, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch46, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U18-ch47, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	23.13
Std Dev	11.64

Gaus mean : 29.0938

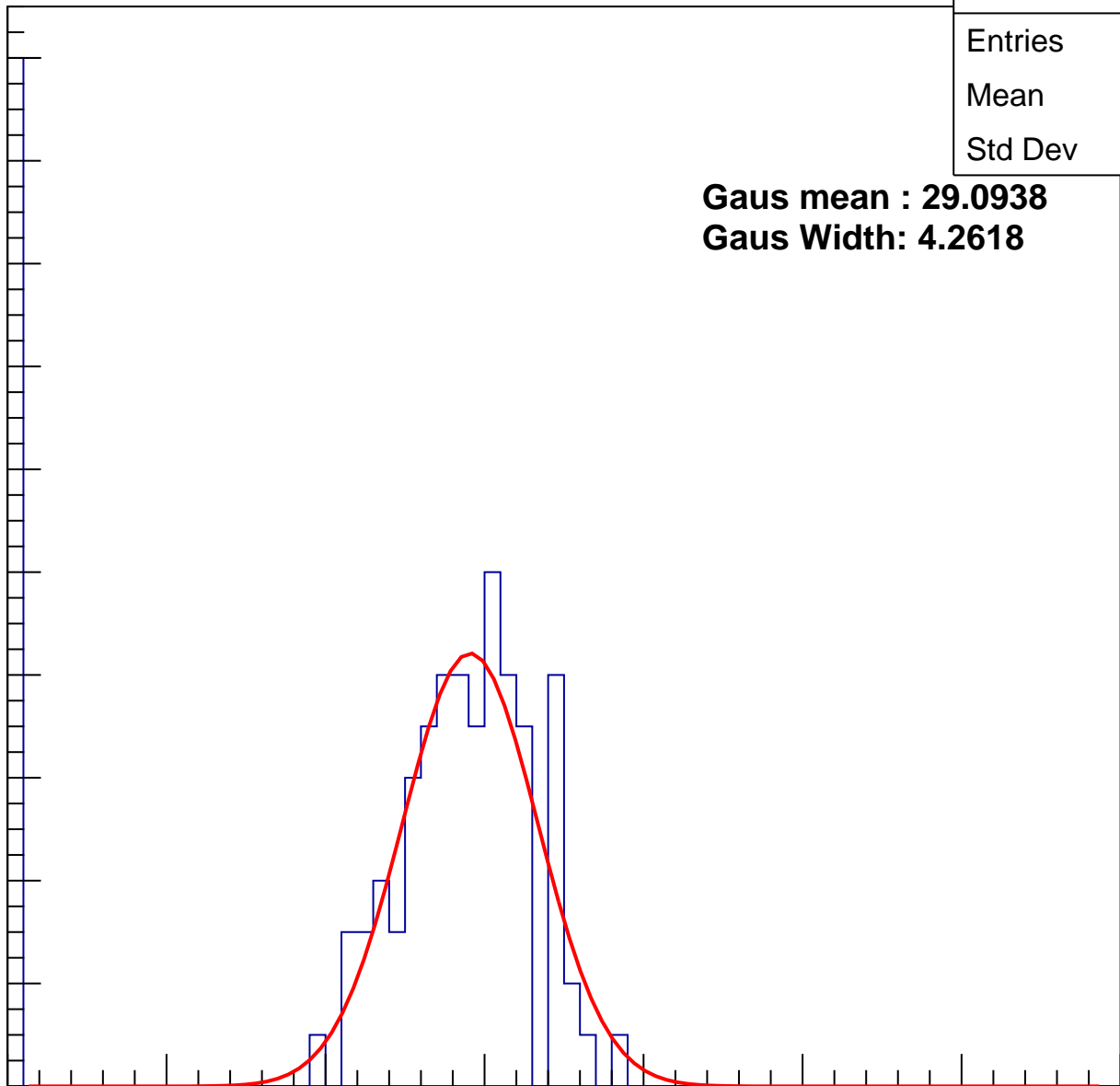
Gaus Width: 4.2618

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch47, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	36.06
Std Dev	3.621

Gaus mean : 36.7177

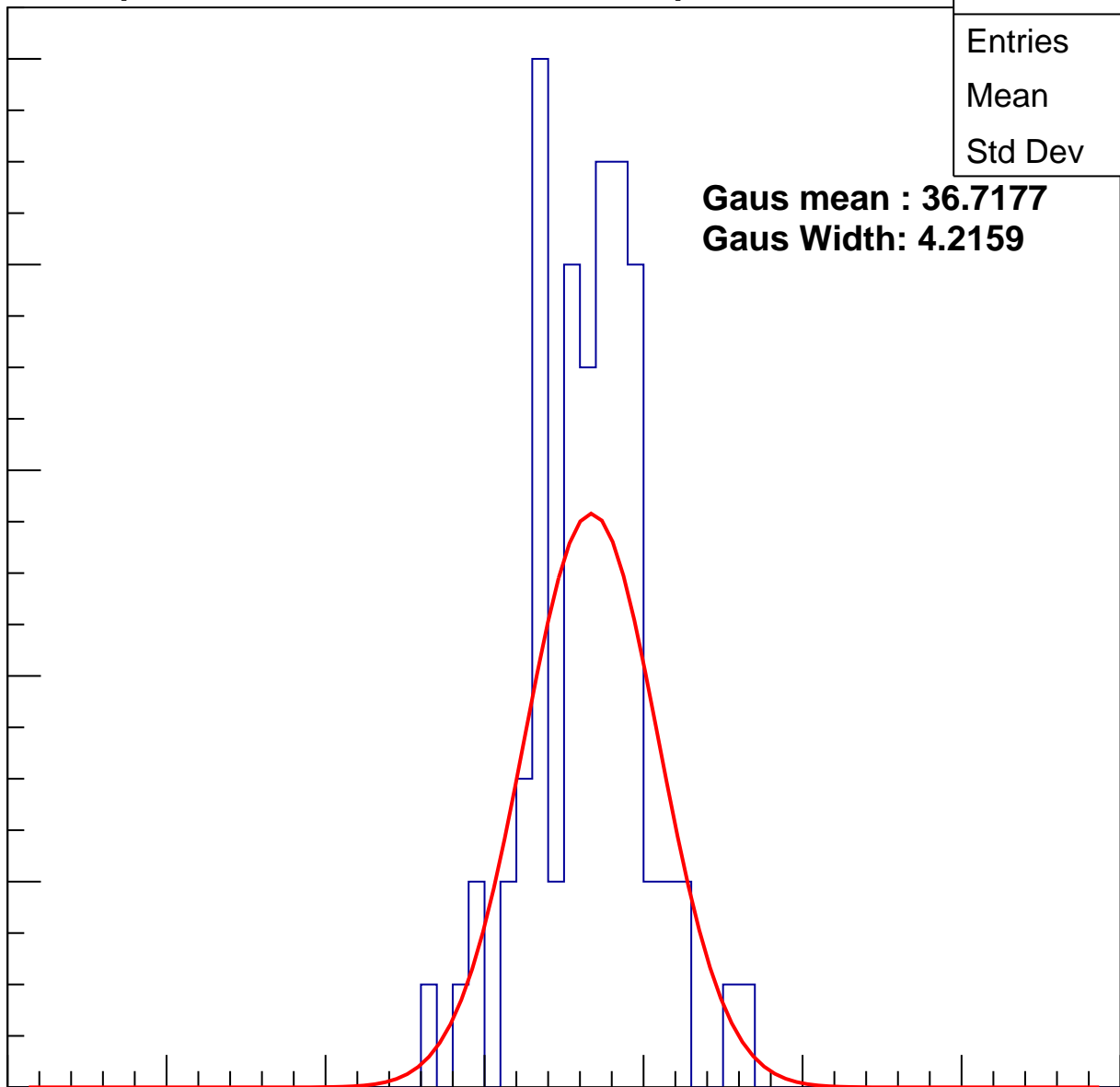
Gaus Width: 4.2159

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch47, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	36.83
Std Dev	15.3

Gaus mean : 43.4149

Gaus Width: 3.1951

Entry

10

8

6

4

2

0

0

10

20

30

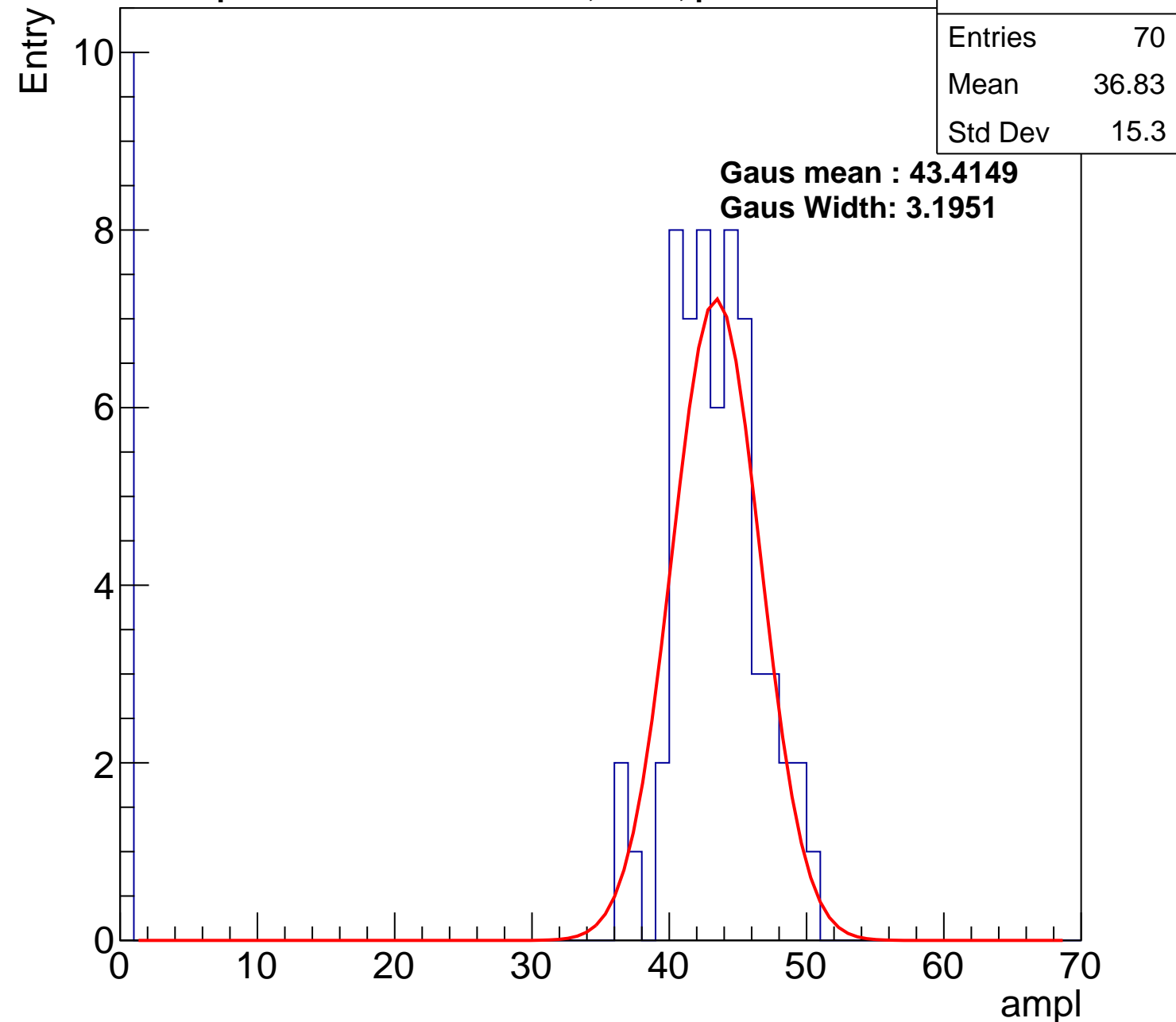
40

50

60

70

ampl

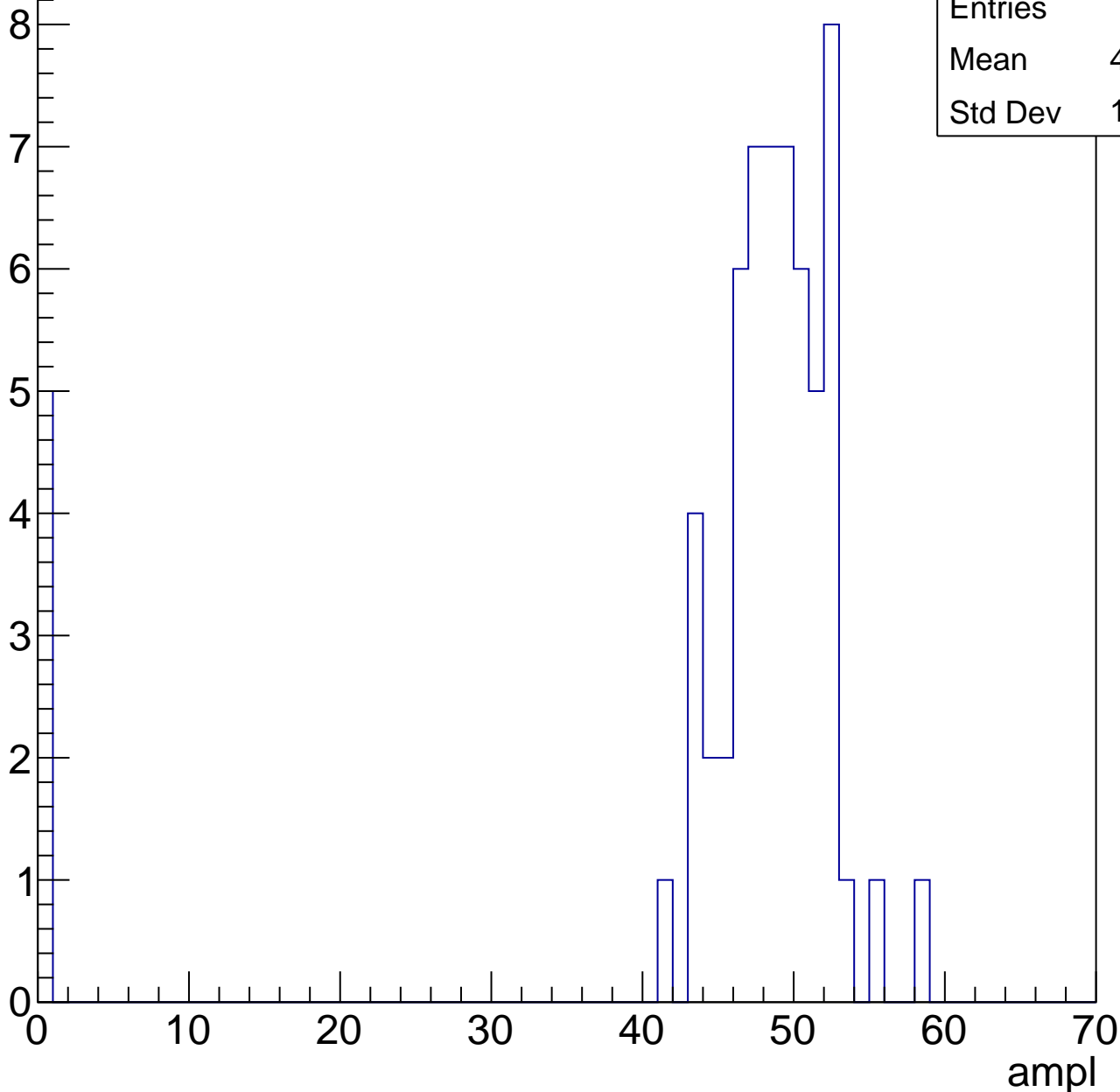


B1L103S, U18-ch47, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	44.63
Std Dev	13.46



B1L103S, U18-ch47, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	57
Mean	53.63
Std Dev	7.784

Entry

10

8

6

4

2

0

0

10

20

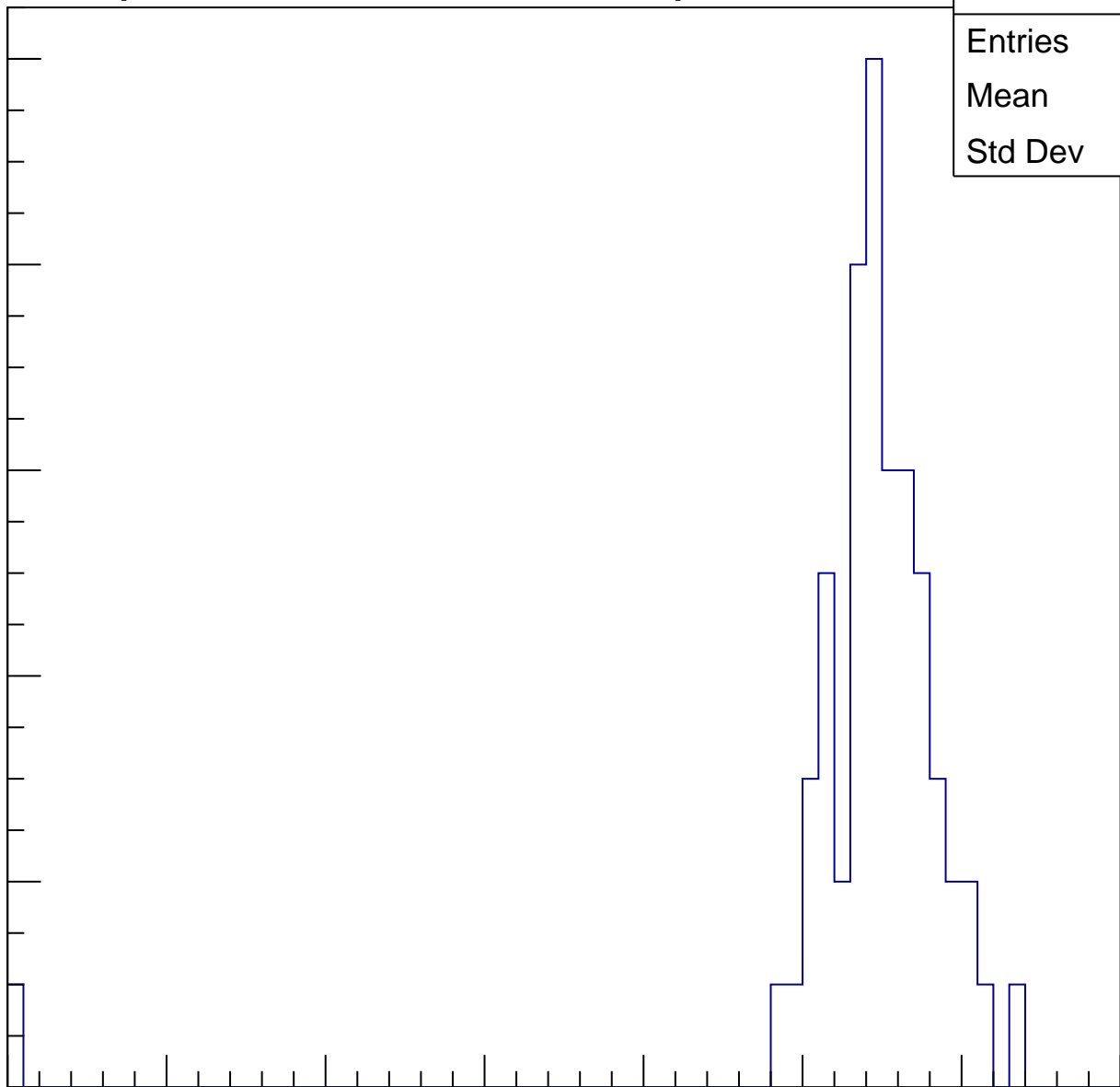
30

40

50

60

ampl

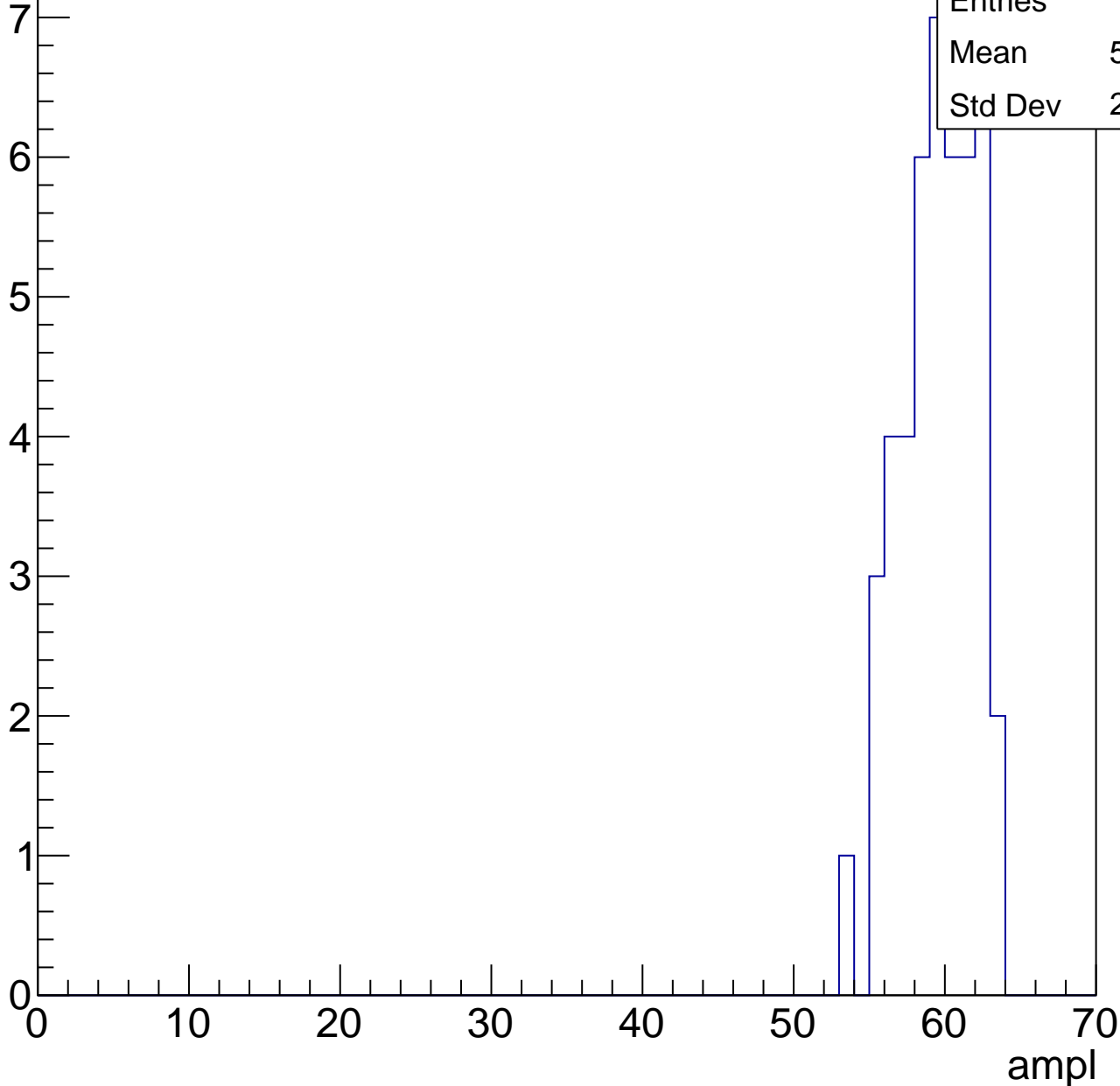


B1L103S, U18-ch47, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	59.07
Std Dev	2.408

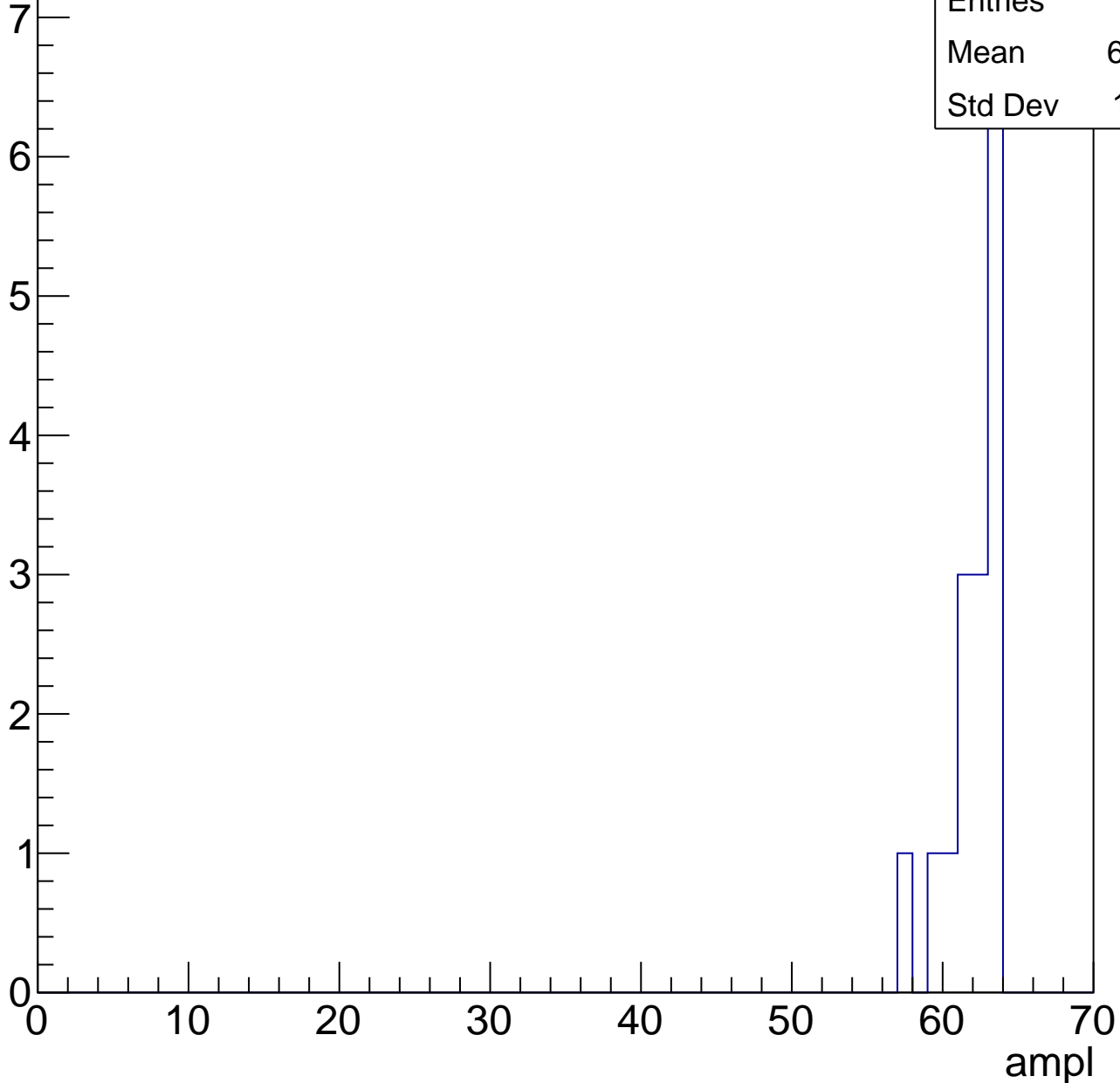


B1L103S, U18-ch47, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.62
Std Dev	1.691



B1L103S, U18-ch47, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



B1L103S, U18-ch48, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	21.94
Std Dev	11

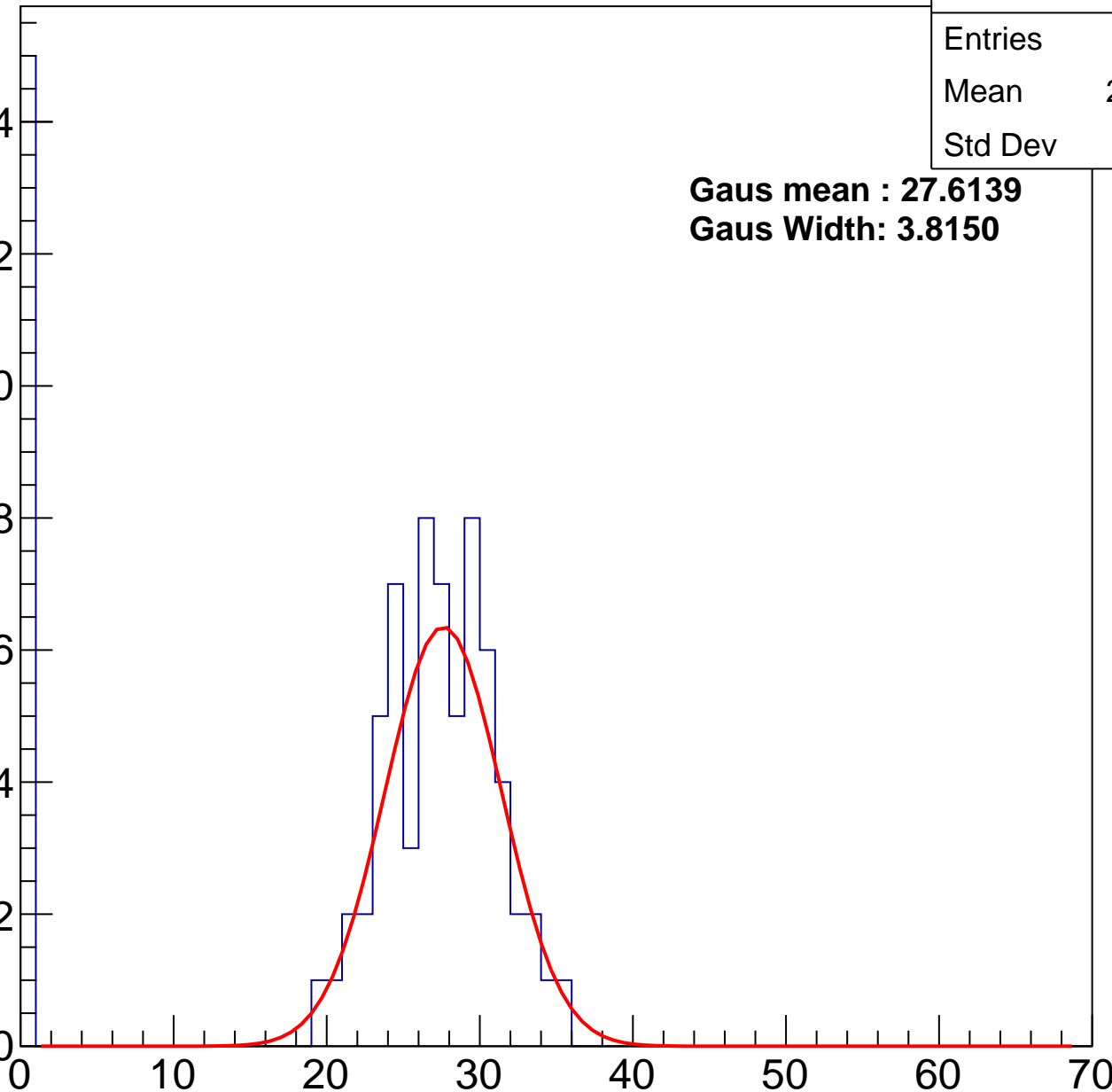
Gaus mean : 27.6139

Gaus Width: 3.8150

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U18-ch48, adc1

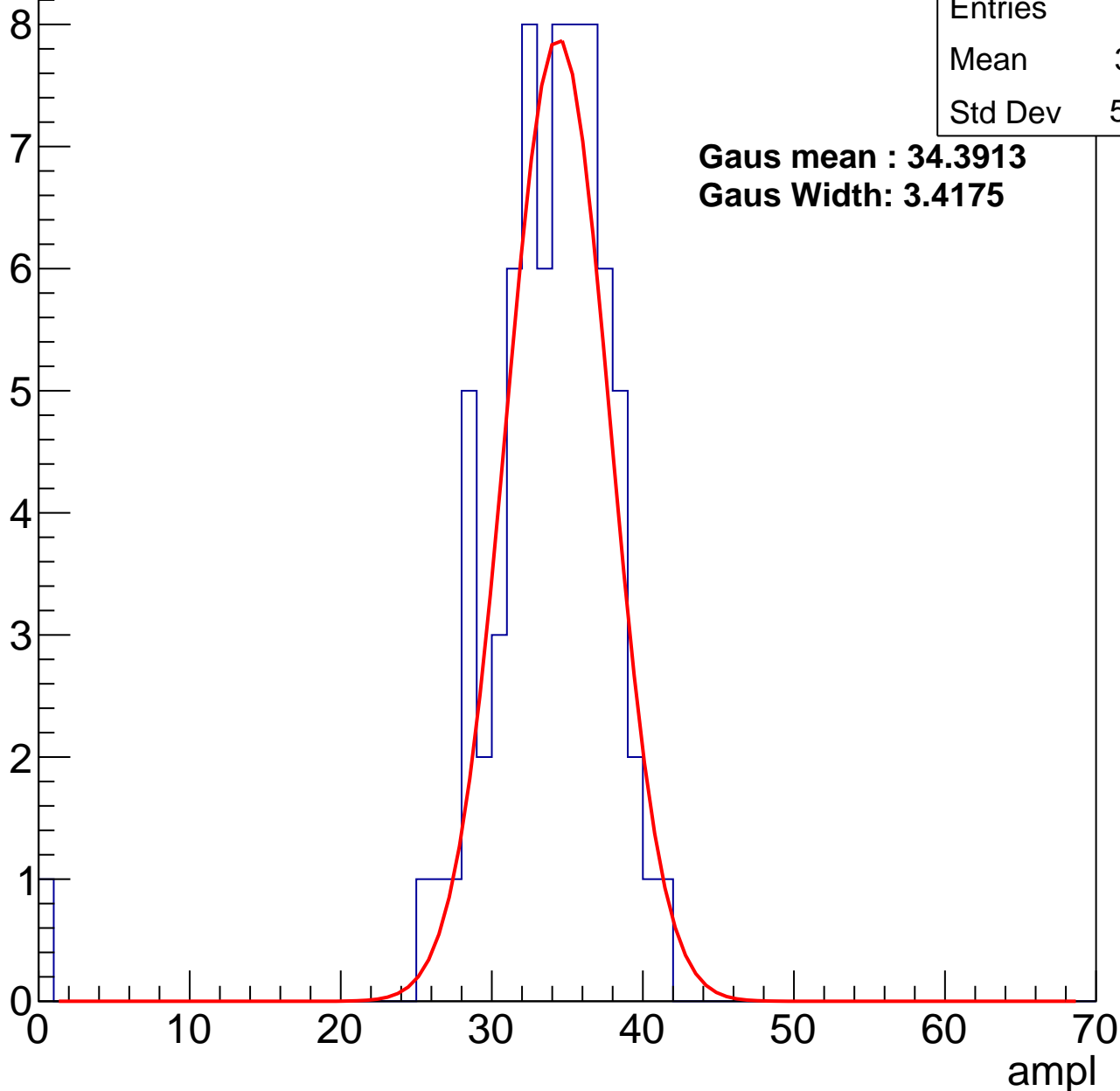
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	33.11
Std Dev	5.196

Gaus mean : 34.3913

Gaus Width: 3.4175



B1L103S, U18-ch48, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	35.06
Std Dev	14.5

Gaus mean : 40.6993

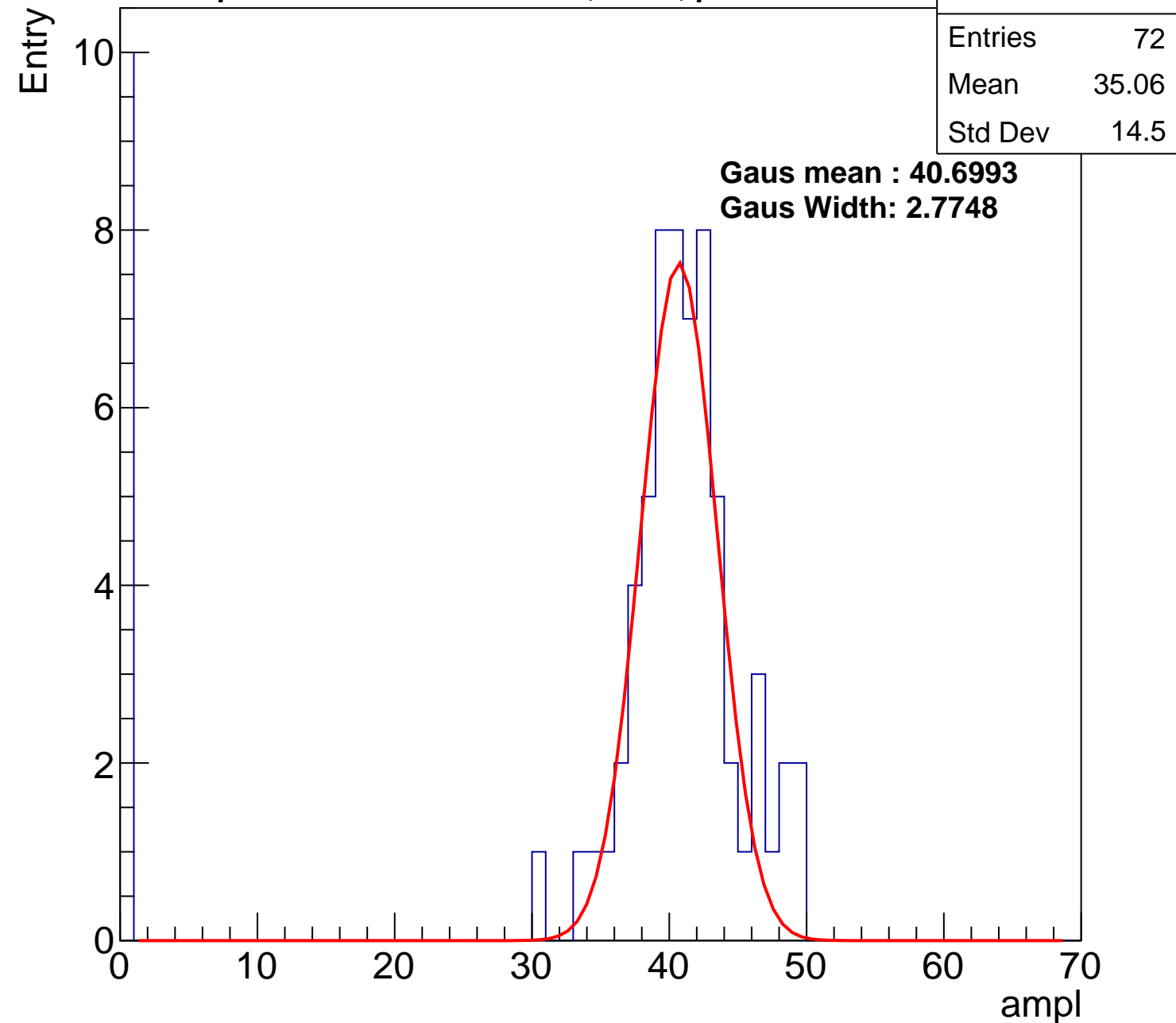
Gaus Width: 2.7748

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

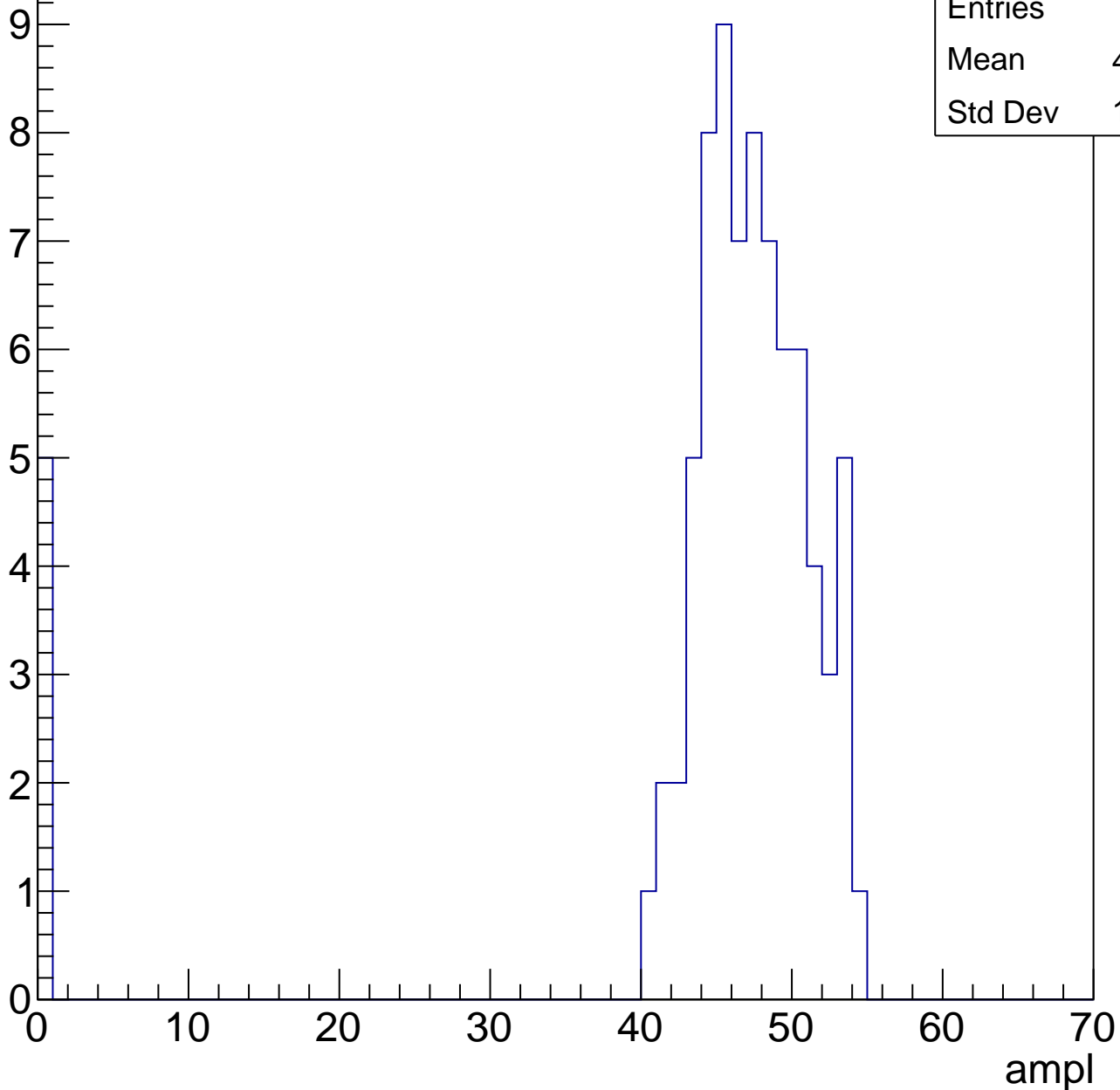


B1L103S, U18-ch48, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	44.11
Std Dev	11.91

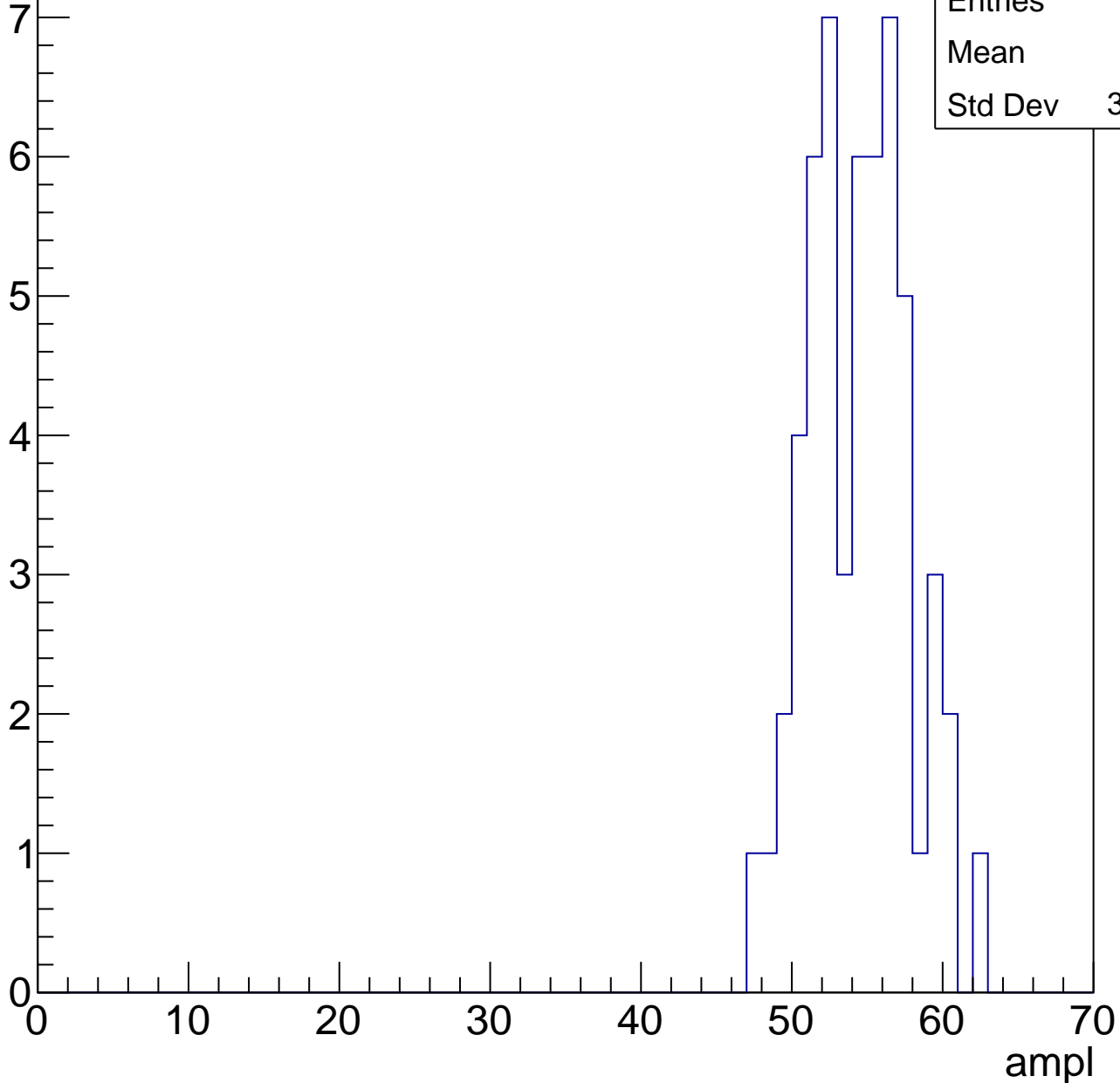


B1L103S, U18-ch48, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

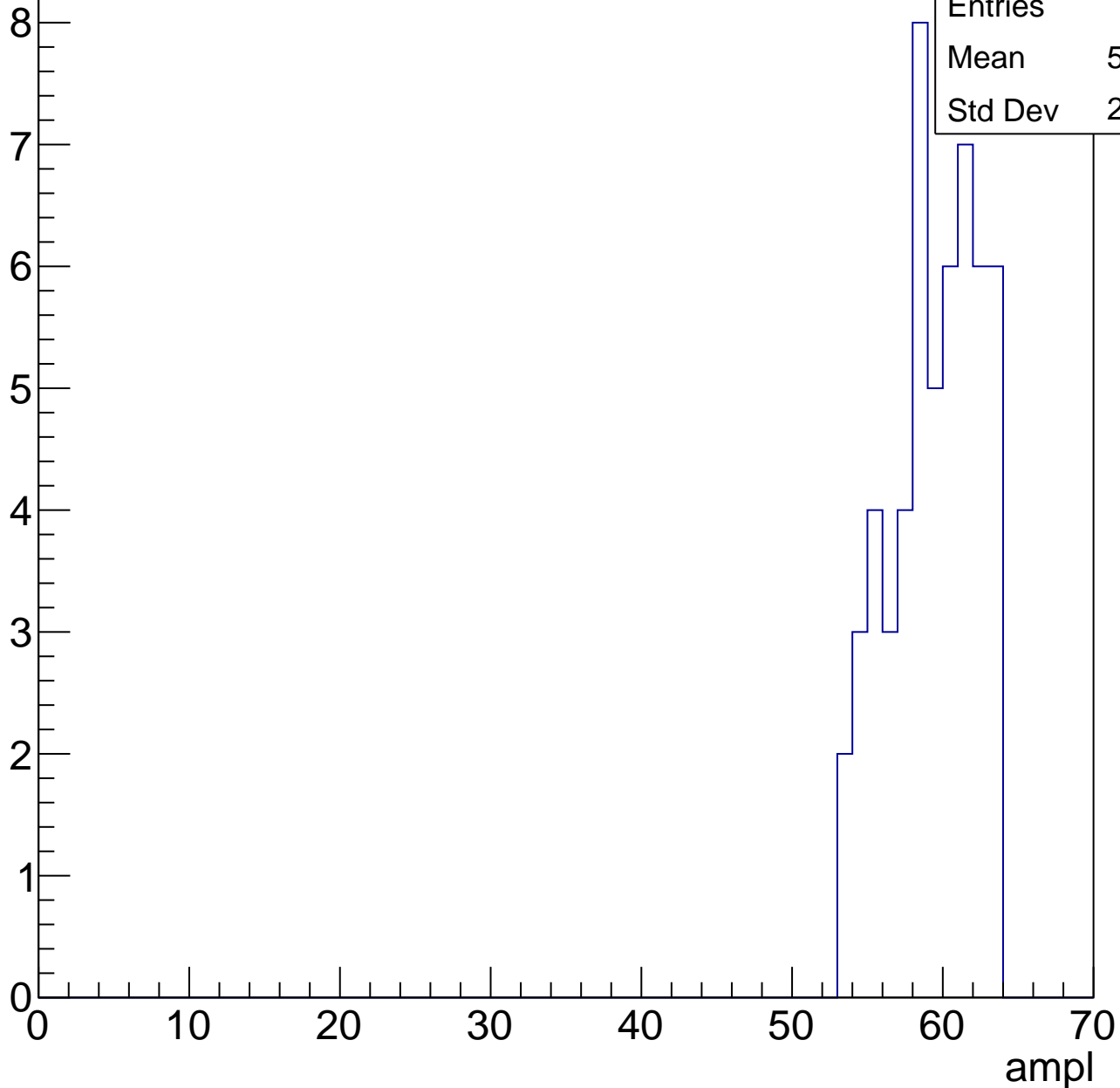
Entries	55
Mean	54
Std Dev	3.275



B1L103S, U18-ch48, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

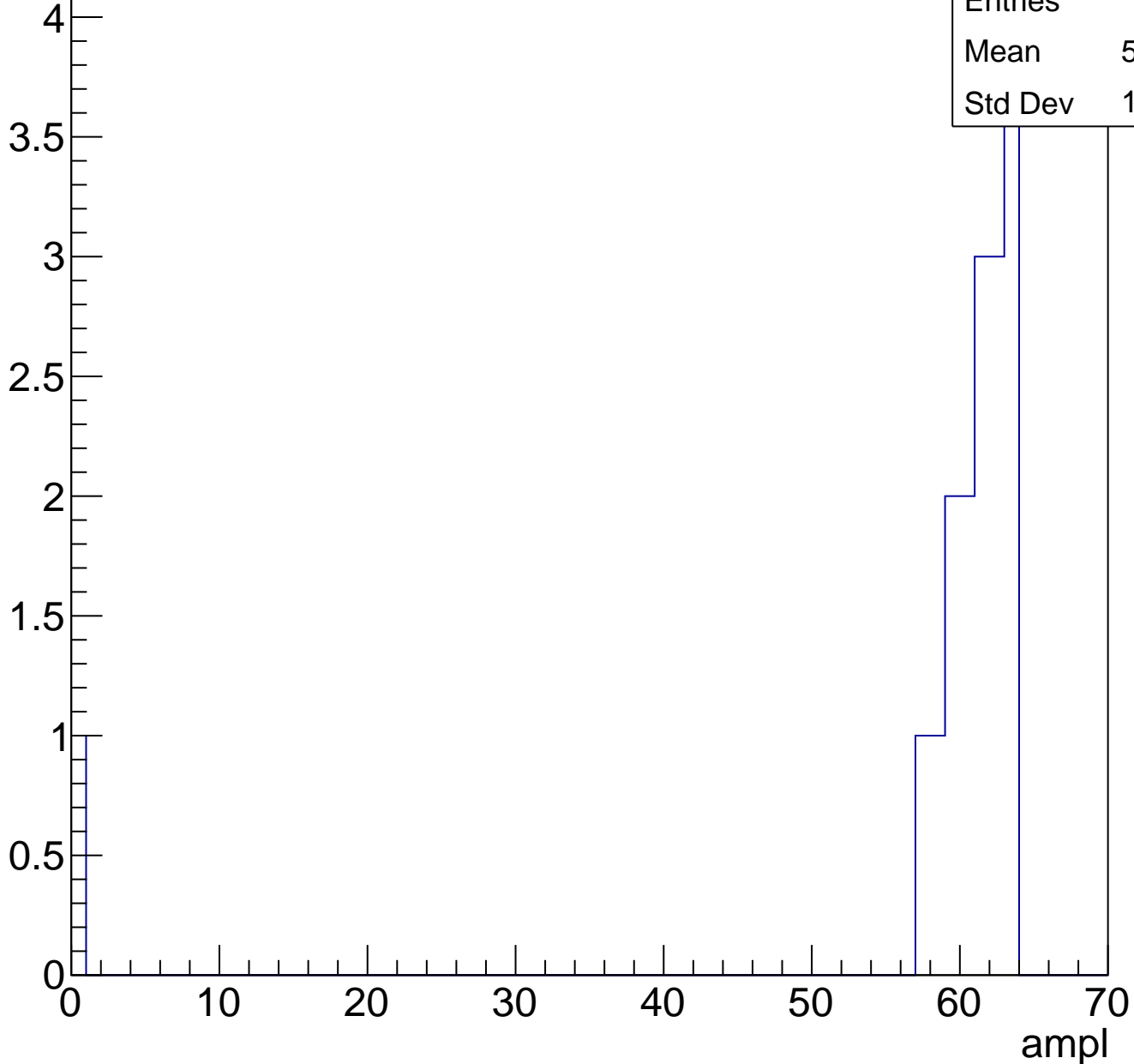


Entries	54
Mean	58.89
Std Dev	2.872

B1L103S, U18-ch48, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch48, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch49, adc0

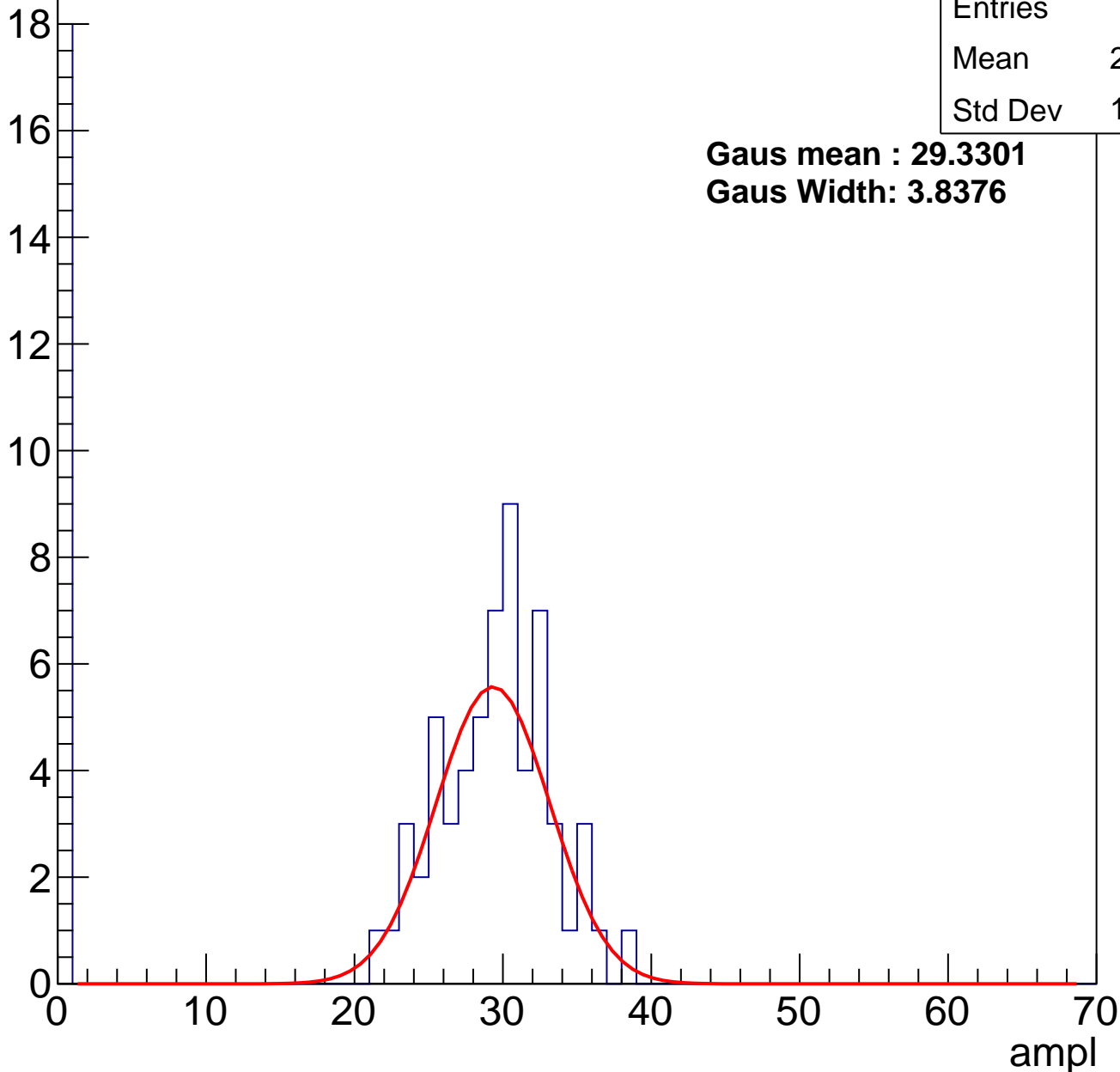
calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	22.36
Std Dev	12.66

Gaus mean : 29.3301

Gaus Width: 3.8376

Entry



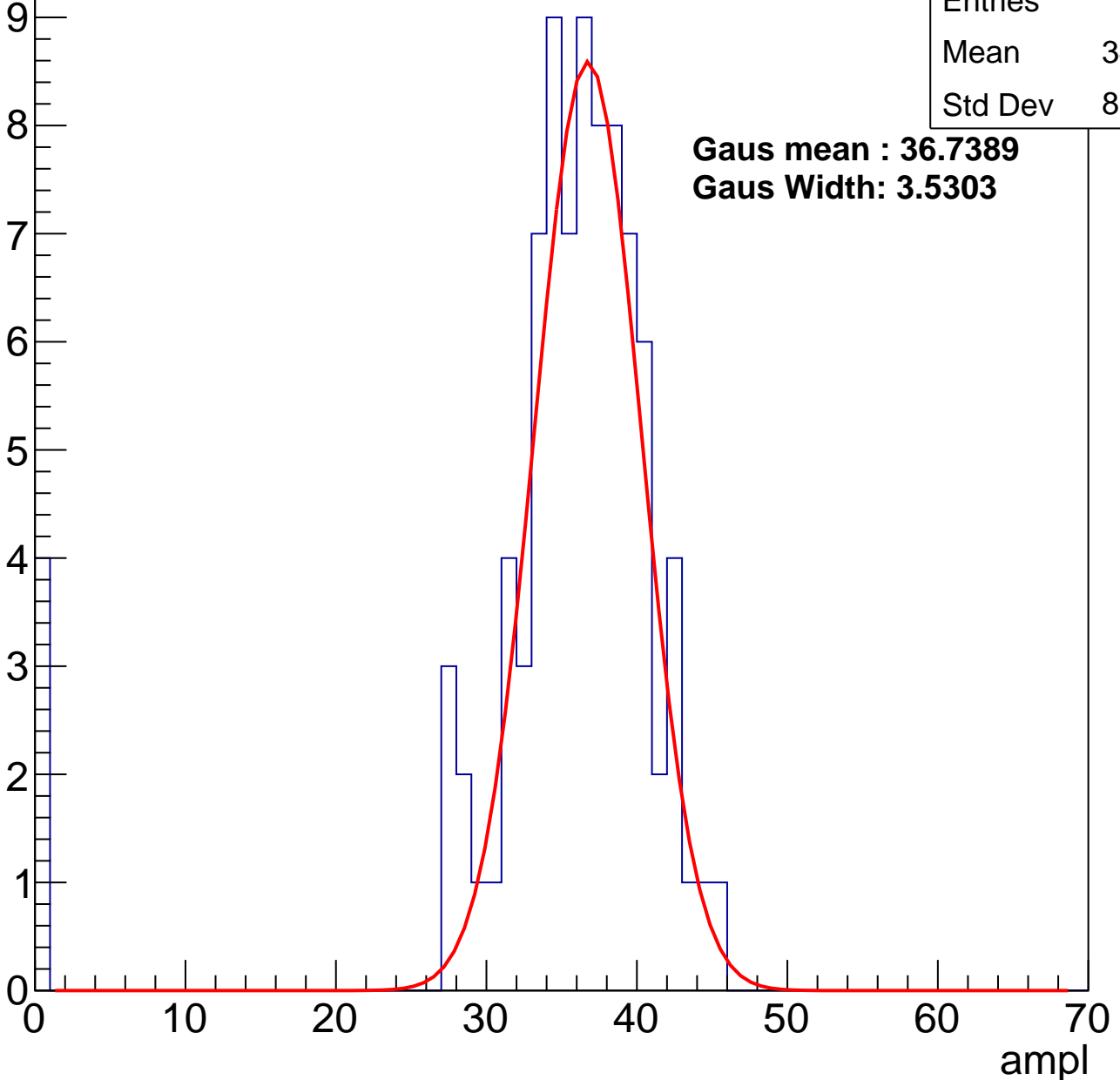
B1L103S, U18-ch49, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	88
Mean	34.28
Std Dev	8.409

Gaus mean : 36.7389
Gaus Width: 3.5303



B1L103S, U18-ch49, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	36.53
Std Dev	16.31

Gaus mean : 44.0481

Gaus Width: 3.4584

Entry

10

8

6

4

2

0

0

10

20

30

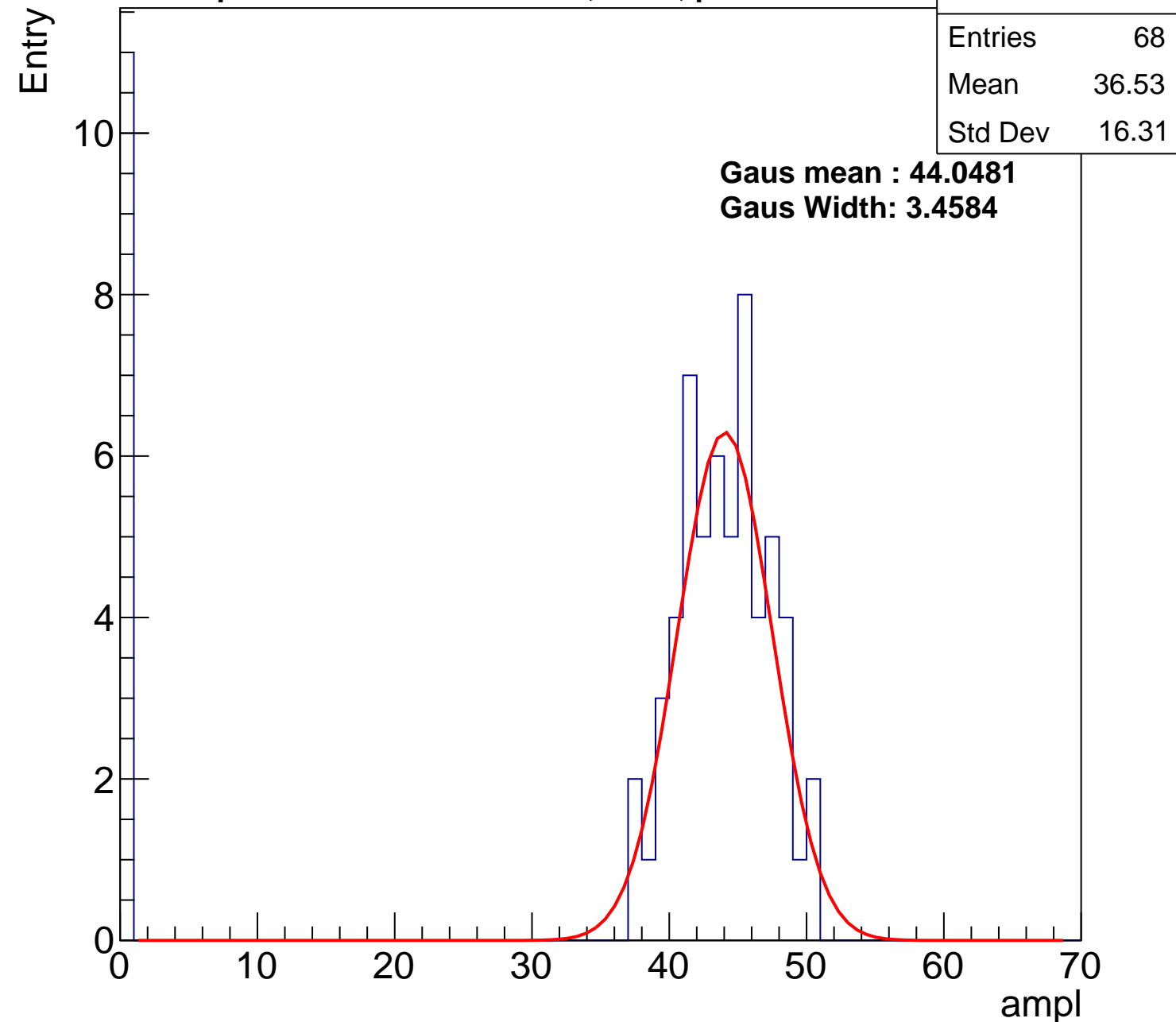
40

50

60

70

ampl

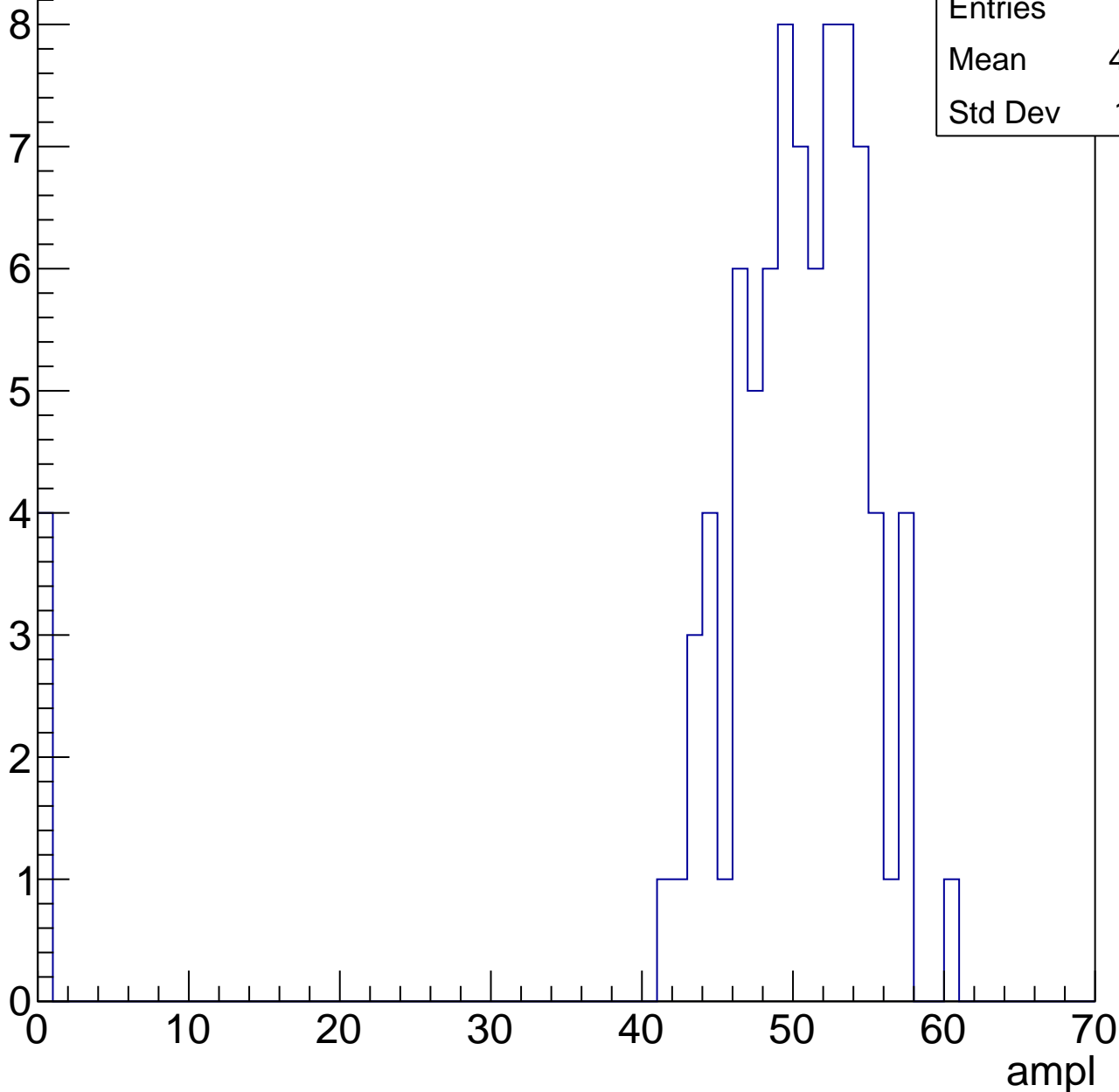


B1L103S, U18-ch49, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	47.79
Std Dev	11.31

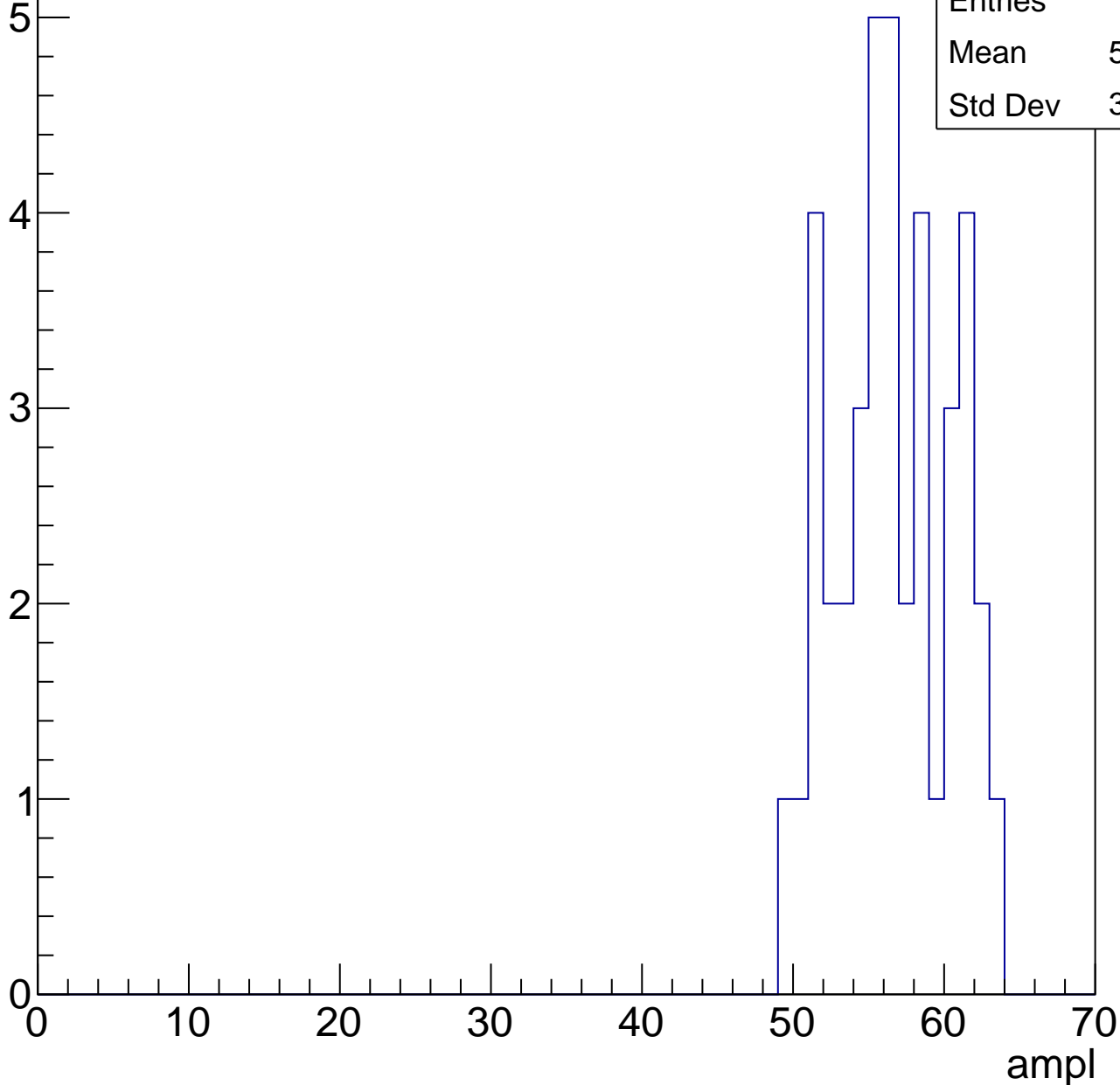


B1L103S, U18-ch49, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

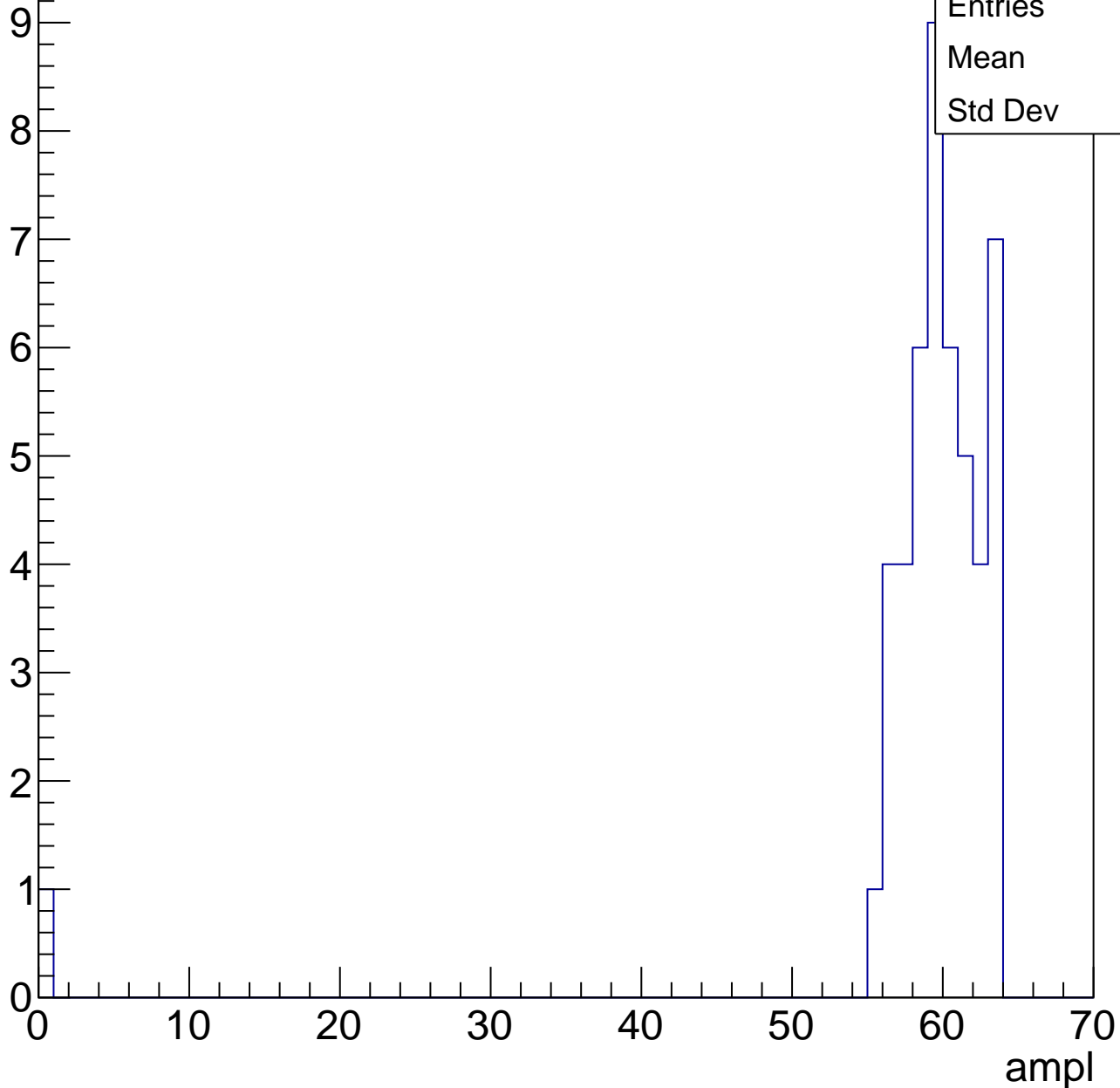
Entries	40
Mean	56.15
Std Dev	3.698



B1L103S, U18-ch49, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch49, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch49, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch50, adc0

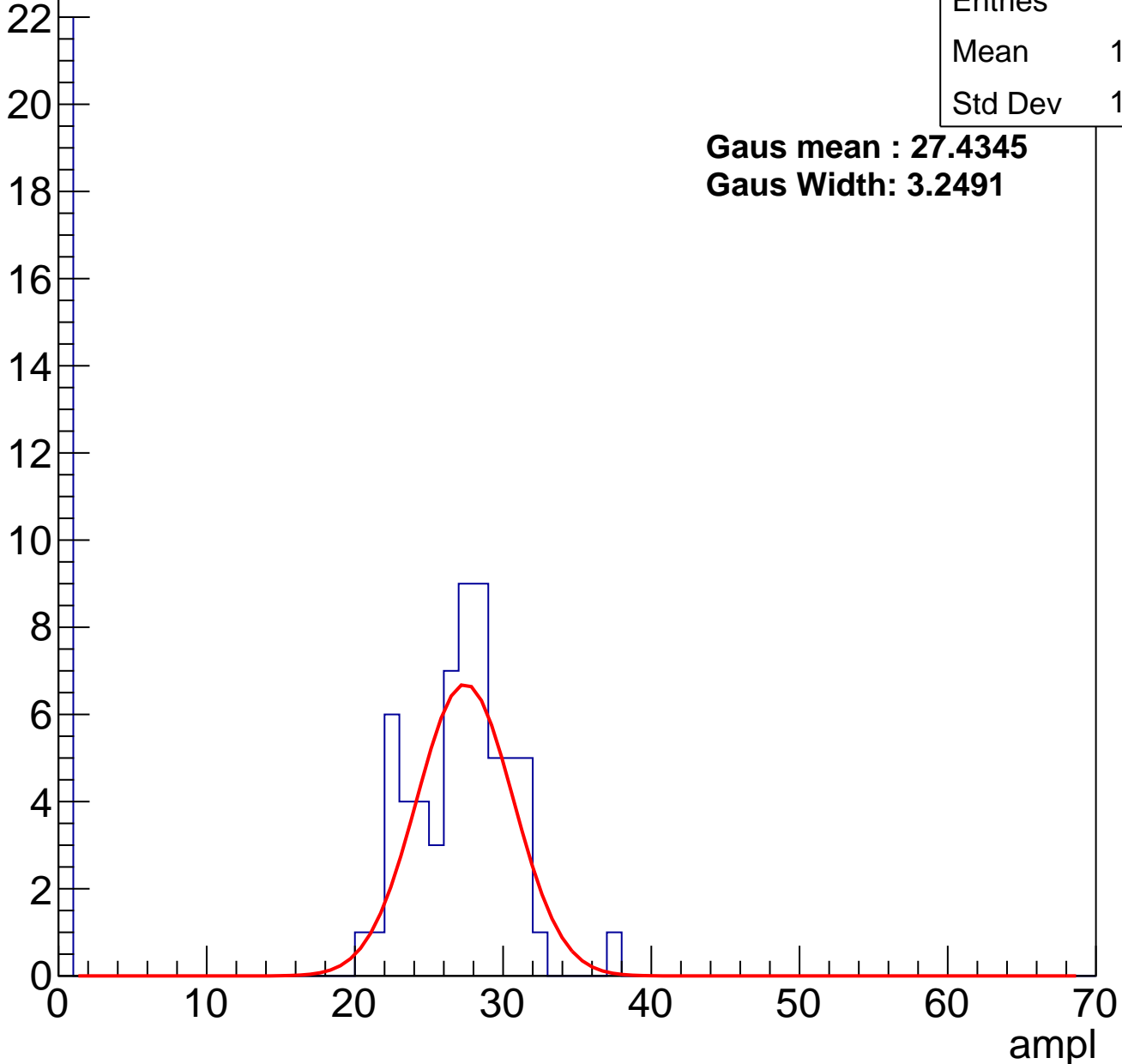
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	19.66
Std Dev	12.13

Gaus mean : 27.4345

Gaus Width: 3.2491

Entry



B1L103S, U18-ch50, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	34.2
Std Dev	3.806

Gaus mean : 34.6492

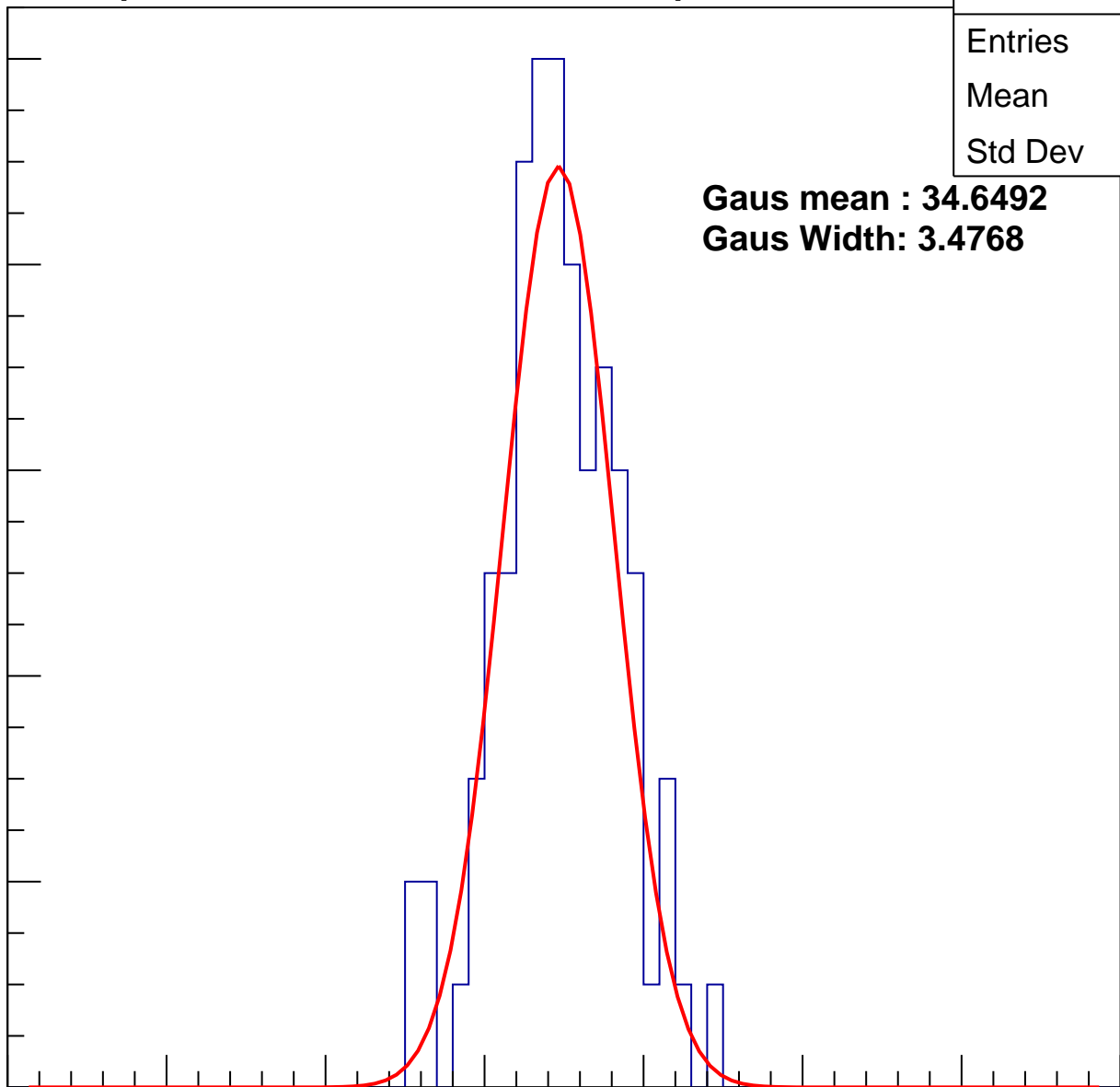
Gaus Width: 3.4768

Entry

10
8
6
4
2
0

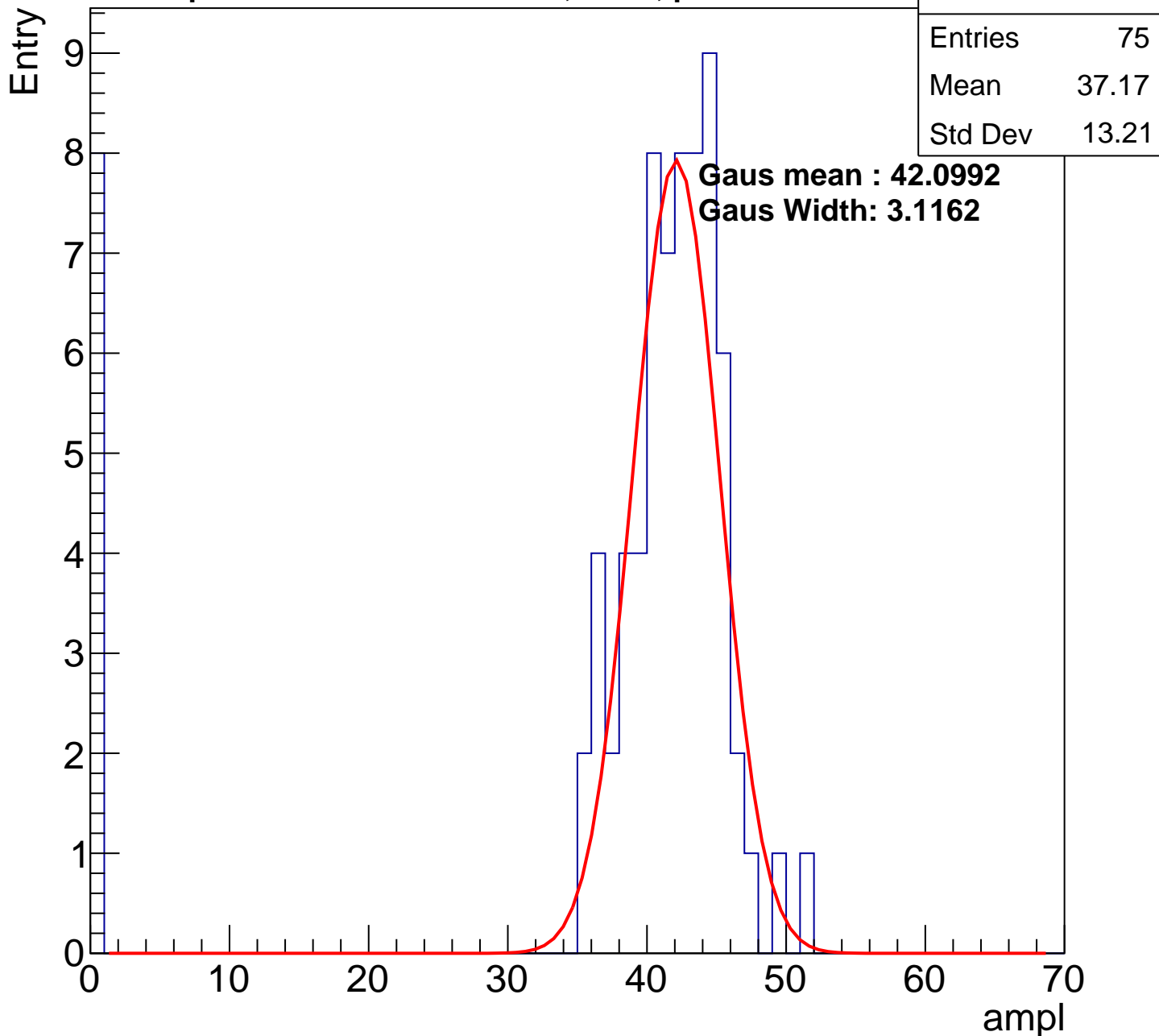
ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch50, adc2

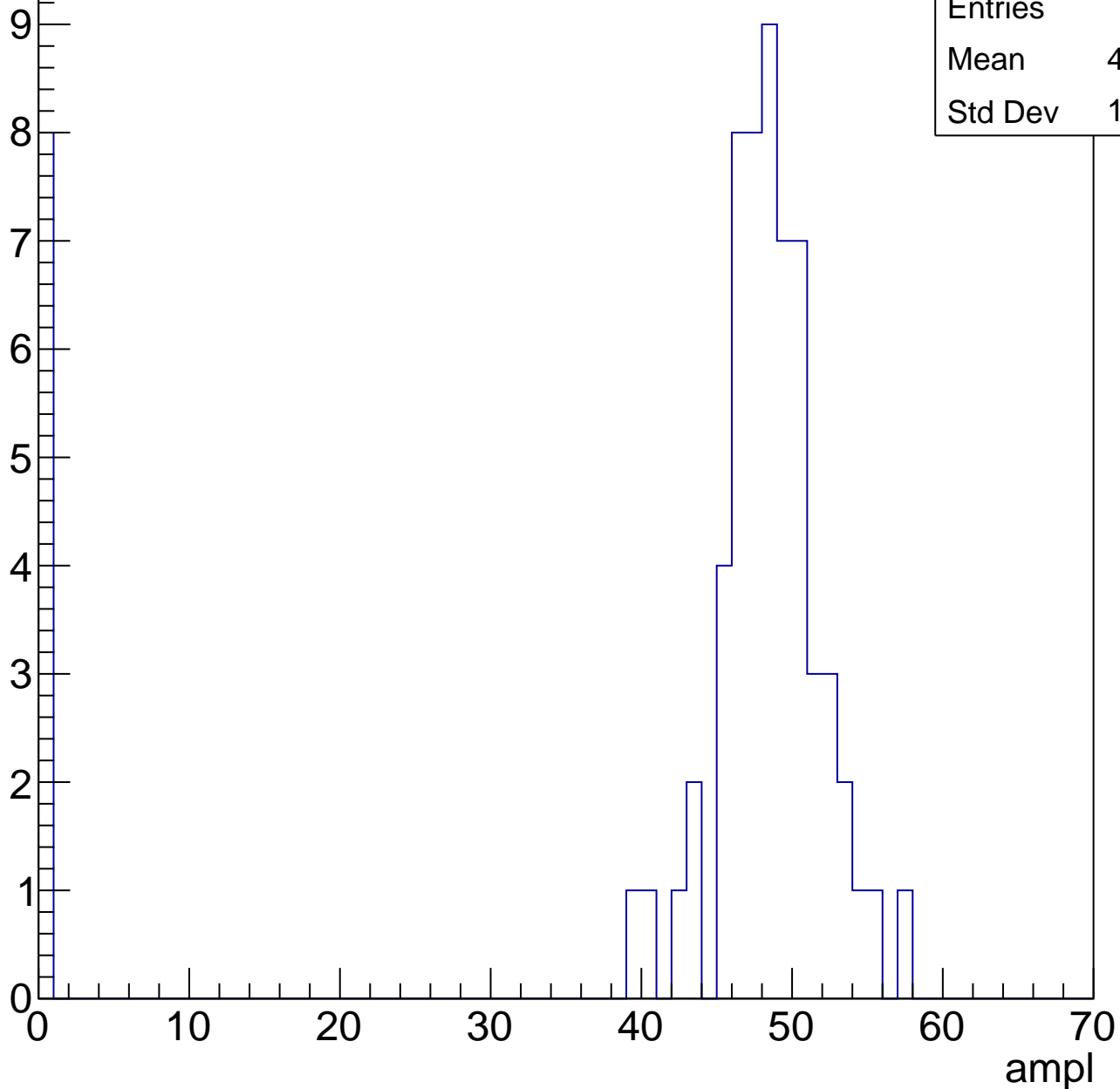
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U18-ch50, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

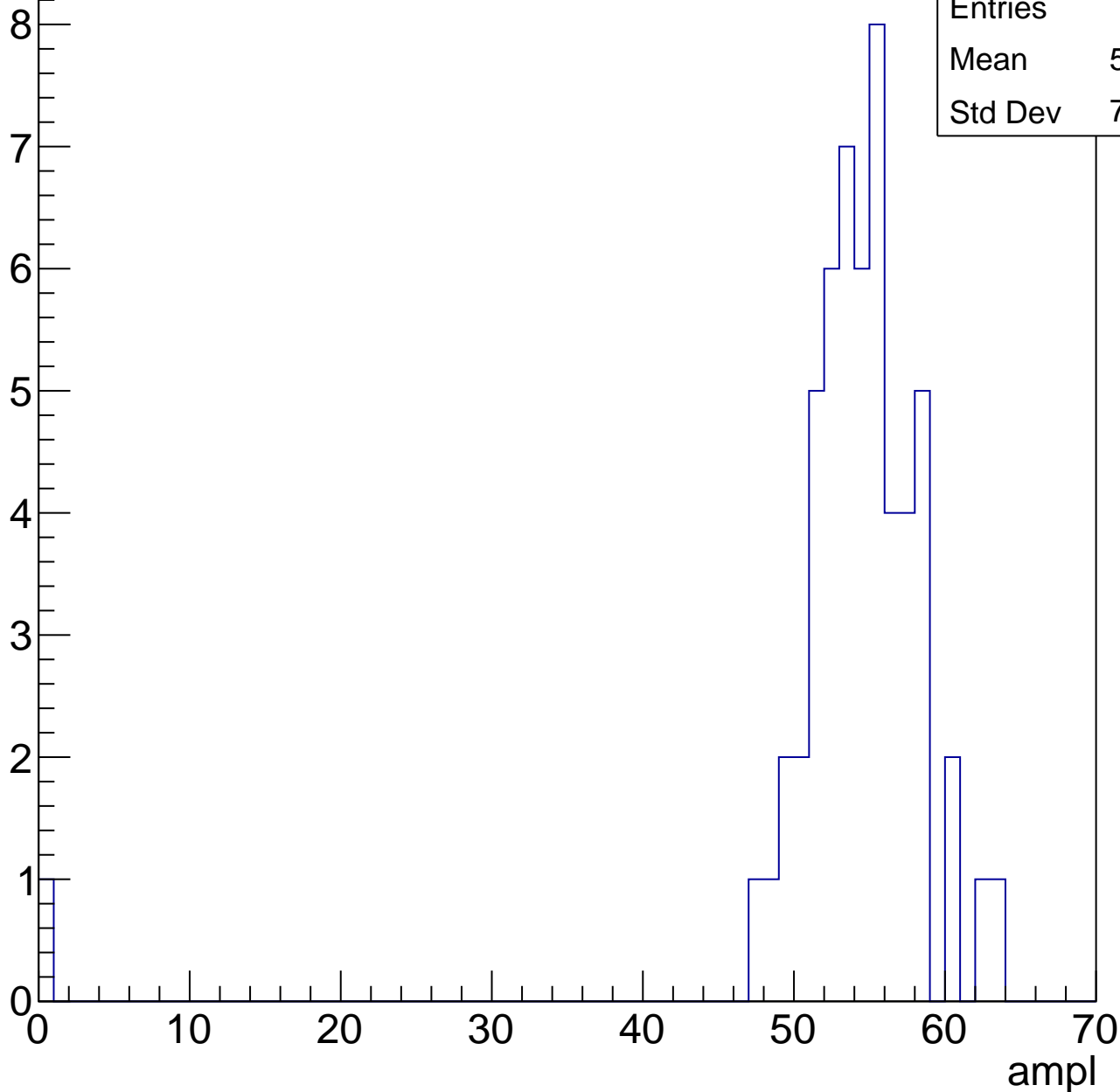


B1L103S, U18-ch50, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

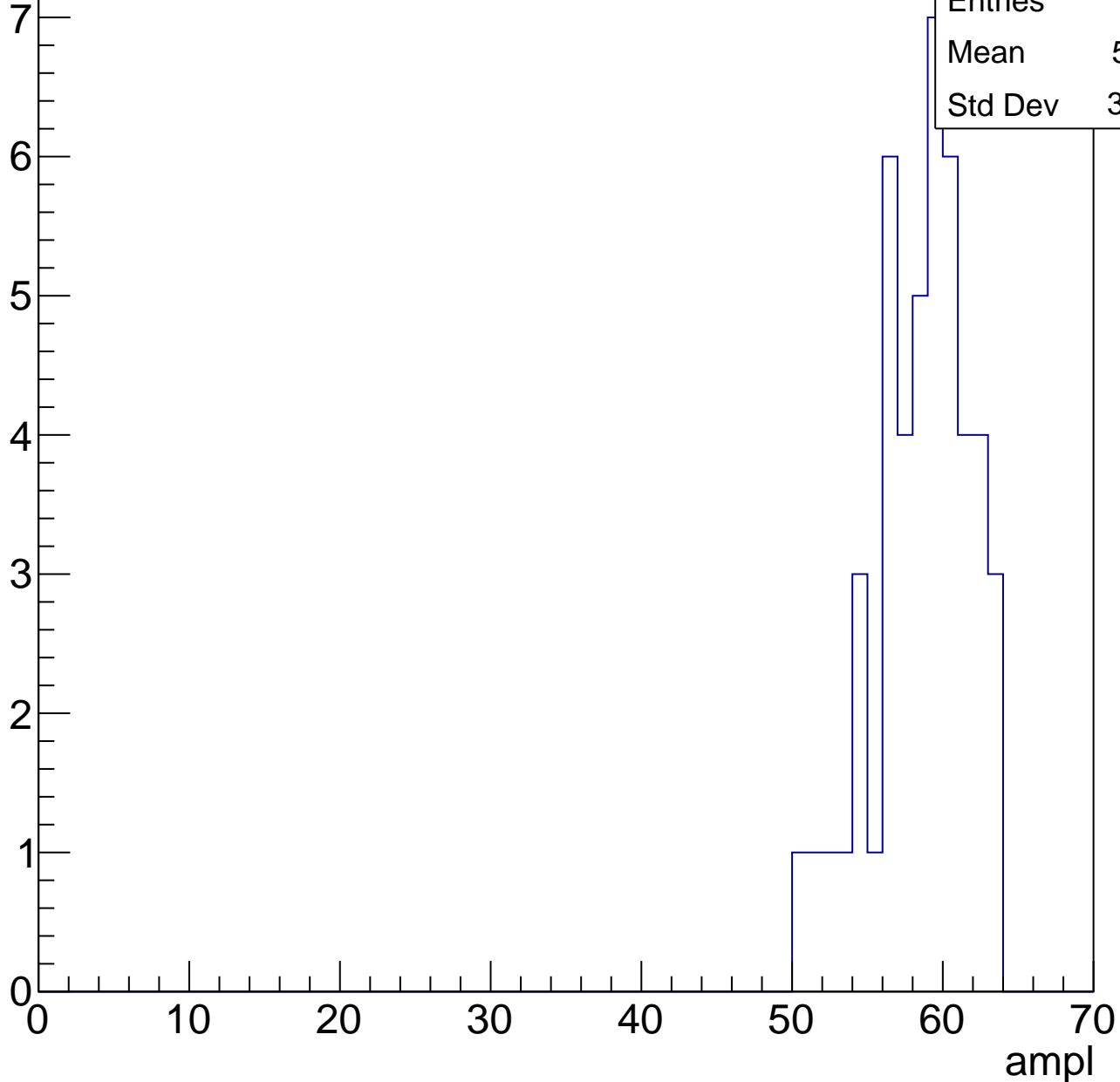
Entries	56
Mean	53.25
Std Dev	7.888



B1L103S, U18-ch50, adc5

calib_packv5_041523_1651.root, FC#0, port C2

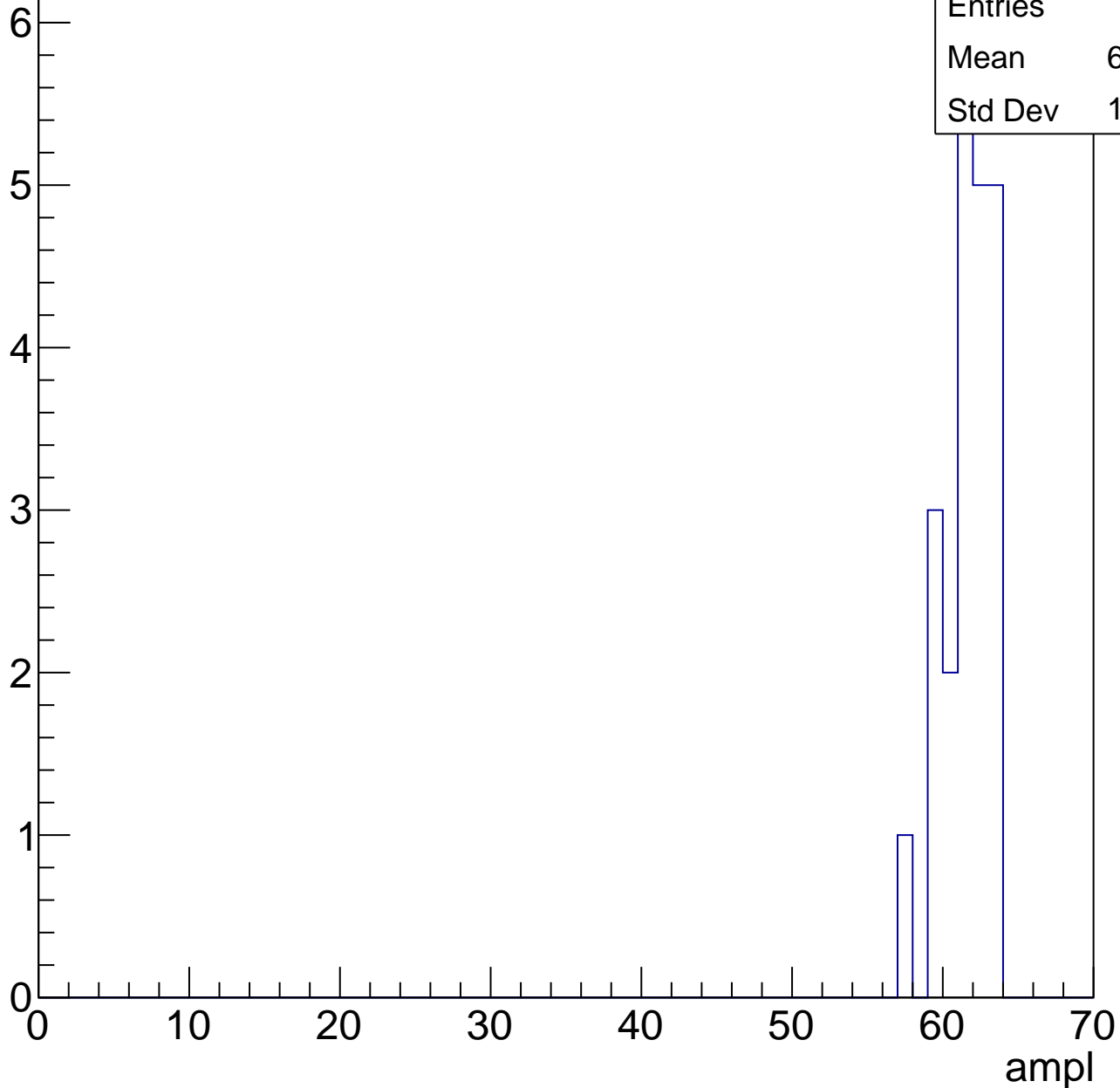
Entry



B1L103S, U18-ch50, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	22
Mean	61.14
Std Dev	1.575

B1L103S, U18-ch50, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U18-ch51, adc0

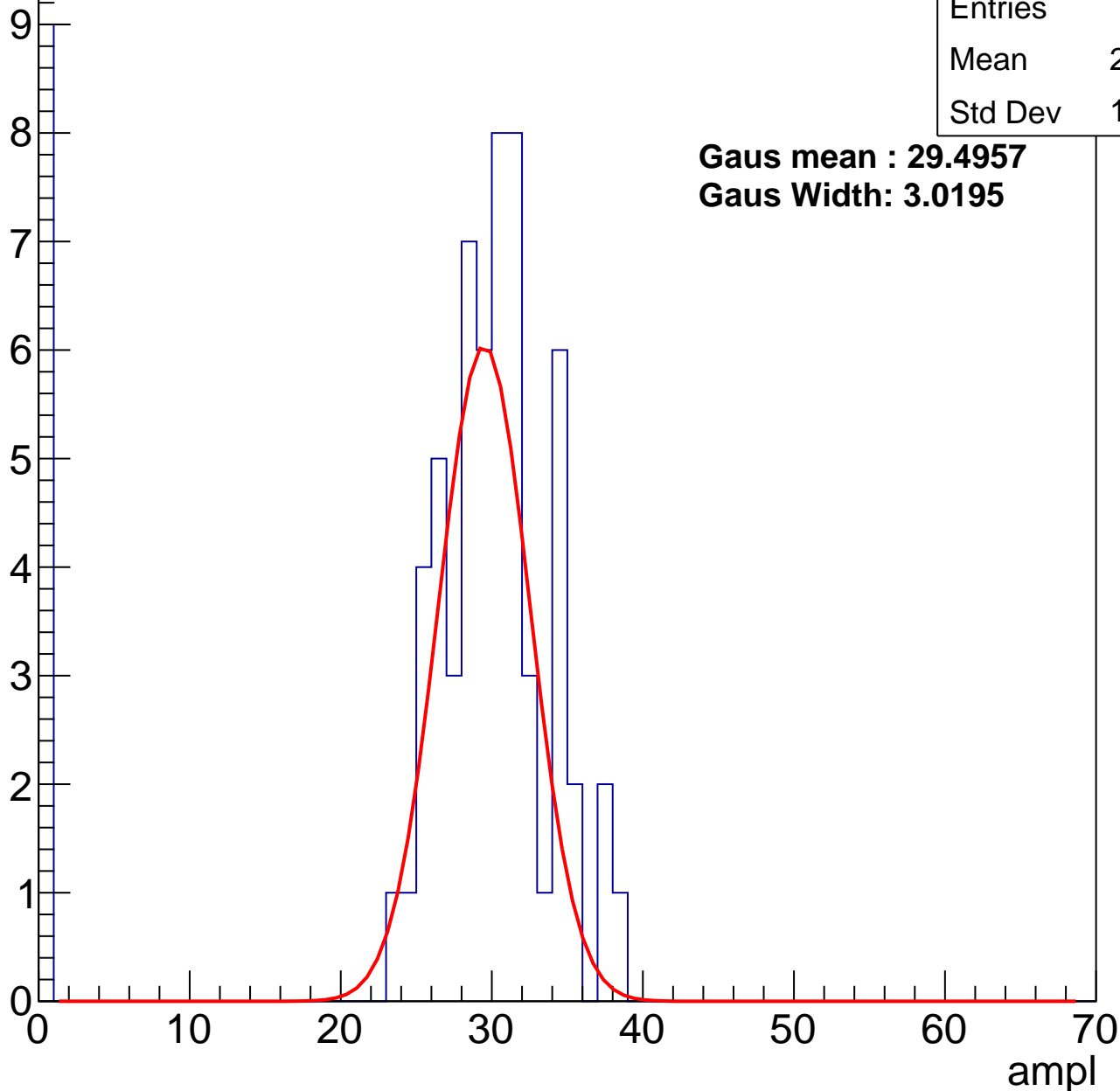
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	25.84
Std Dev	10.65

Gaus mean : 29.4957

Gaus Width: 3.0195



B1L103S, U18-ch51, adc1

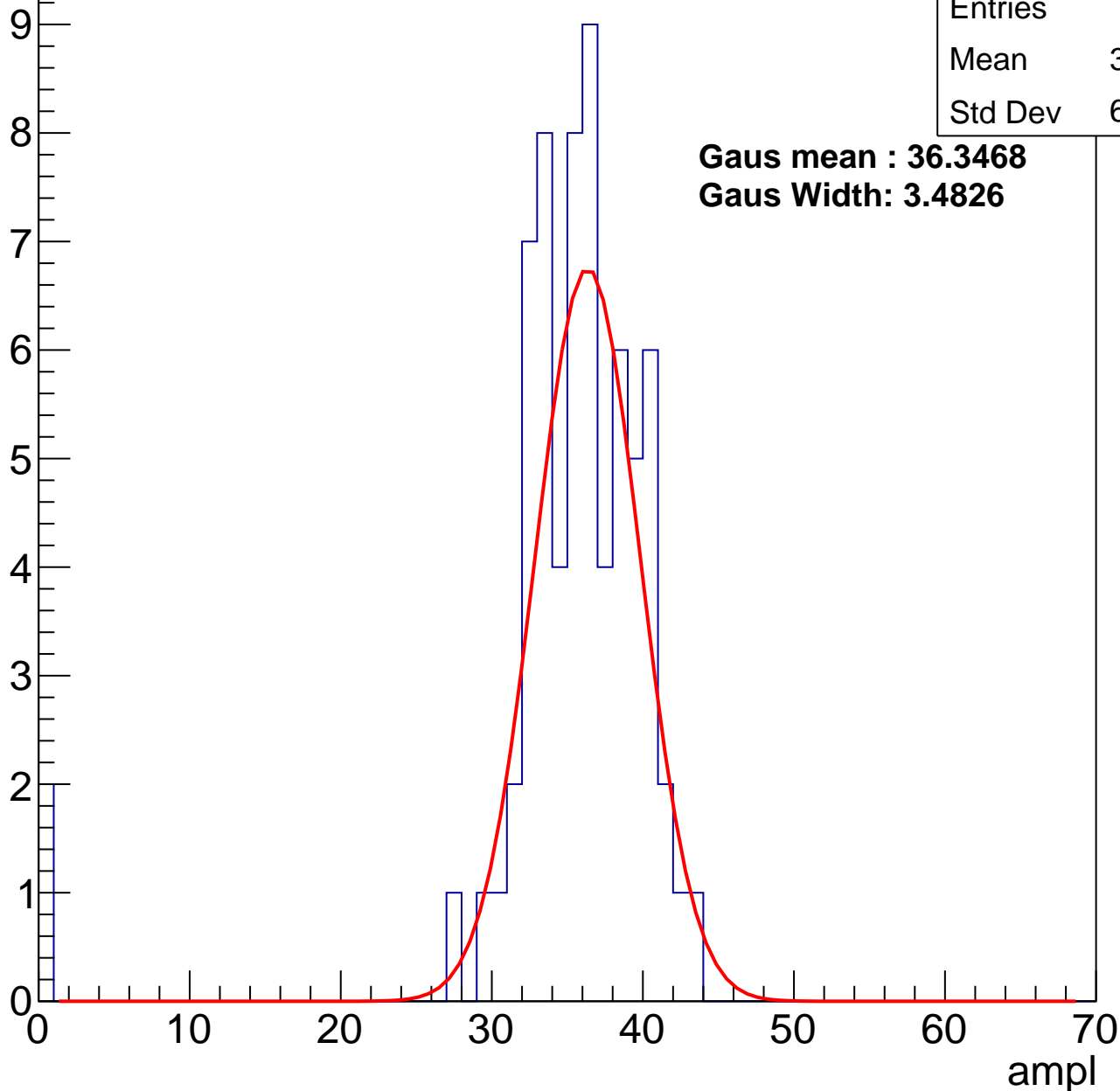
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34.62
Std Dev	6.849

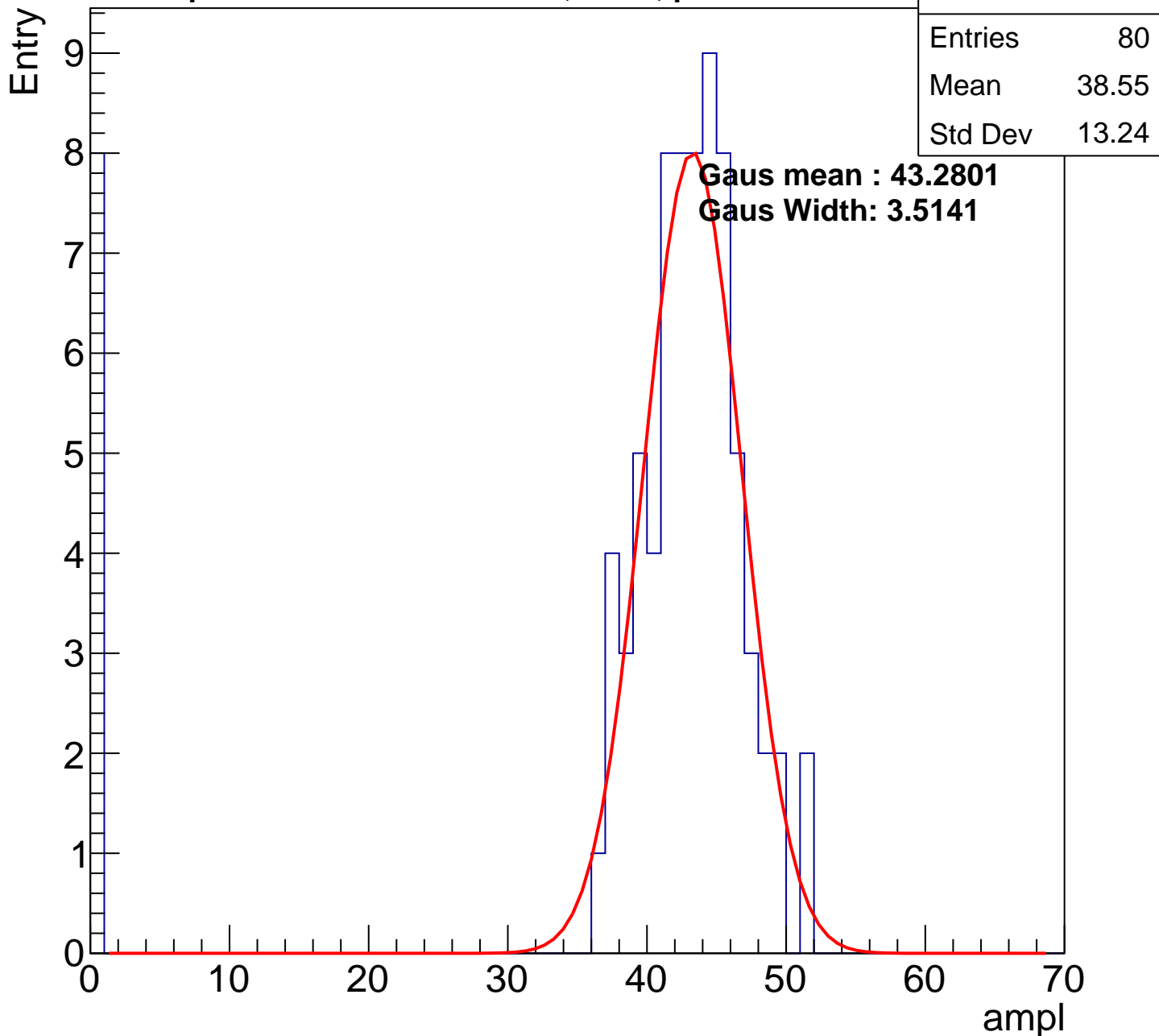
Gaus mean : 36.3468

Gaus Width: 3.4826



B1L103S, U18-ch51, adc2

calib_packv5_041523_1651.root, FC#0, port C2

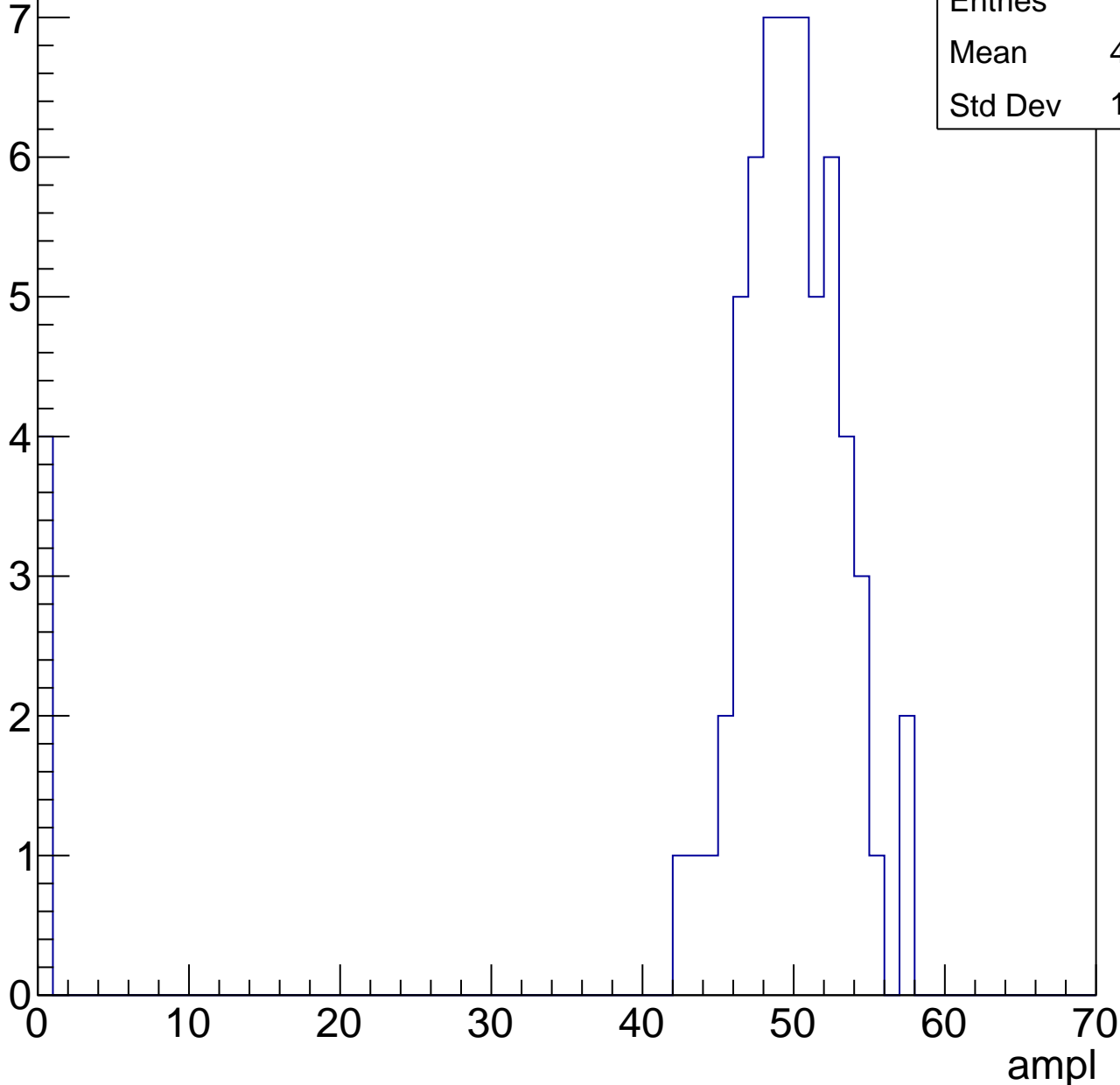


B1L103S, U18-ch51, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	46.29
Std Dev	12.54

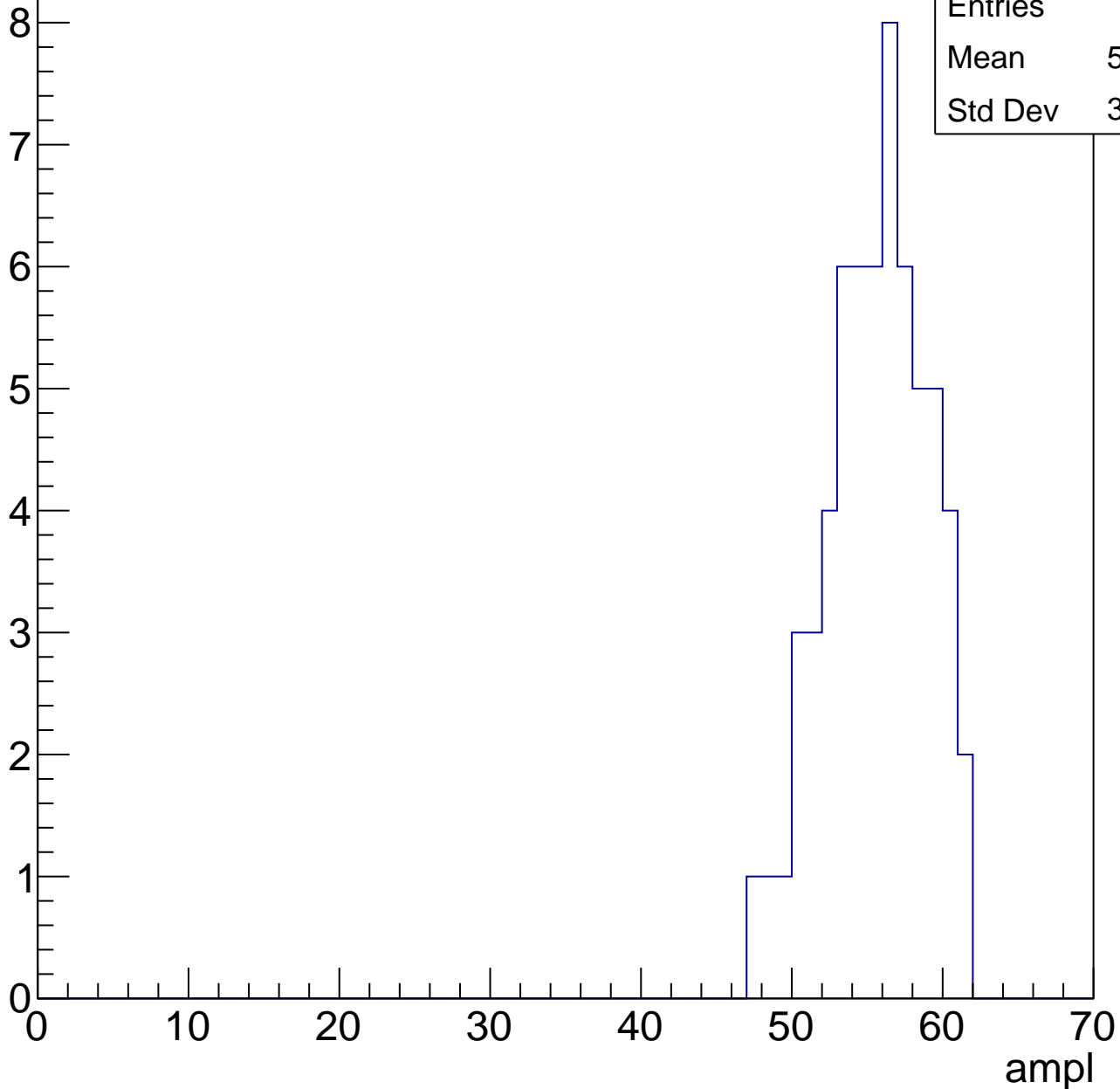


B1L103S, U18-ch51, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	55.15
Std Dev	3.303

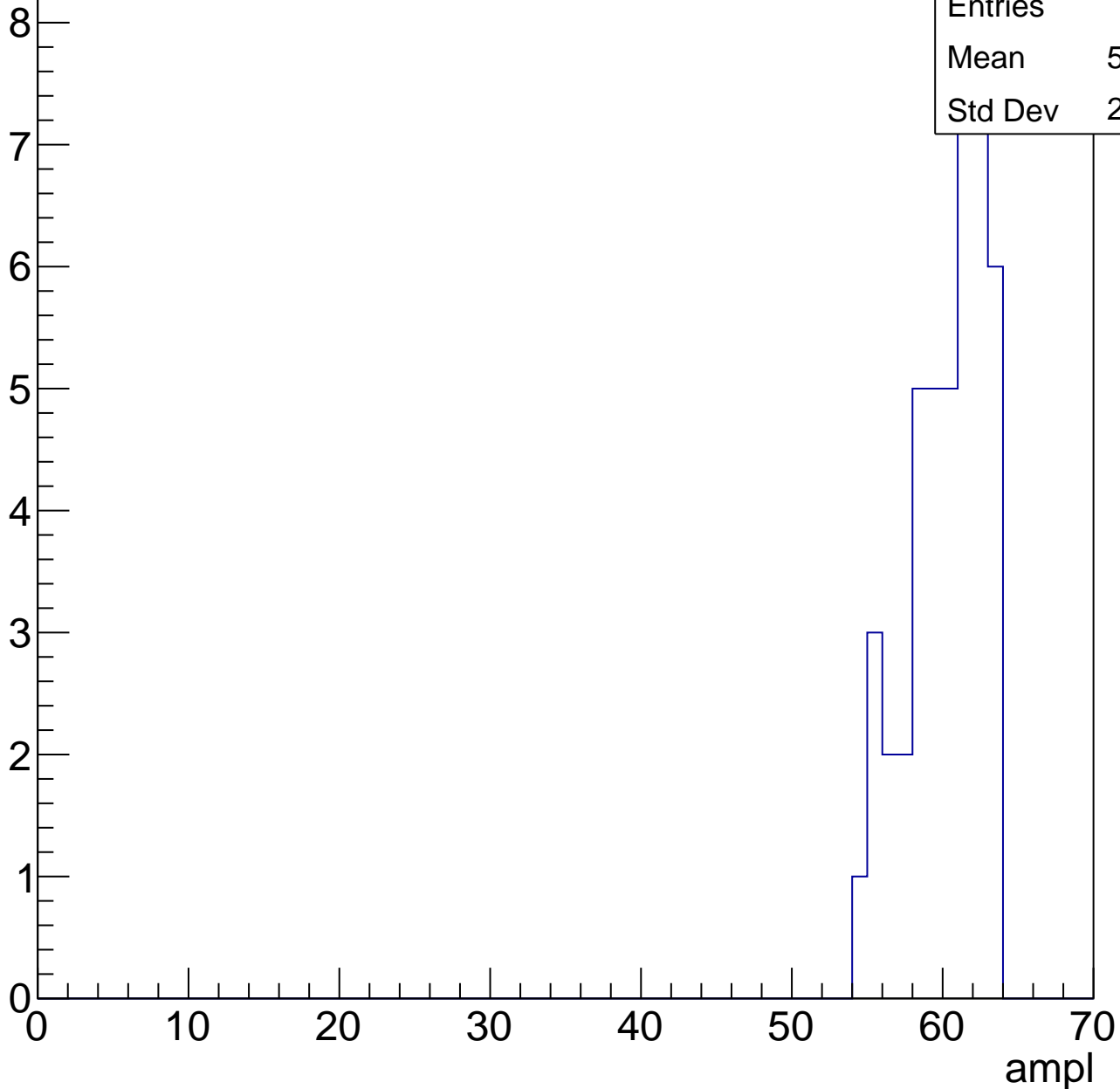


B1L103S, U18-ch51, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

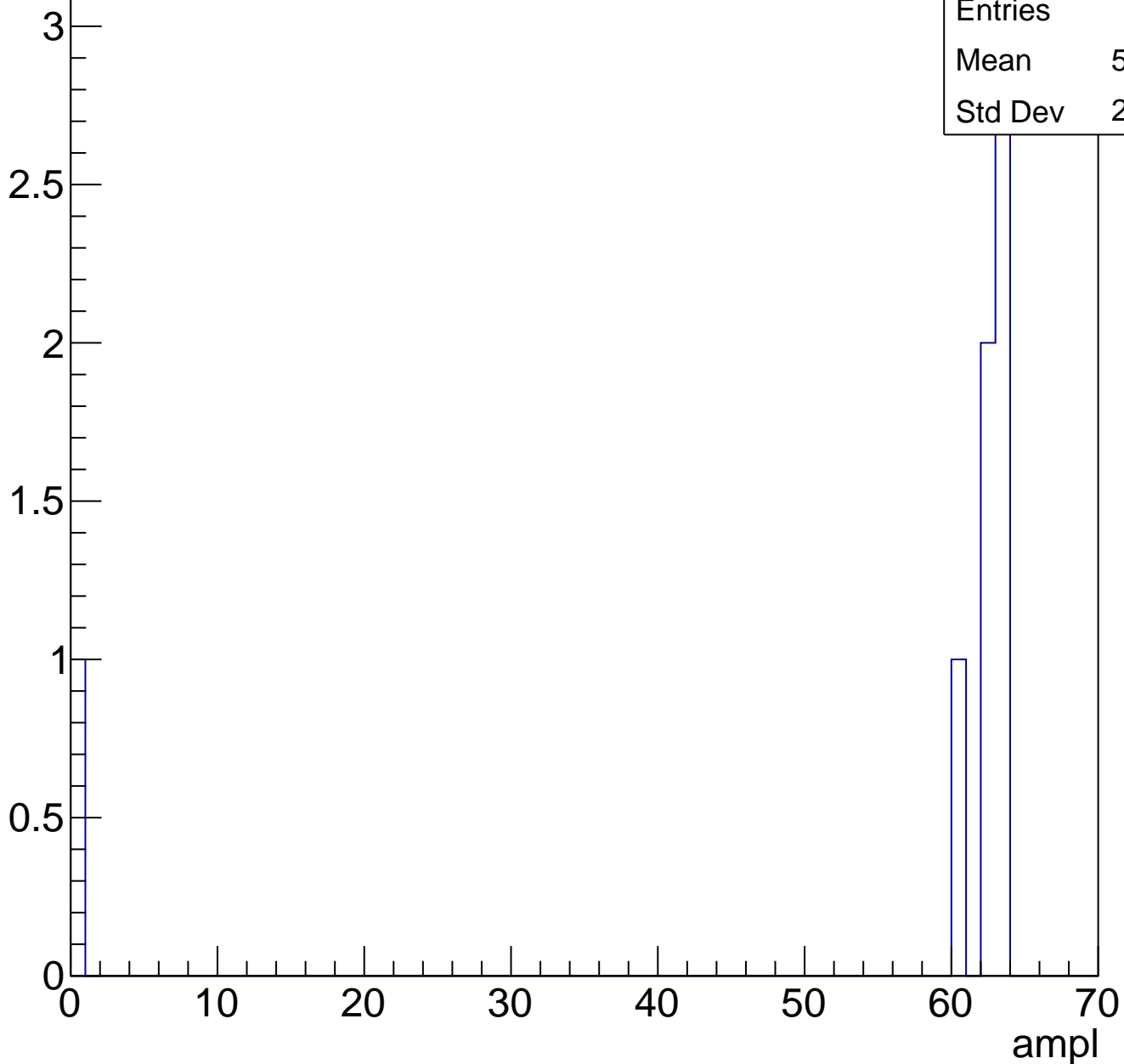
Entries	45
Mean	59.82
Std Dev	2.488



B1L103S, U18-ch51, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

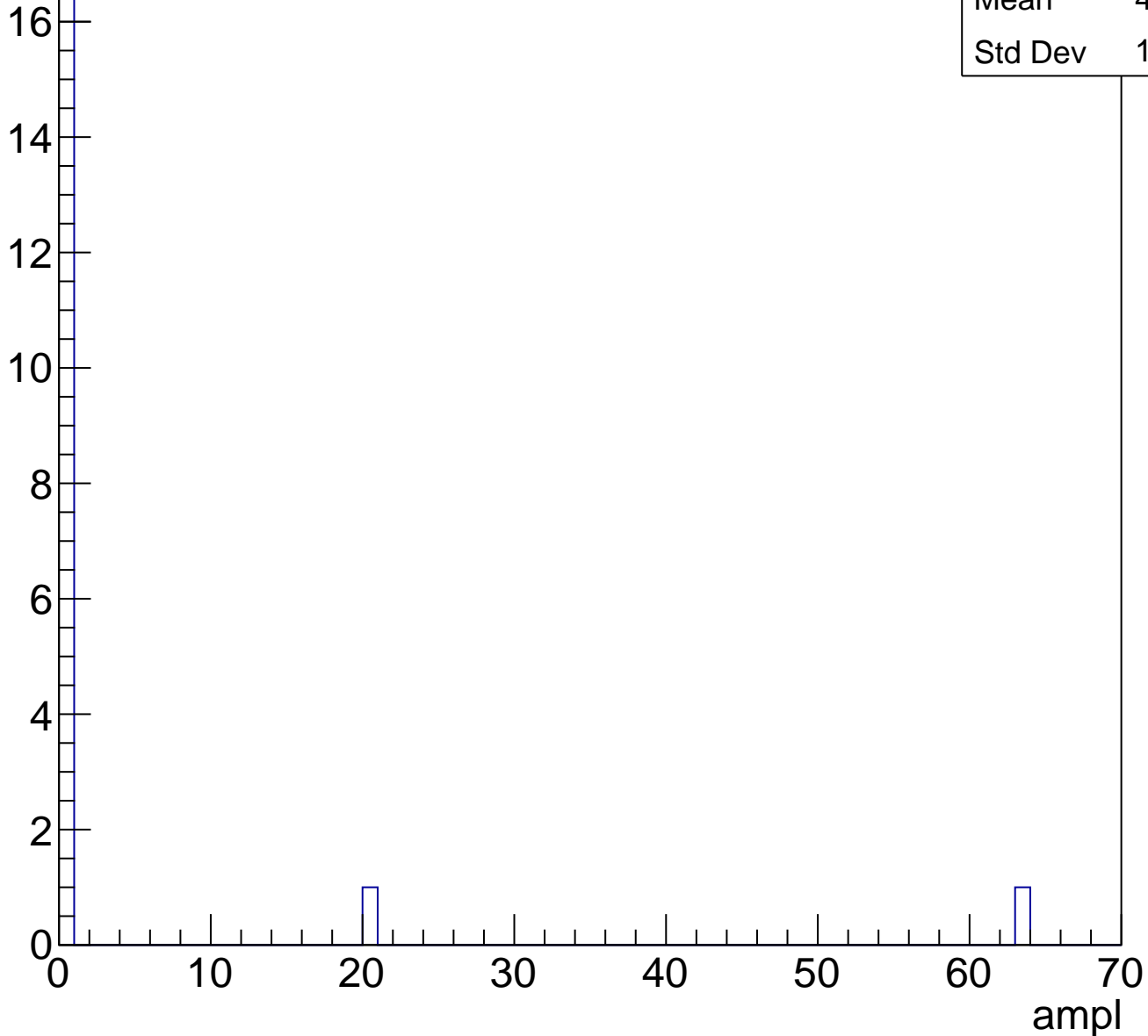


B1L103S, U18-ch51, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.368
Std Dev	14.52

Entry



B1L103S, U18-ch52, adc0

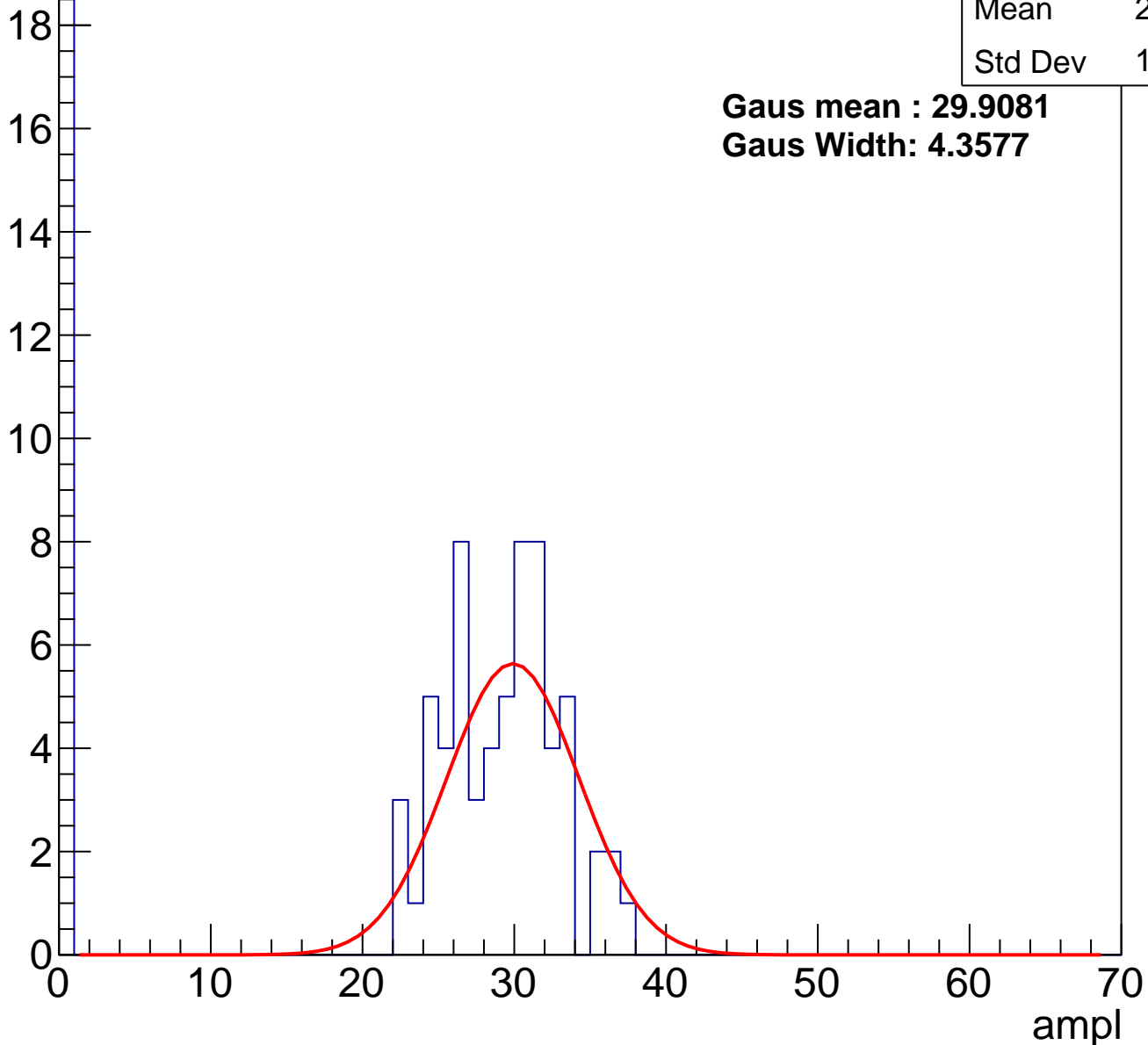
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	22.13
Std Dev	12.57

Gaus mean : 29.9081

Gaus Width: 4.3577

Entry



B1L103S, U18-ch52, adc1

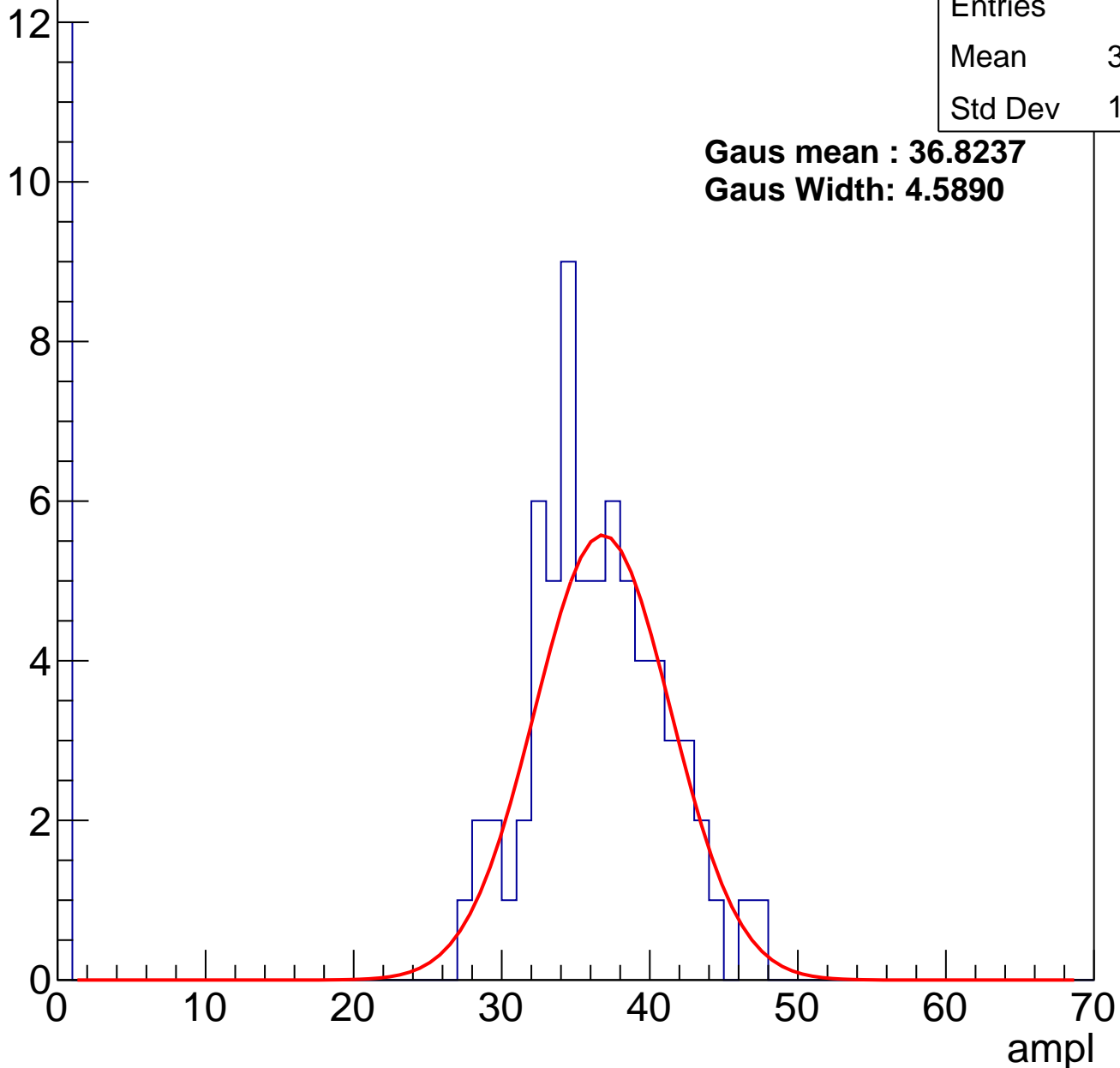
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	30.64
Std Dev	13.47

Gaus mean : 36.8237

Gaus Width: 4.5890

Entry



B1L103S, U18-ch52, adc2

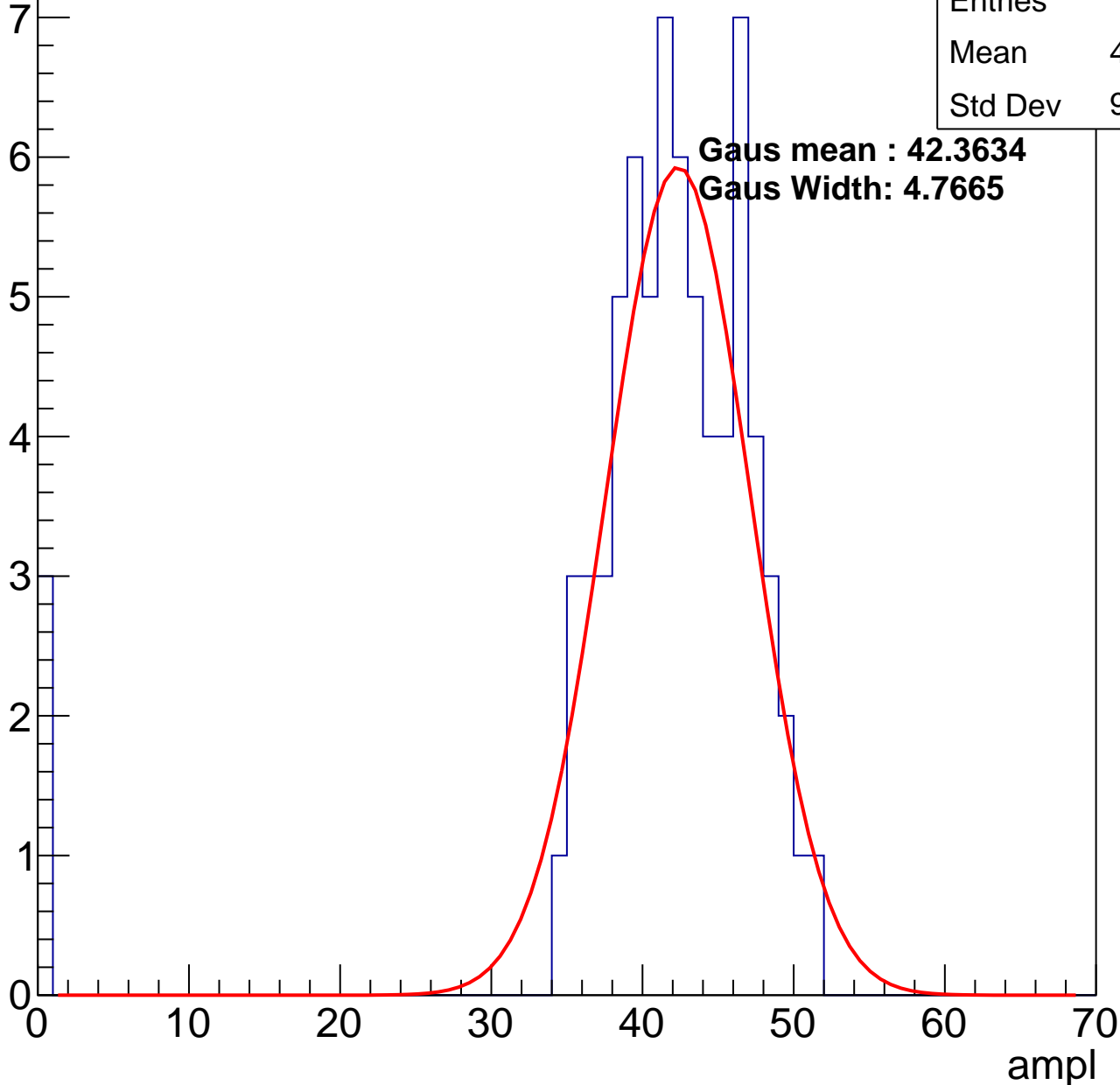
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	40.34
Std Dev	9.265

Gaus mean : 42.3634

Gaus Width: 4.7665

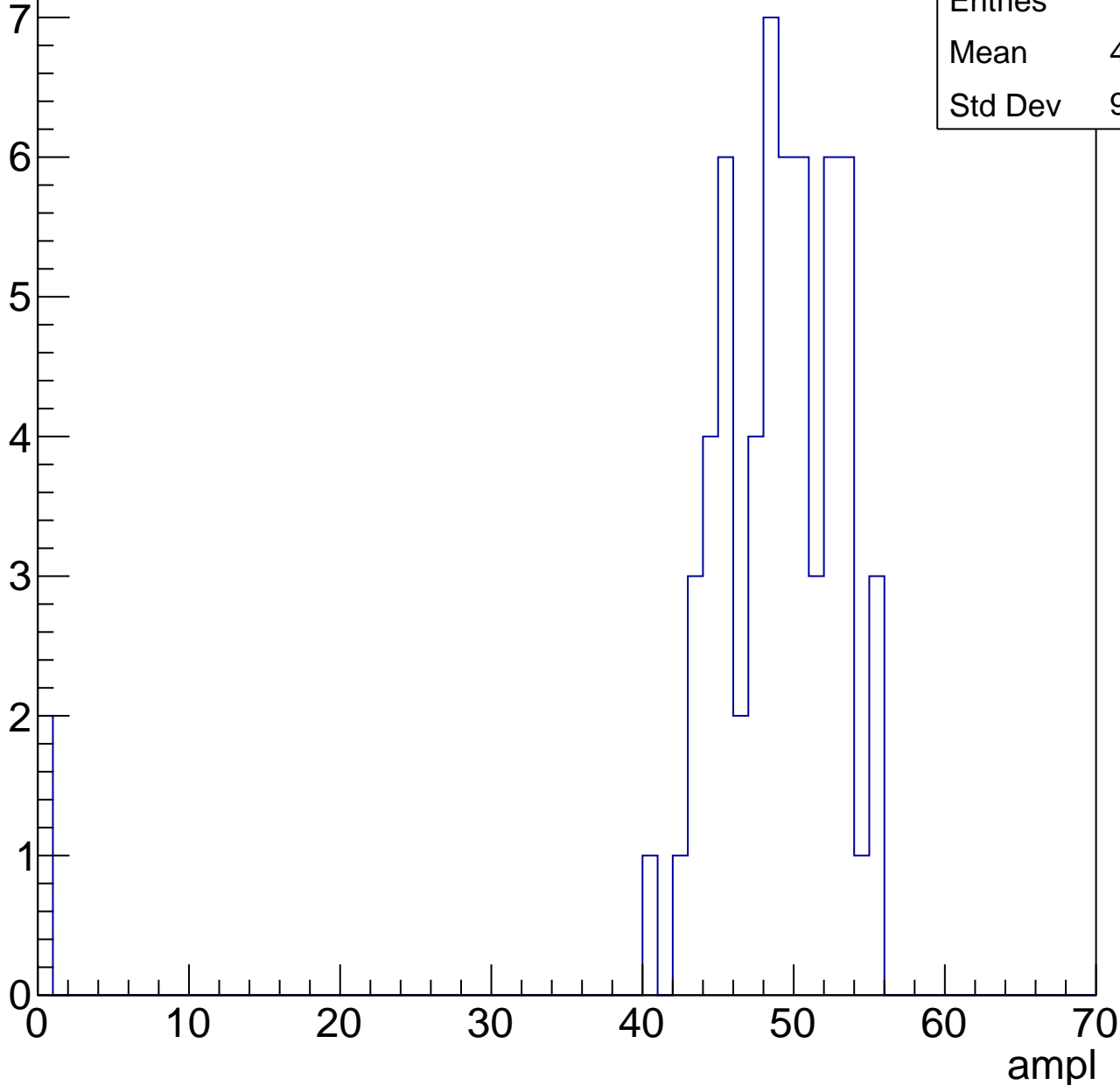


B1L103S, U18-ch52, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	47.03
Std Dev	9.358

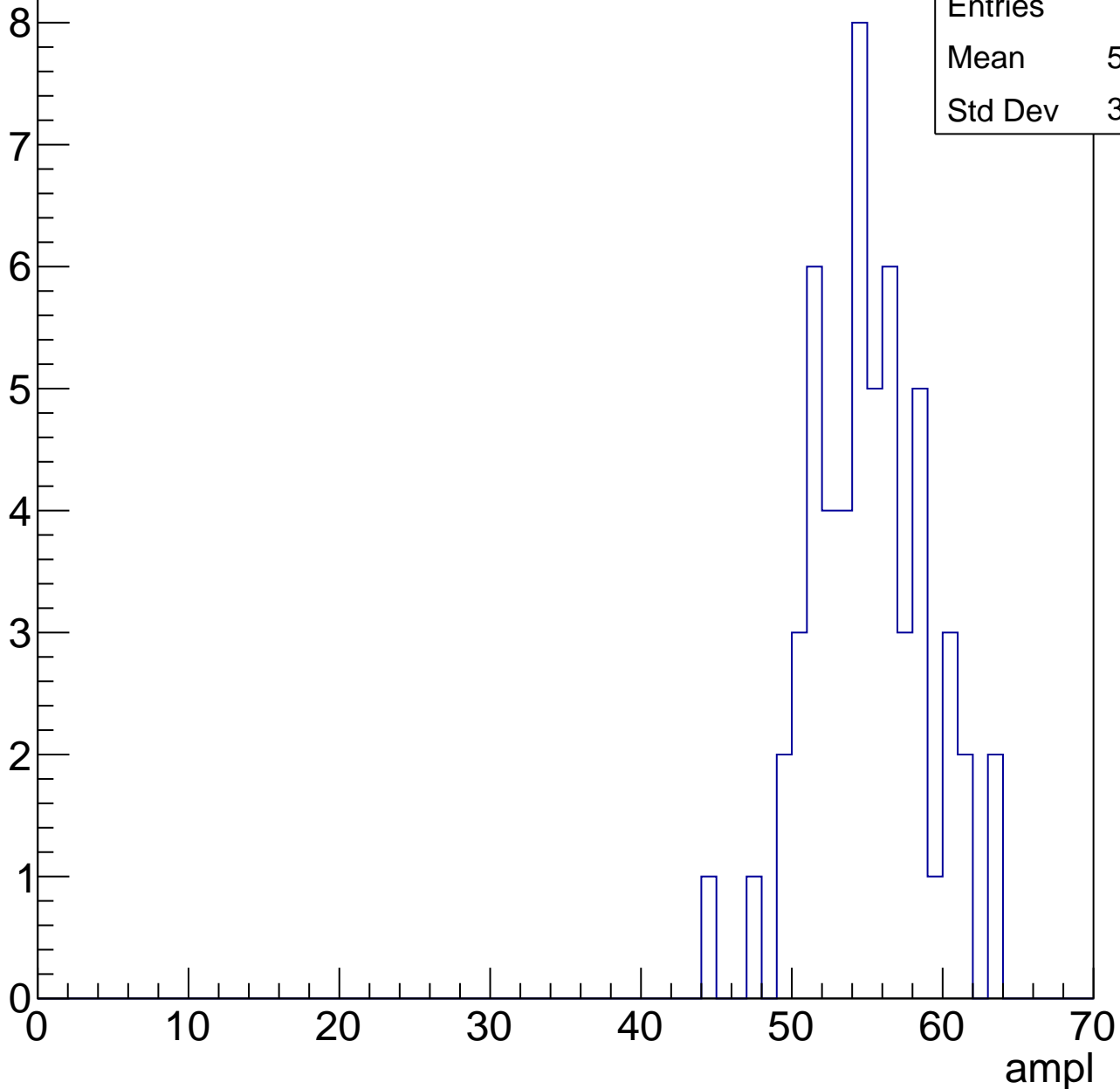


B1L103S, U18-ch52, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	54.57
Std Dev	3.854

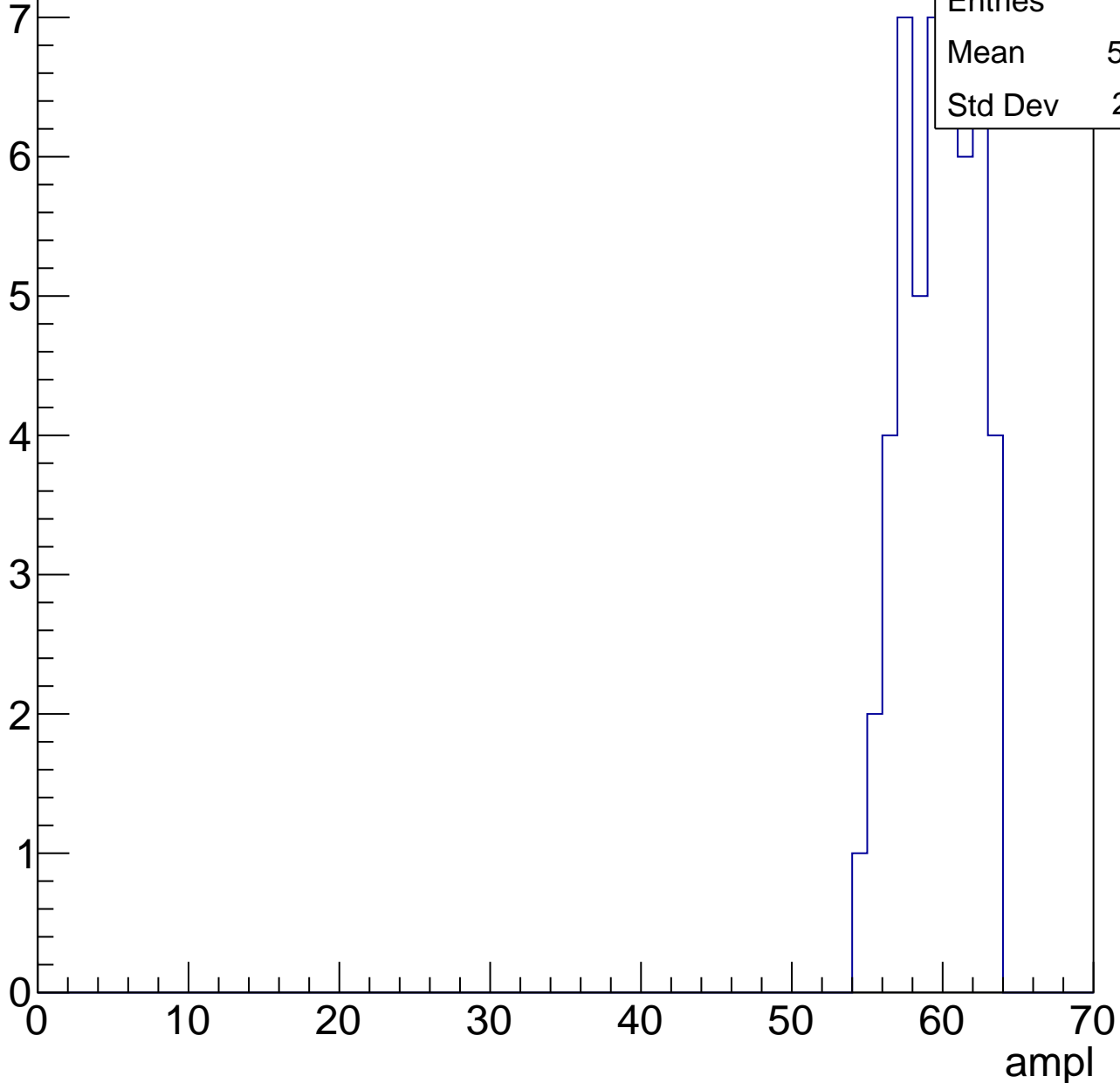


B1L103S, U18-ch52, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

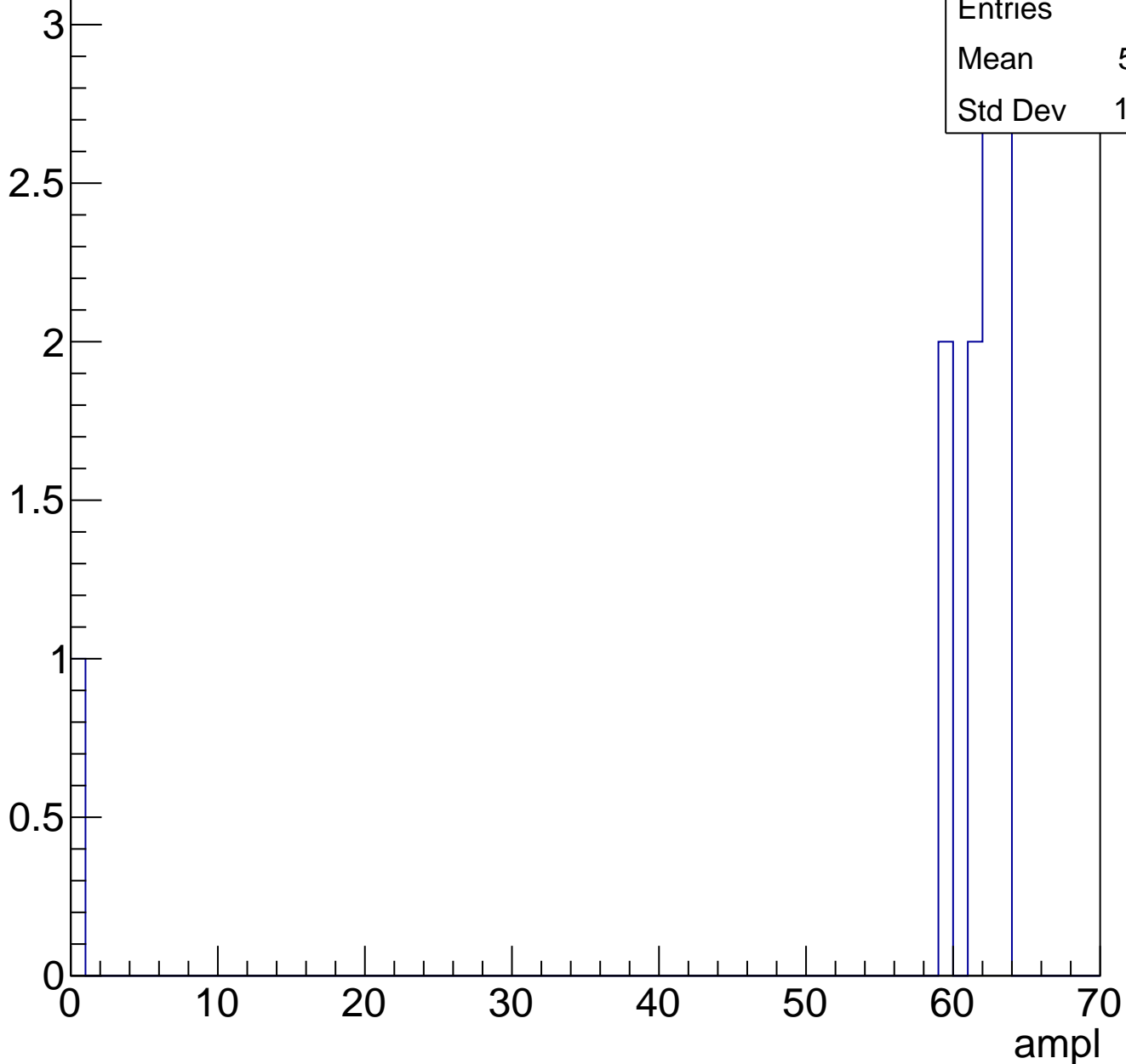
Entries	50
Mean	59.24
Std Dev	2.371



B1L103S, U18-ch52, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch52, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U18-ch53, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	21.47
Std Dev	13.38

Gaus mean : 29.5498

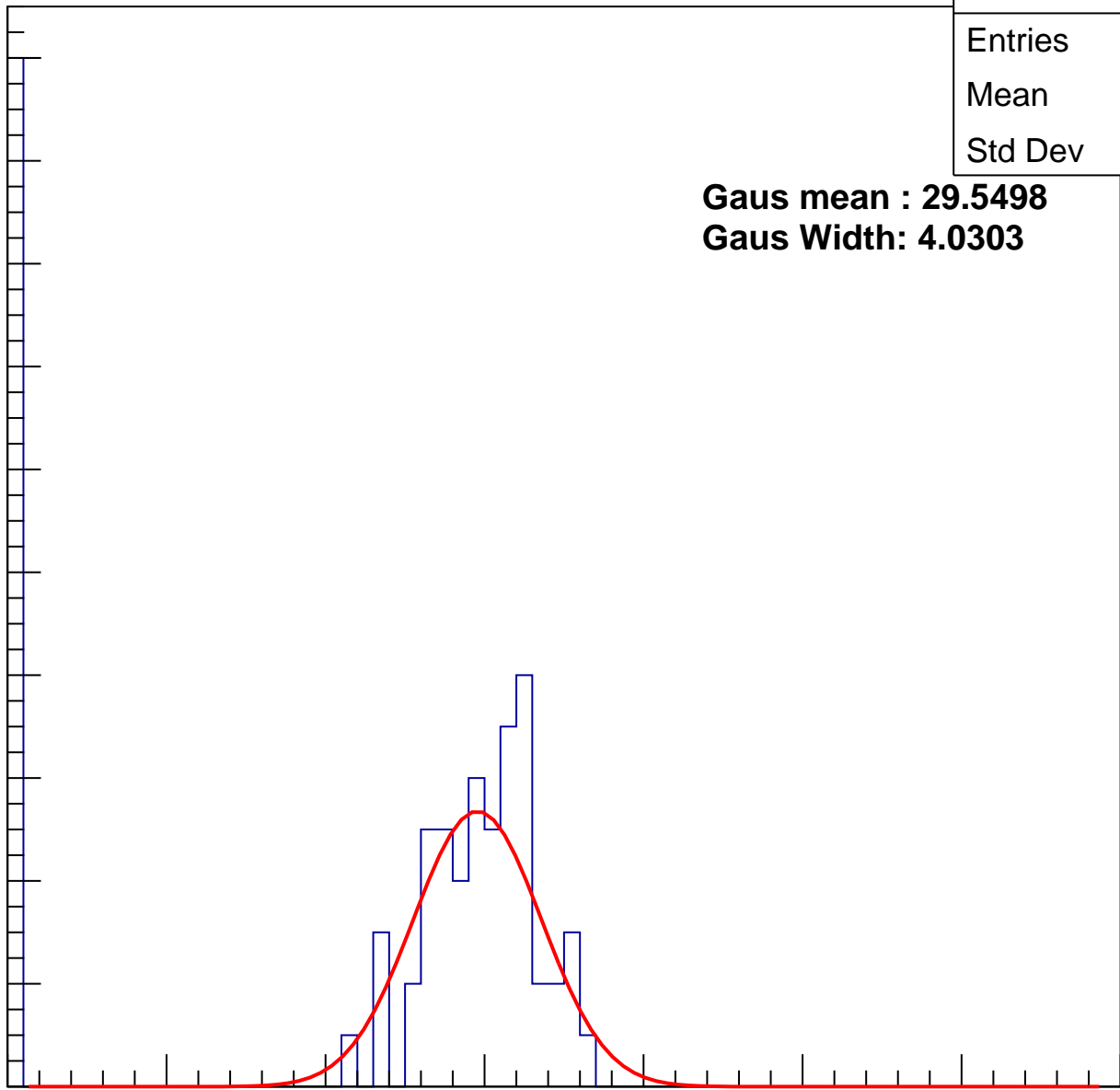
Gaus Width: 4.0303

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch53, adc1

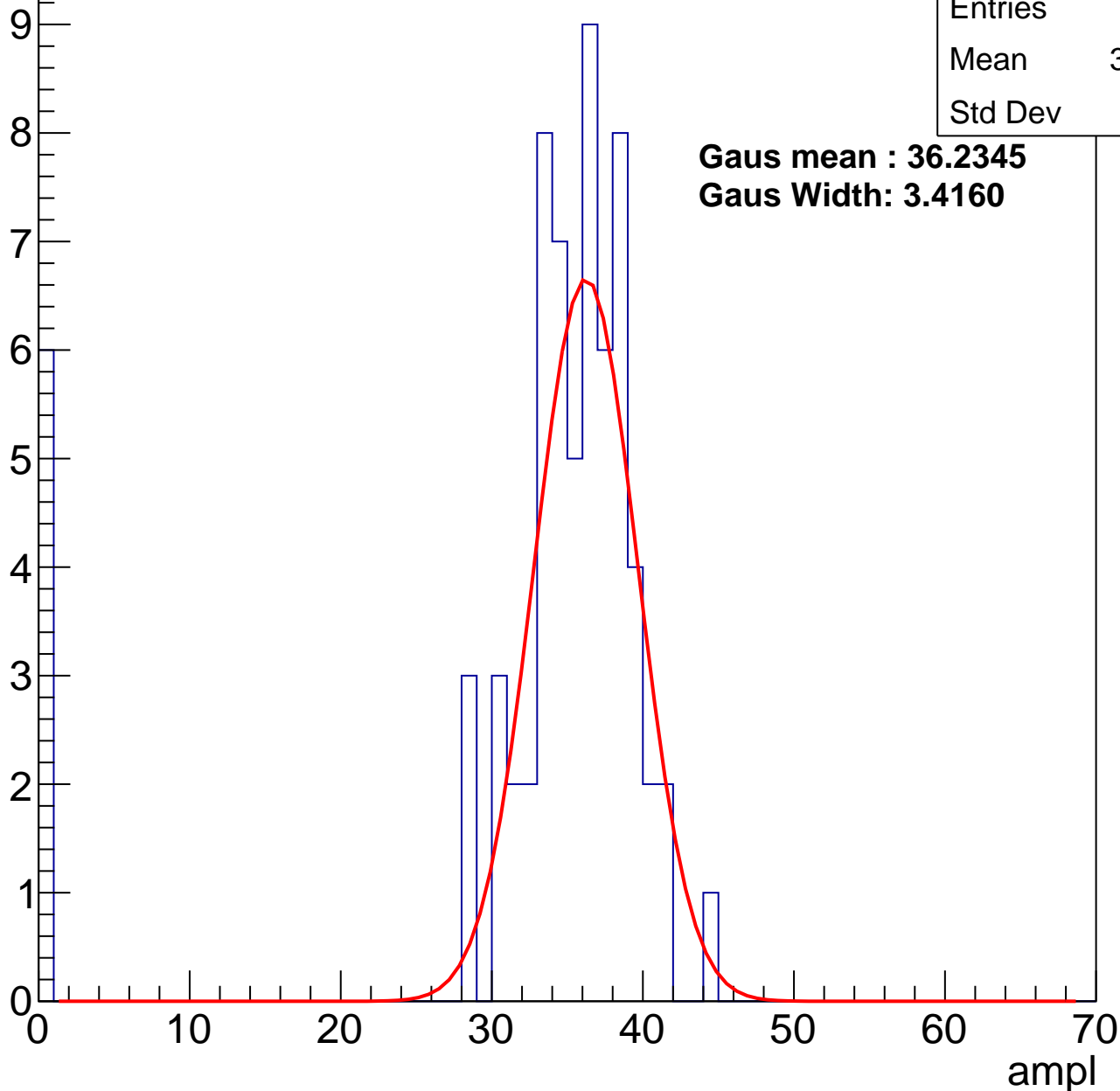
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	32.19
Std Dev	10.5

Gaus mean : 36.2345

Gaus Width: 3.4160



B1L103S, U18-ch53, adc2

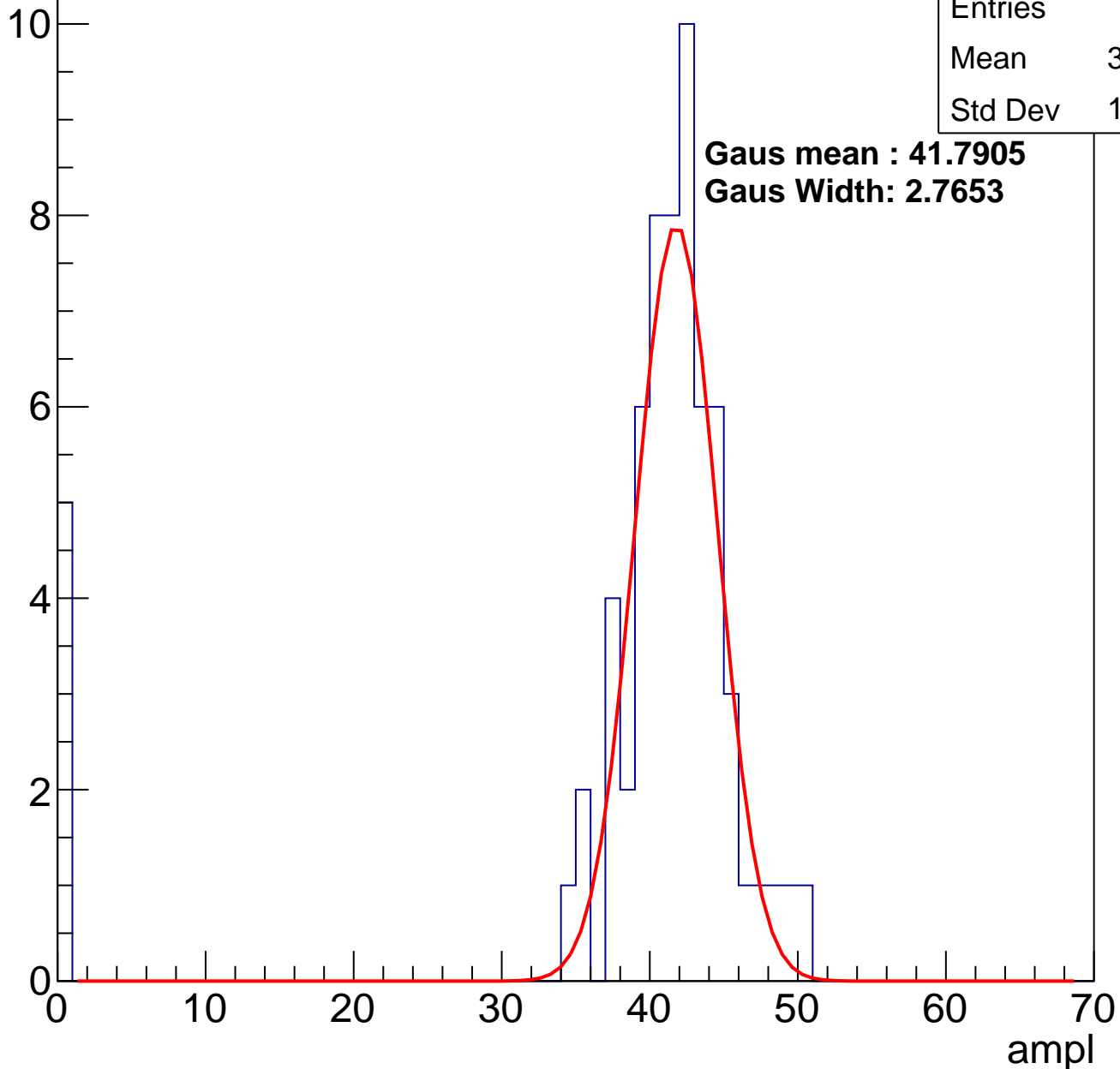
calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	38.29
Std Dev	11.38

Gaus mean : 41.7905

Gaus Width: 2.7653

Entry

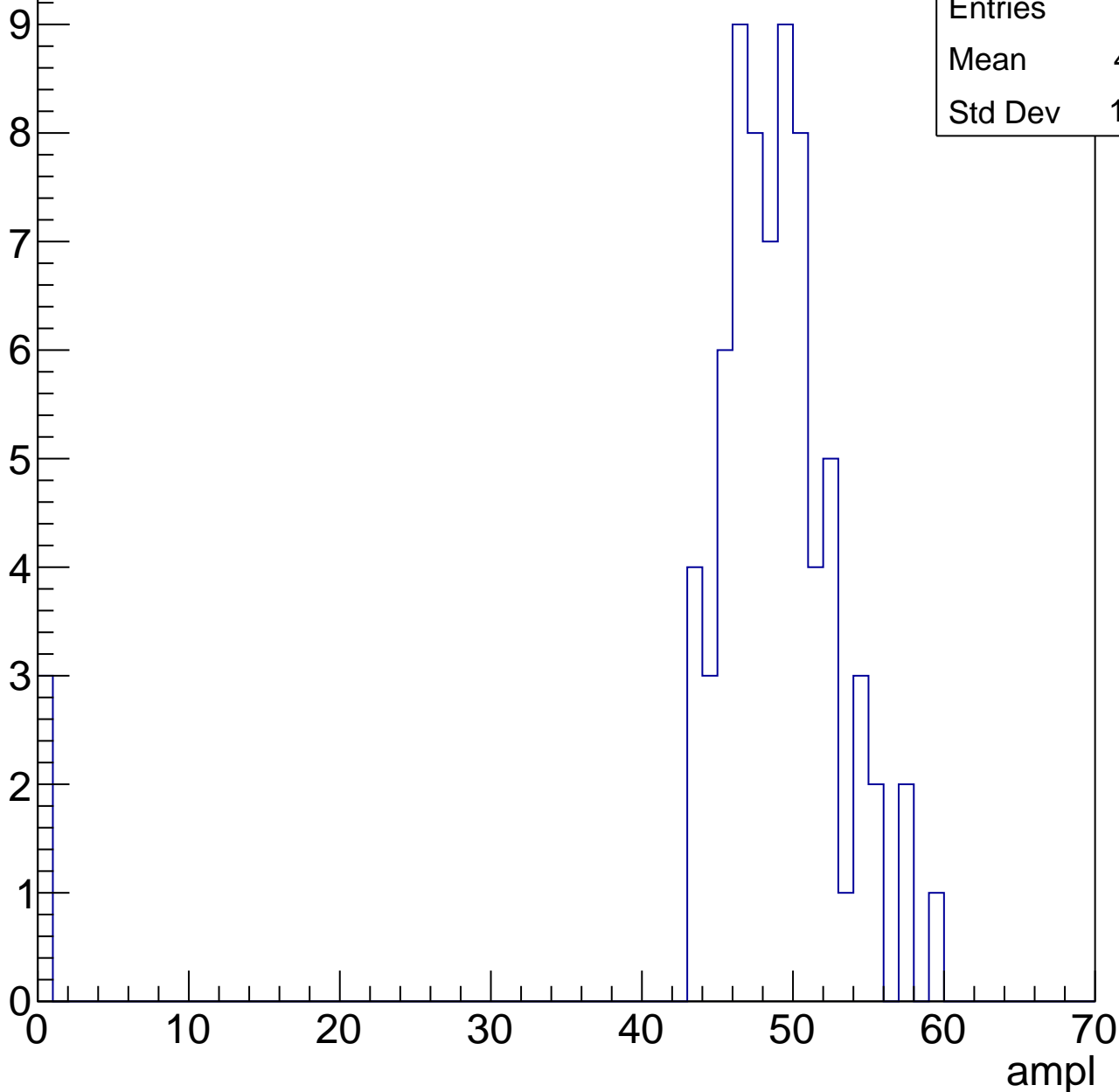


B1L103S, U18-ch53, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	46.71
Std Dev	10.13

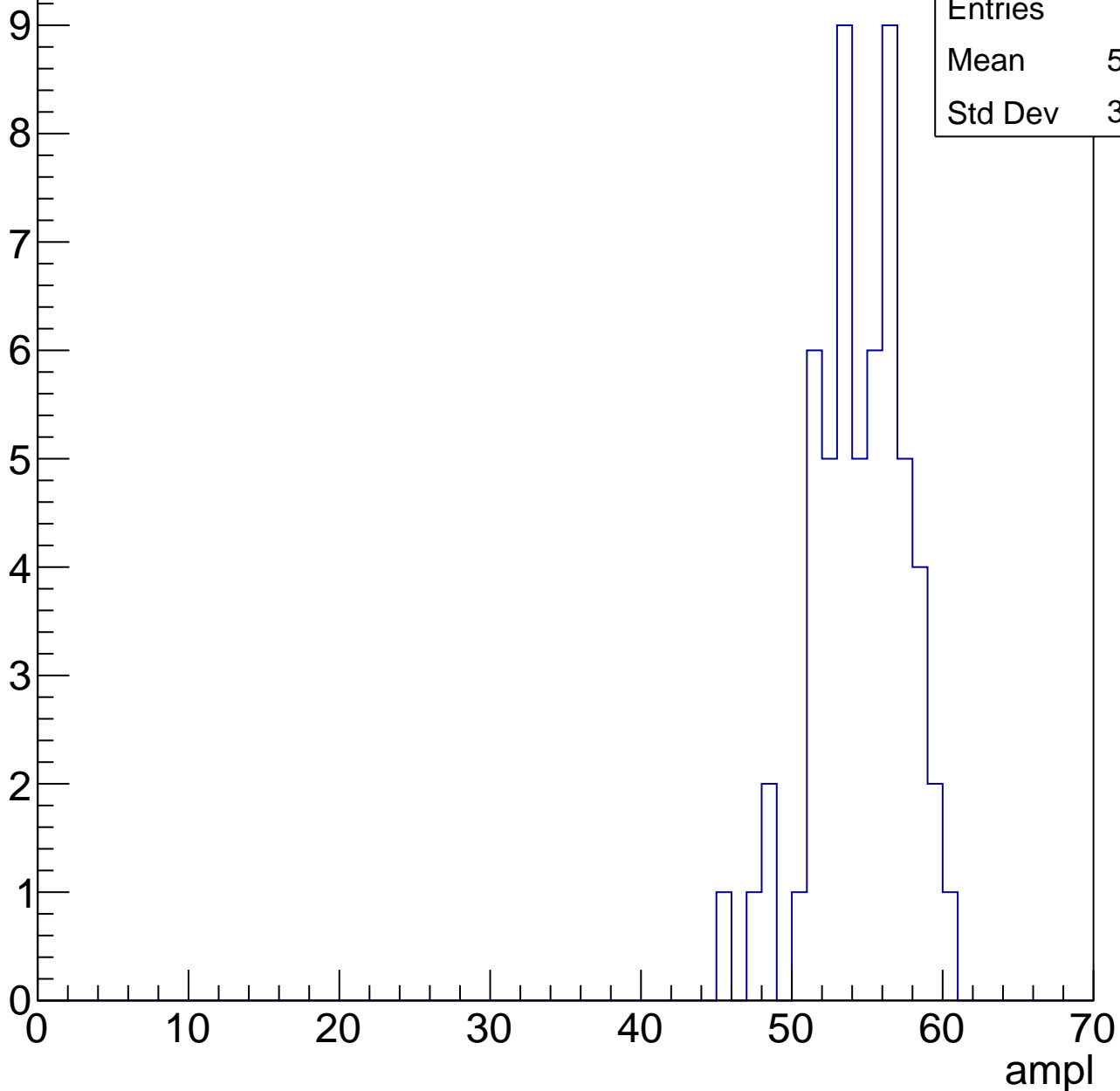


B1L103S, U18-ch53, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.04
Std Dev	3.072



B1L103S, U18-ch53, adc5

calib_packv5_041523_1651.root, FC#0, port C2

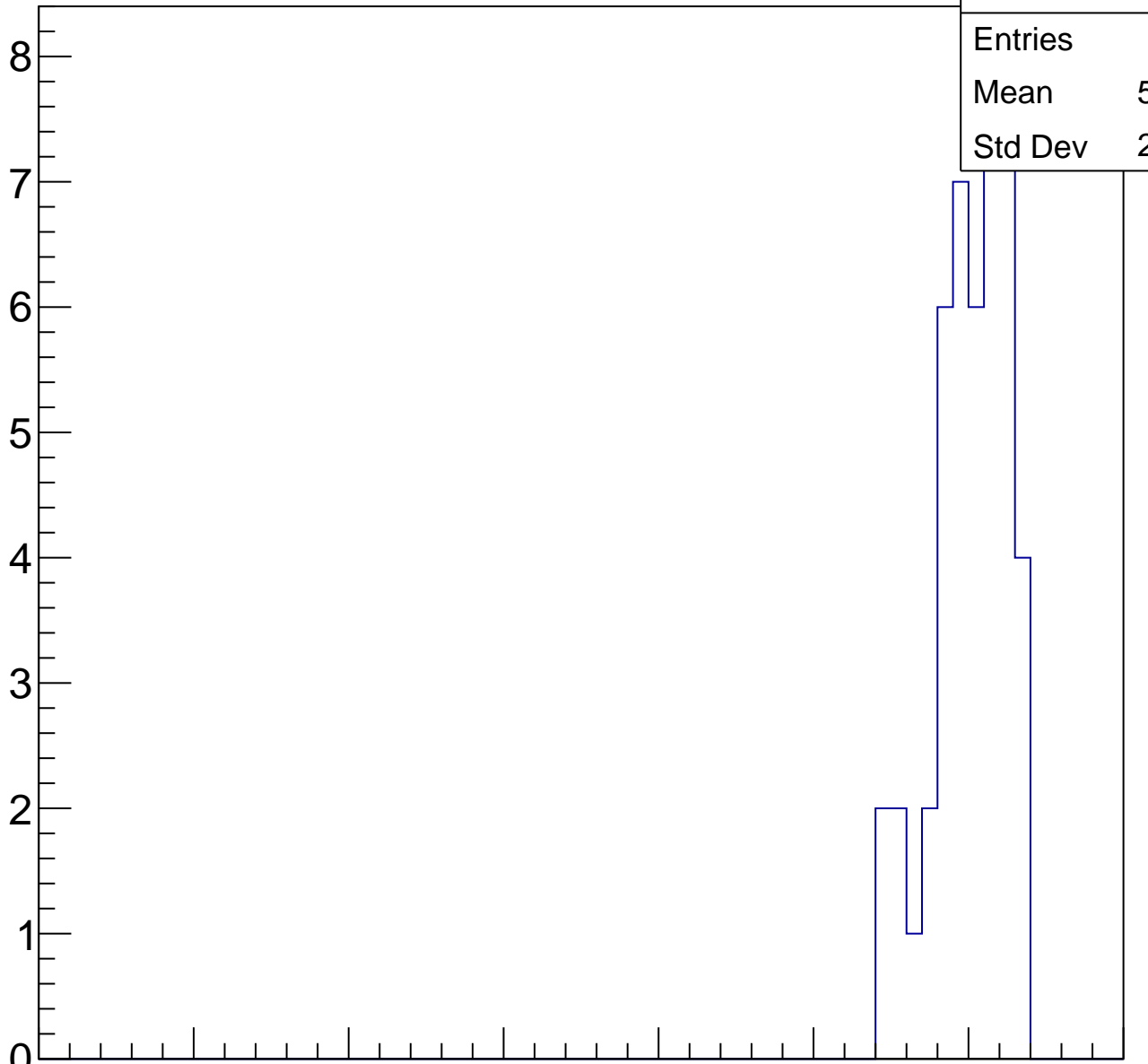
Entry

Entries	46
Mean	59.67
Std Dev	2.369

8
7
6
5
4
3
2
1
0

0 10 20 30 40 50 60 70

ampl

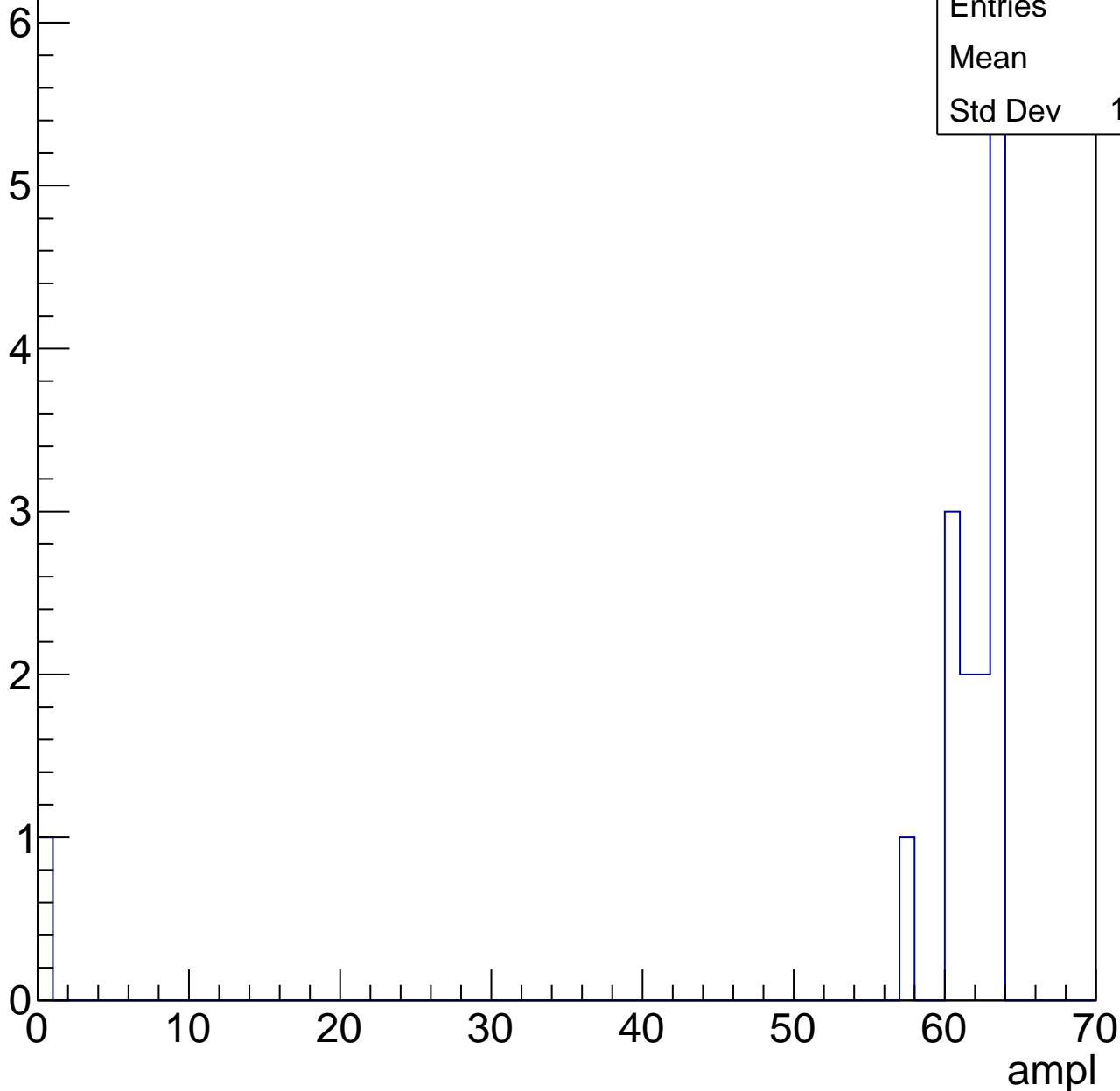


B1L103S, U18-ch53, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.4
Std Dev	15.43



B1L103S, U18-ch53, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch54, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	23.17
Std Dev	9.988

Gaus mean : 27.3666

Gaus Width: 3.4758

Entry

10

8

6

4

2

0

0

10

20

30

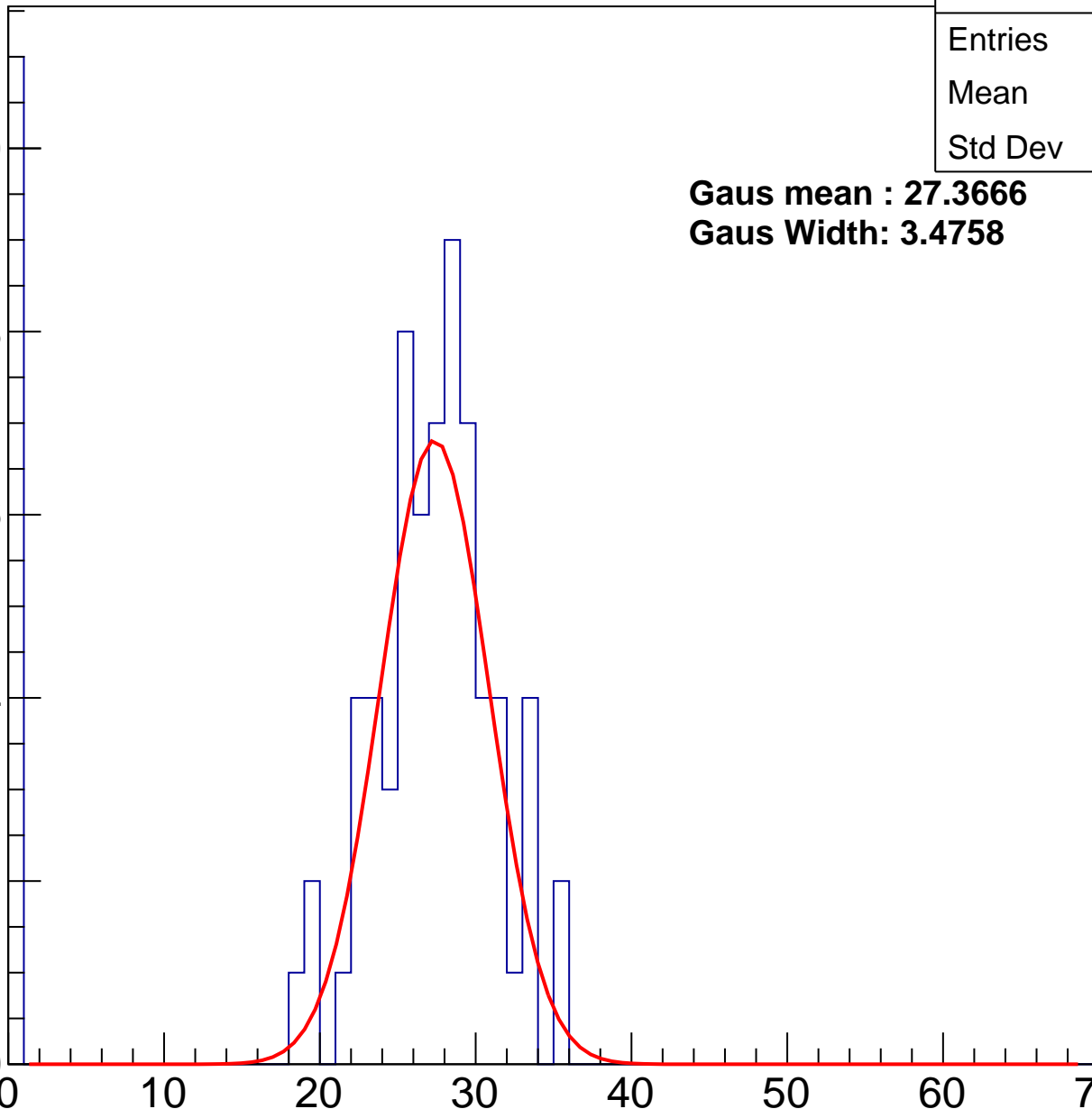
40

50

60

70

ampl



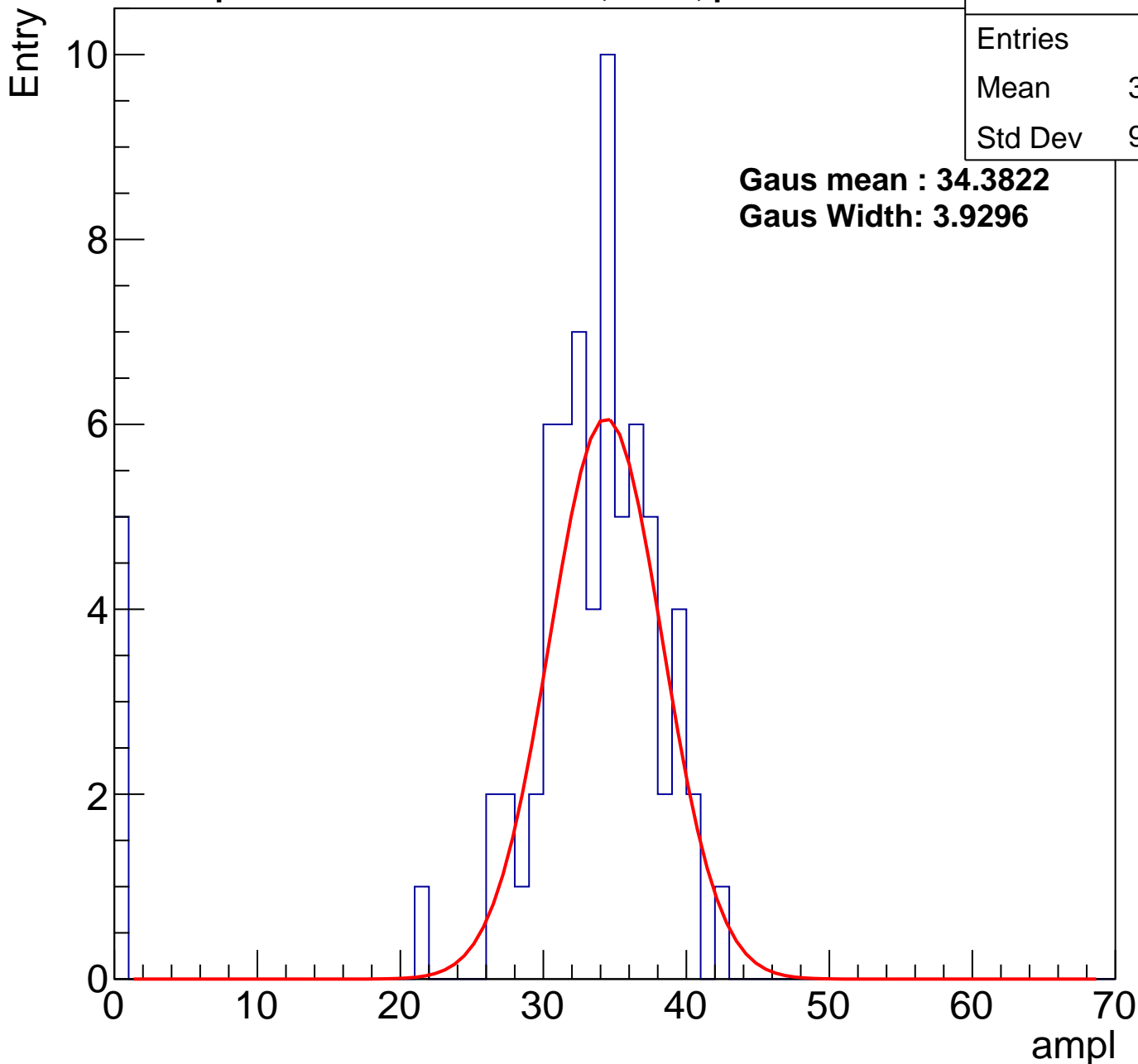
B1L103S, U18-ch54, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	31.06
Std Dev	9.322

Gaus mean : 34.3822

Gaus Width: 3.9296



B1L103S, U18-ch54, adc2

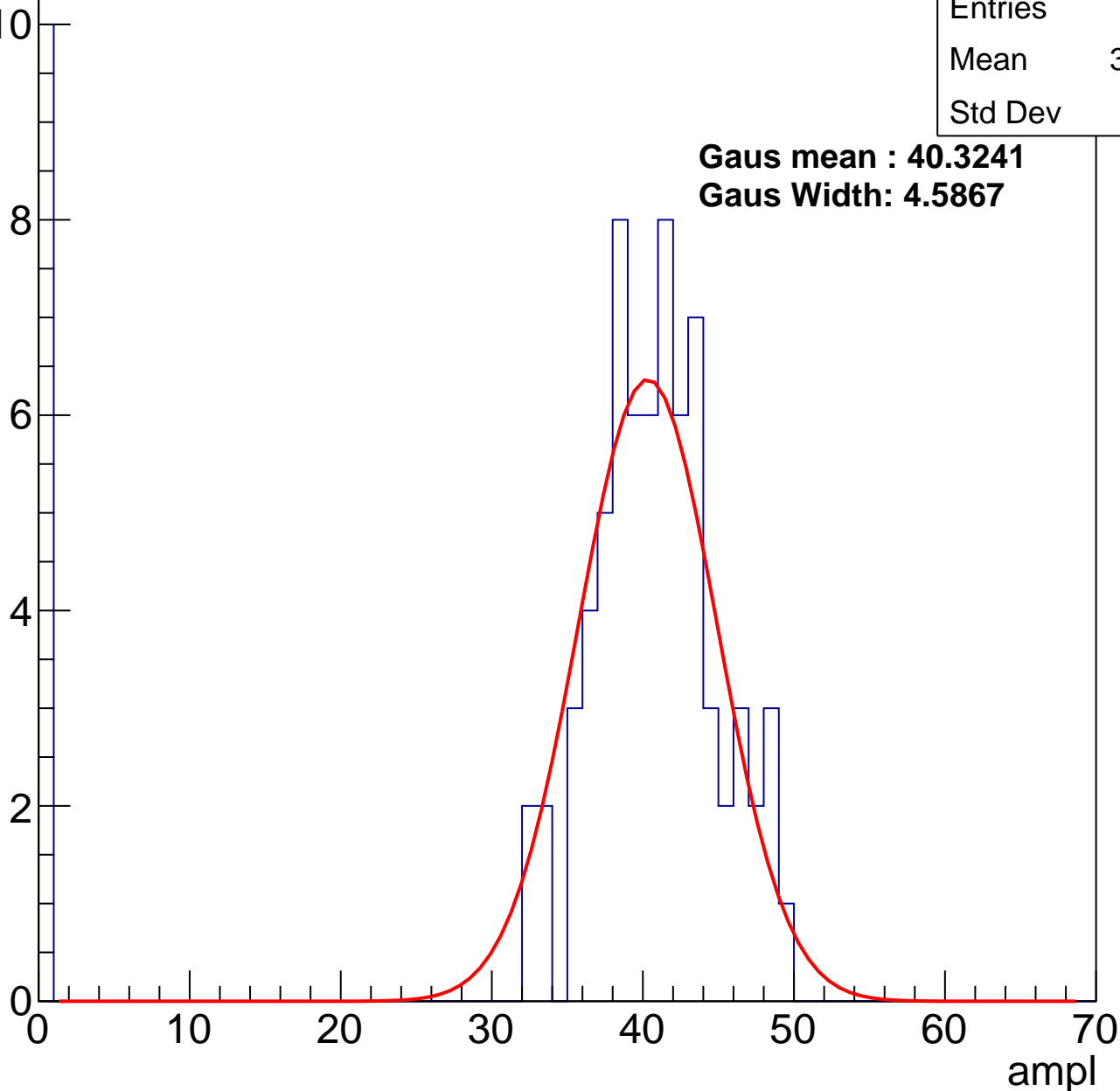
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	35.43
Std Dev	13.8

Gaus mean : 40.3241

Gaus Width: 4.5867

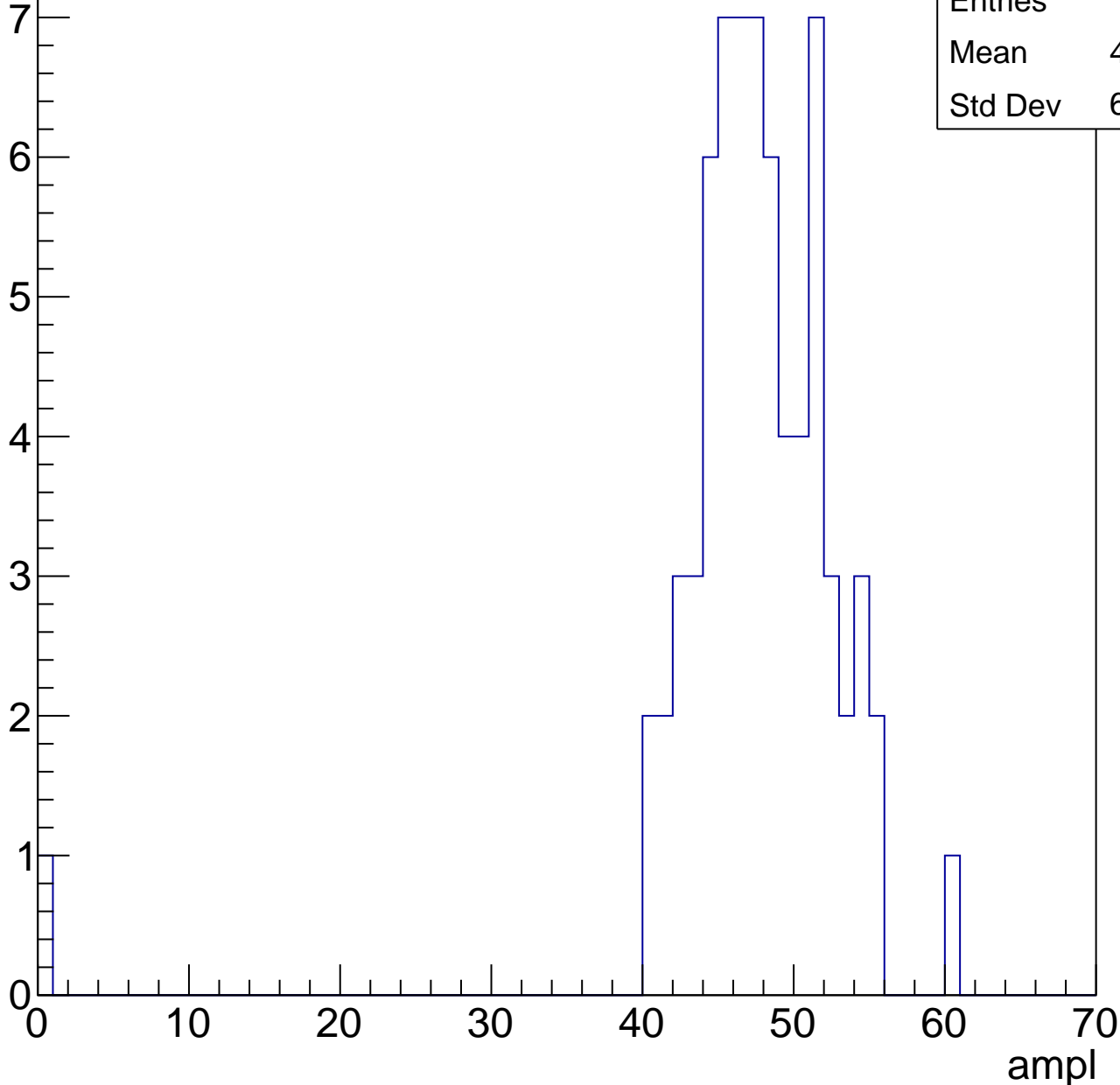


B1L103S, U18-ch54, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	46.89
Std Dev	6.919

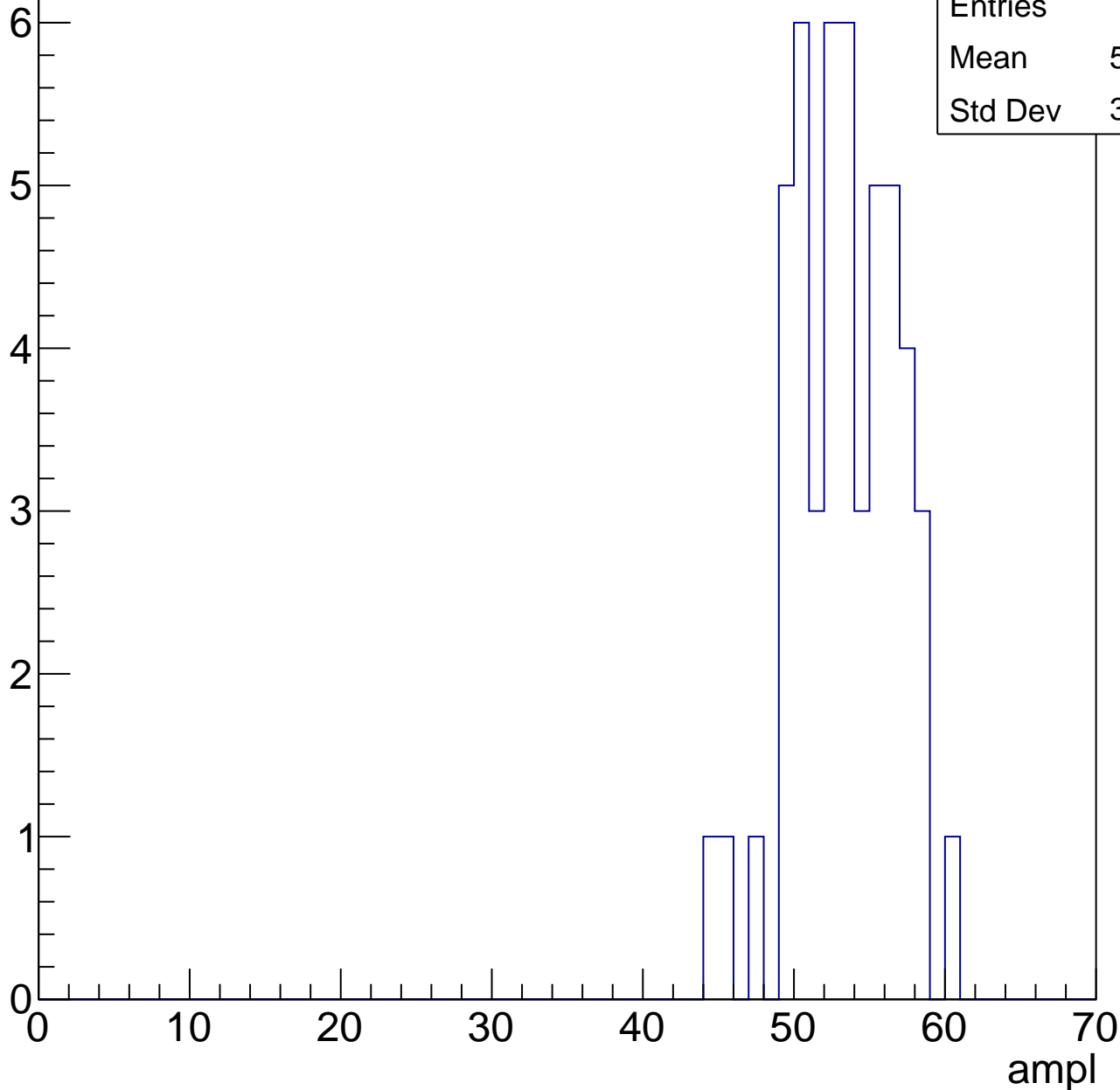


B1L103S, U18-ch54, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

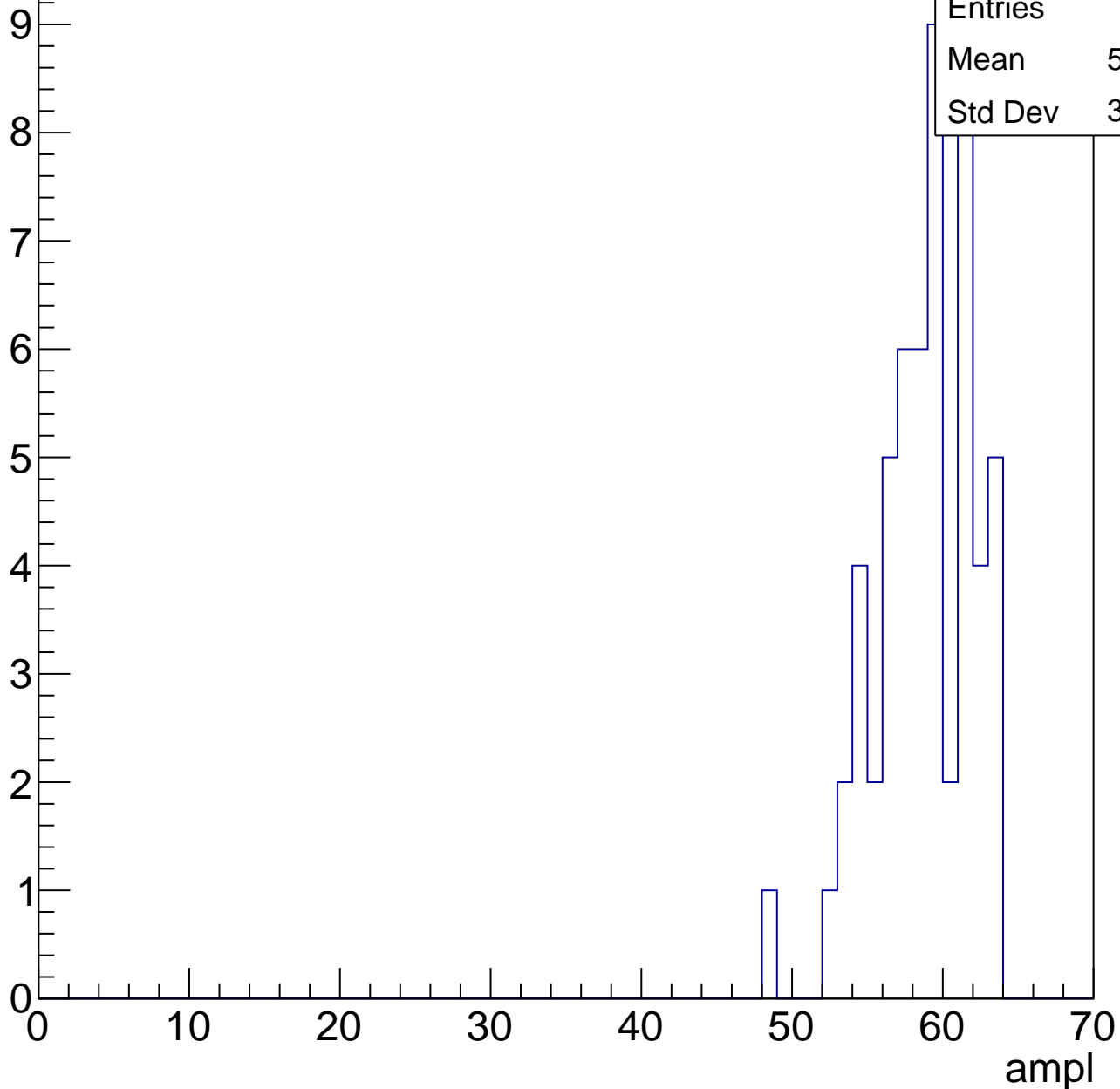
Entries	50
Mean	52.86
Std Dev	3.429



B1L103S, U18-ch54, adc5

calib_packv5_041523_1651.root, FC#0, port C2

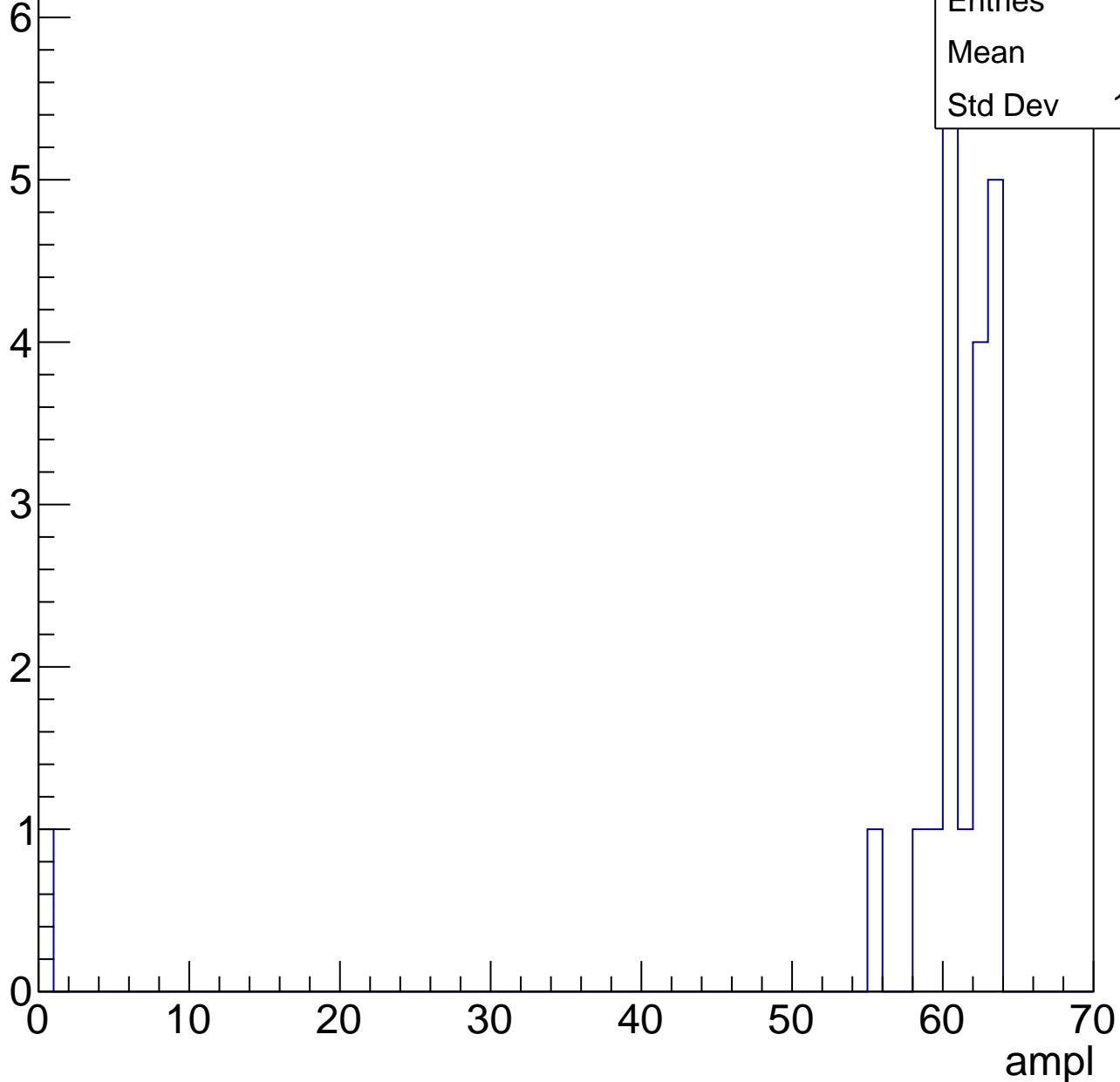
Entry



B1L103S, U18-ch54, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

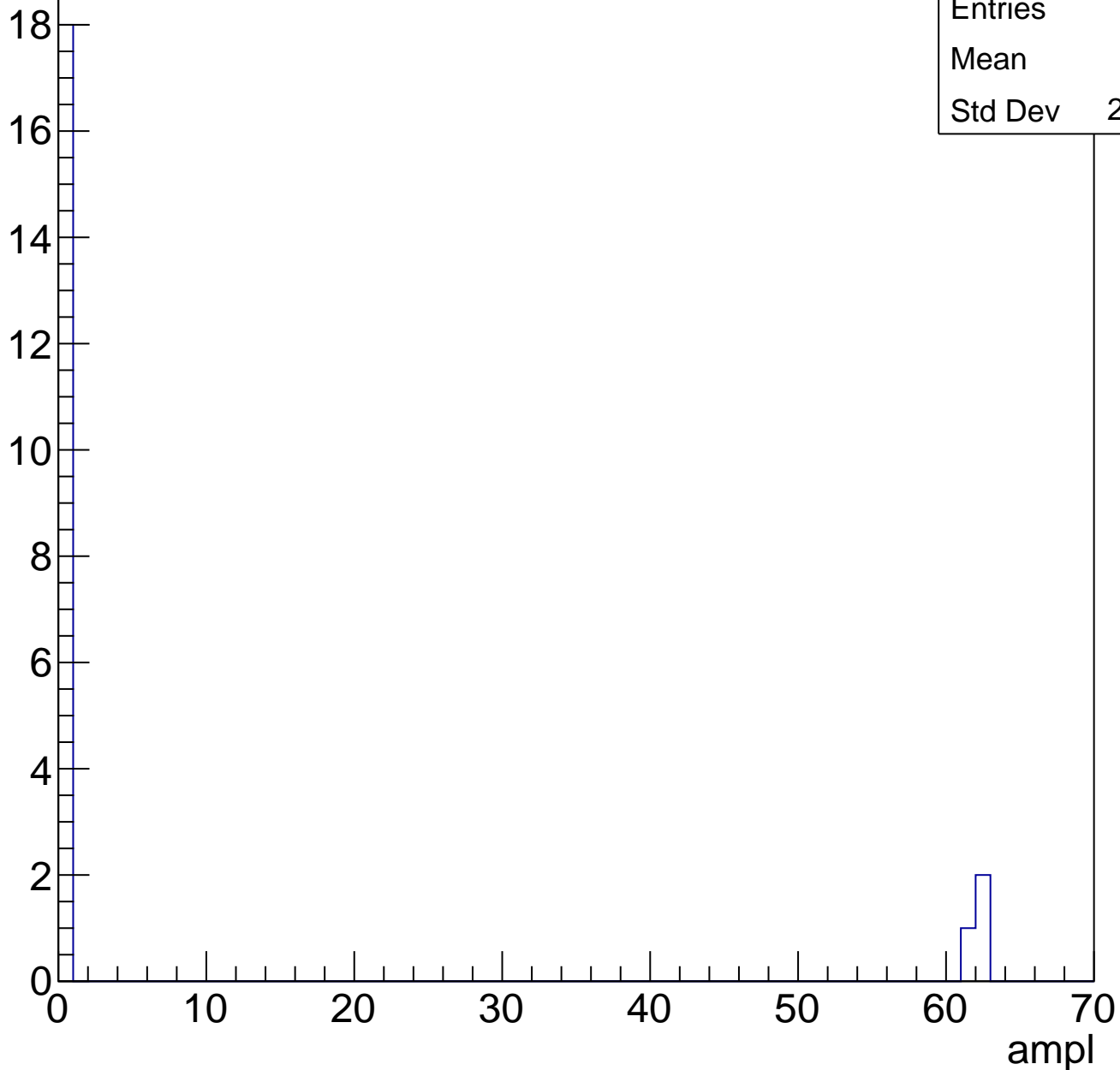


B1L103S, U18-ch54, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	8.81
Std Dev	21.58

Entry



B1L103S, U18-ch55, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	68
Mean	24.15
Std Dev	13.7

Gaus mean : 32.3681

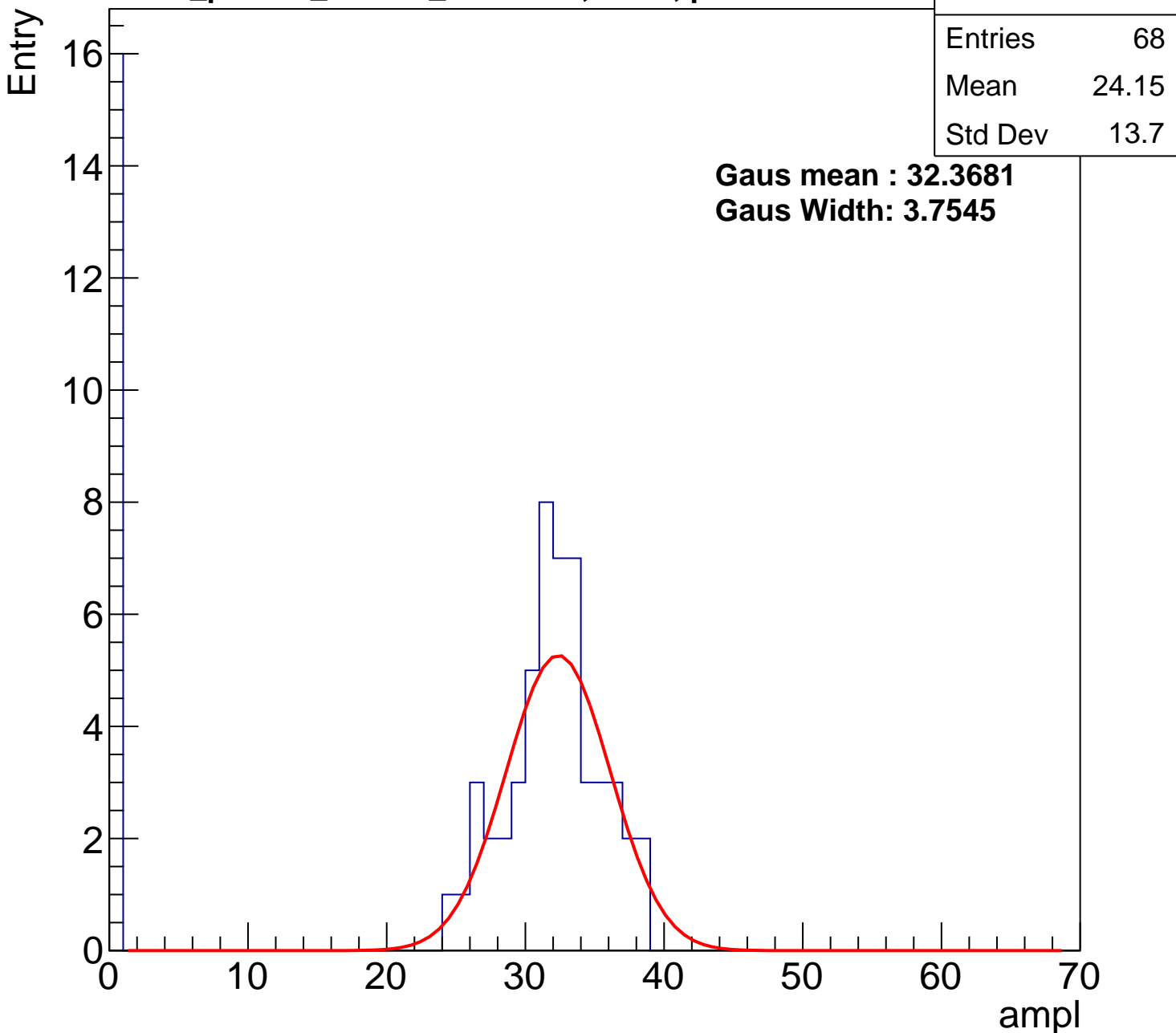
Gaus Width: 3.7545

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch55, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	37.64
Std Dev	6.725

Gaus mean : 39.123

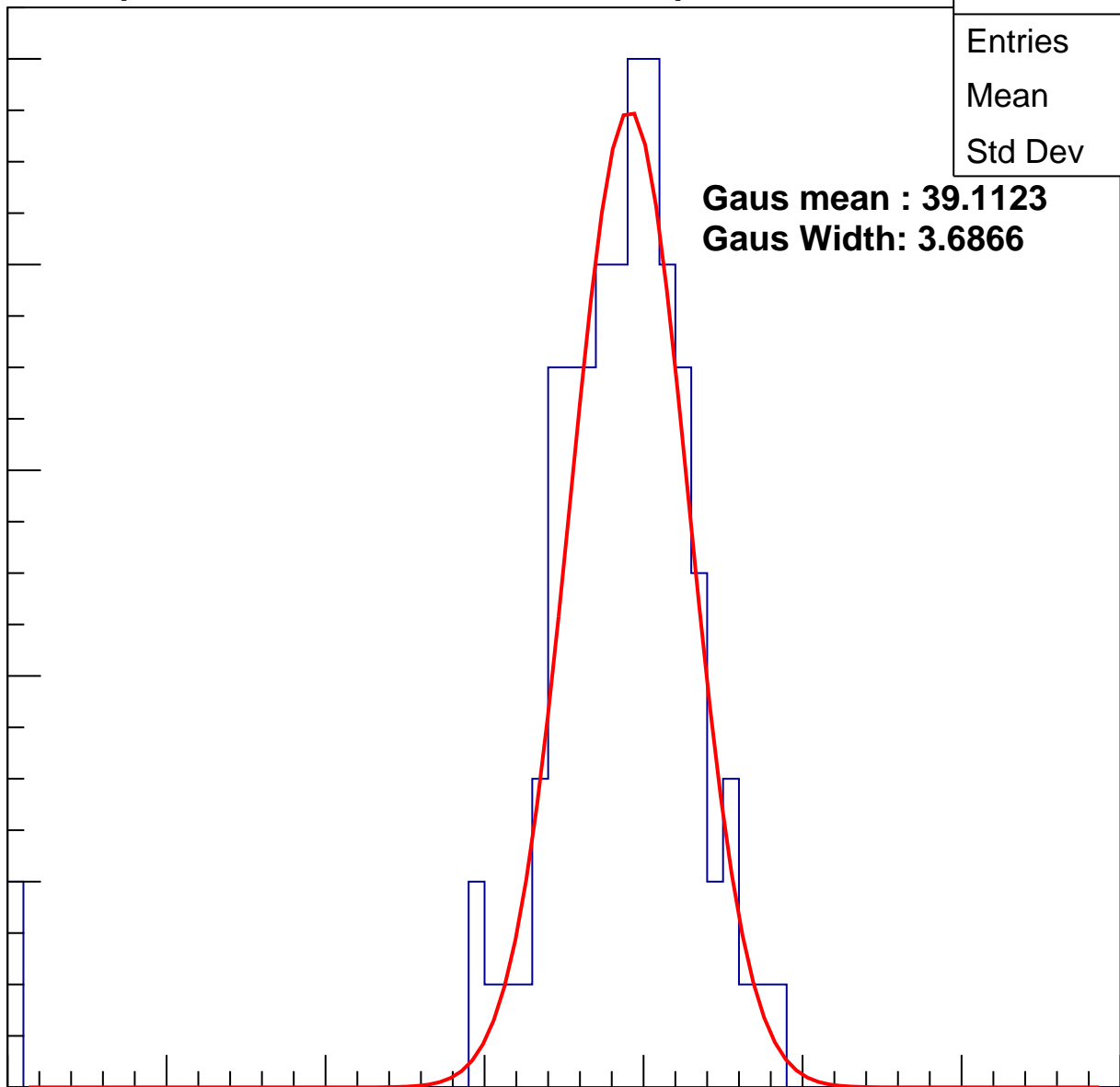
Gaus Width: 3.6866

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch55, adc2

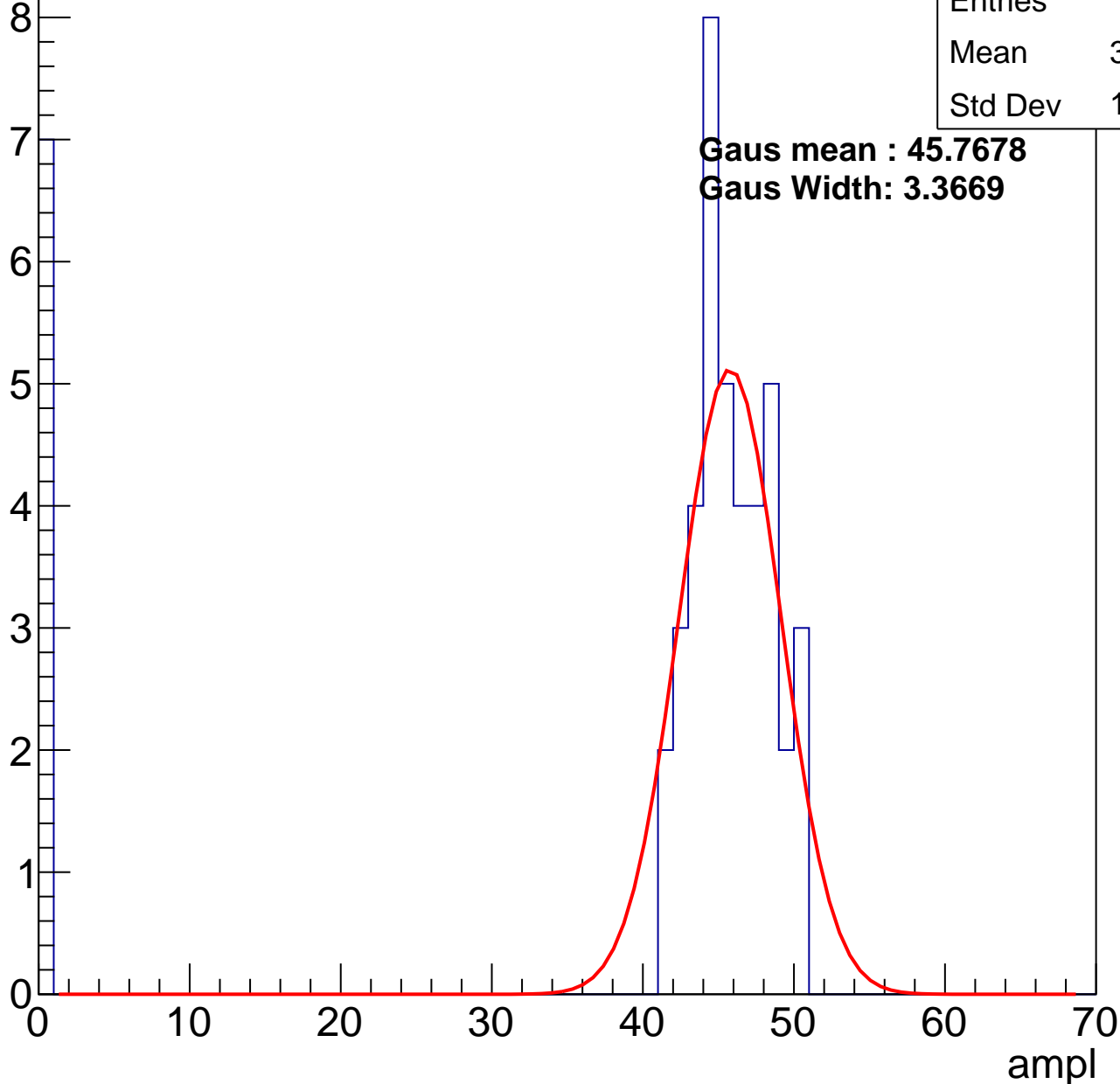
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	38.66
Std Dev	16.33

Gaus mean : 45.7678

Gaus Width: 3.3669

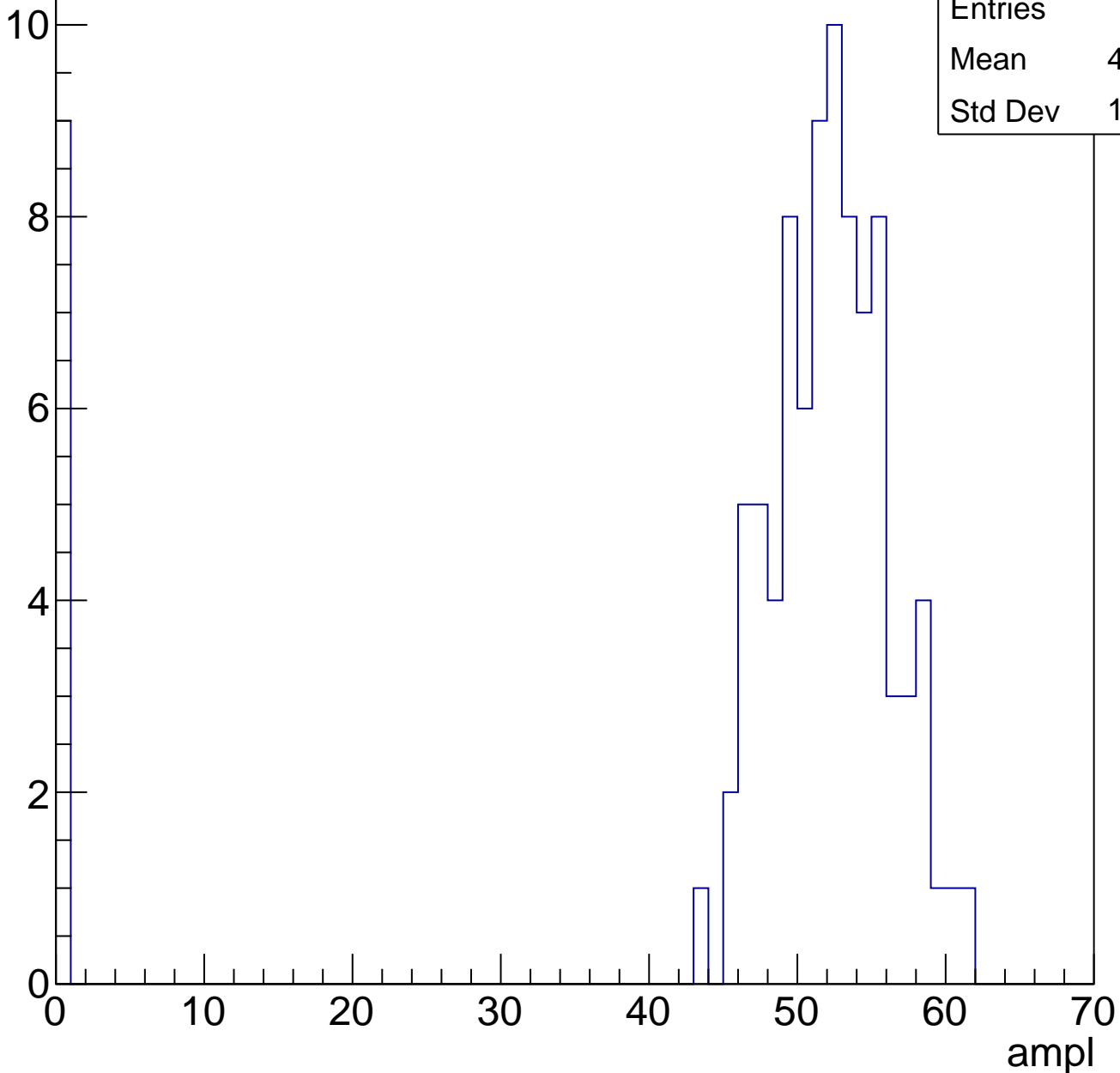


B1L103S, U18-ch55, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	46.88
Std Dev	15.58

Entry

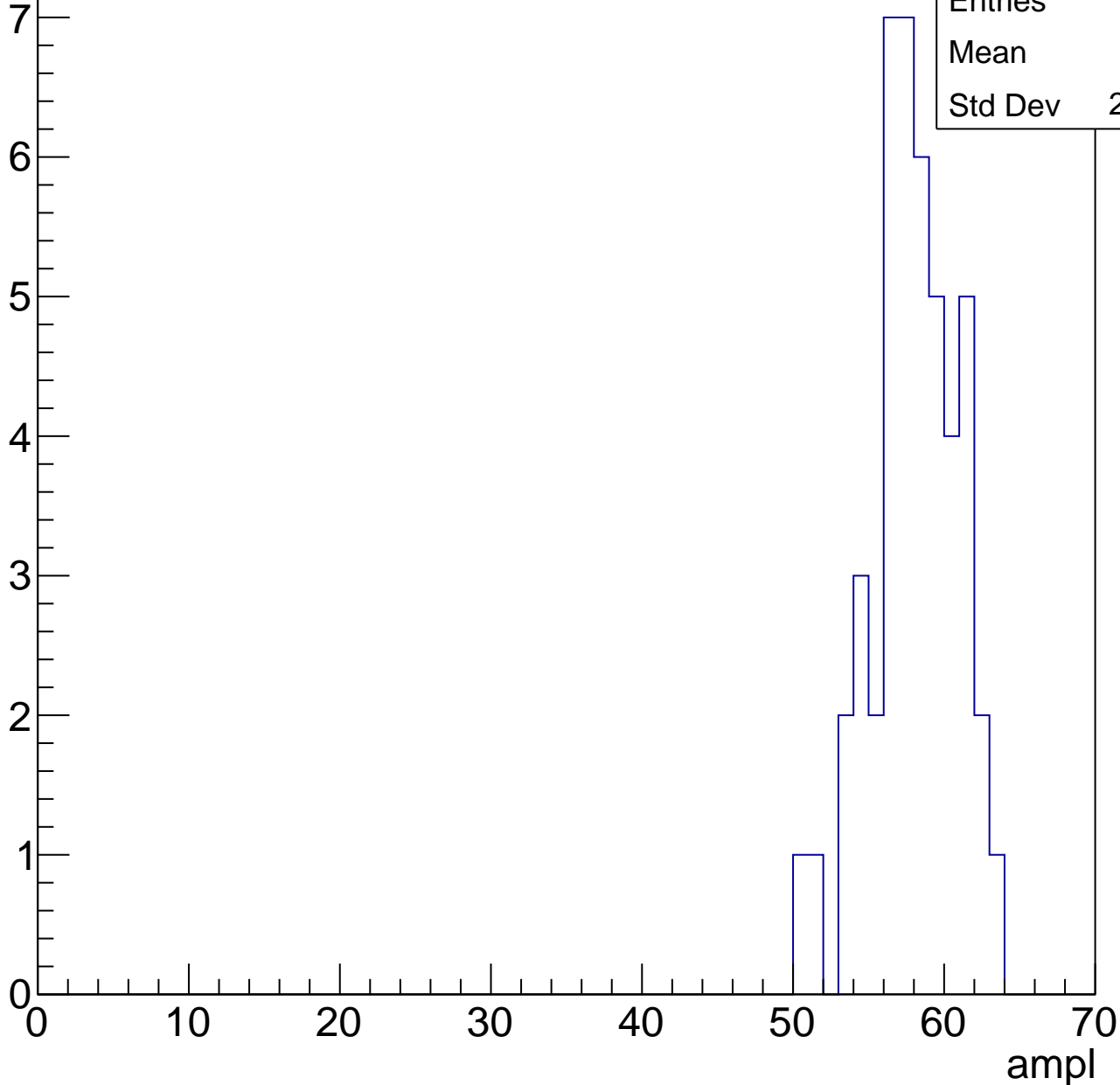


B1L103S, U18-ch55, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	57.5
Std Dev	2.857

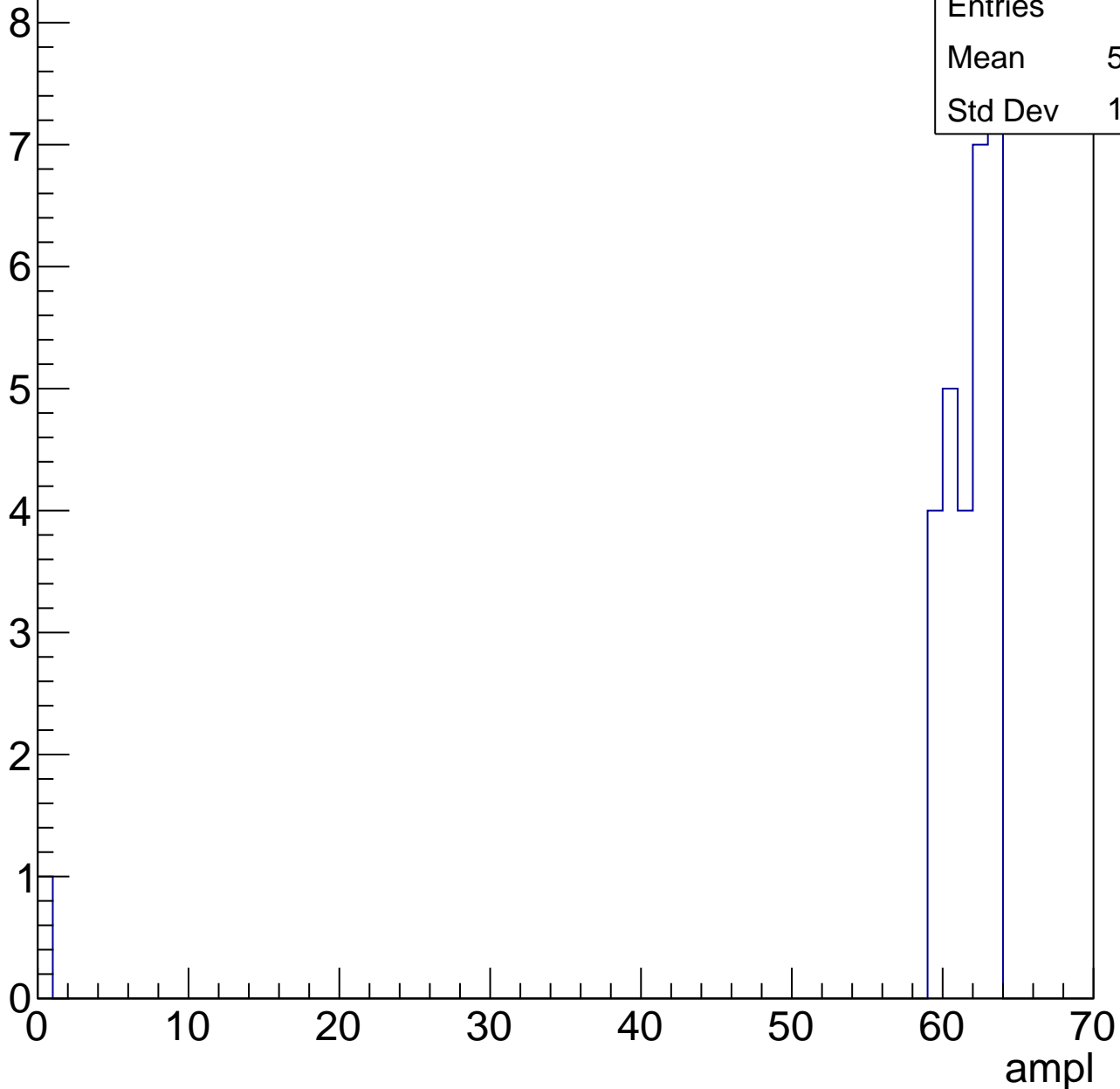


B1L103S, U18-ch55, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	59.24
Std Dev	11.28



B1L103S, U18-ch55, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch55, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch56, adc0

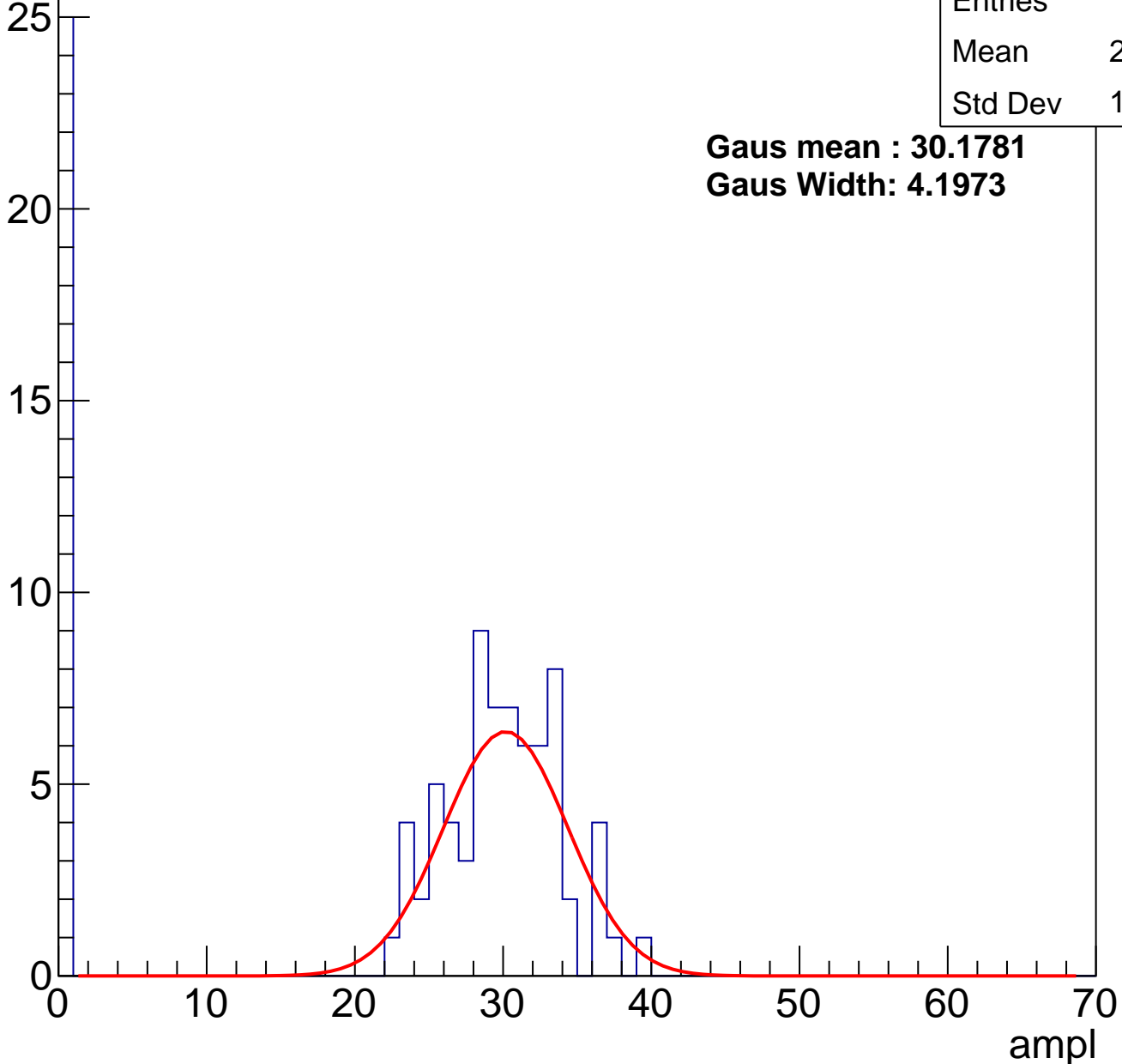
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	21.76
Std Dev	13.39

Gaus mean : 30.1781

Gaus Width: 4.1973

Entry



B1L103S, U18-ch56, adc1

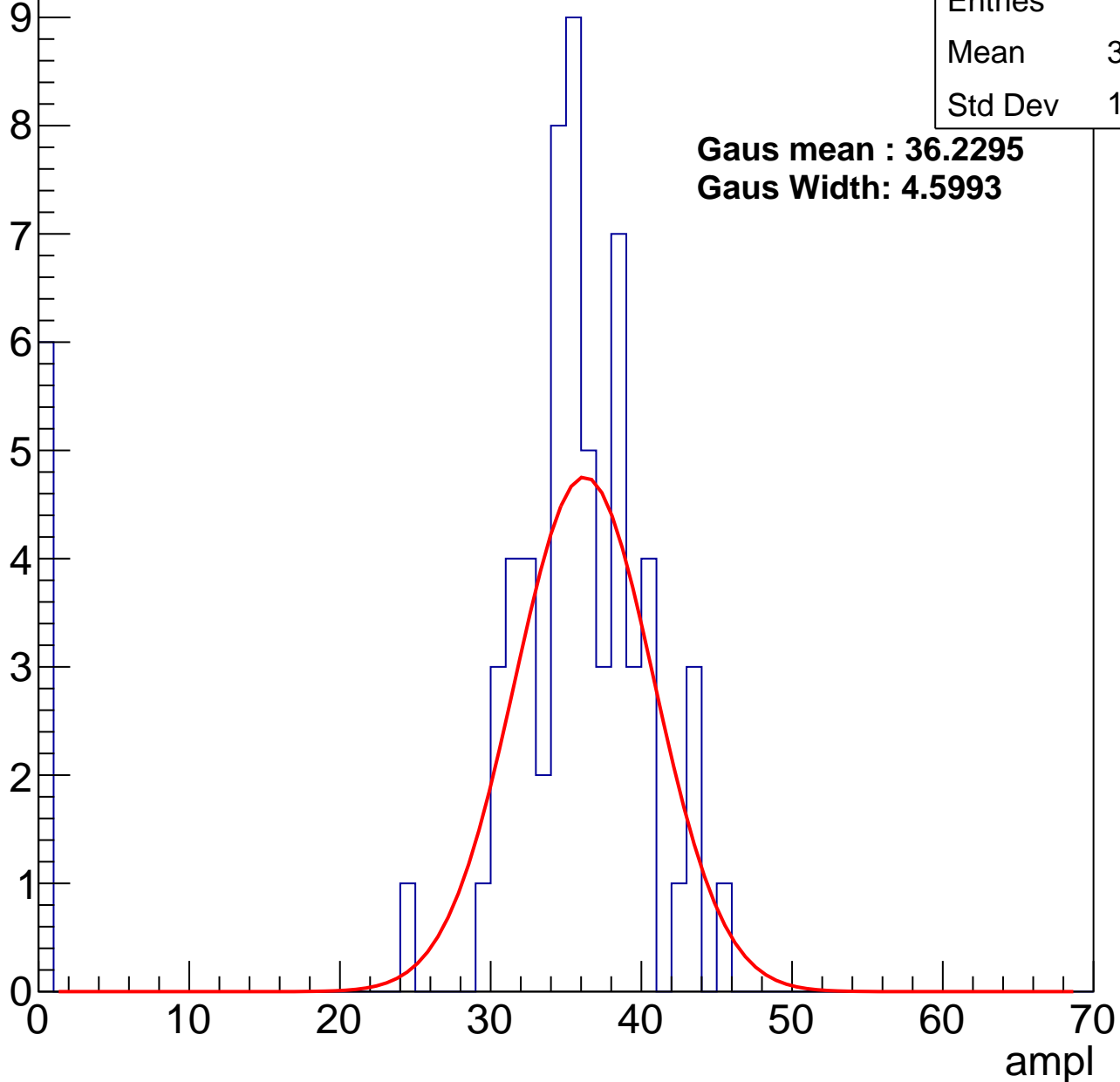
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	32.28
Std Dev	10.94

Gaus mean : 36.2295

Gaus Width: 4.5993



B1L103S, U18-ch56, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	38.14
Std Dev	12.38

Gaus mean : 42.5209

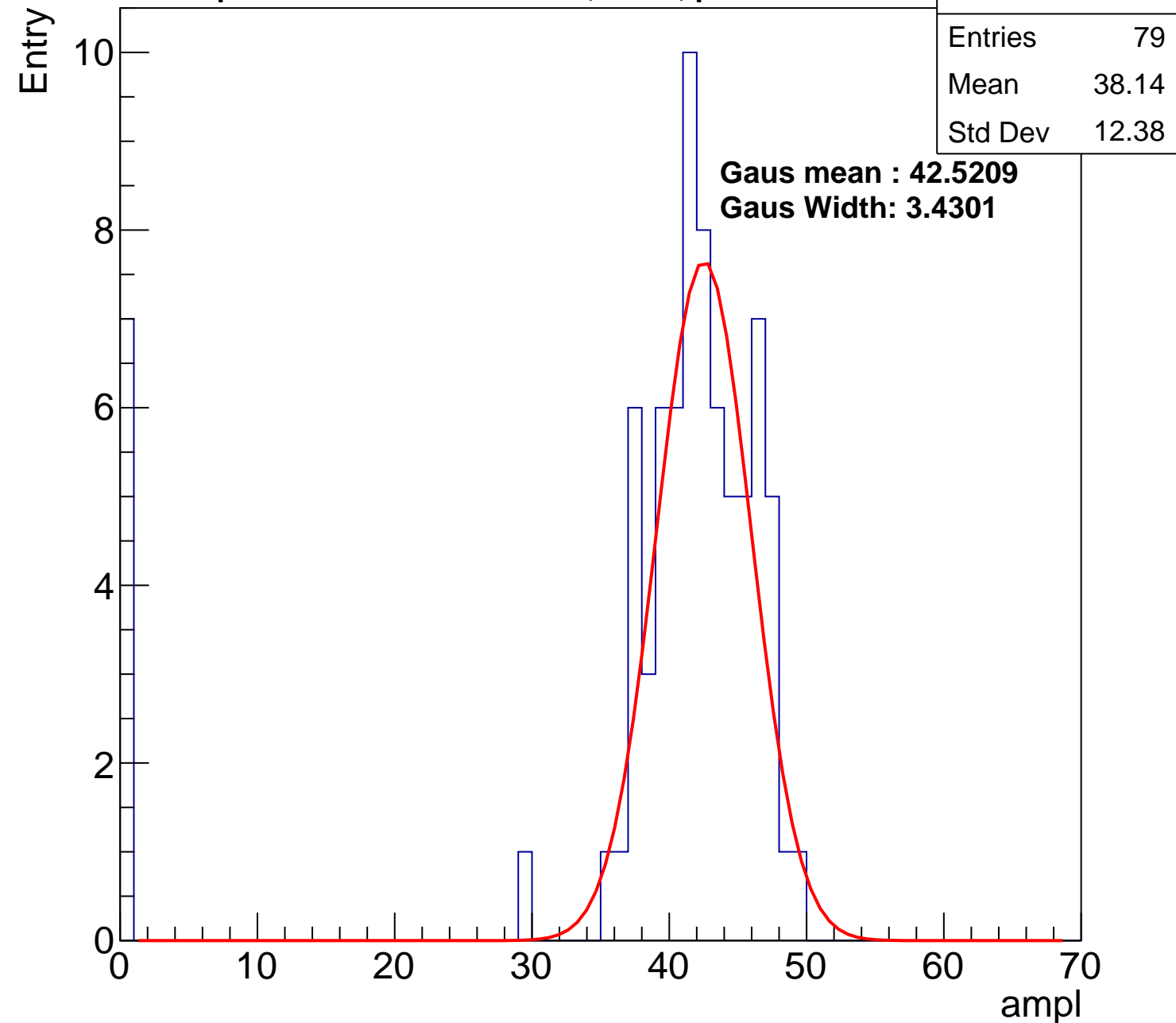
Gaus Width: 3.4301

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

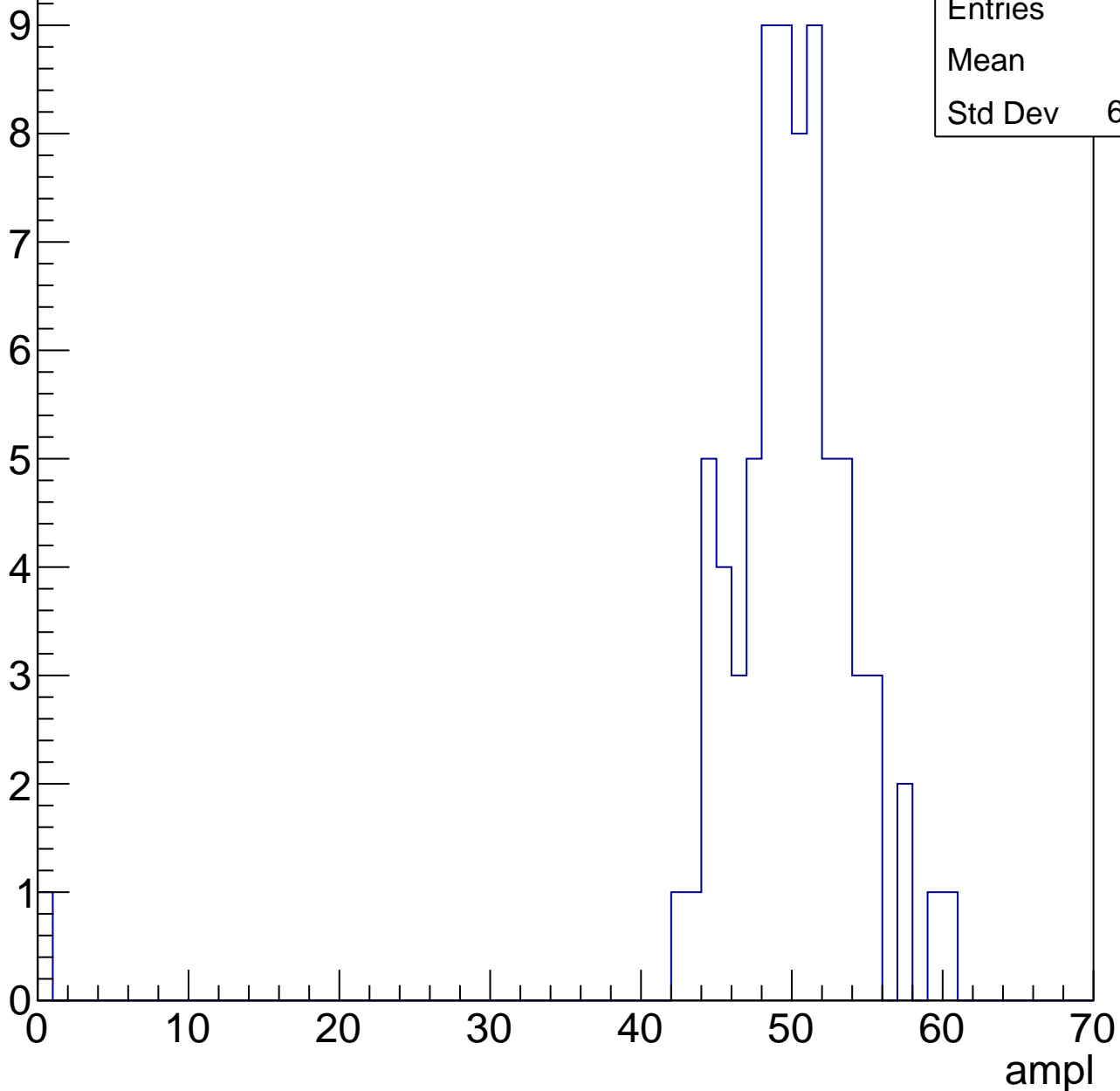


B1L103S, U18-ch56, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	49
Std Dev	6.765

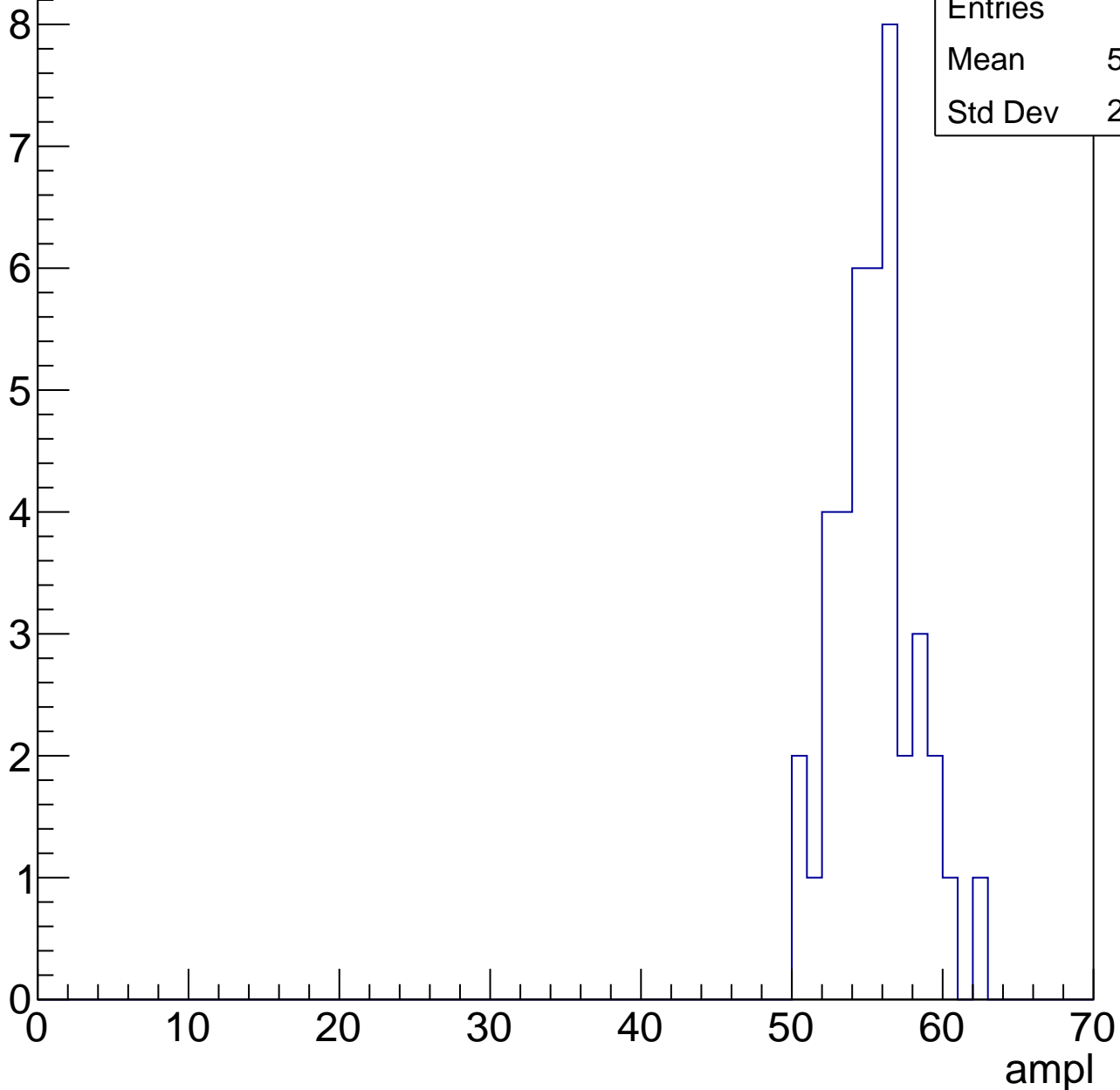


B1L103S, U18-ch56, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

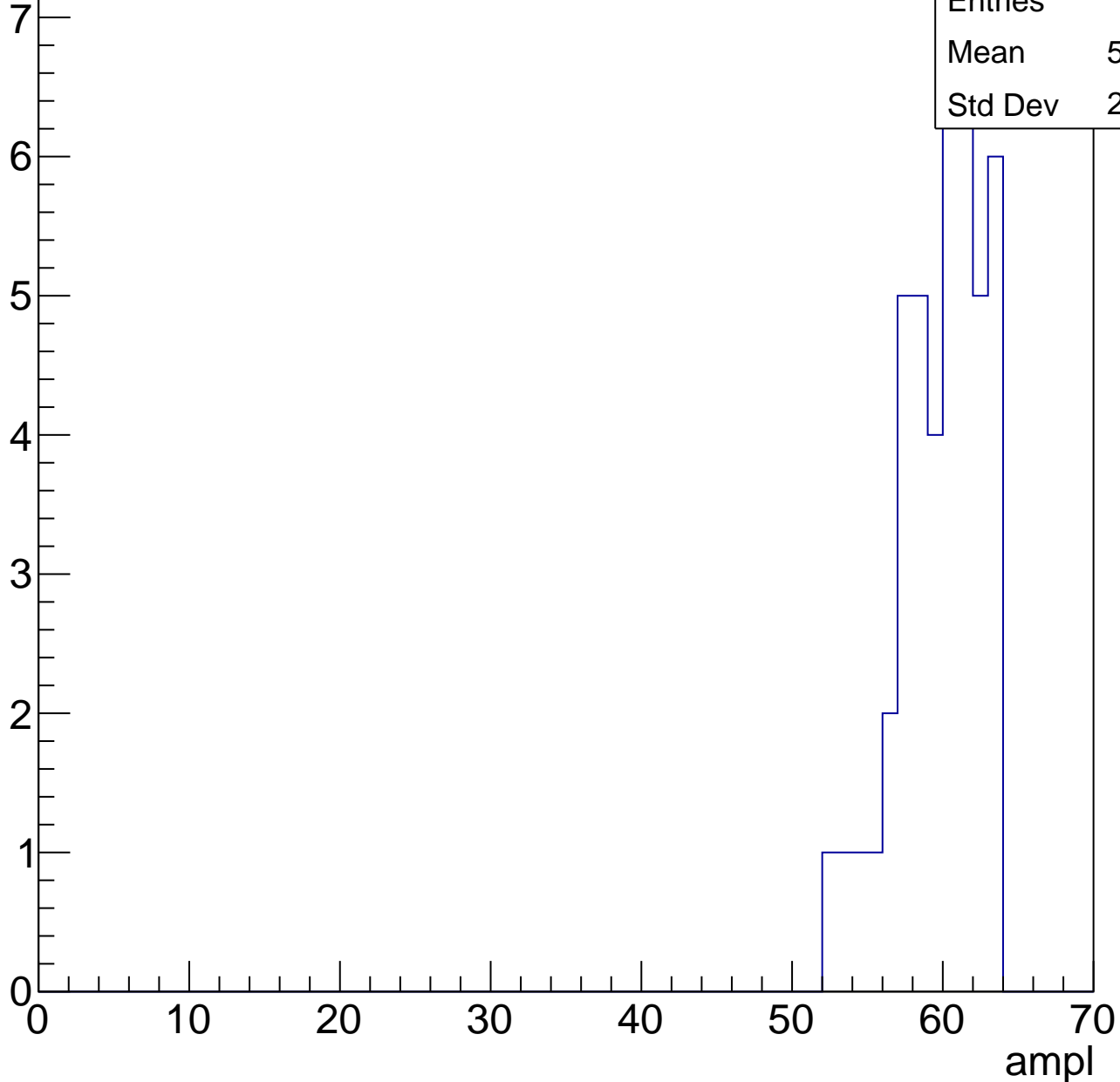
Entries	40
Mean	55.02
Std Dev	2.612



B1L103S, U18-ch56, adc5

calib_packv5_041523_1651.root, FC#0, port C2

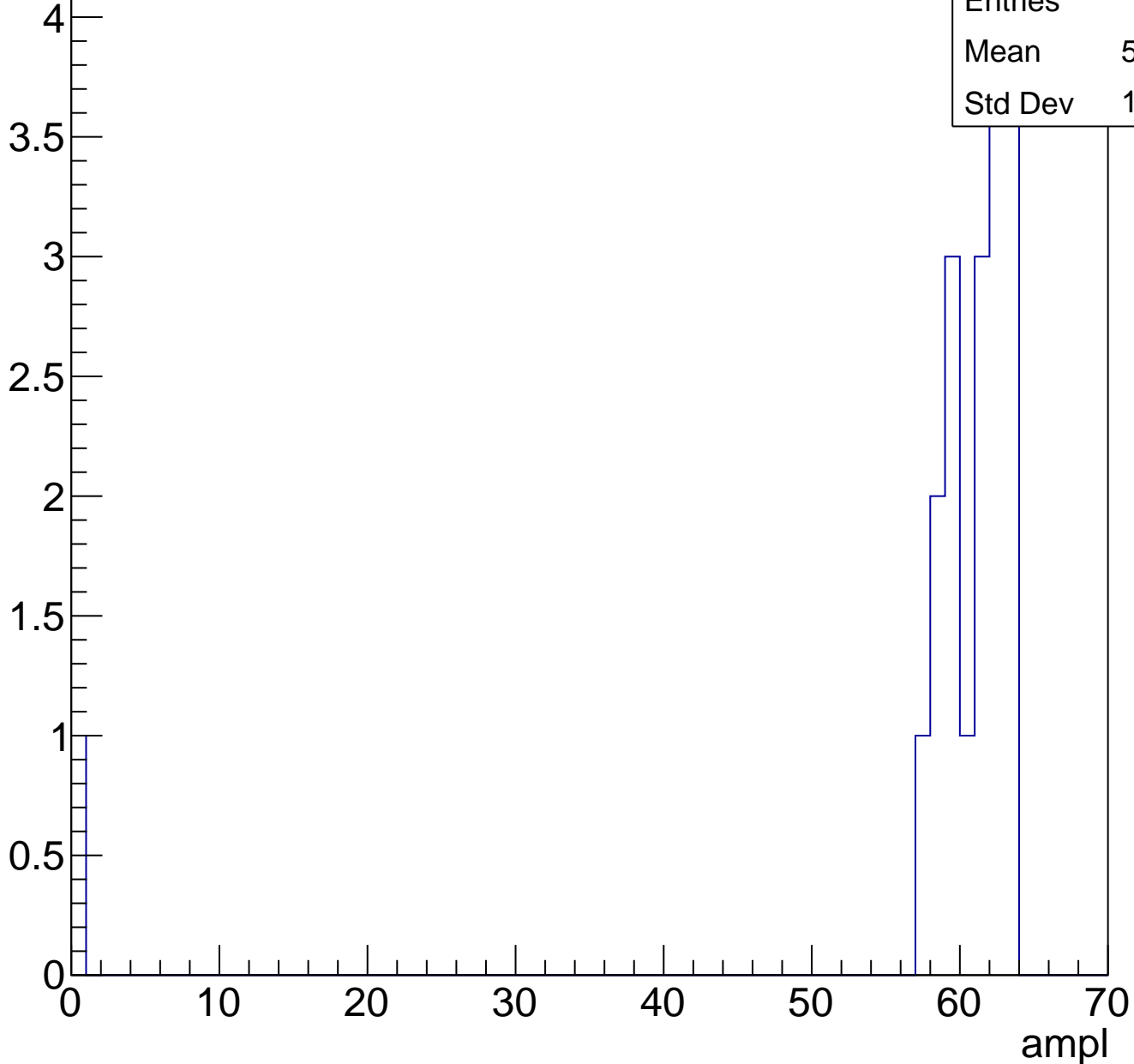
Entry



B1L103S, U18-ch56, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch56, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch57, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	24.53
Std Dev	11.17

Gaus mean : 29.8778

Gaus Width: 4.0727

Entry

12

10

8

6

4

2

0

0

10

20

30

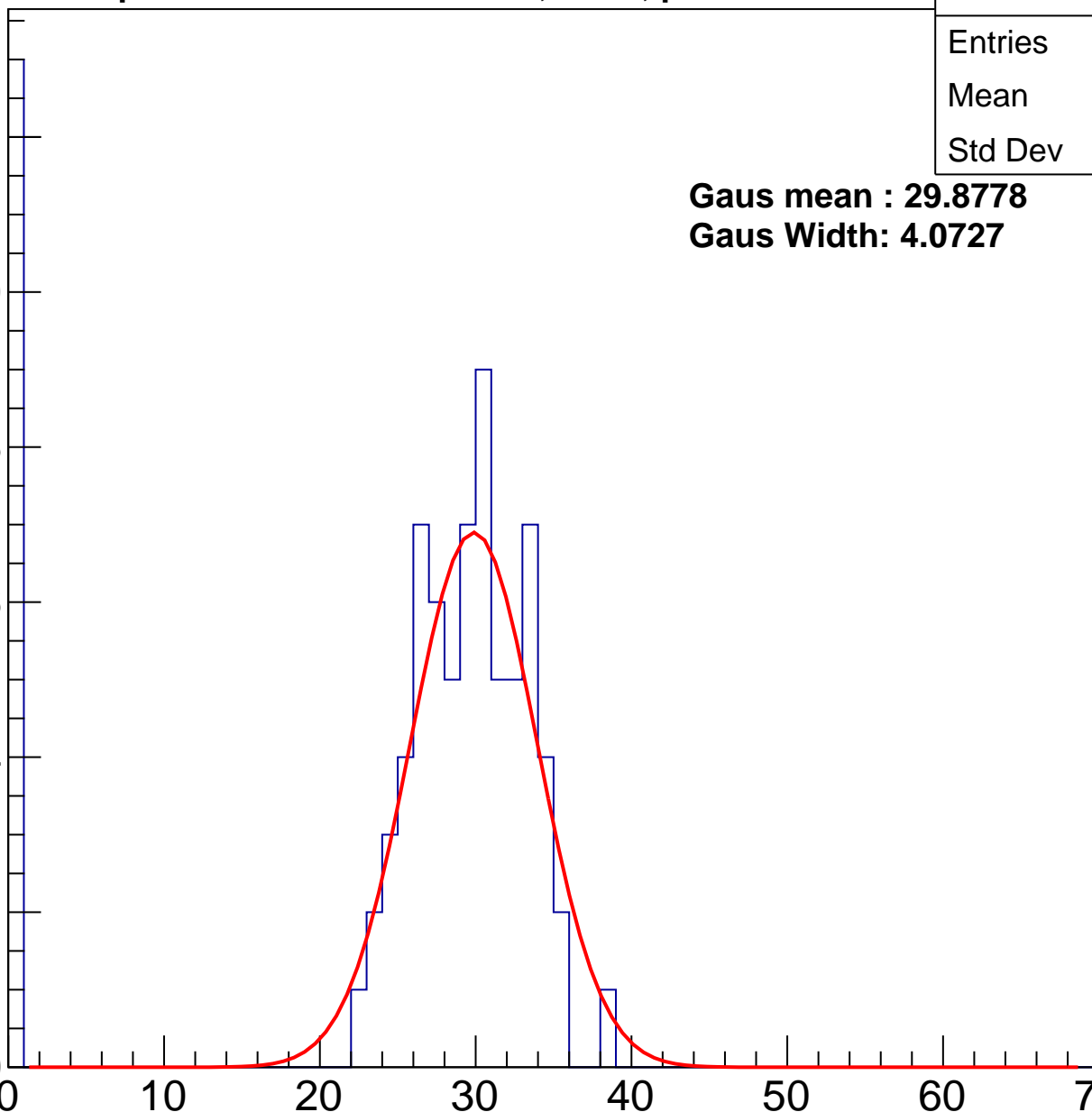
40

50

60

70

ampl



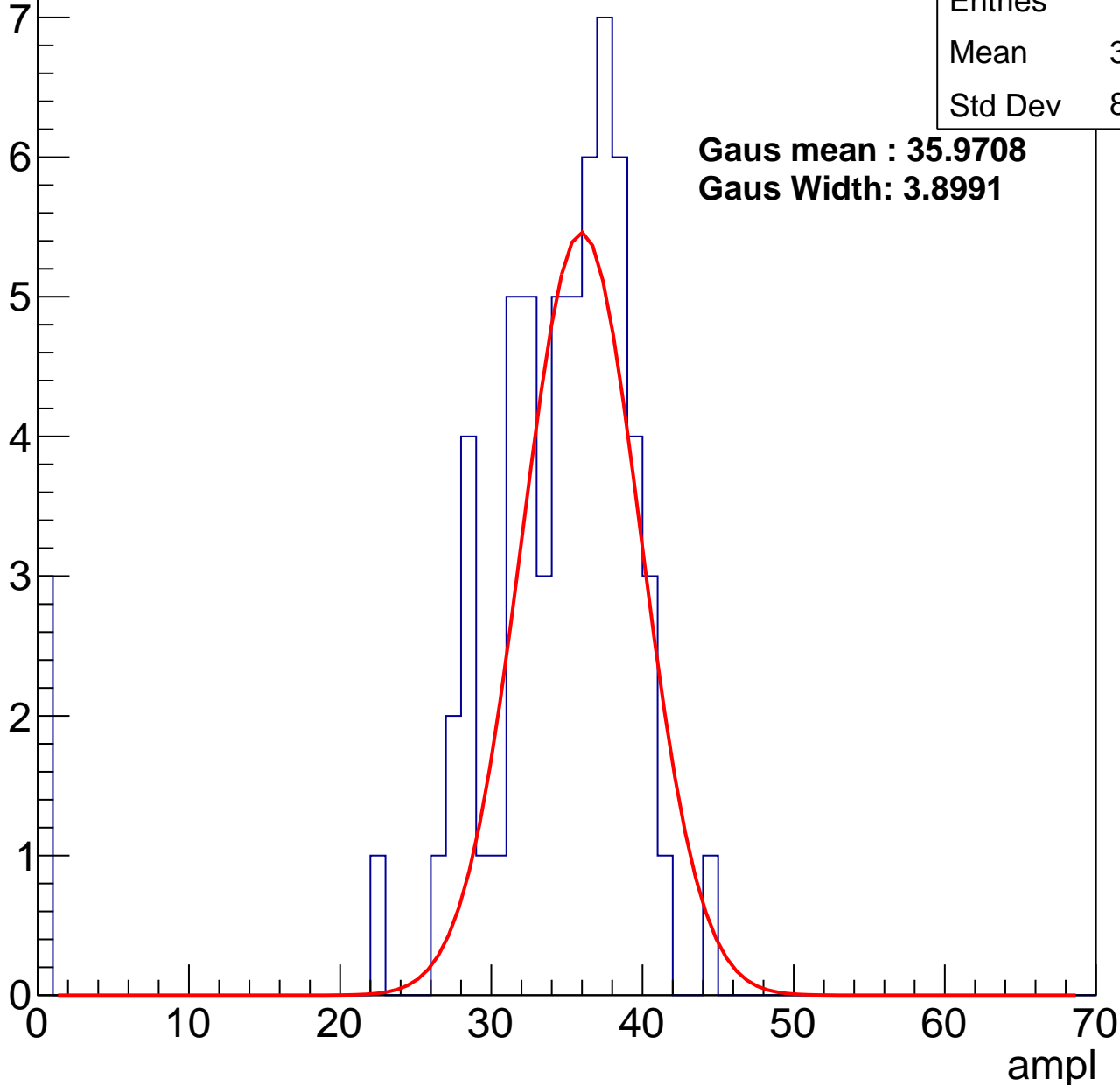
B1L103S, U18-ch57, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	32.75
Std Dev	8.359

Gaus mean : 35.9708
Gaus Width: 3.8991



B1L103S, U18-ch57, adc2

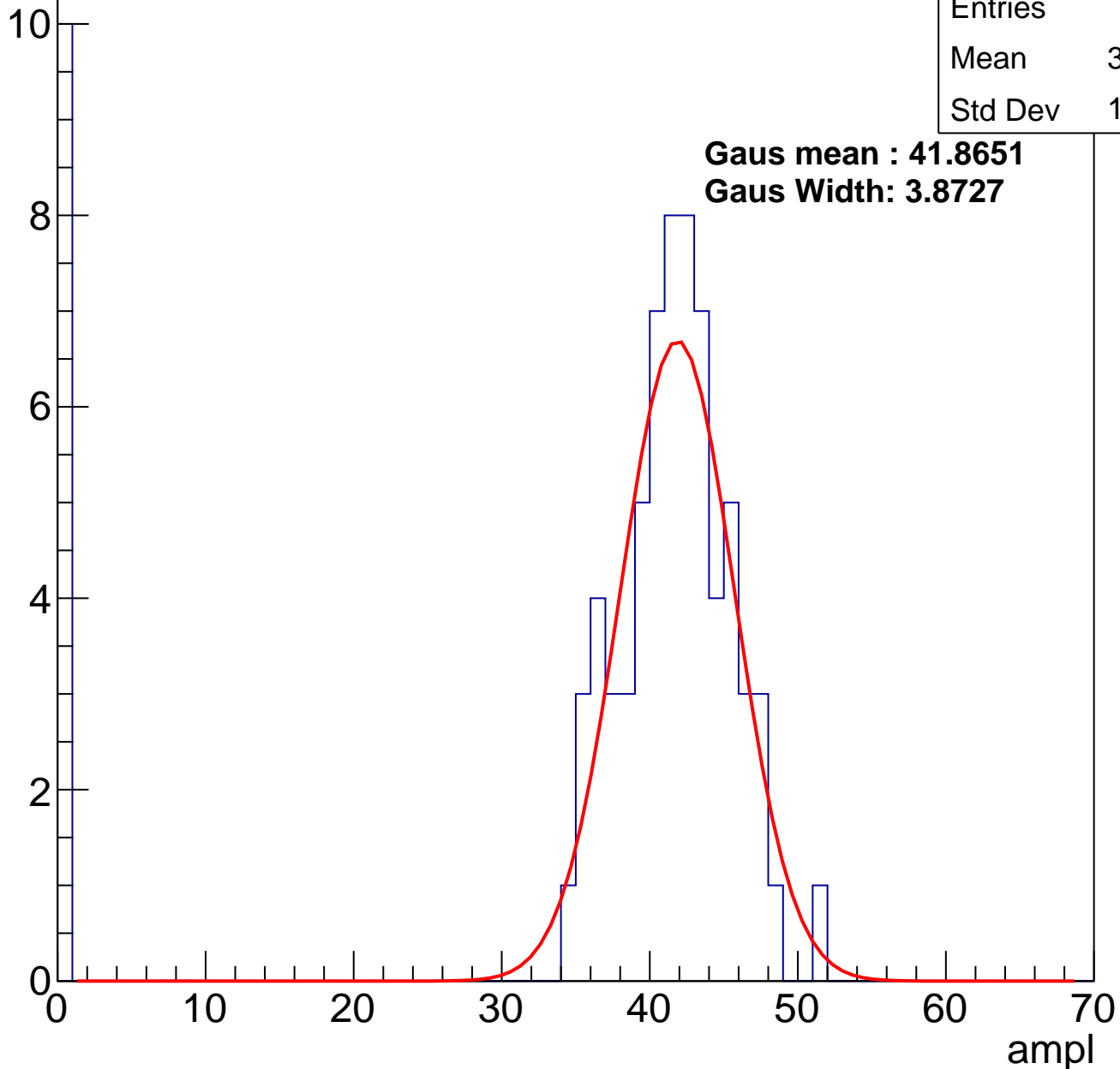
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	35.88
Std Dev	14.36

Gaus mean : 41.8651

Gaus Width: 3.8727

Entry

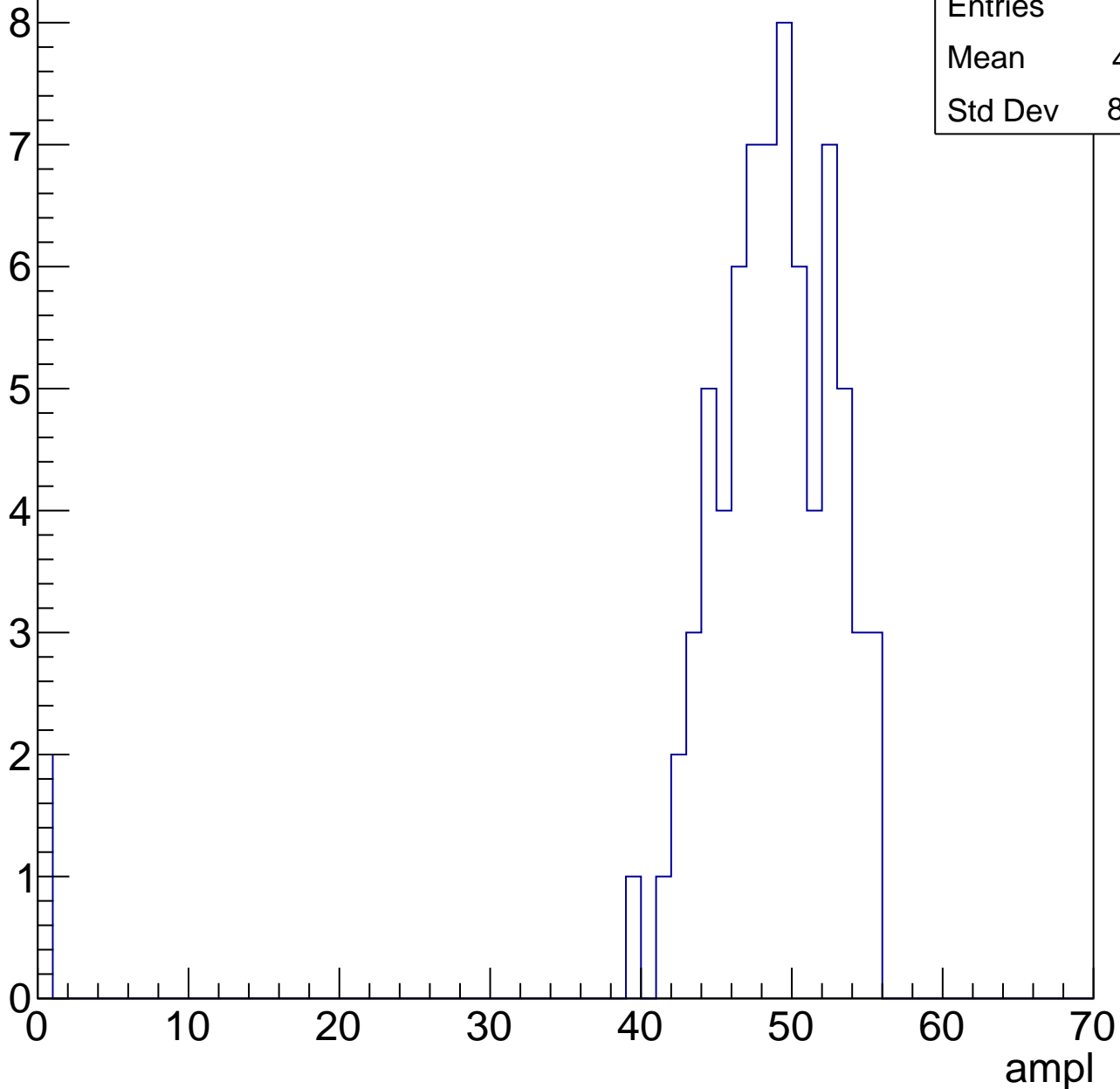


B1L103S, U18-ch57, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	47.11
Std Dev	8.647

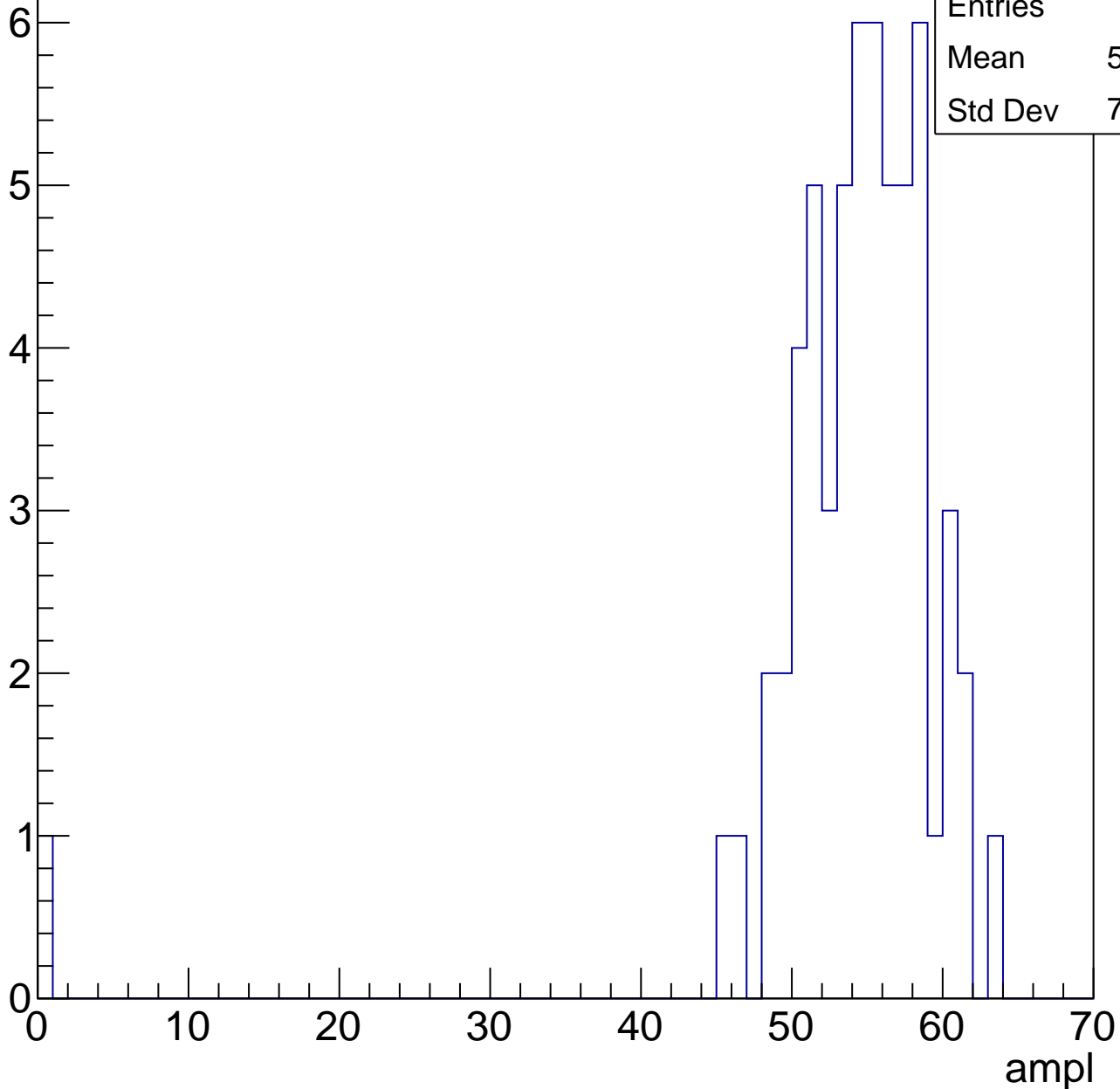


B1L103S, U18-ch57, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

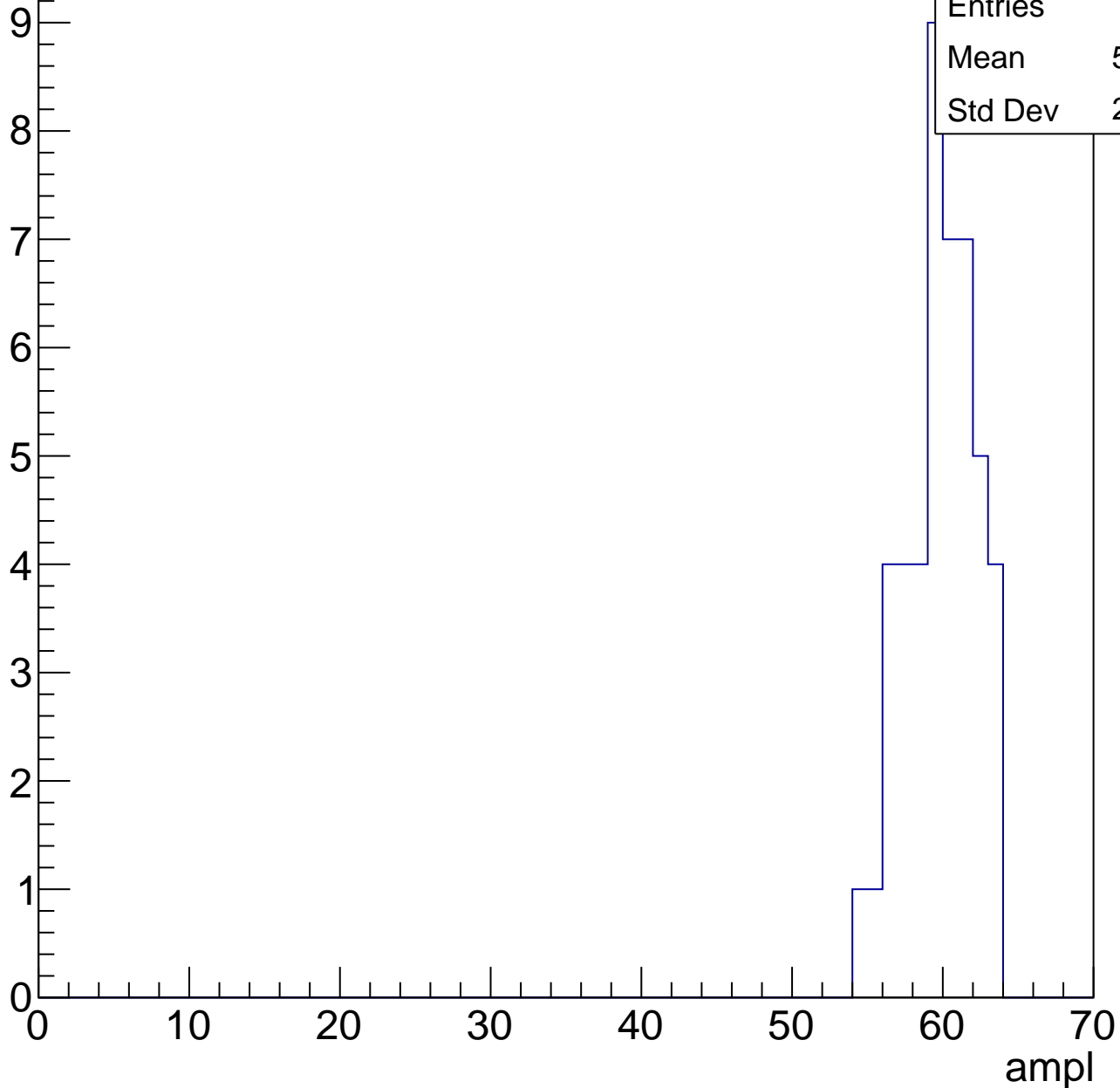
Entries	59
Mean	53.42
Std Dev	7.992



B1L103S, U18-ch57, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



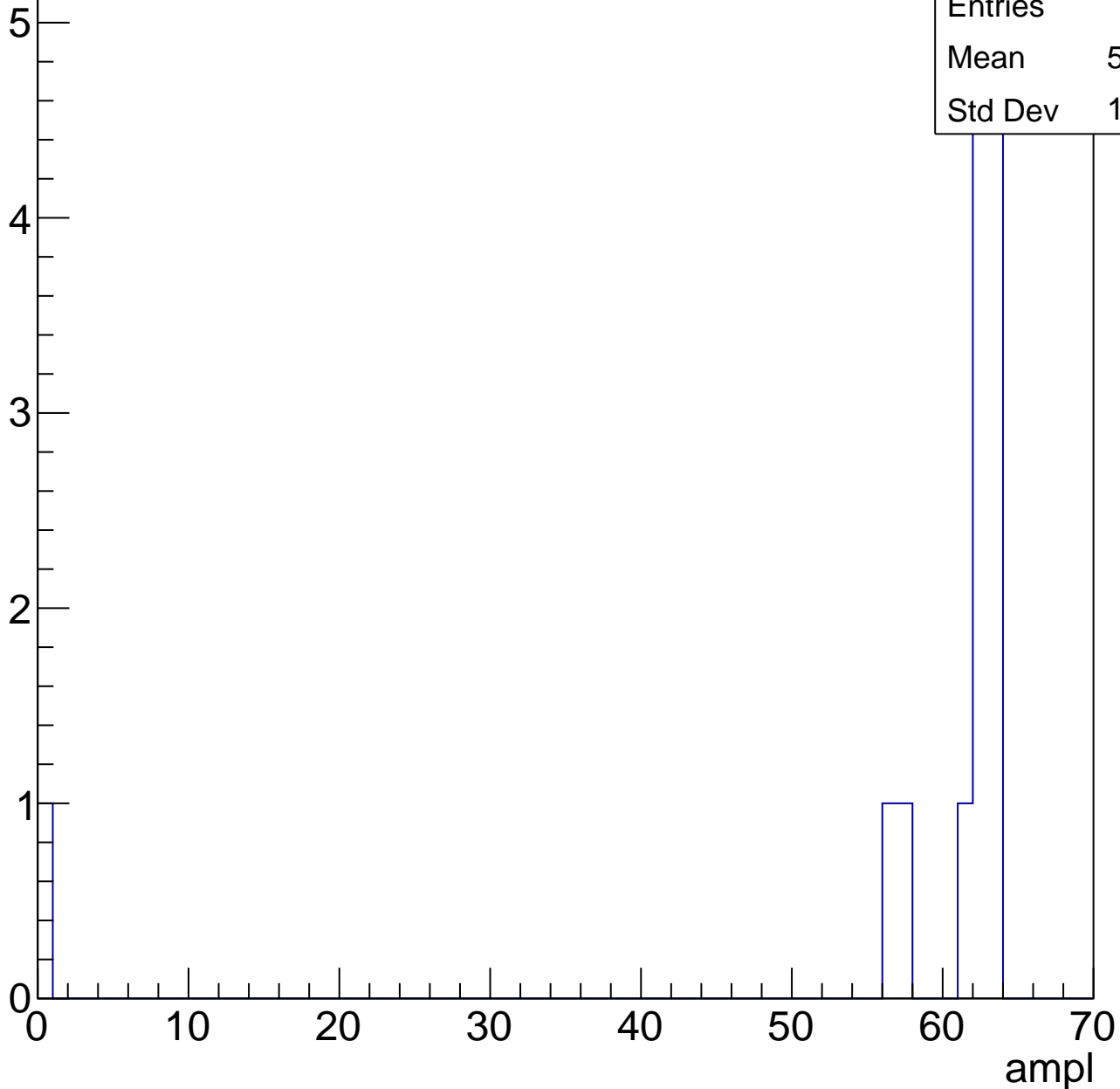
Entries	46
Mean	59.41
Std Dev	2.251

B1L103S, U18-ch57, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57.07
Std Dev	15.97



B1L103S, U18-ch57, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch58, adc0

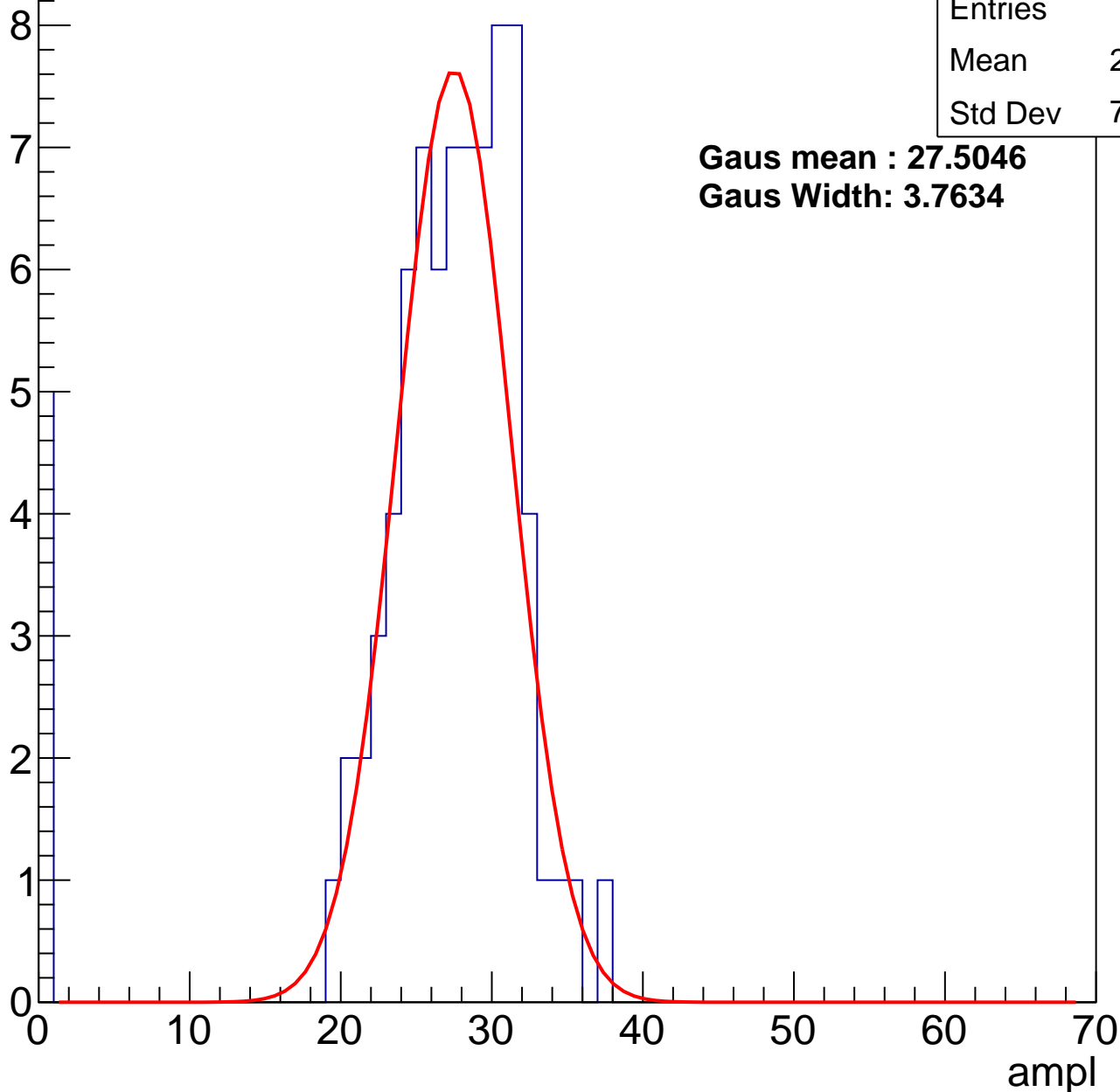
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	25.64
Std Dev	7.487

Gaus mean : 27.5046

Gaus Width: 3.7634



B1L103S, U18-ch58, adc1

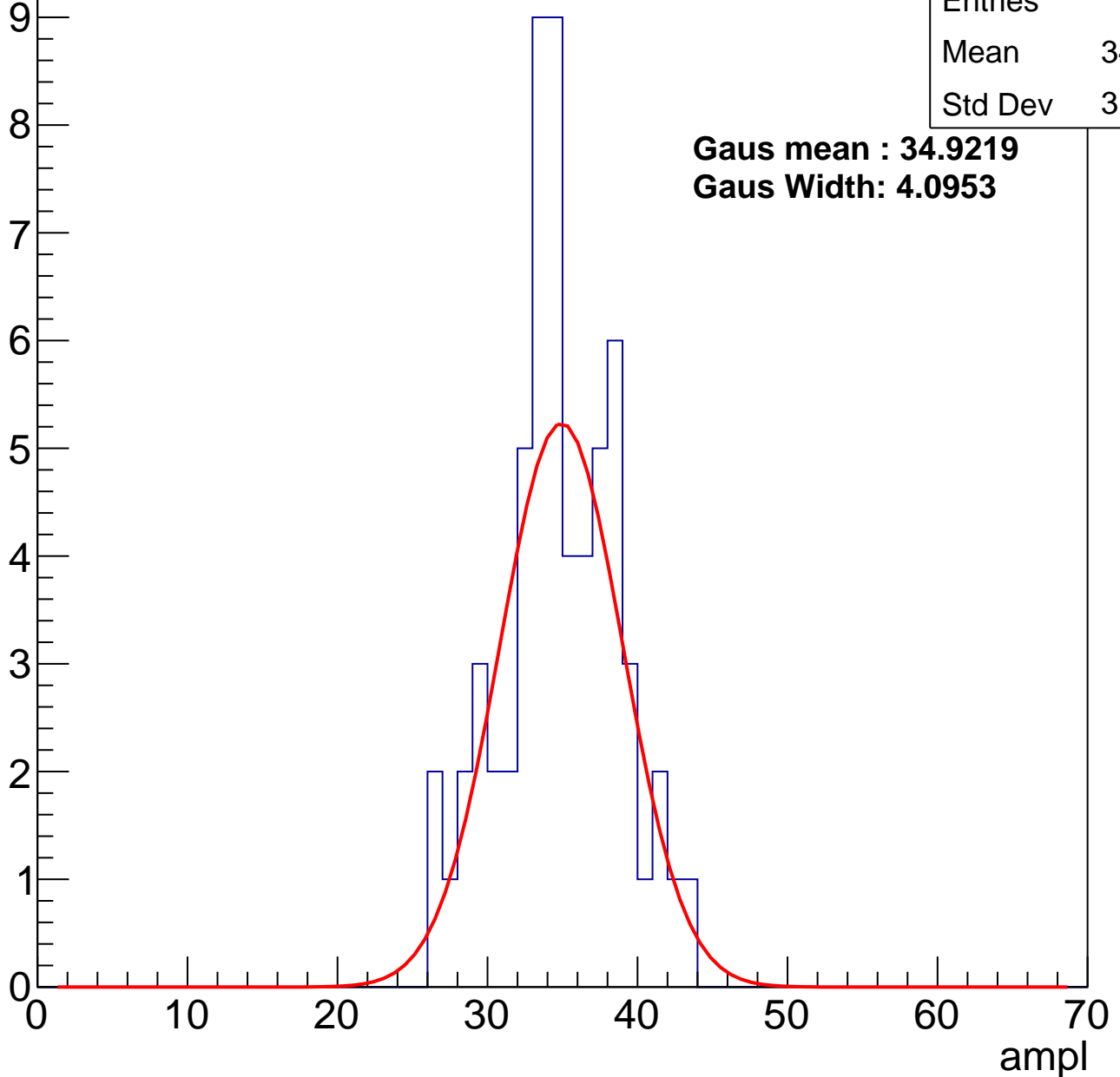
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	34.32
Std Dev	3.834

Gaus mean : 34.9219

Gaus Width: 4.0953



B1L103S, U18-ch58, adc2

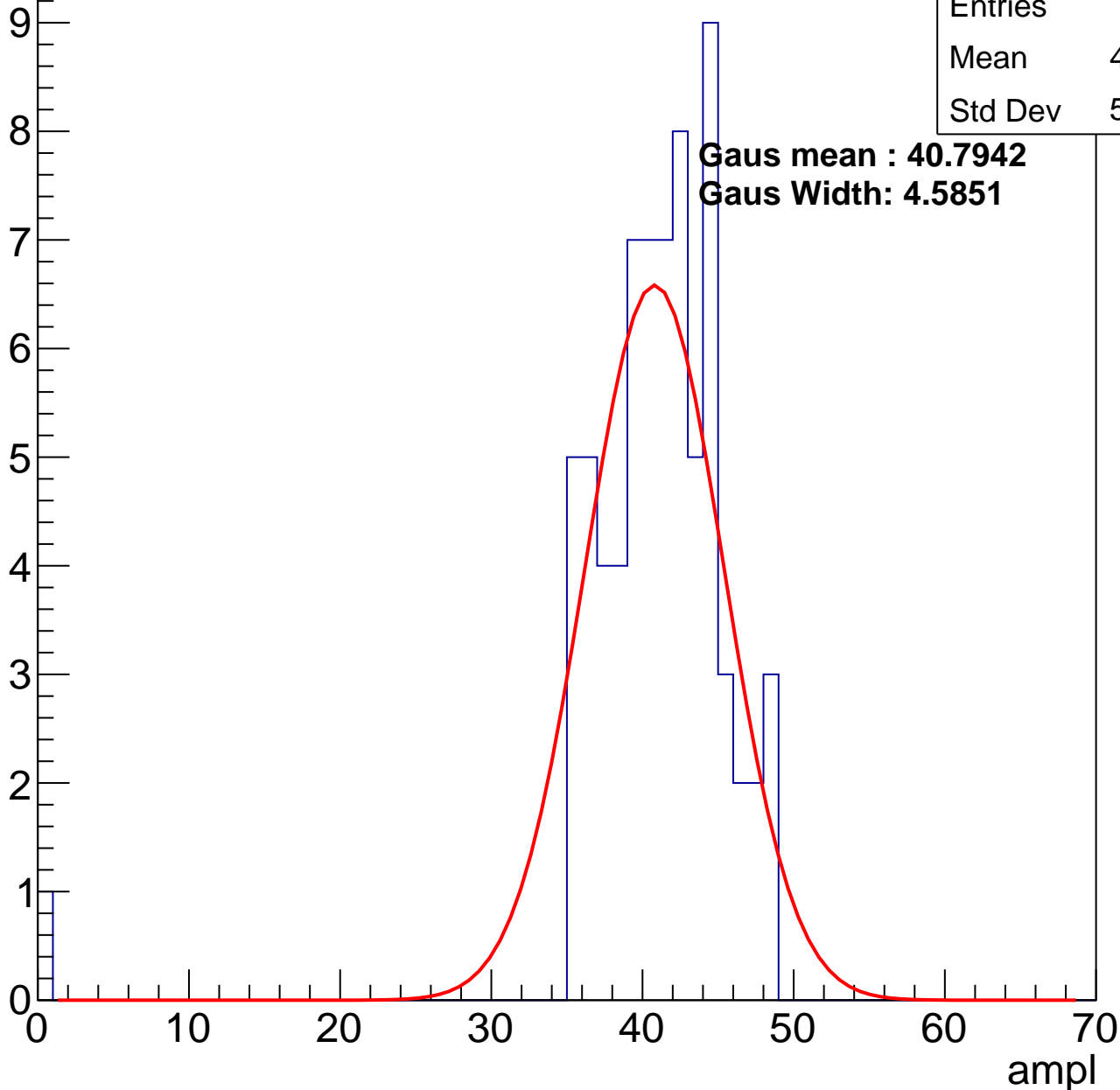
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	40.38
Std Dev	5.915

Gaus mean : 40.7942

Gaus Width: 4.5851

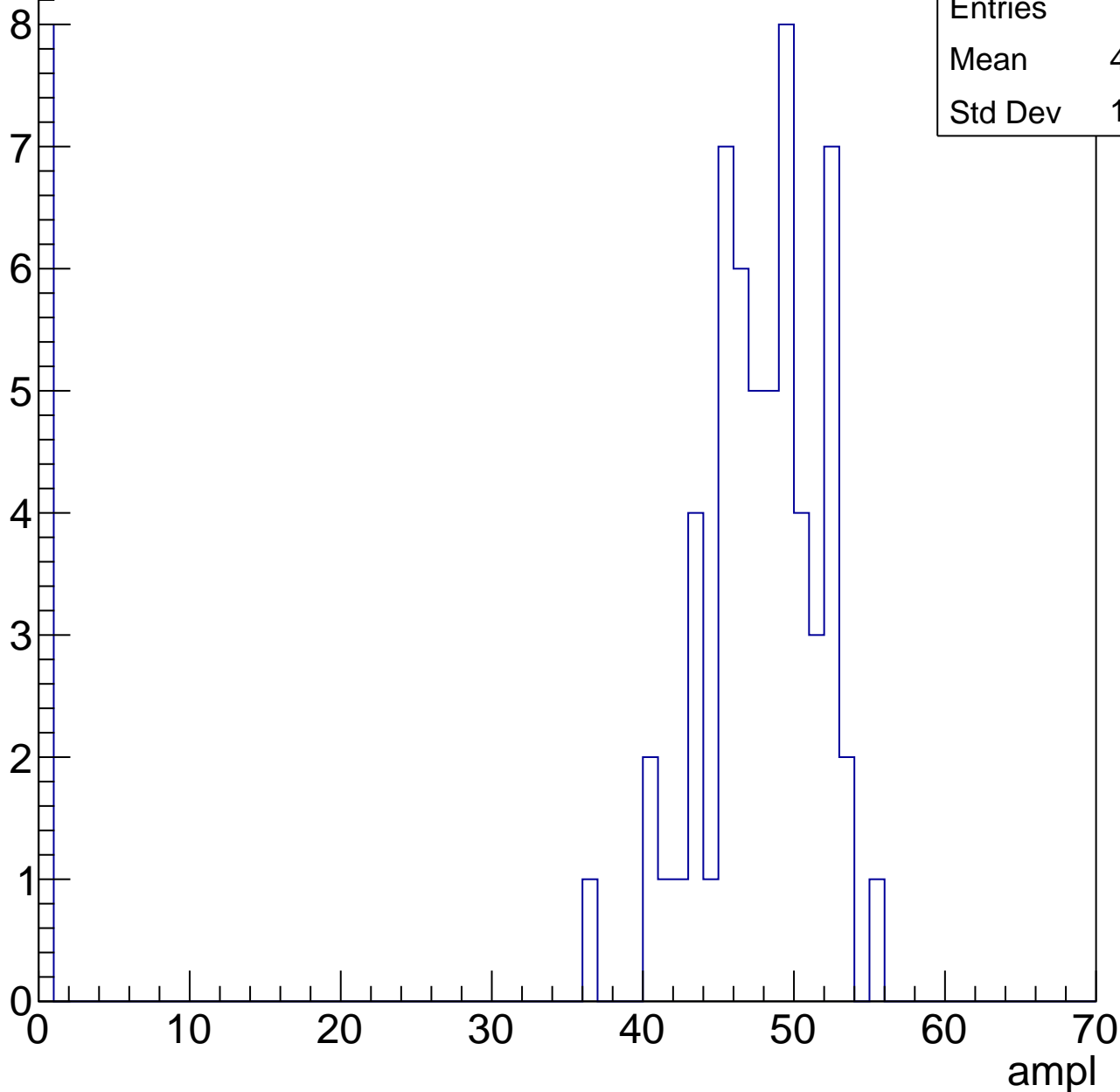


B1L103S, U18-ch58, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	41.68
Std Dev	15.87

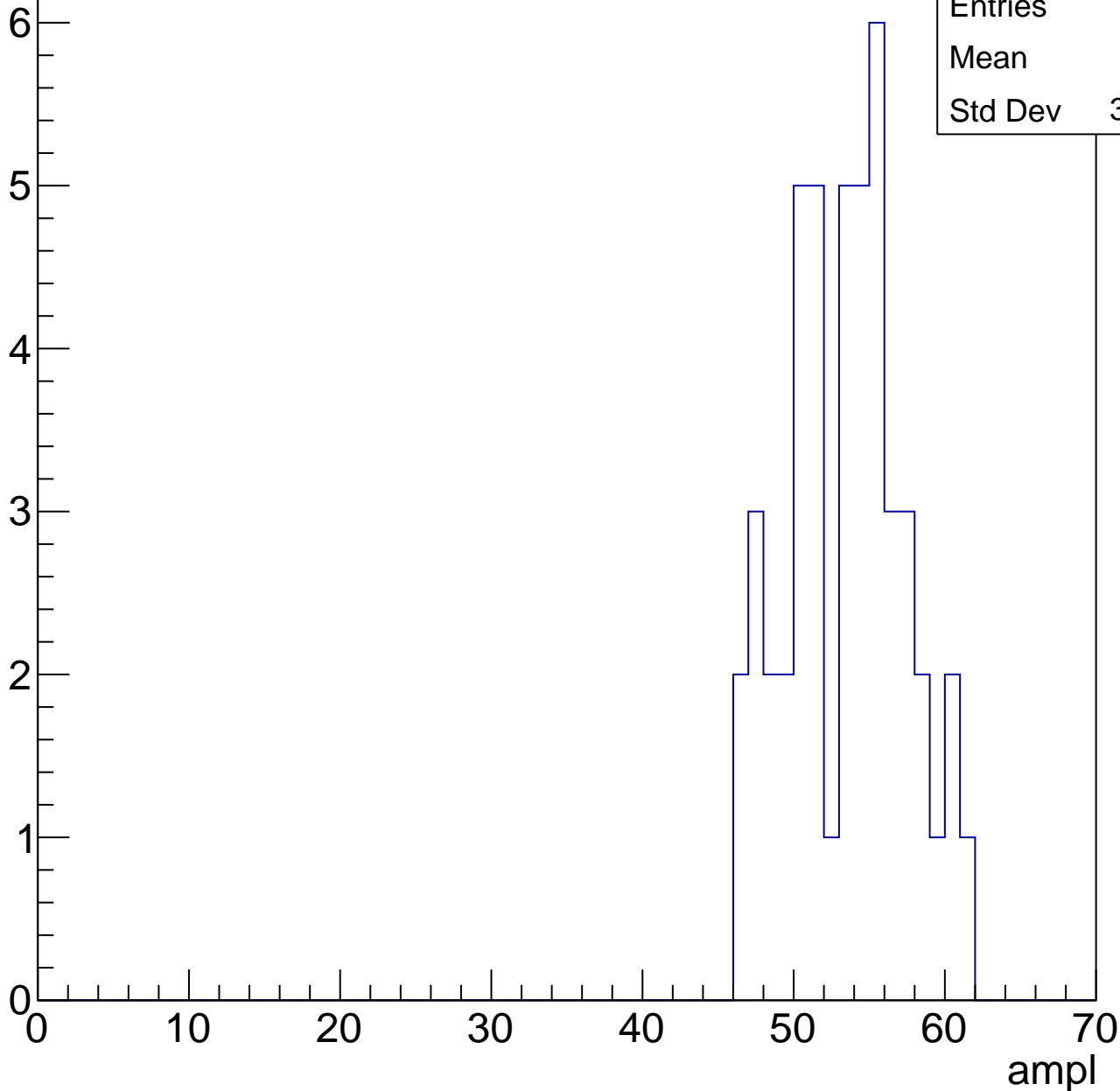


B1L103S, U18-ch58, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

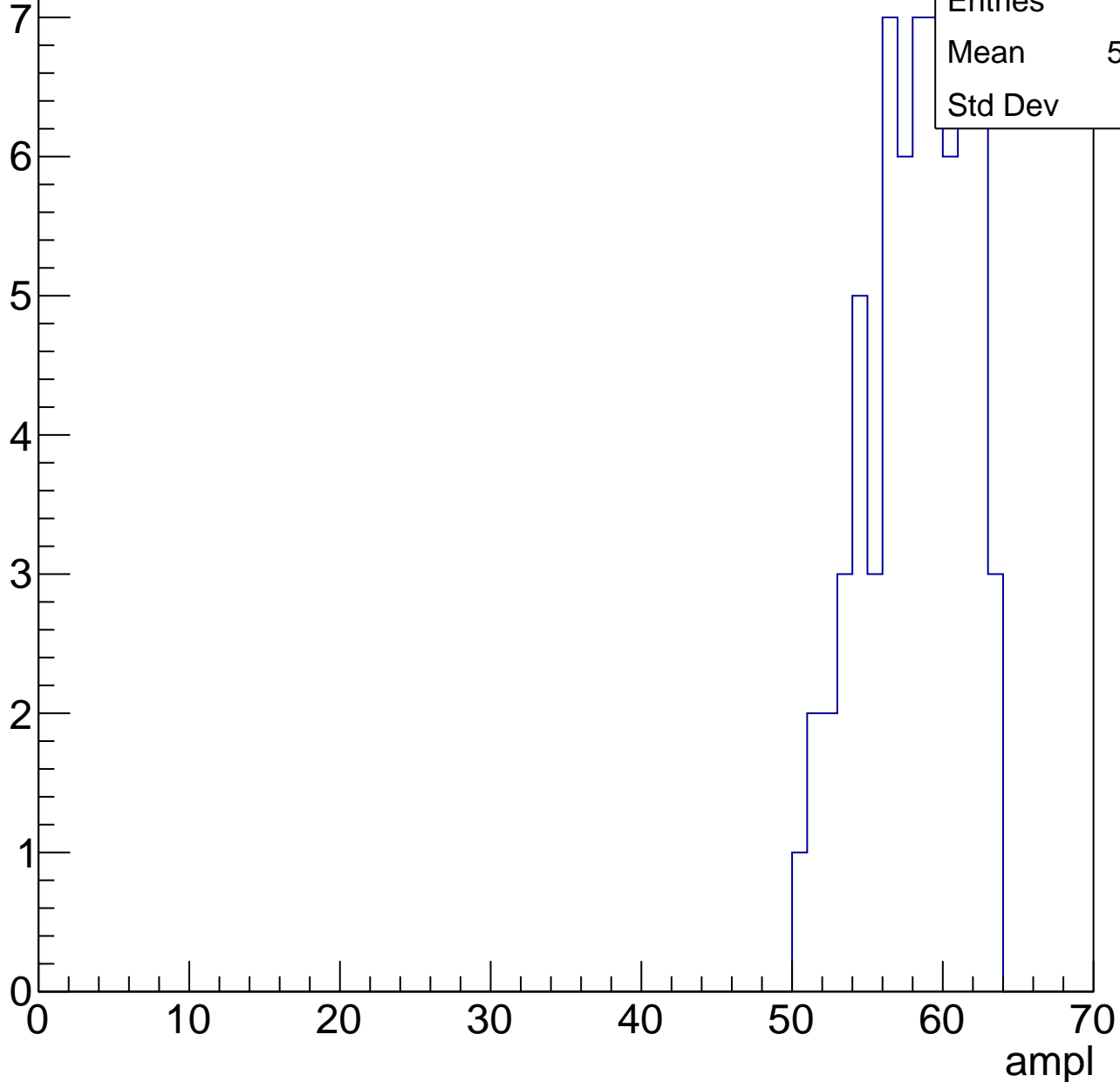
Entries	48
Mean	53
Std Dev	3.835



B1L103S, U18-ch58, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

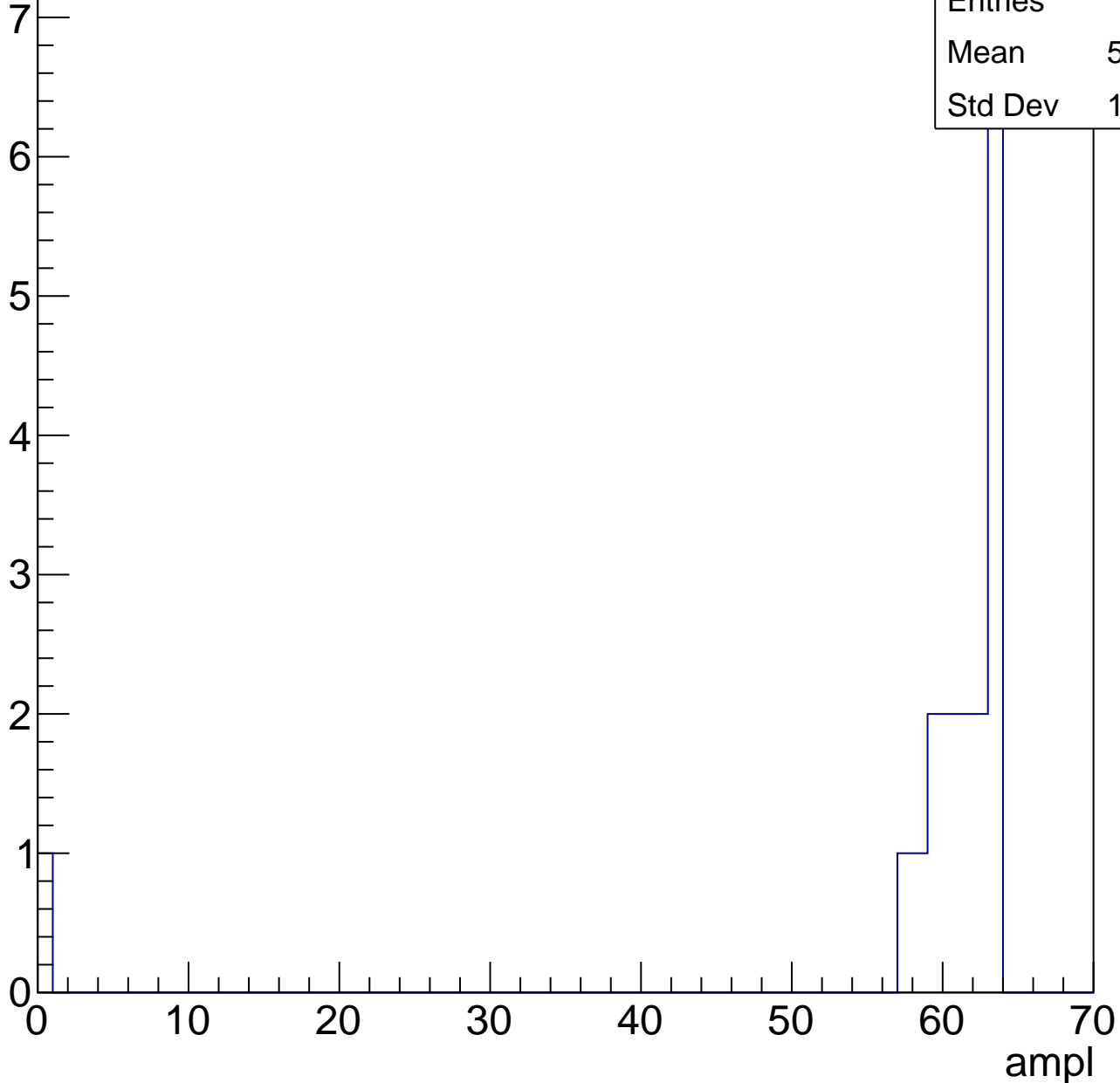


B1L103S, U18-ch58, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.78
Std Dev	14.14



B1L103S, U18-ch58, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U18-ch59, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	117
Mean	22.1
Std Dev	12.91

Gaus mean : 29.0813

Gaus Width: 4.1590

Entry

25

20

15

10

5

0

0

10

20

30

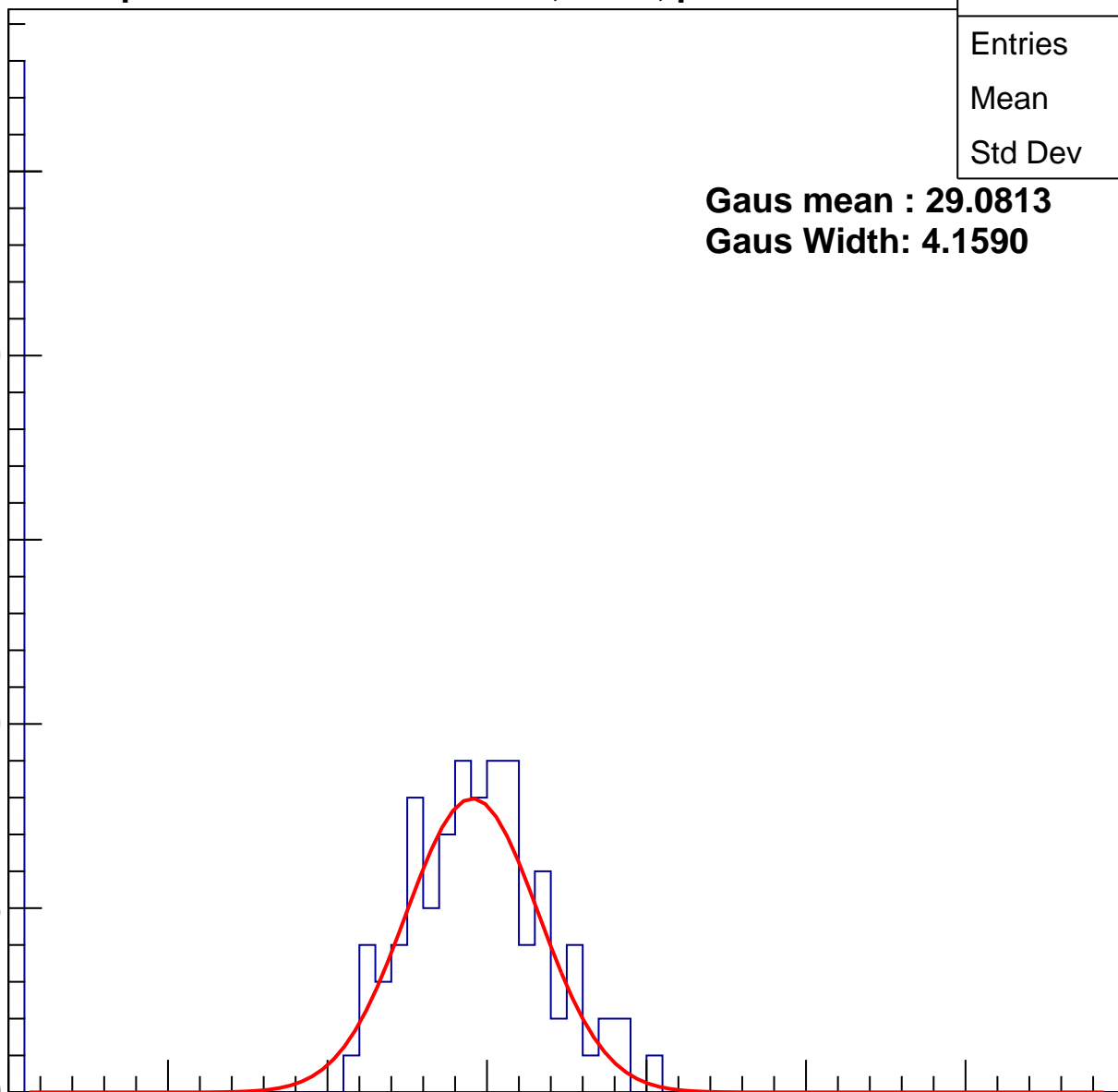
40

50

60

70

ampl



B1L103S, U18-ch59, adc1

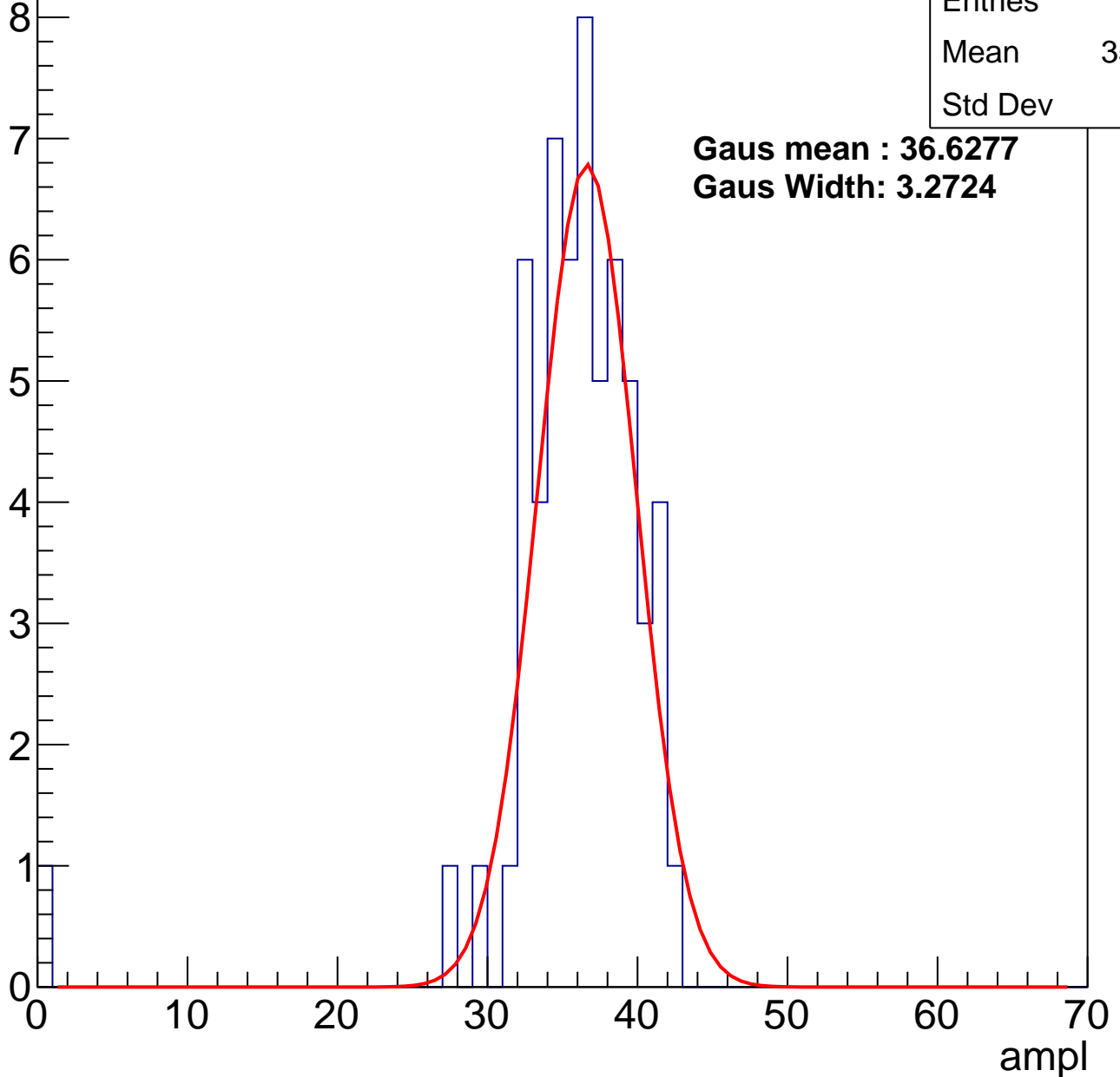
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.27
Std Dev	5.59

Gaus mean : 36.6277

Gaus Width: 3.2724



B1L103S, U18-ch59, adc2

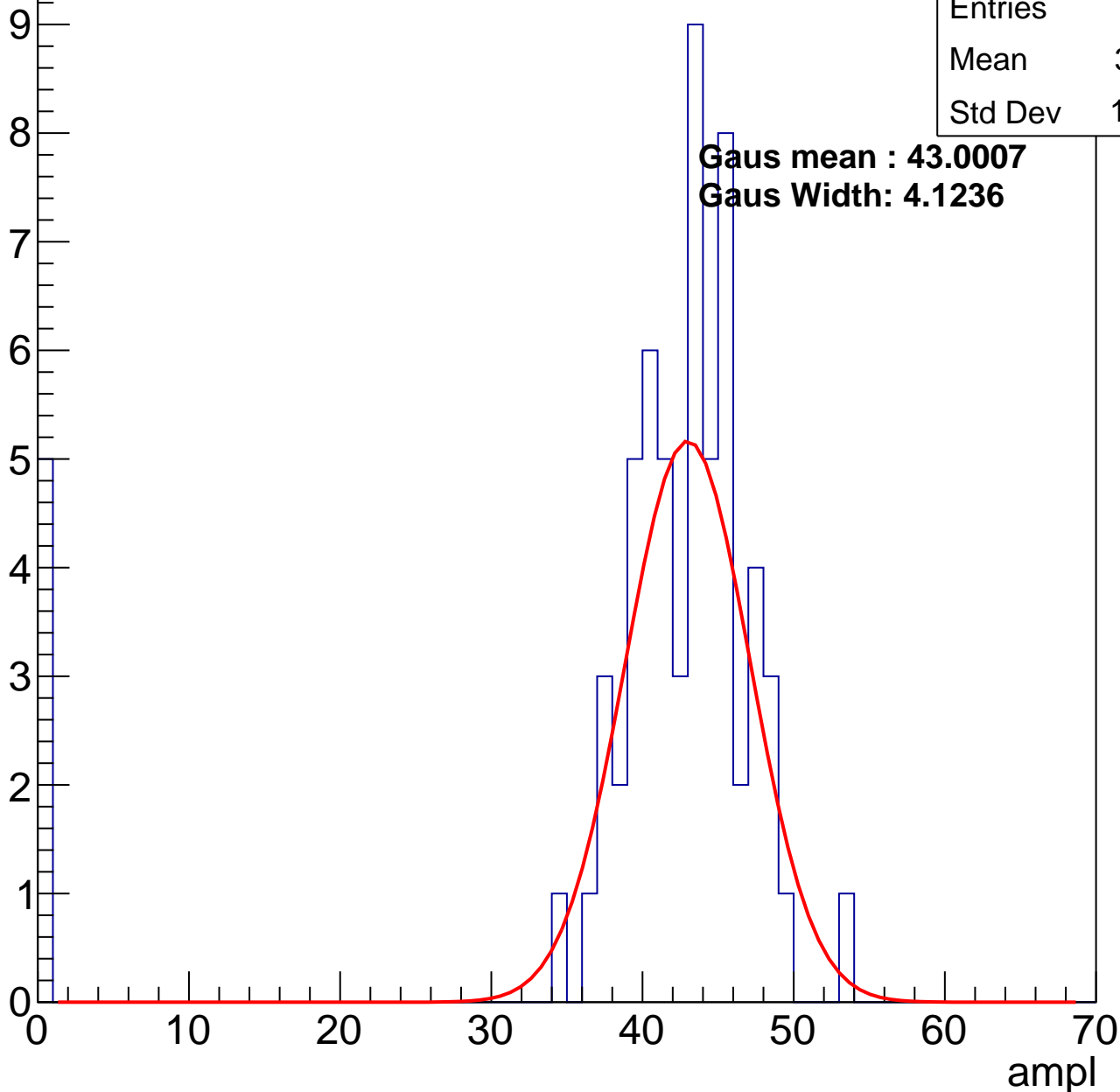
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	39.31
Std Dev	11.96

Gaus mean : 43.0007

Gaus Width: 4.1236

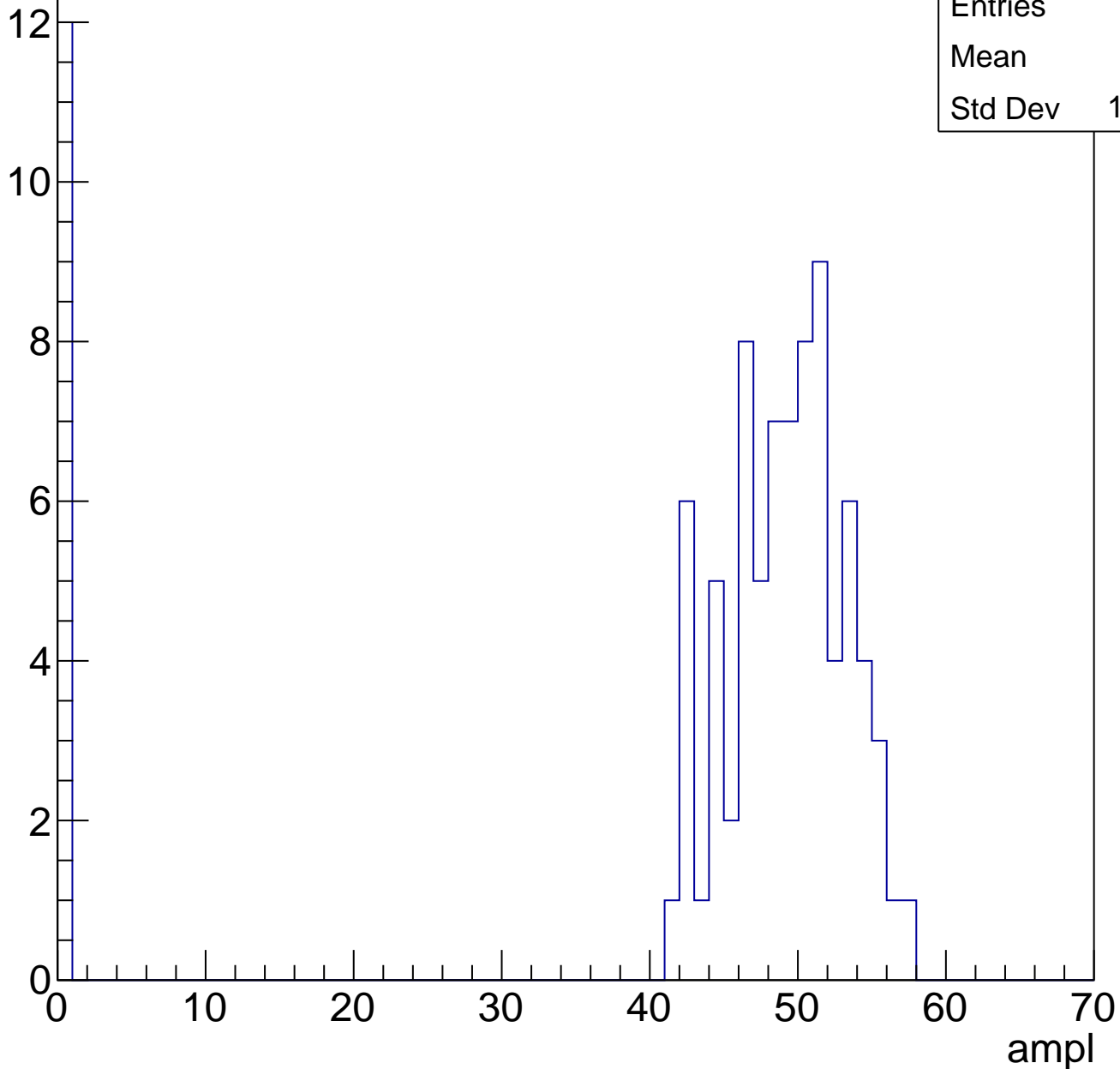


B1L103S, U18-ch59, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	42.3
Std Dev	16.97

Entry

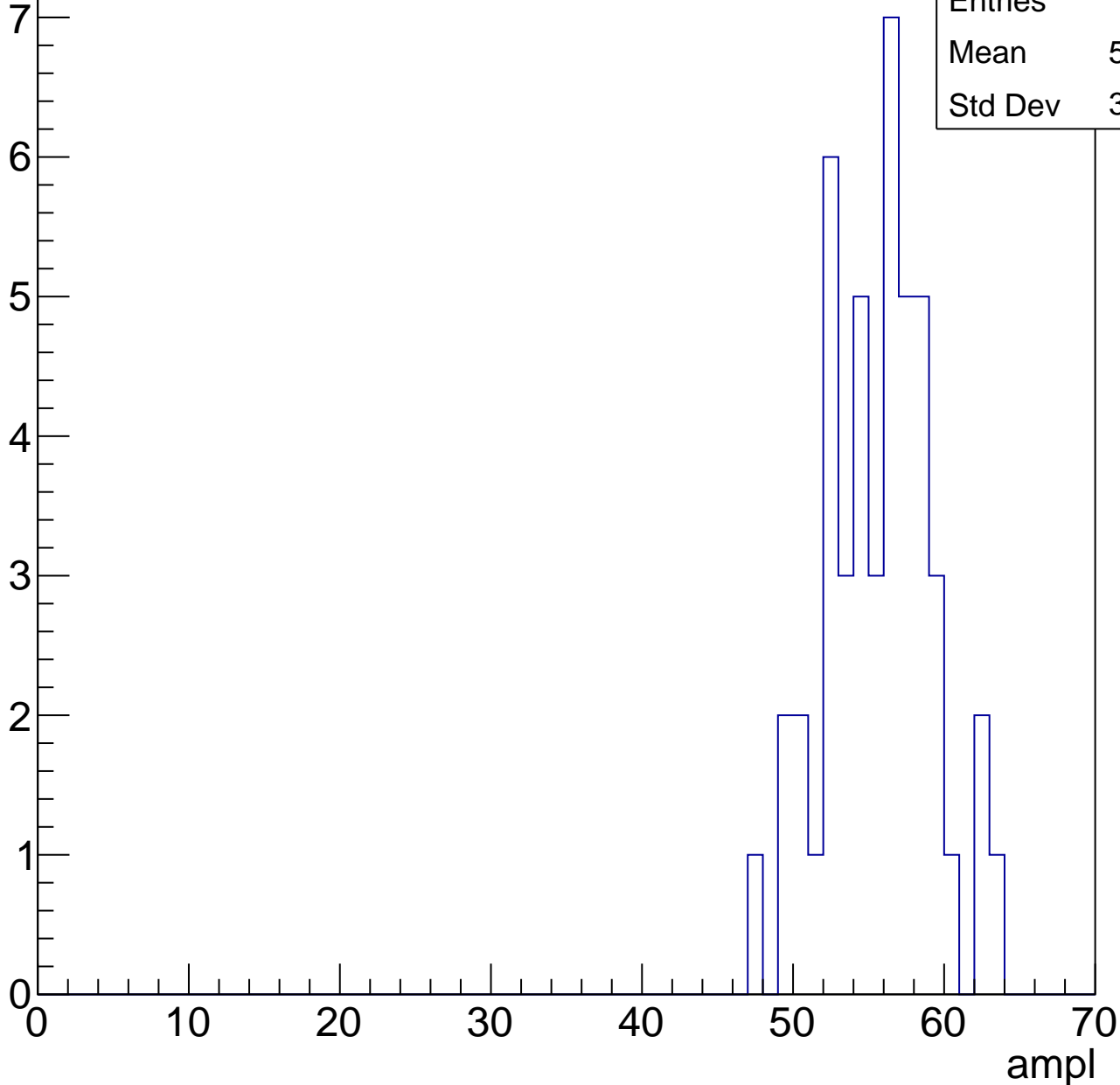


B1L103S, U18-ch59, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	55.17
Std Dev	3.509



B1L103S, U18-ch59, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

Entries 54

Mean 58.52

Std Dev 8.386

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

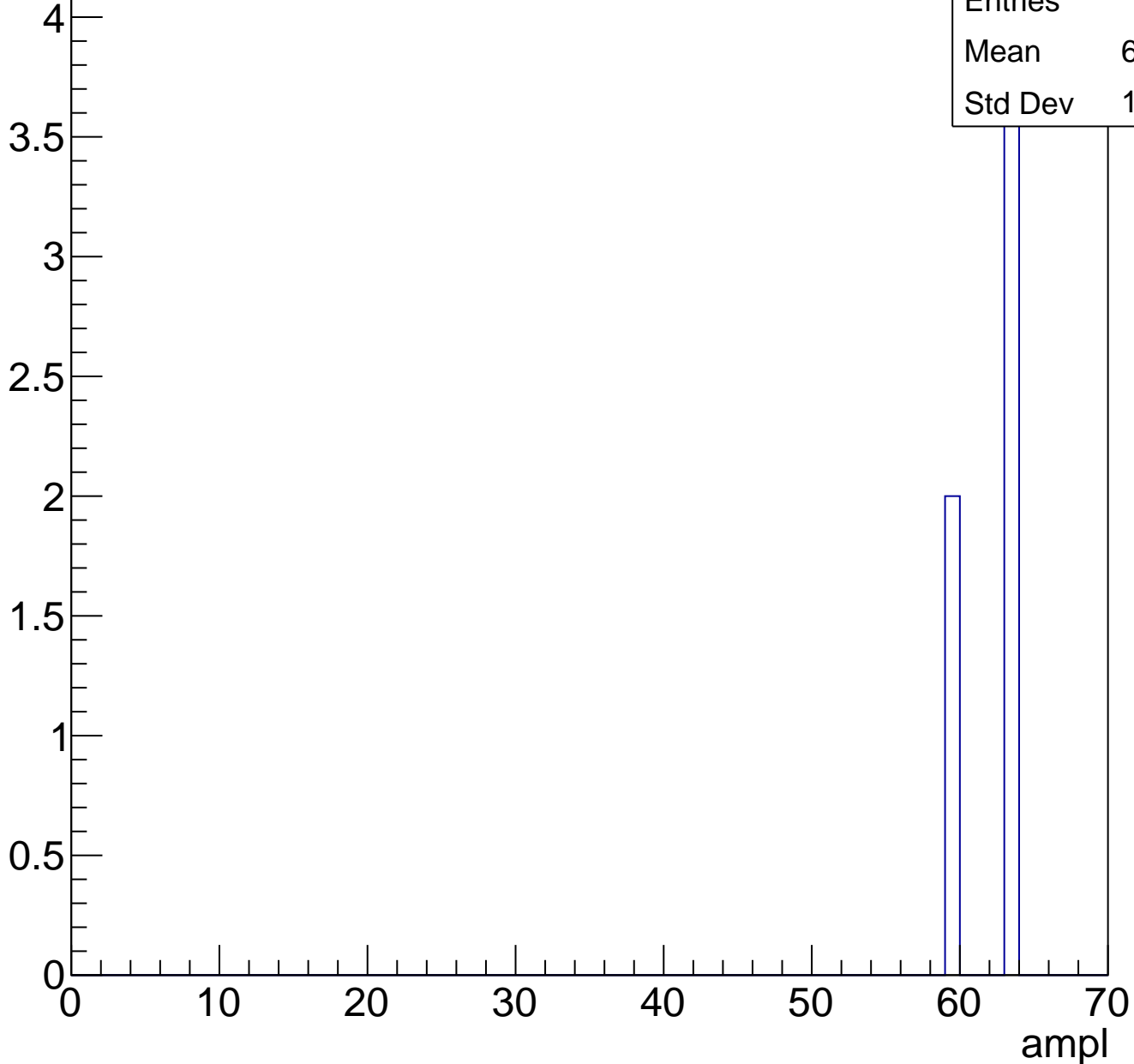
8

10

B1L103S, U18-ch59, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch59, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch60, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	90
Mean	20.33
Std Dev	13.3

Gaus mean : 29.3703

Gaus Width: 3.2951

Entry

25

20

15

10

5

0

0

10

20

30

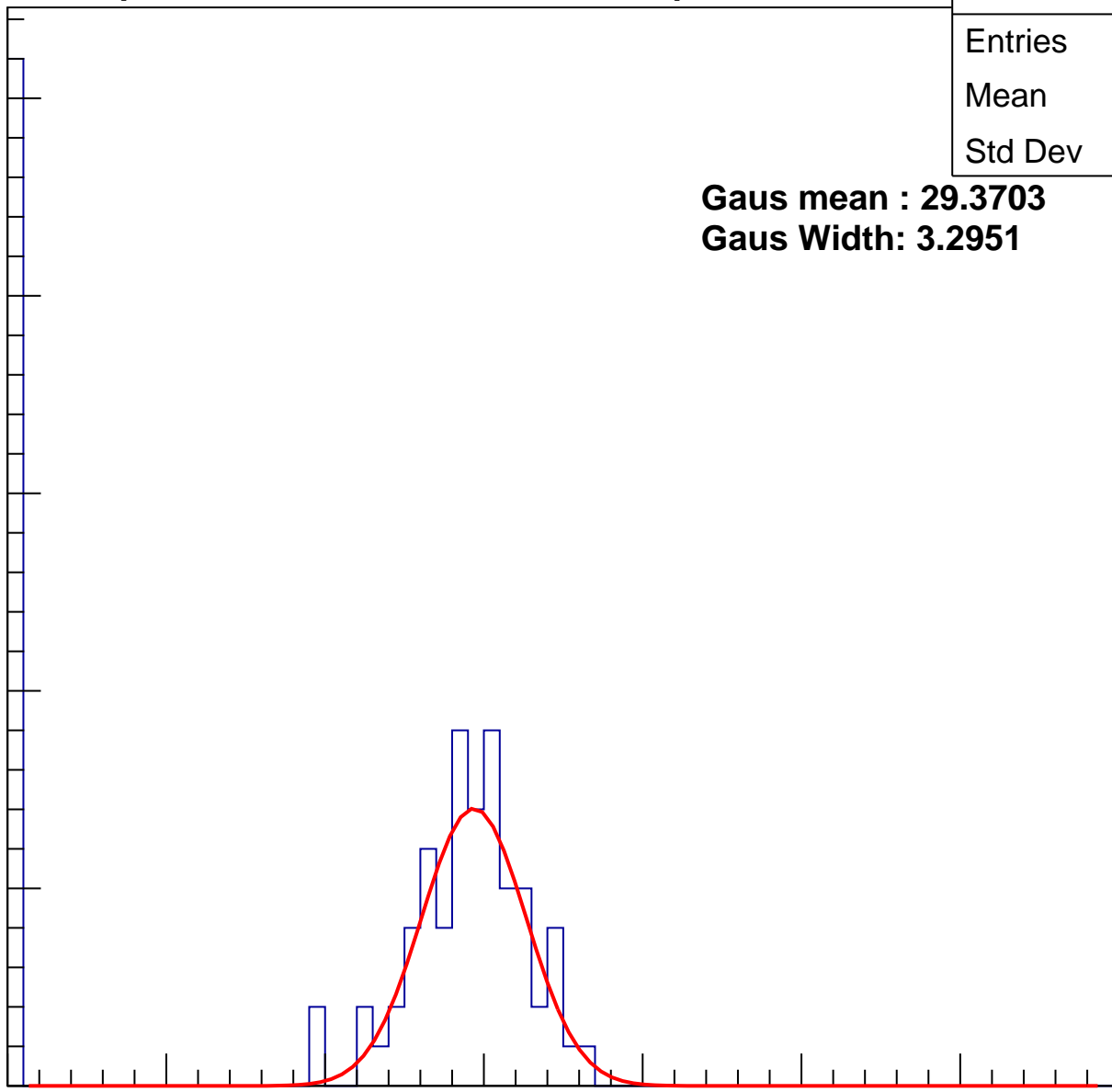
40

50

60

70

ampl



B1L103S, U18-ch60, adc1

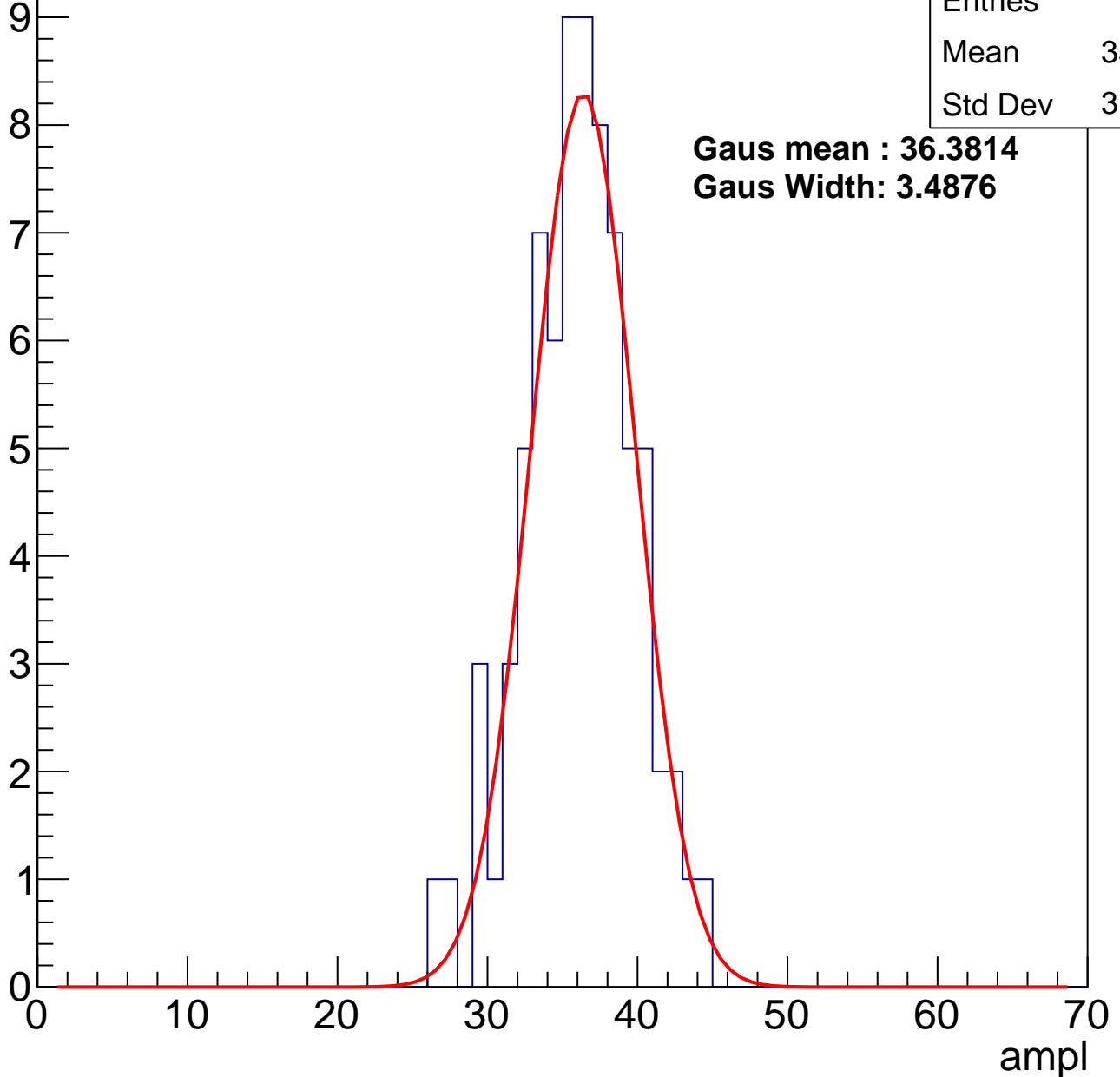
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	35.62
Std Dev	3.627

Gaus mean : 36.3814

Gaus Width: 3.4876



B1L103S, U18-ch60, adc2

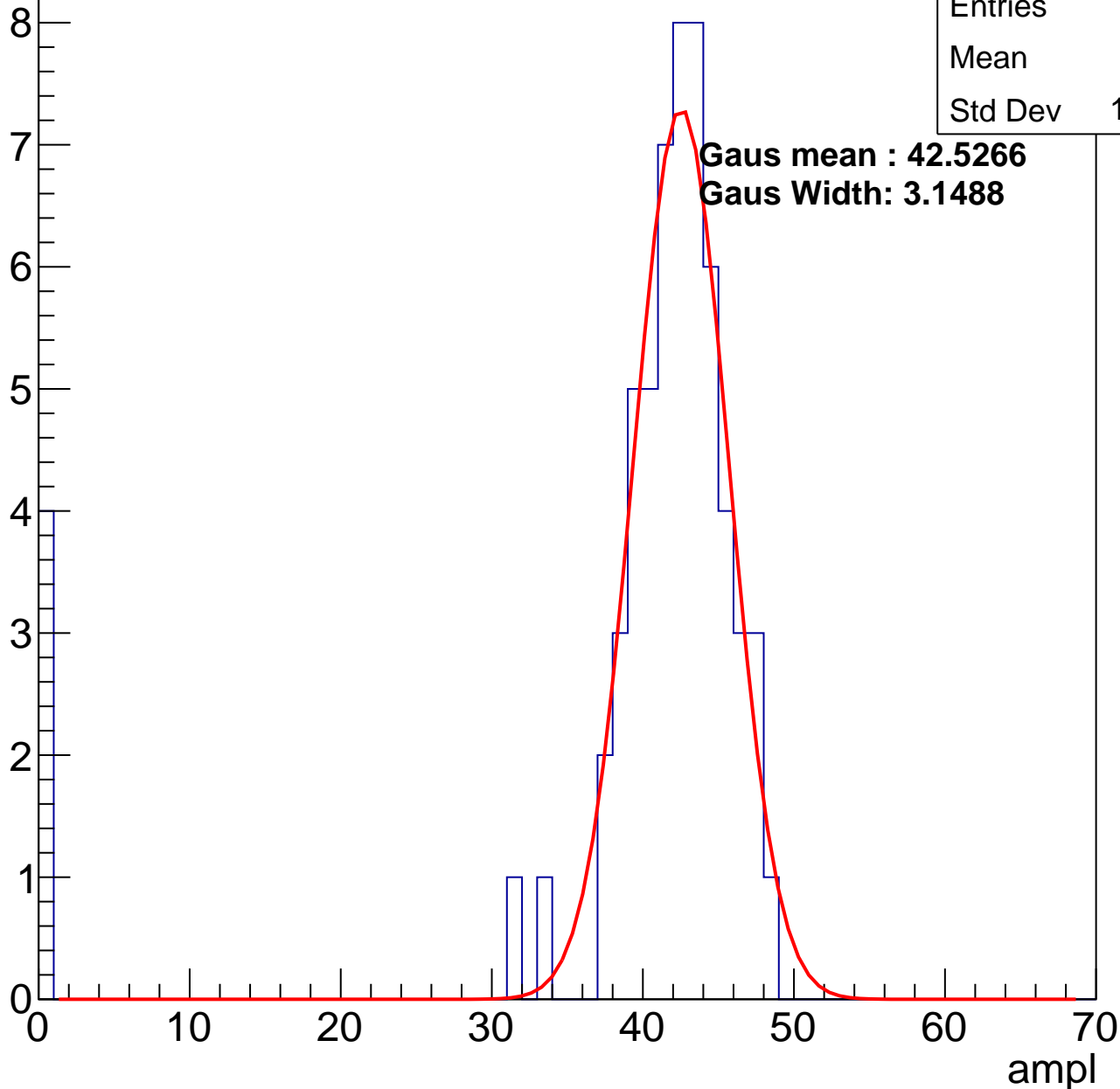
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	39.1
Std Dev	10.82

Gaus mean : 42.5266

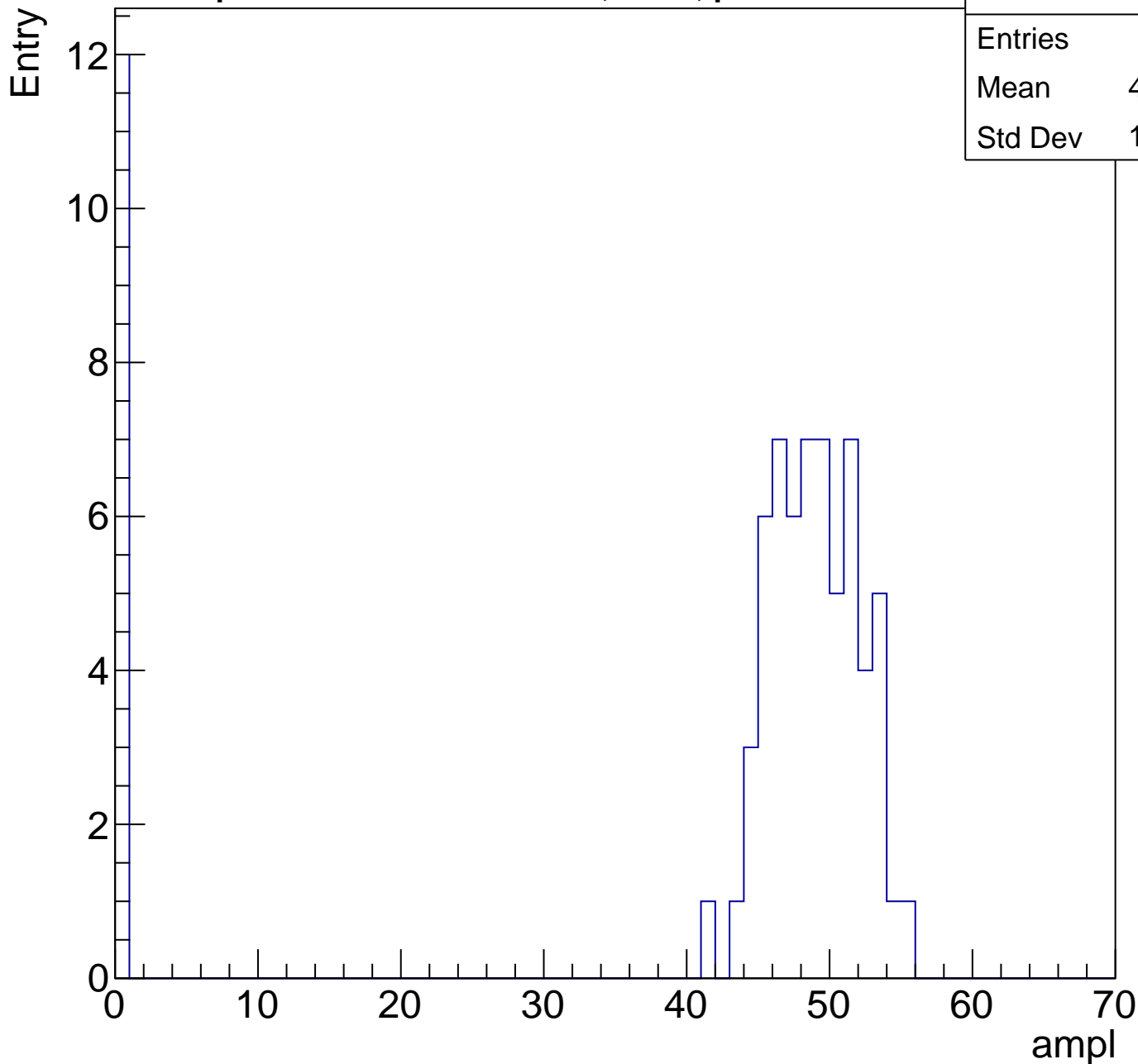
Gaus Width: 3.1488



B1L103S, U18-ch60, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	40.52
Std Dev	18.18

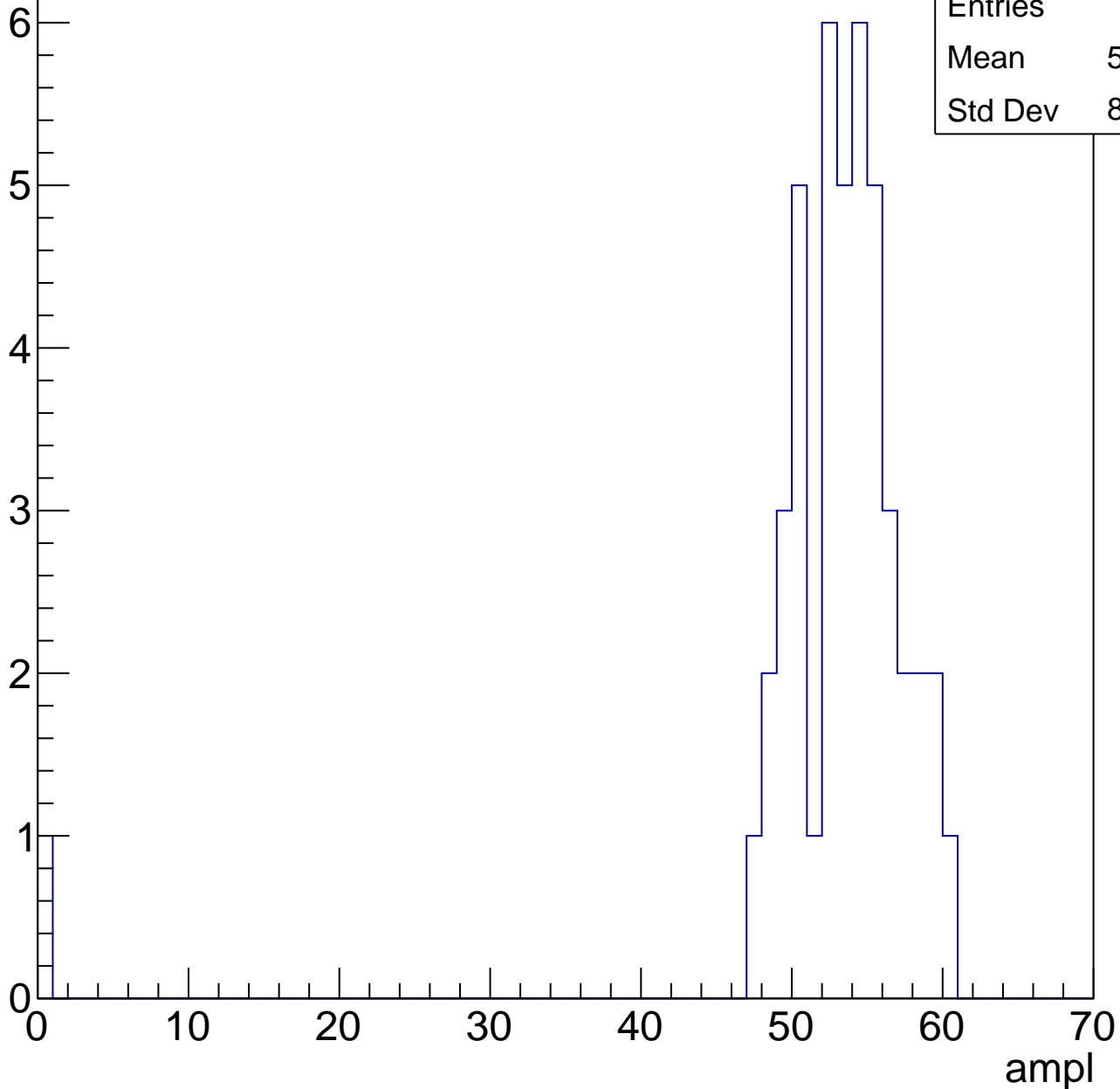


B1L103S, U18-ch60, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	52.07
Std Dev	8.452

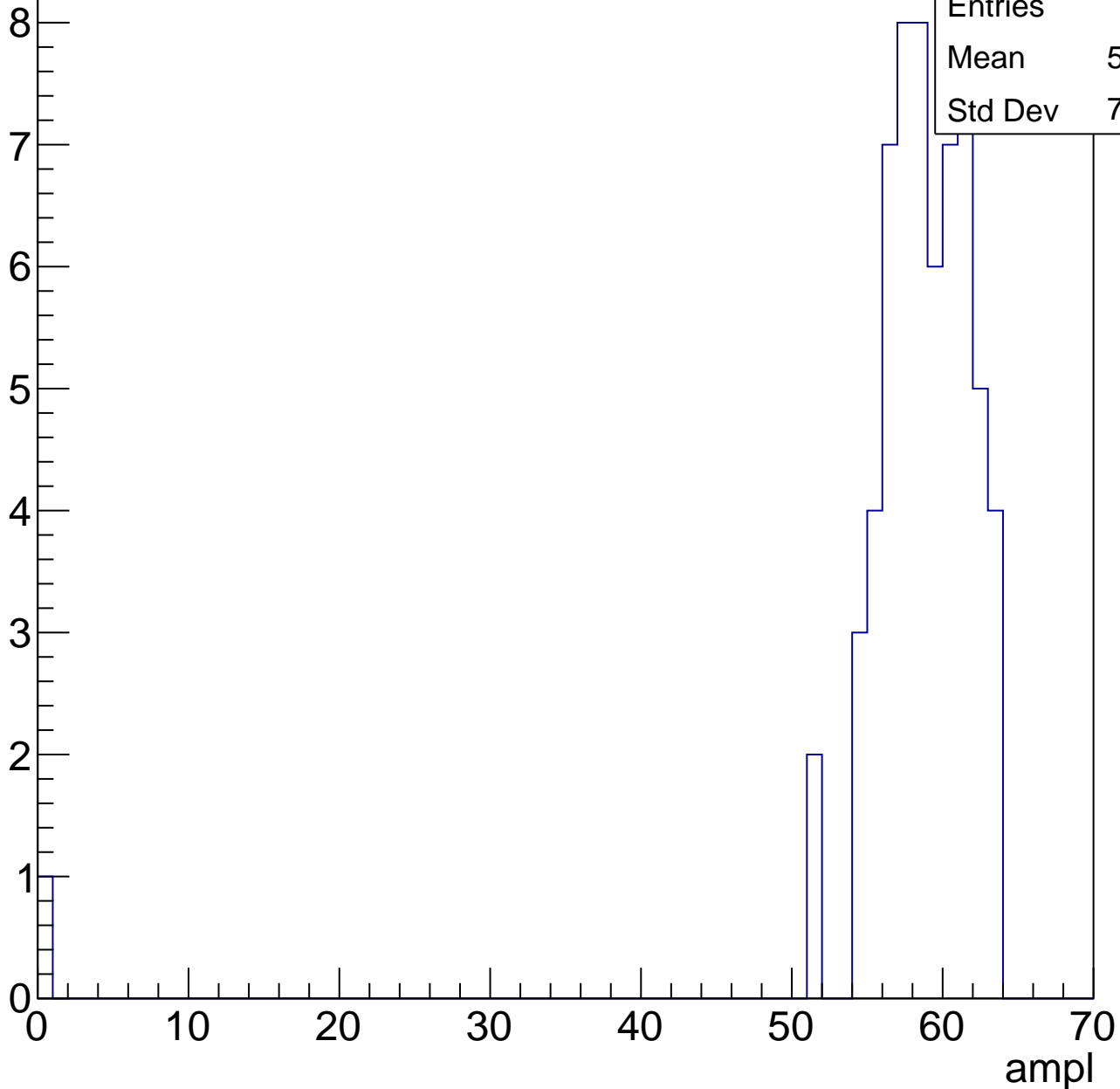


B1L103S, U18-ch60, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	57.46
Std Dev	7.817

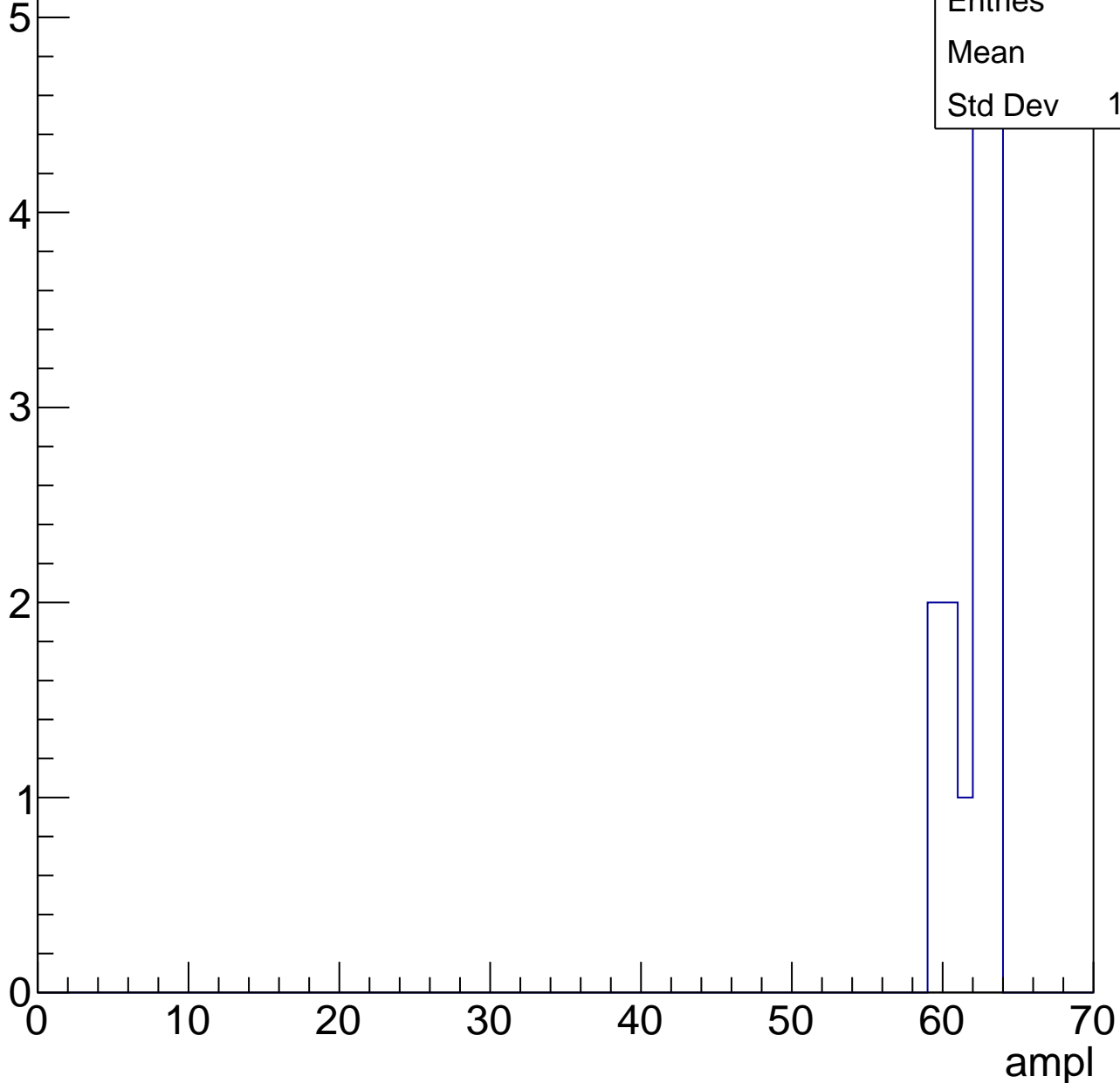


B1L103S, U18-ch60, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.6
Std Dev	1.405



B1L103S, U18-ch60, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U18-ch61, adc0

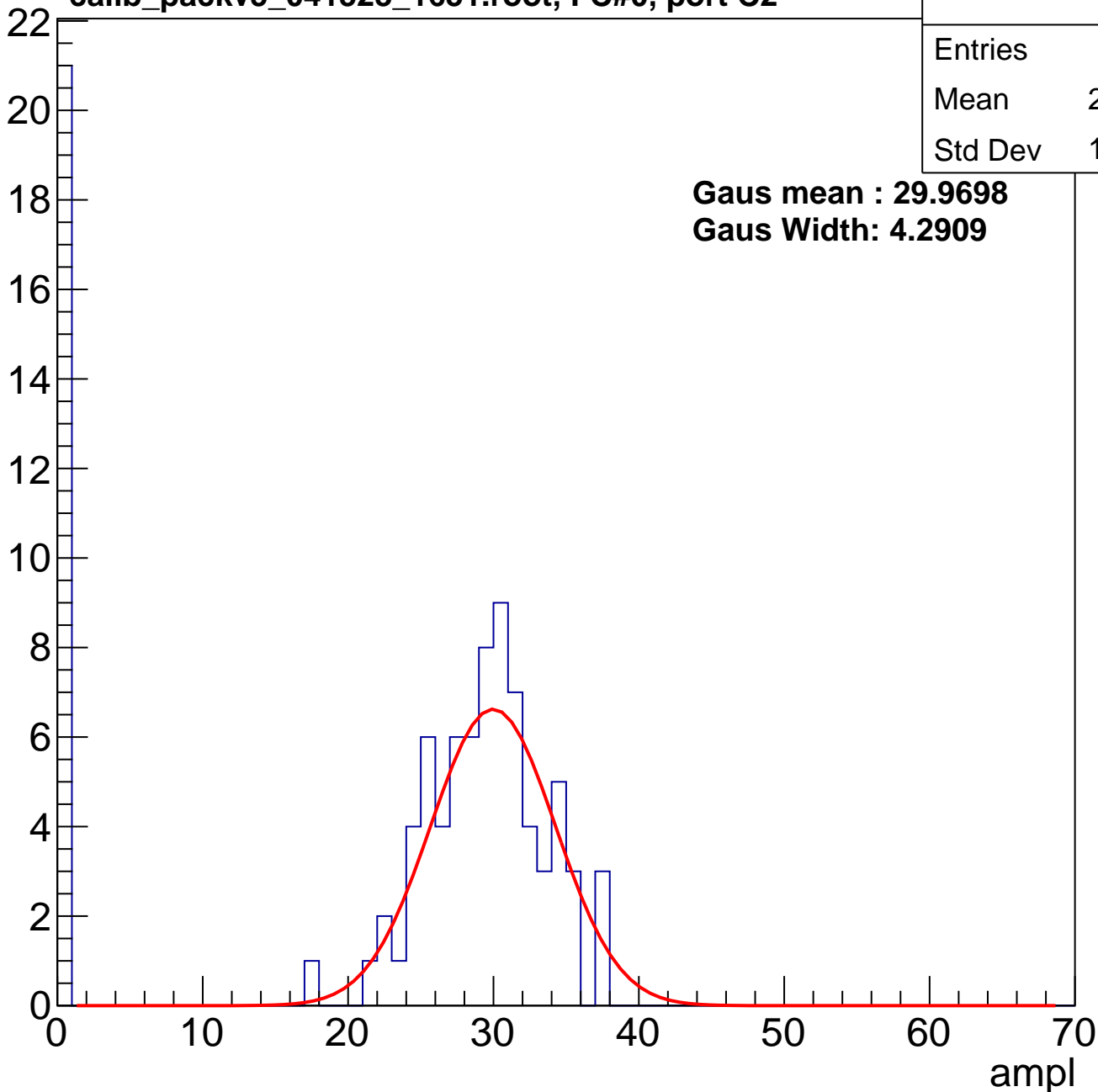
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	22.52
Std Dev	12.58

Gaus mean : 29.9698

Gaus Width: 4.2909

Entry



B1L103S, U18-ch61, adc1

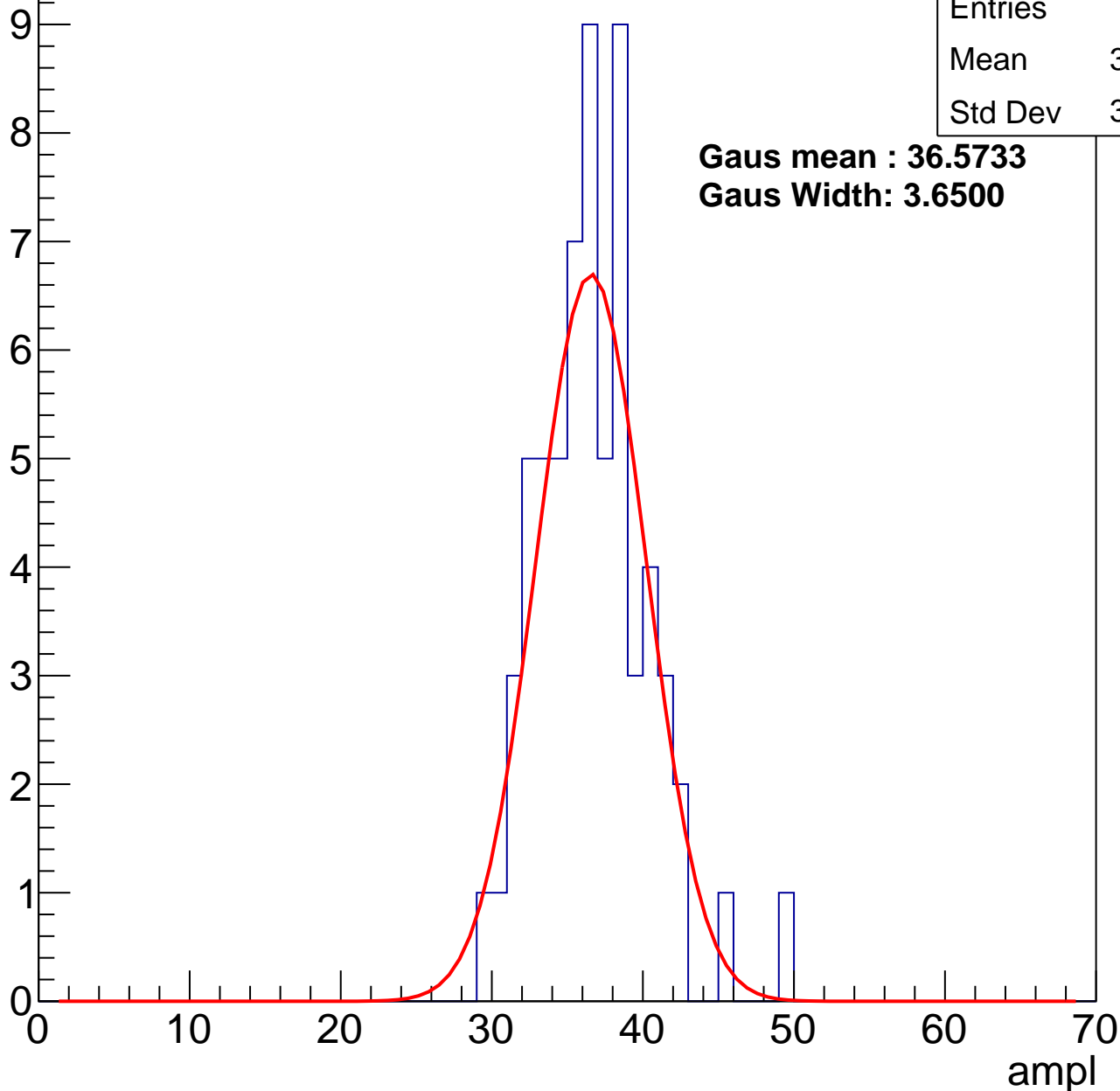
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.27
Std Dev	3.615

Gaus mean : 36.5733

Gaus Width: 3.6500



B1L103S, U18-ch61, adc2

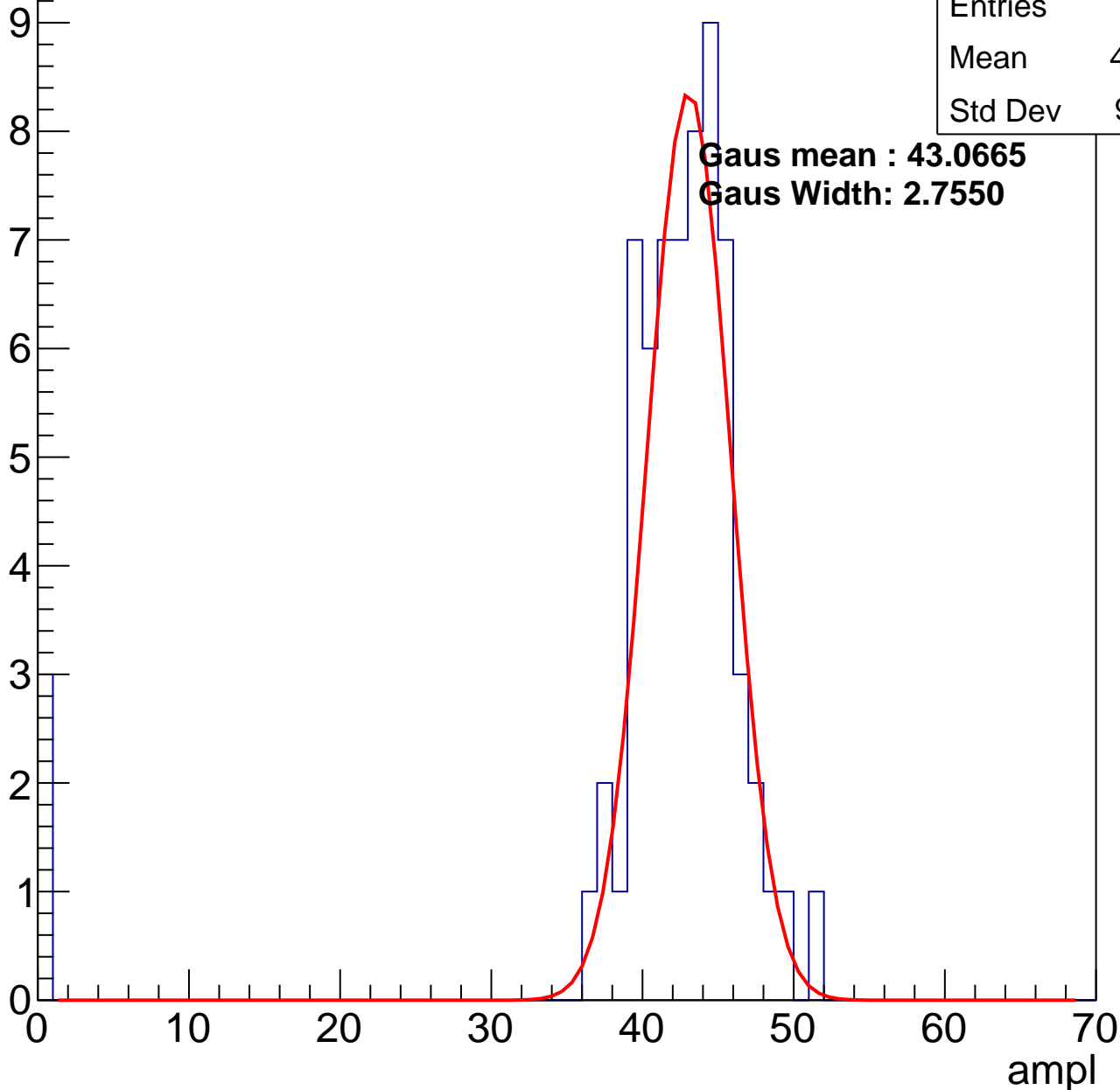
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	40.56
Std Dev	9.311

Gaus mean : 43.0665

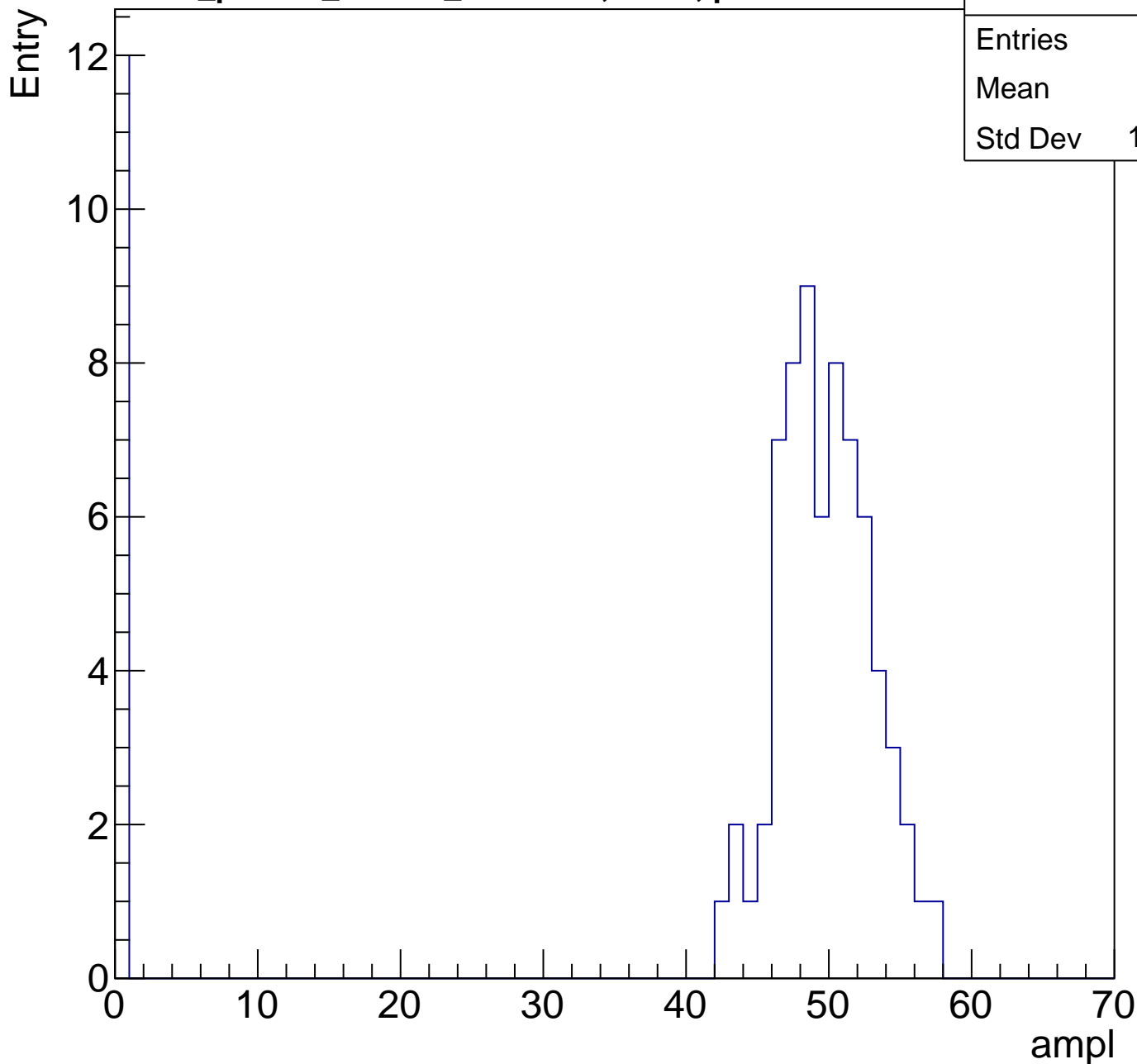
Gaus Width: 2.7550



B1L103S, U18-ch61, adc3

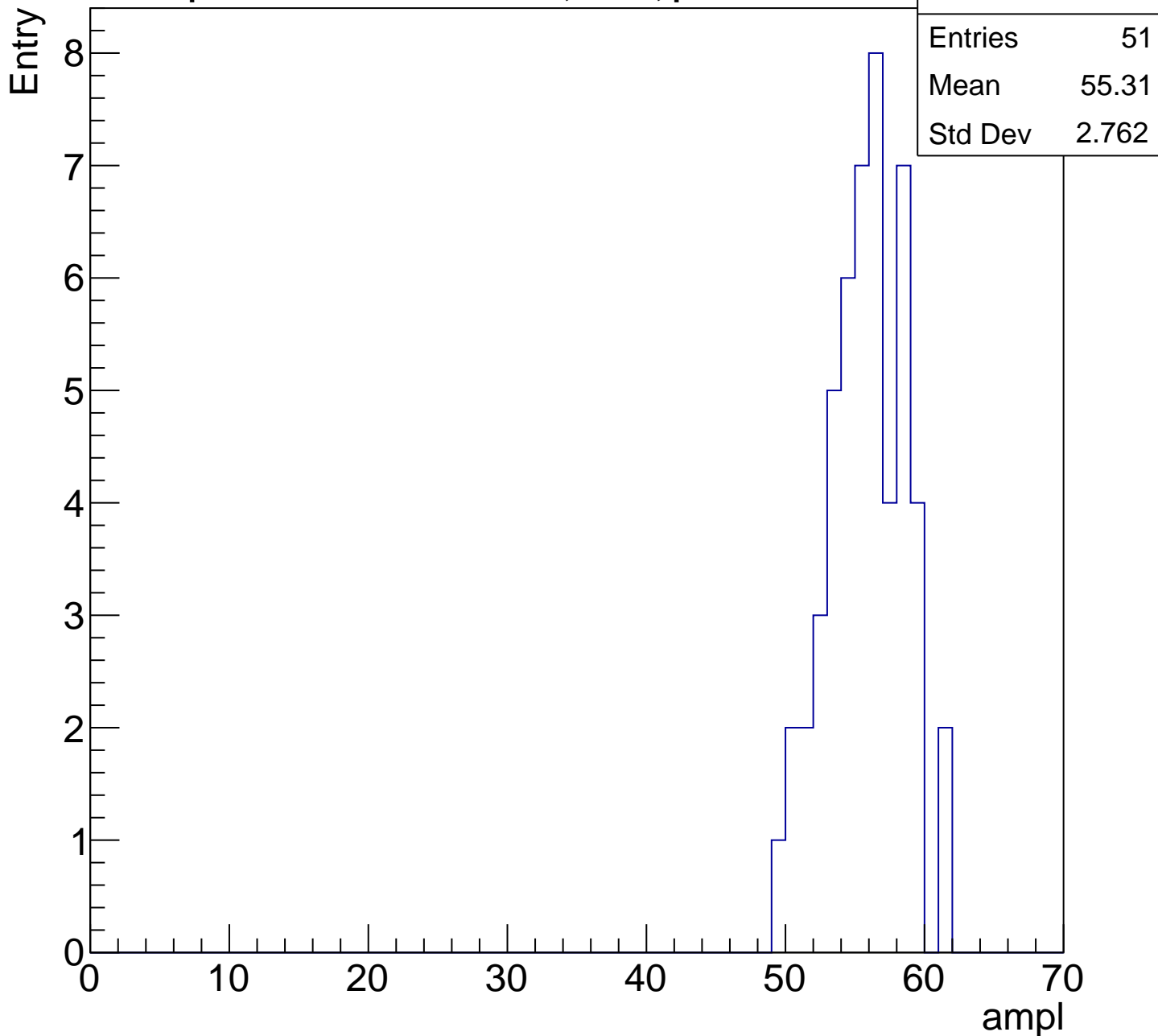
calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	41.9
Std Dev	17.84



B1L103S, U18-ch61, adc4

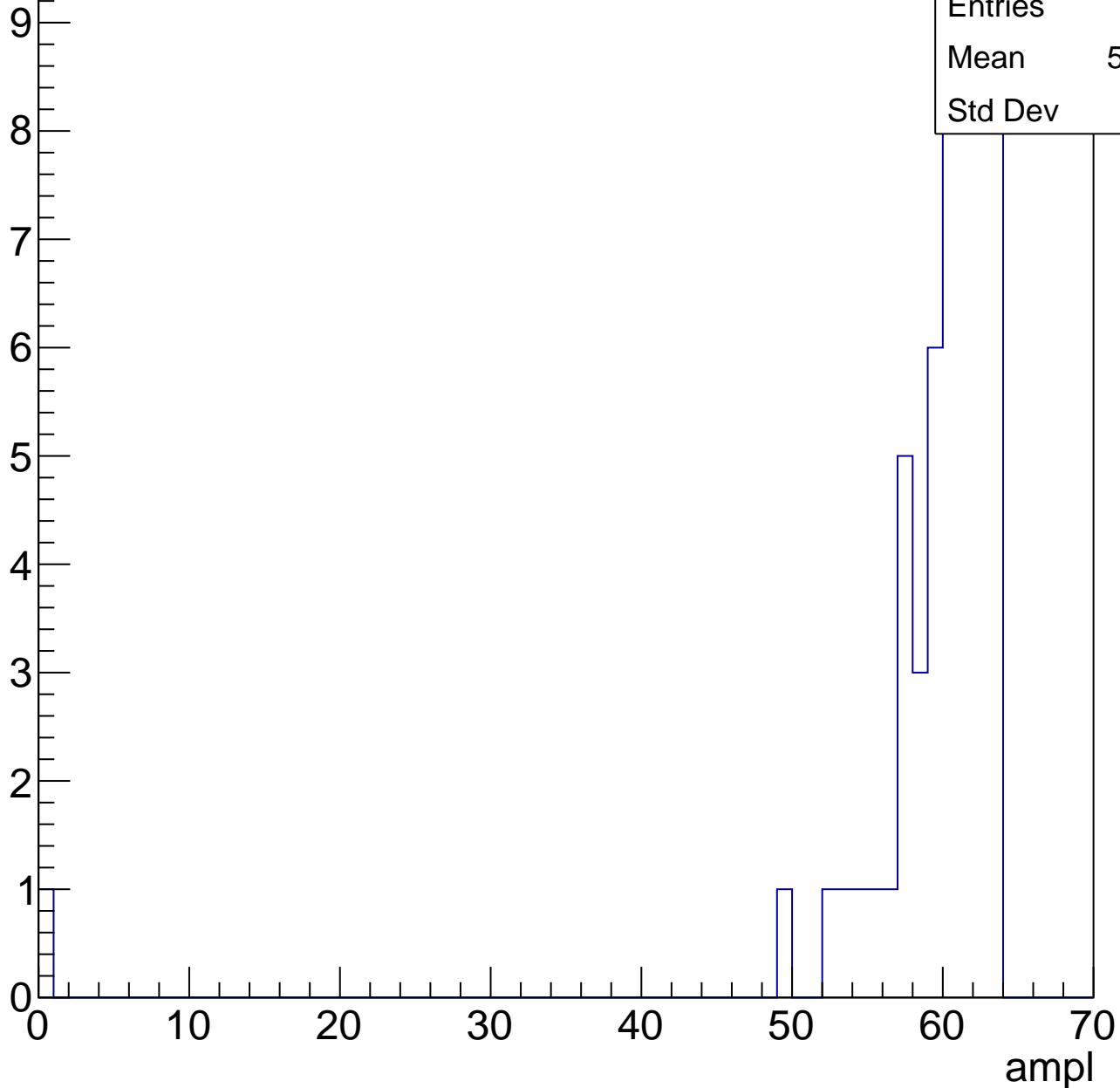
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U18-ch61, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch61, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch61, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch62, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	101
Mean	24.05
Std Dev	9.809

Gaus mean : 27.8790

Gaus Width: 4.1638

Entry

12

10

8

6

4

2

0

0

10

20

30

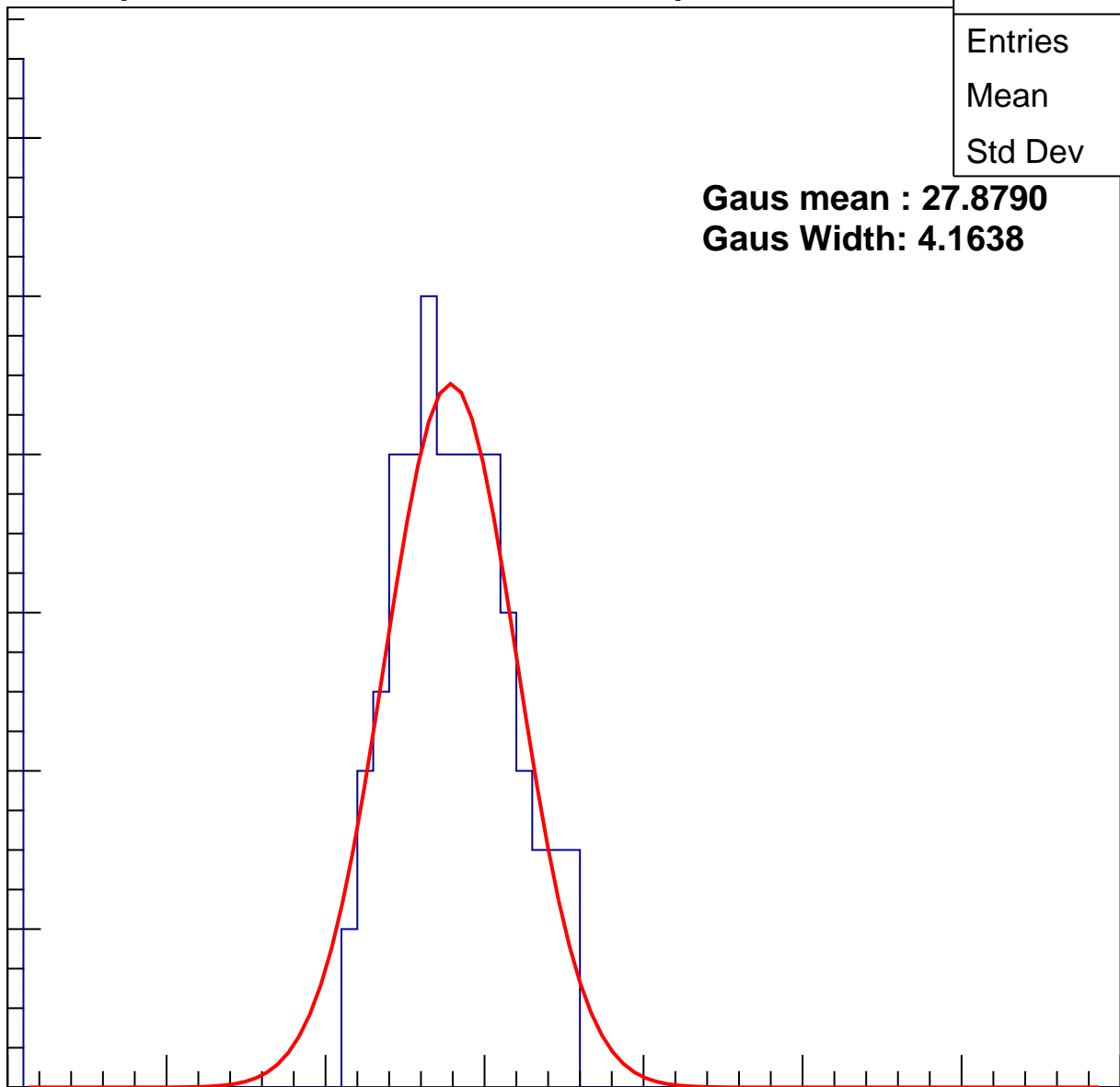
40

50

60

70

ampl



B1L103S, U18-ch62, adc1

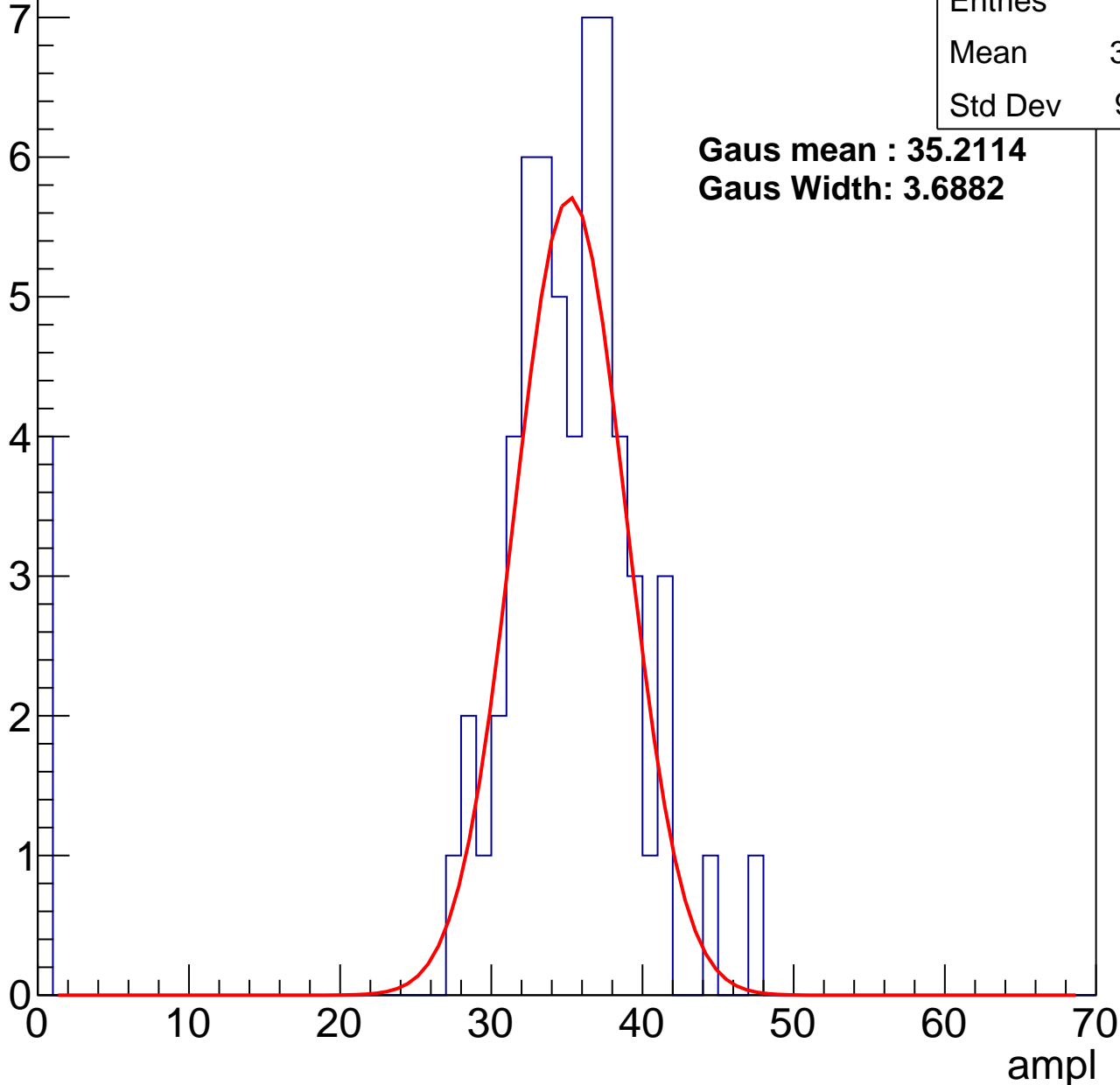
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	32.74
Std Dev	9.381

Gaus mean : 35.2114

Gaus Width: 3.6882



B1L103S, U18-ch62, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	34.2
Std Dev	15.45

Gaus mean : 41.1482

Gaus Width: 4.0872

Entry

12

10

8

6

4

2

0

0

10

20

30

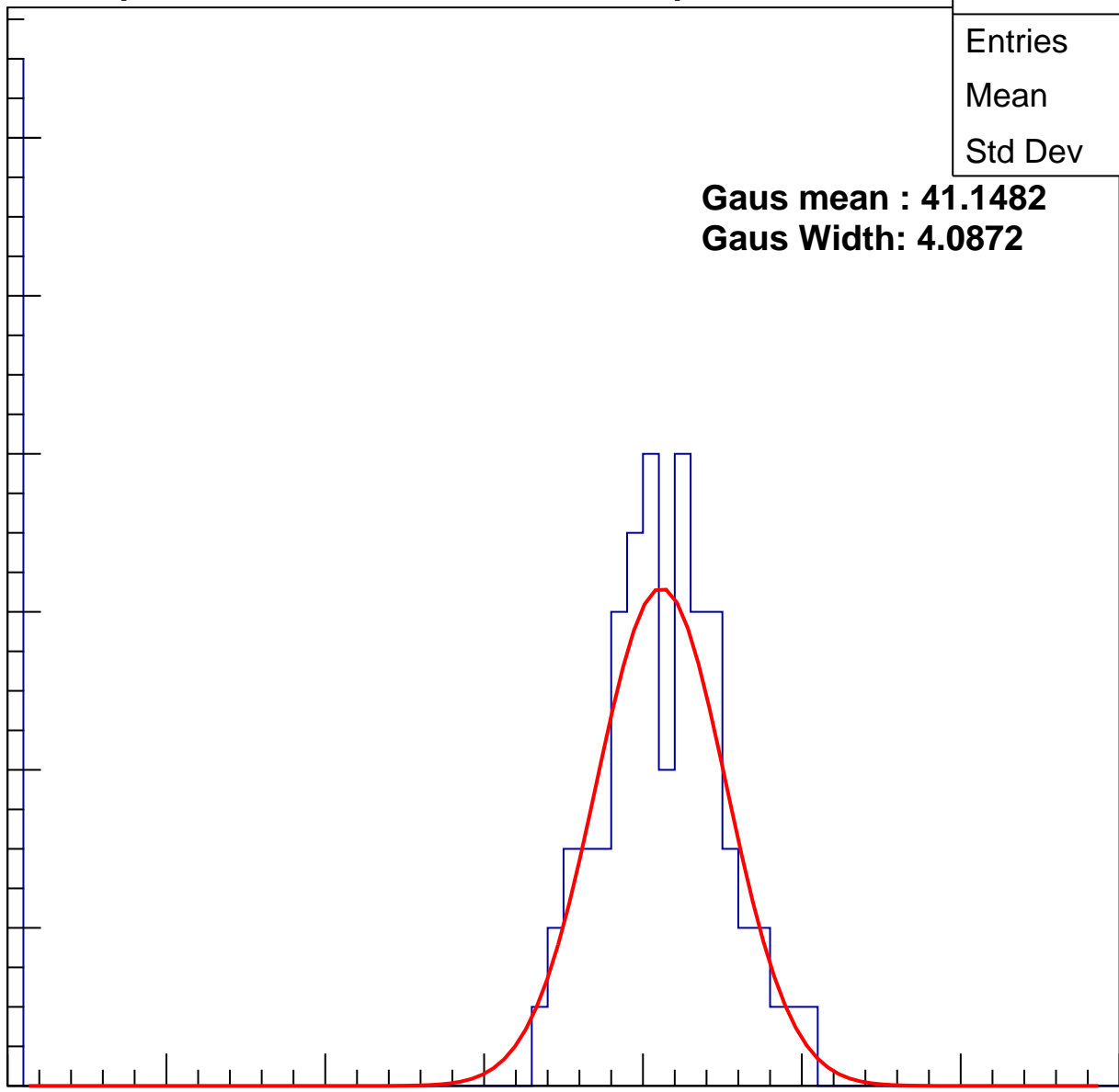
40

50

60

70

ampl

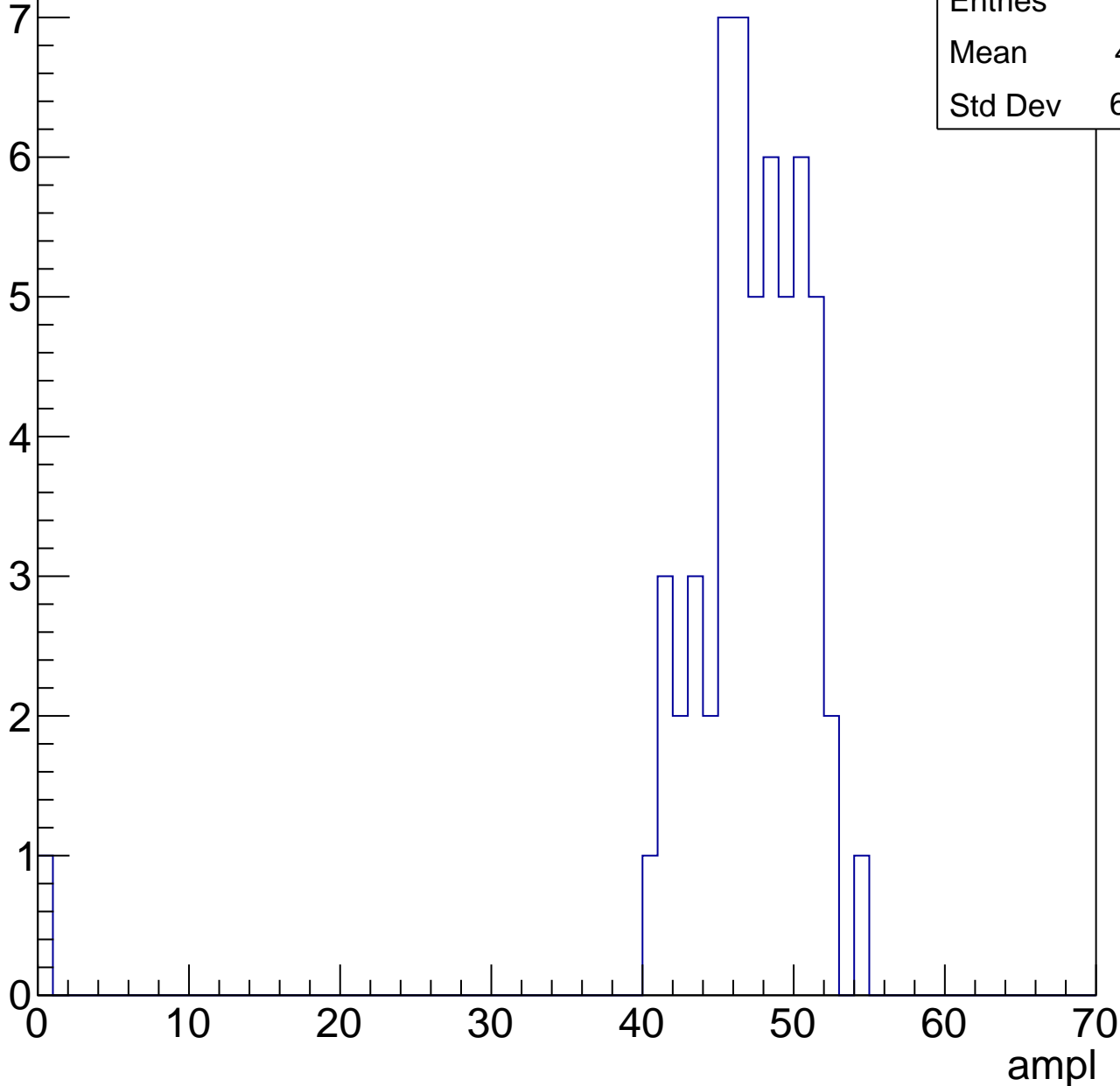


B1L103S, U18-ch62, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	46.11
Std Dev	6.984



B1L103S, U18-ch62, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	53.18
Std Dev	3.892

Entry

10

8

6

4

2

0

0

10

20

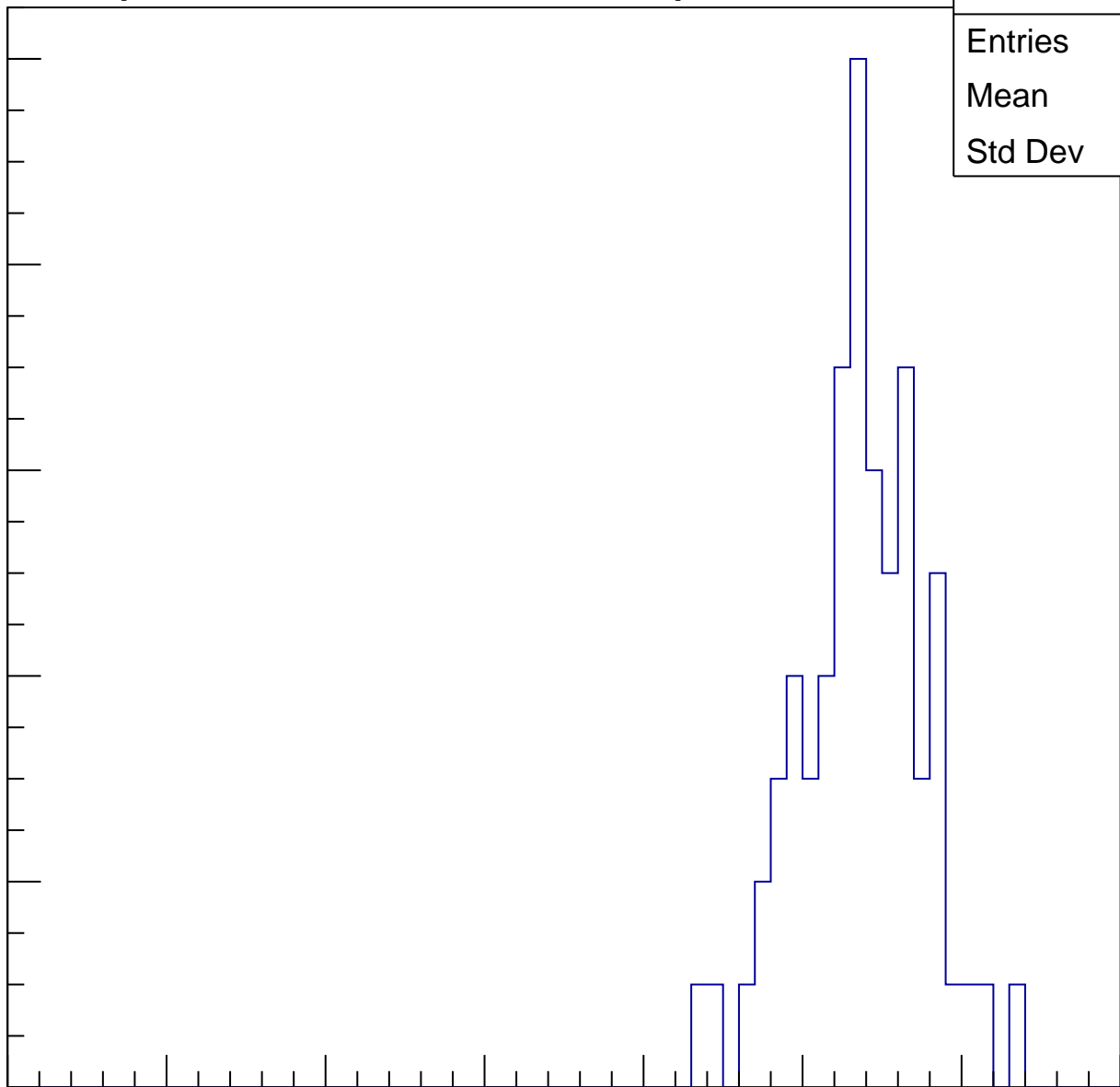
30

40

50

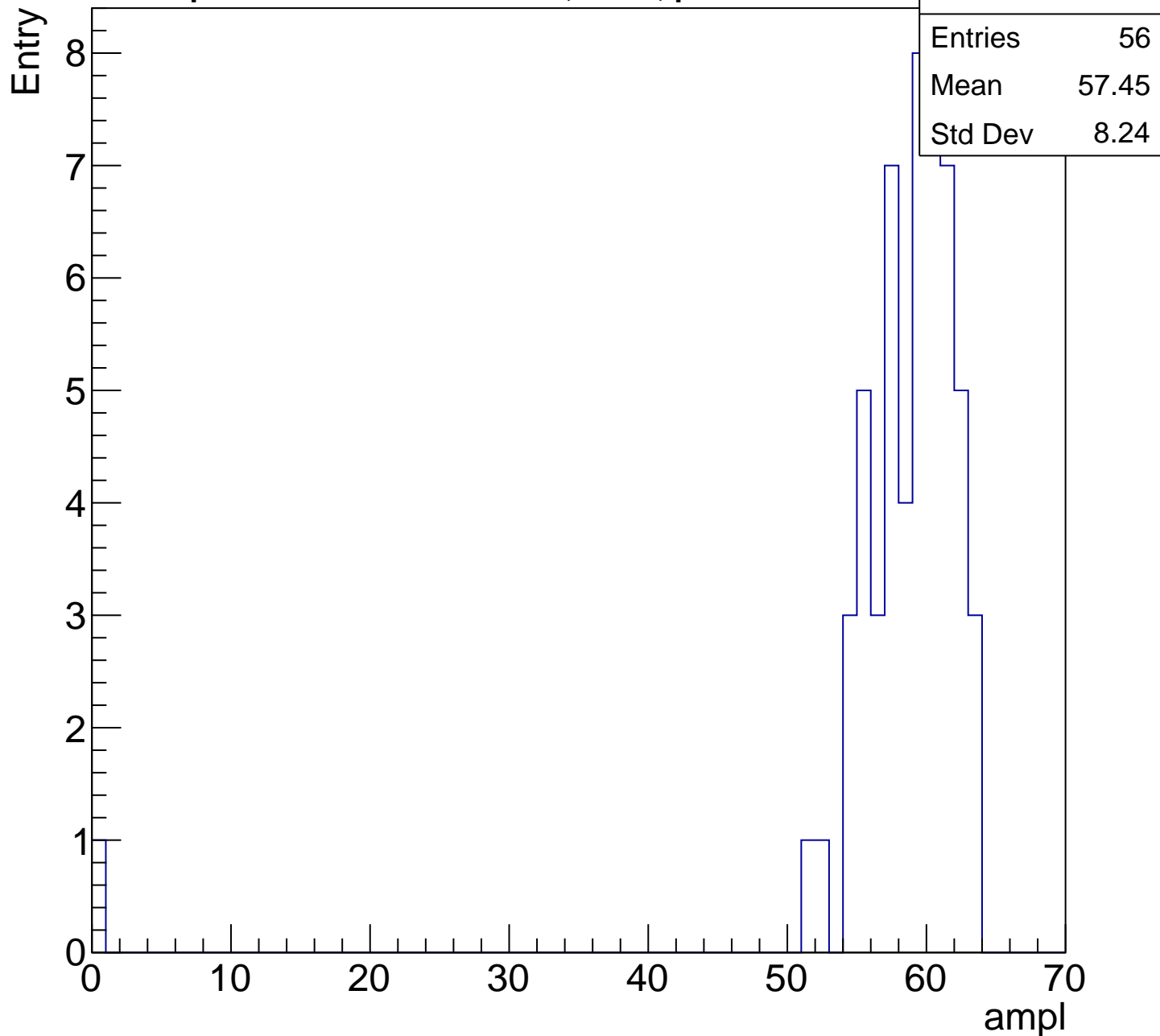
60

ampl



B1L103S, U18-ch62, adc5

calib_packv5_041523_1651.root, FC#0, port C2

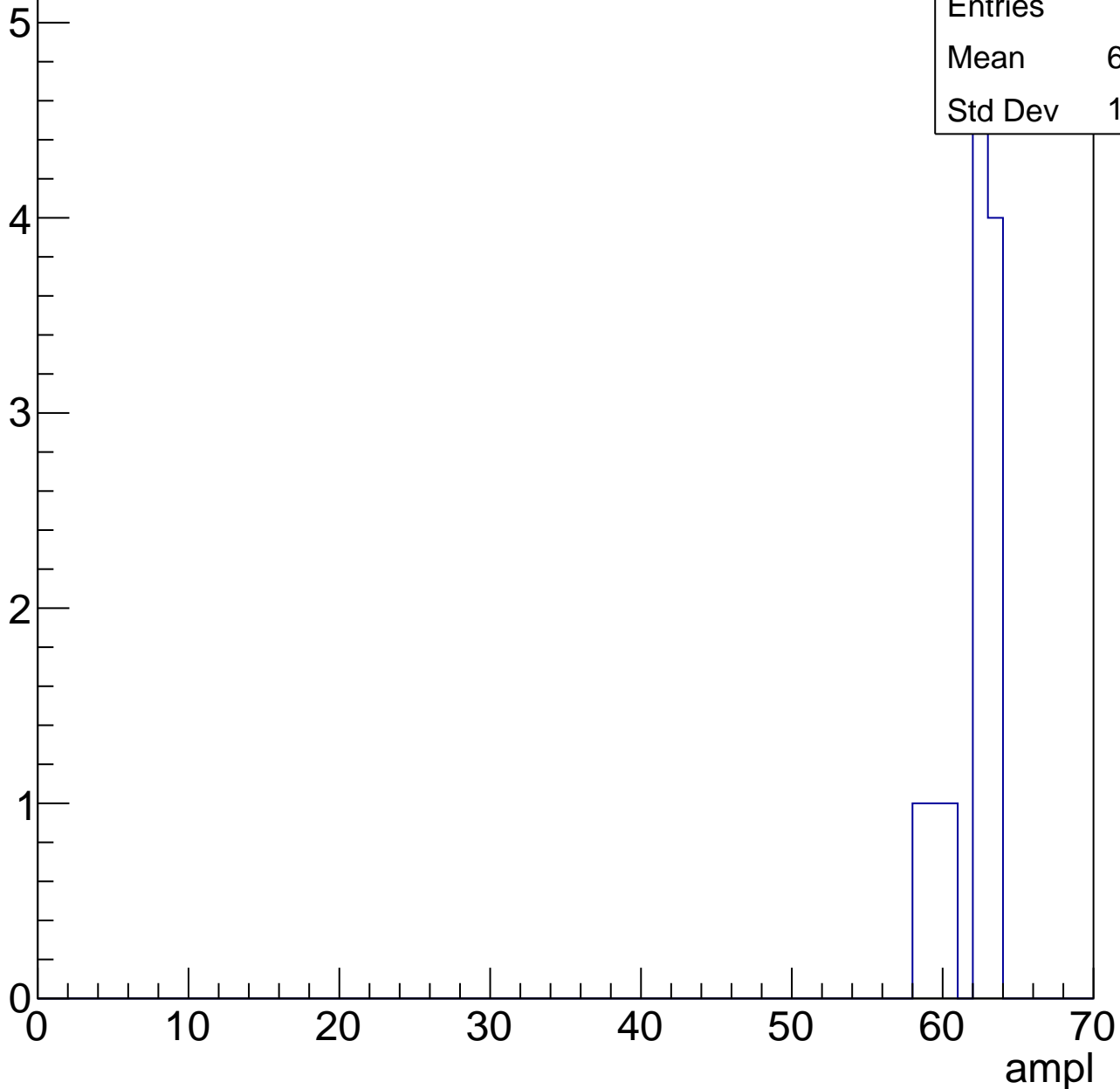


B1L103S, U18-ch62, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.58
Std Dev	1.605

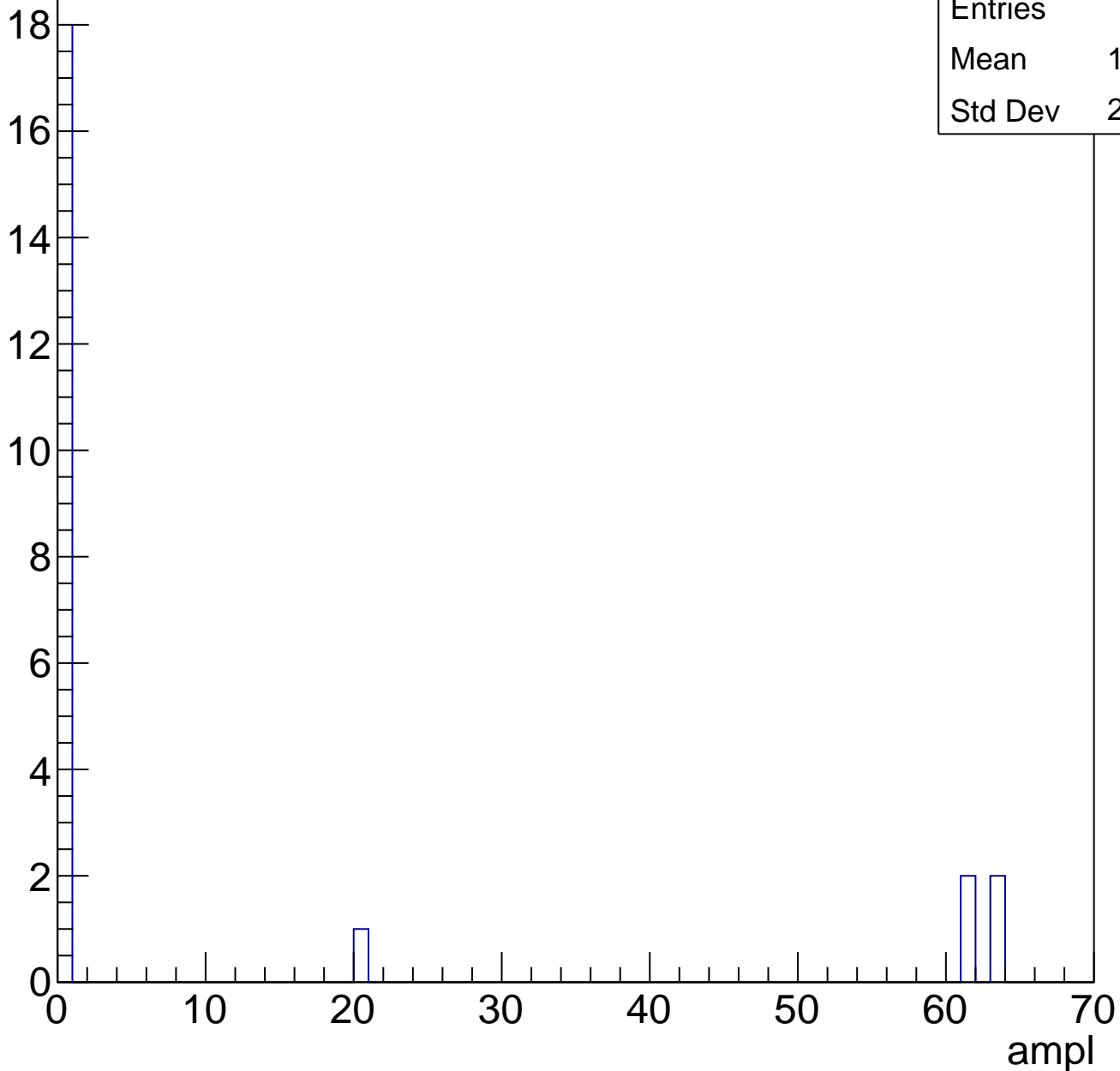


B1L103S, U18-ch62, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	11.65
Std Dev	23.46

Entry



B1L103S, U18-ch63, adc0

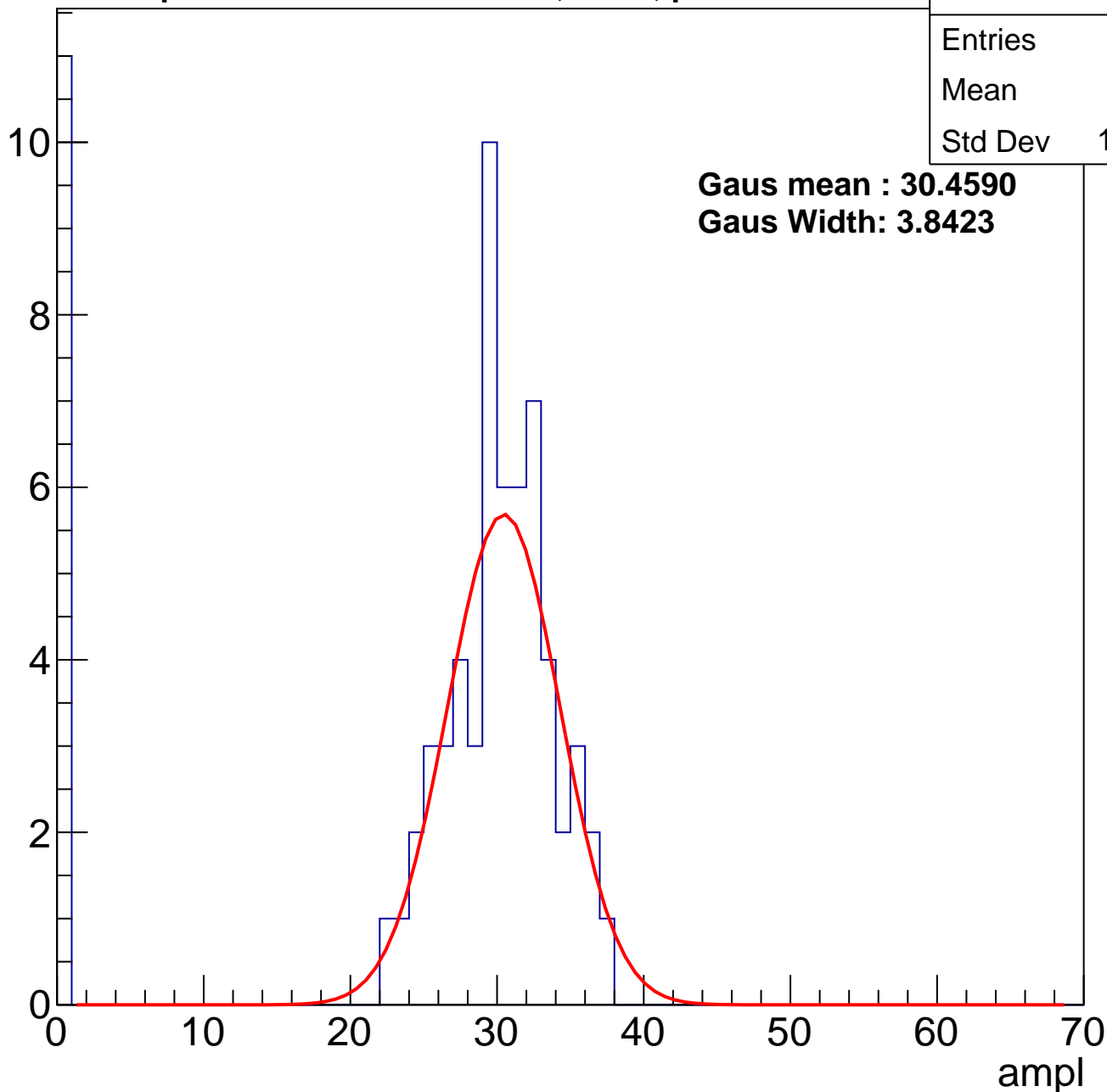
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	25.1
Std Dev	11.36

Gaus mean : 30.4590

Gaus Width: 3.8423

Entry



B1L103S, U18-ch63, adc1

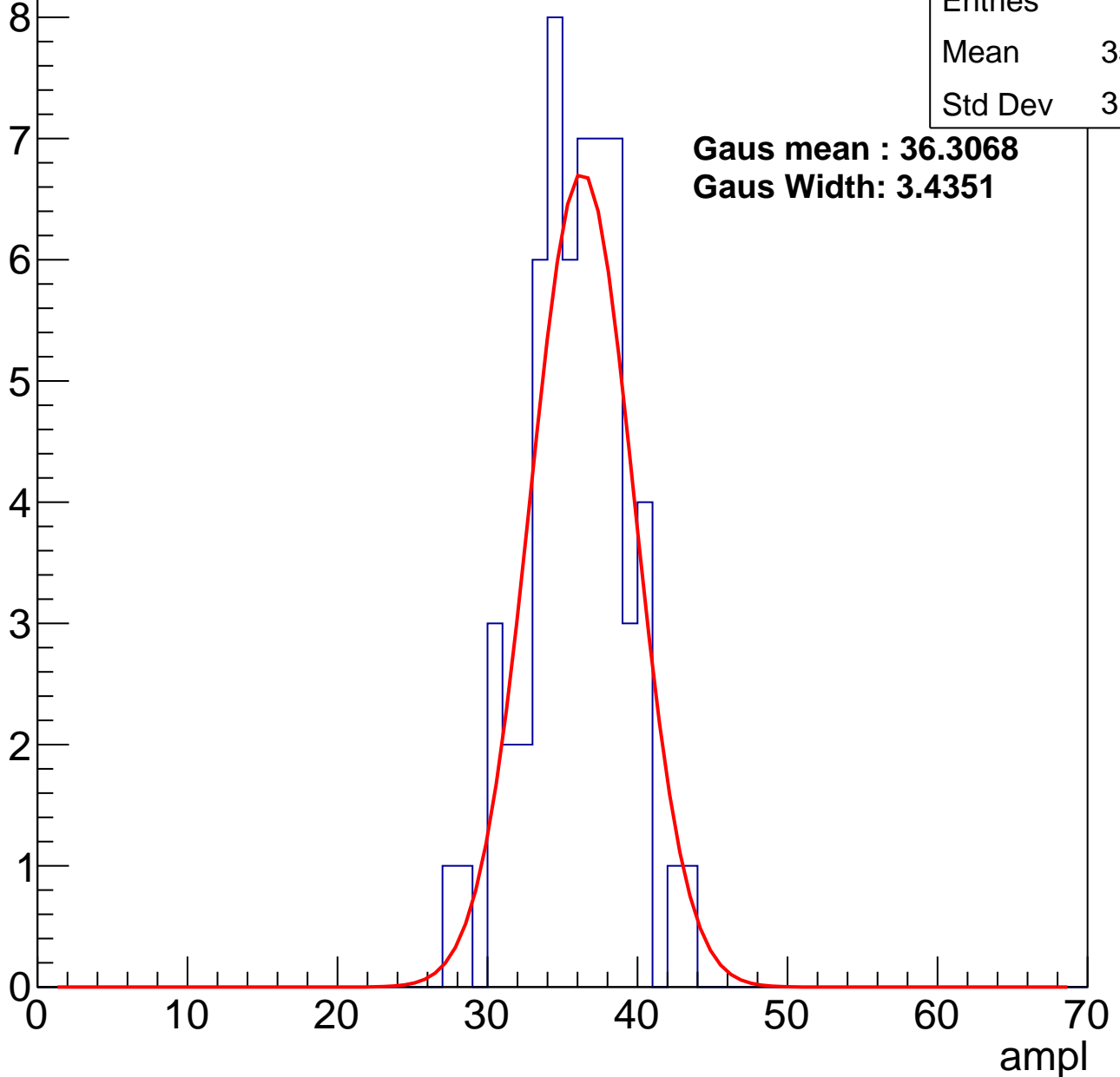
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	35.42
Std Dev	3.238

Gaus mean : 36.3068

Gaus Width: 3.4351



B1L103S, U18-ch63, adc2

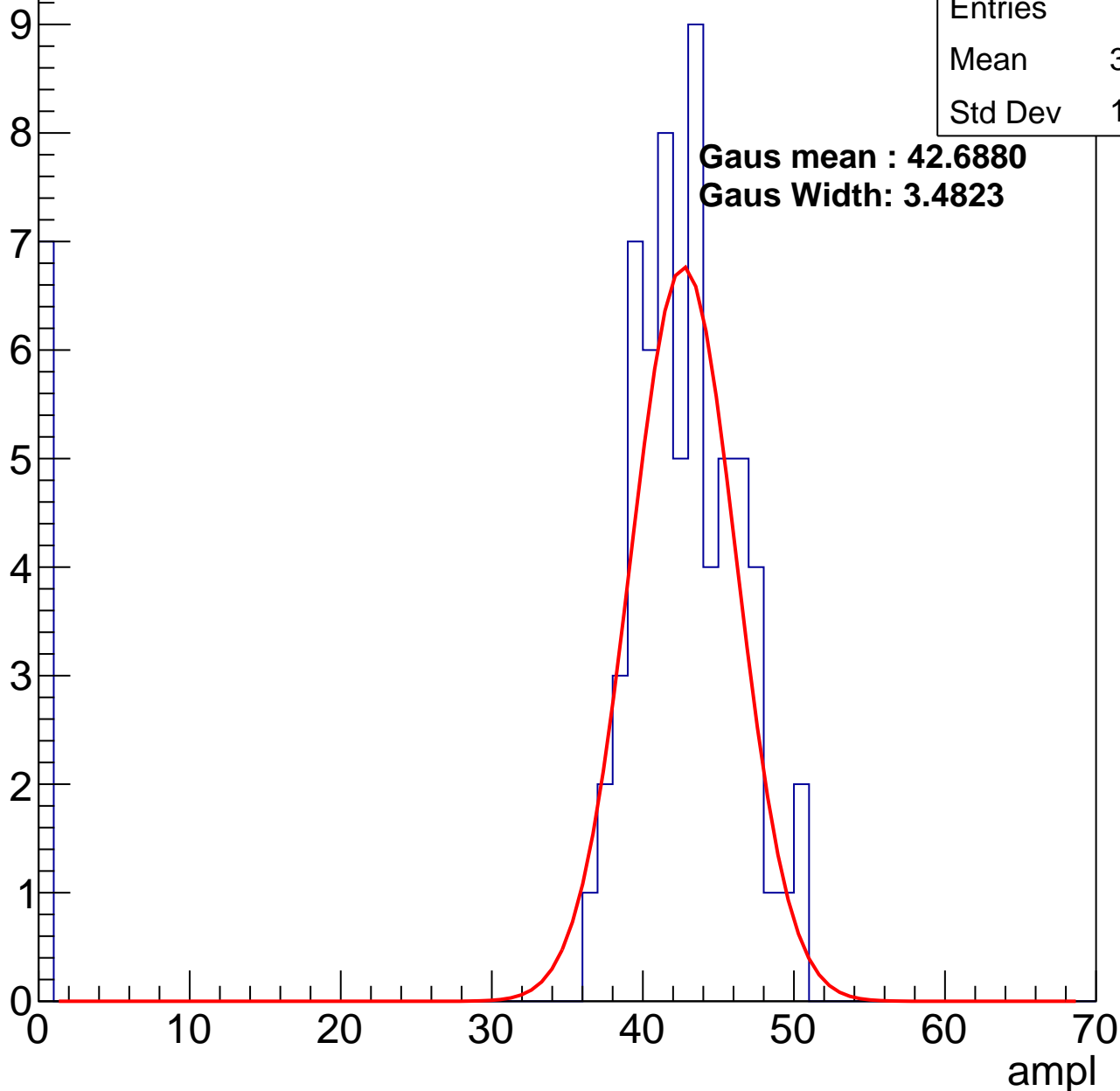
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	38.26
Std Dev	13.13

Gaus mean : 42.6880

Gaus Width: 3.4823

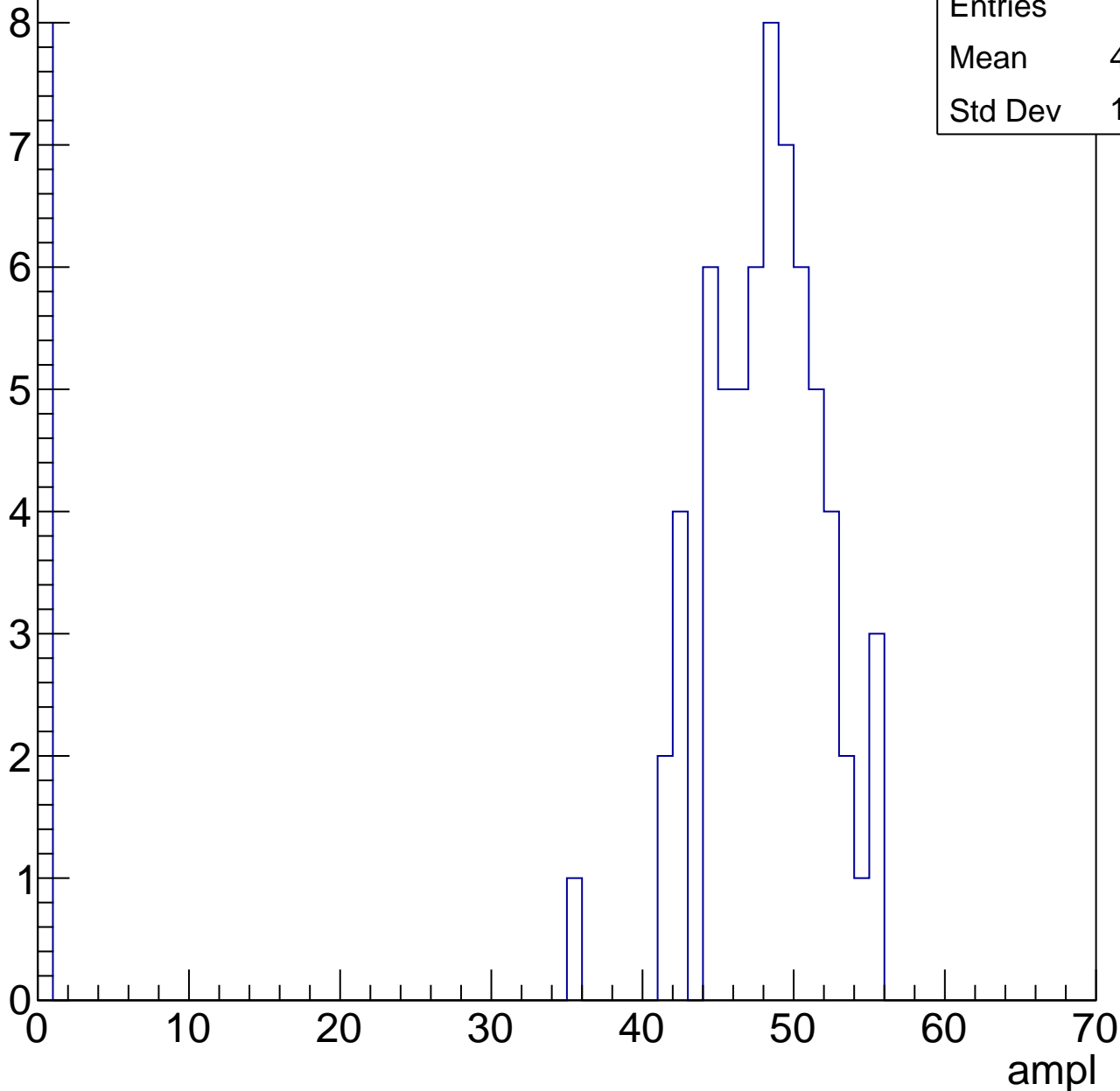


B1L103S, U18-ch63, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	42.48
Std Dev	15.33

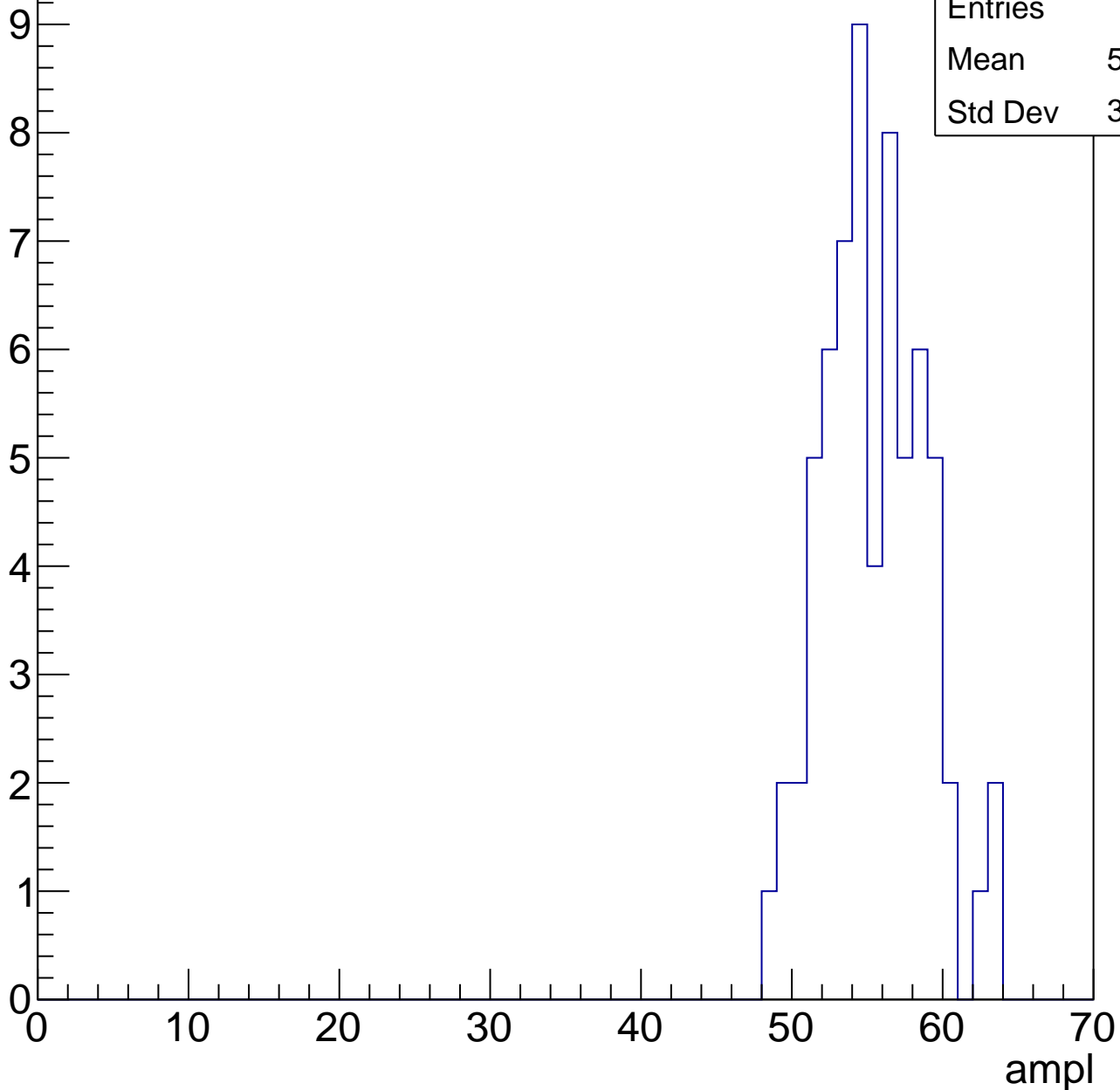


B1L103S, U18-ch63, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

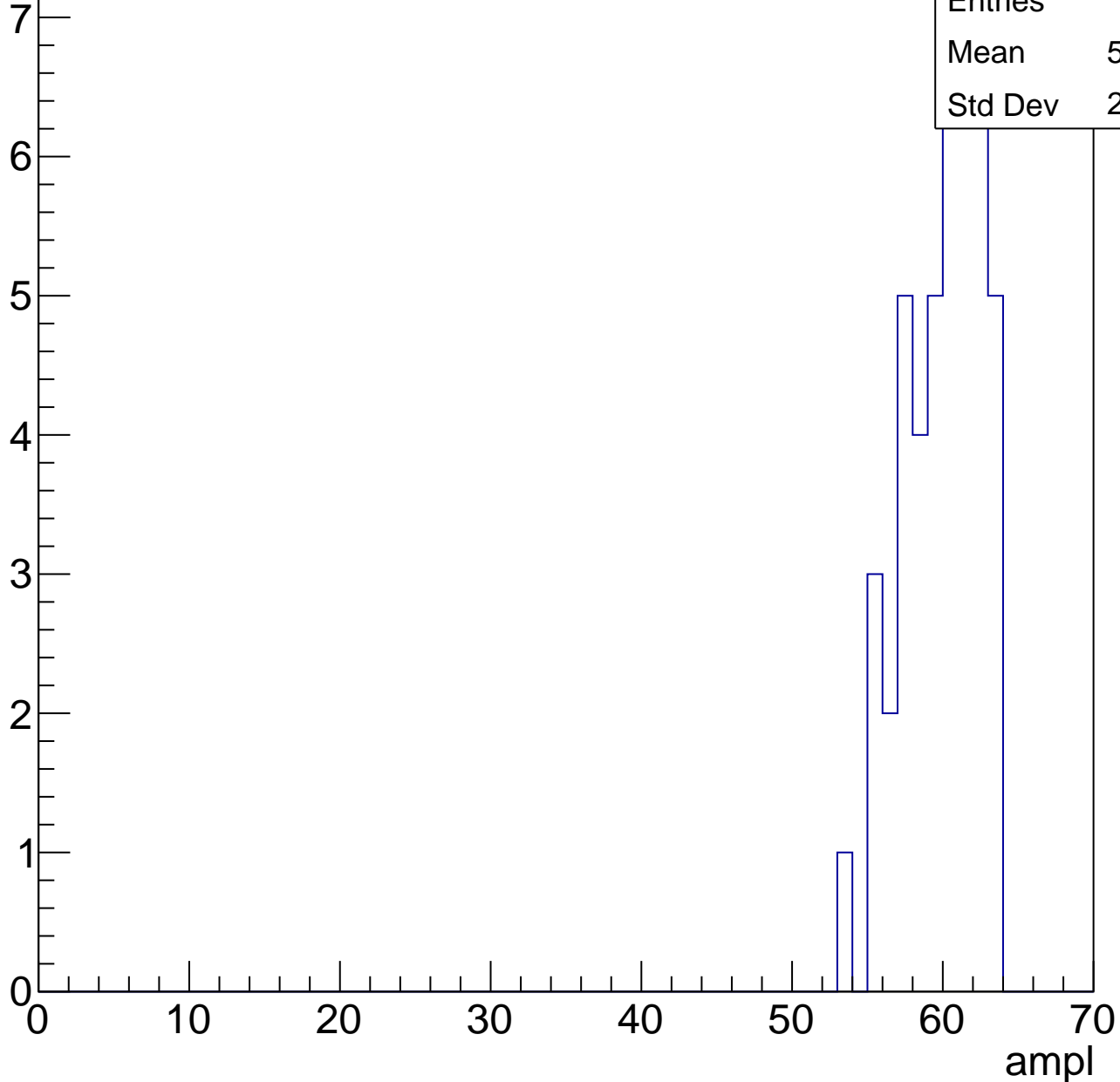
Entries	65
Mean	54.98
Std Dev	3.353



B1L103S, U18-ch63, adc5

calib_packv5_041523_1651.root, FC#0, port C2

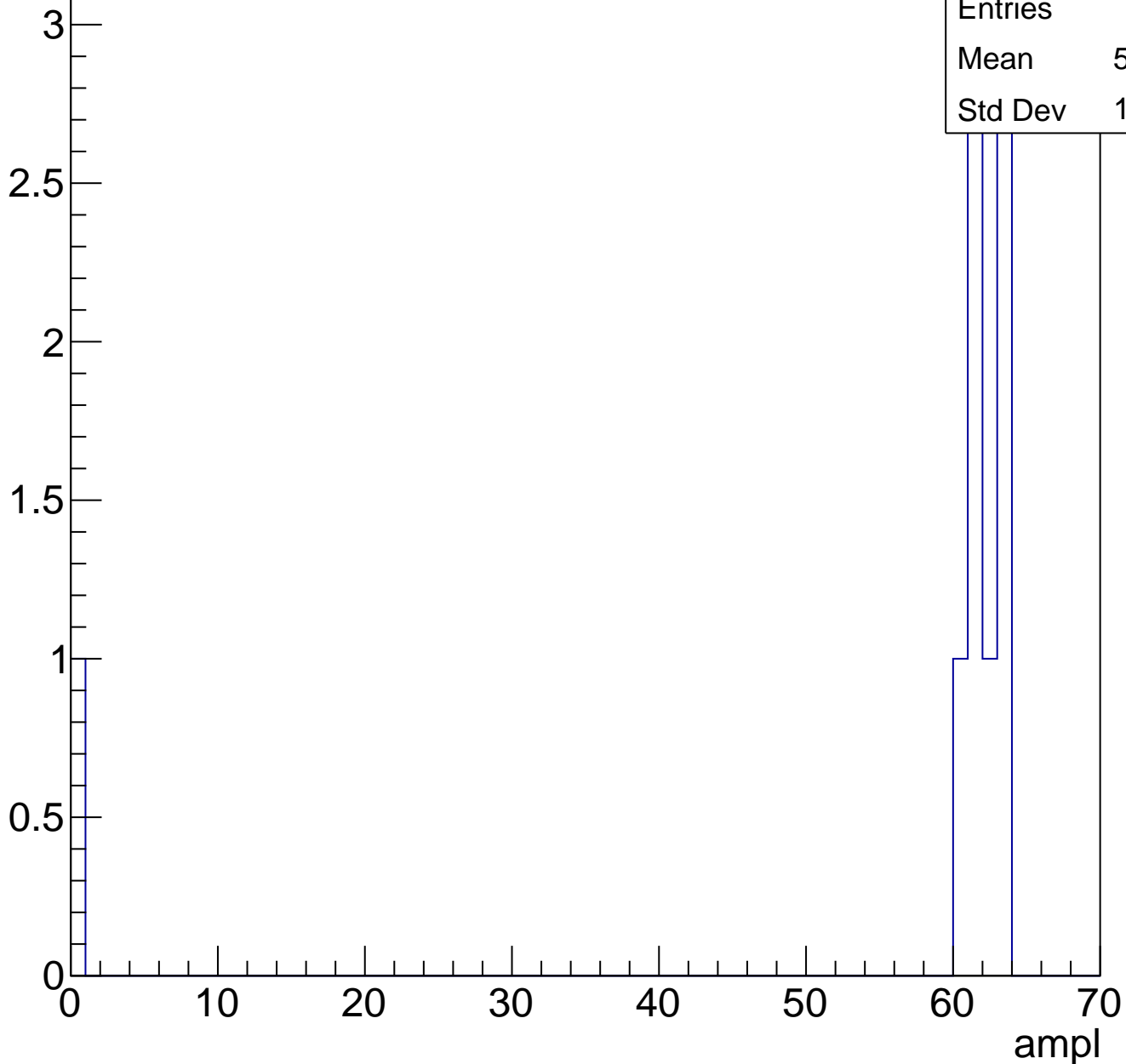
Entry



B1L103S, U18-ch63, adc6

calib_packv5_041523_1651.root, FC#0, port C2

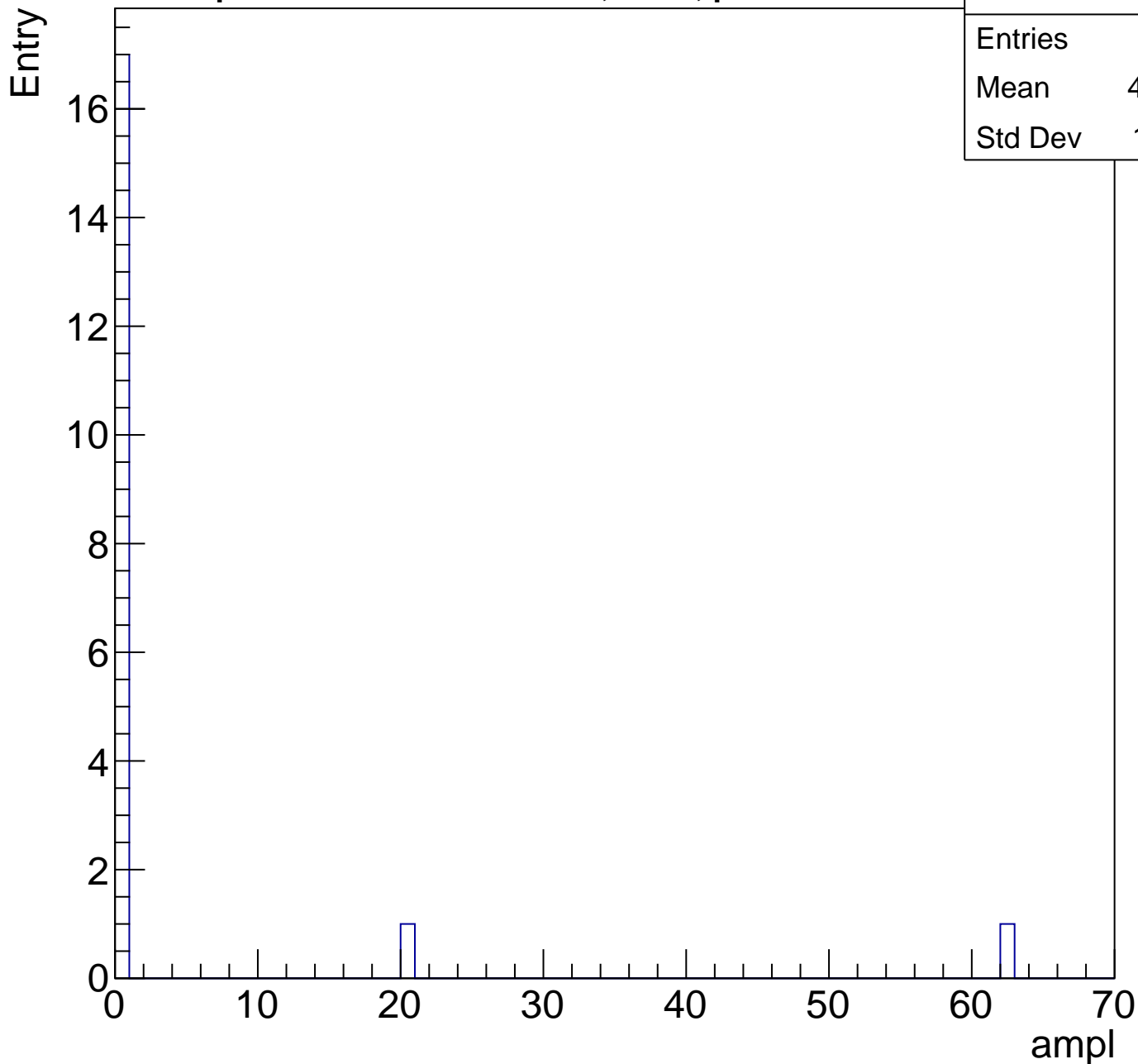
Entry



B1L103S, U18-ch63, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.316
Std Dev	14.31



B1L103S, U18-ch64, adc0

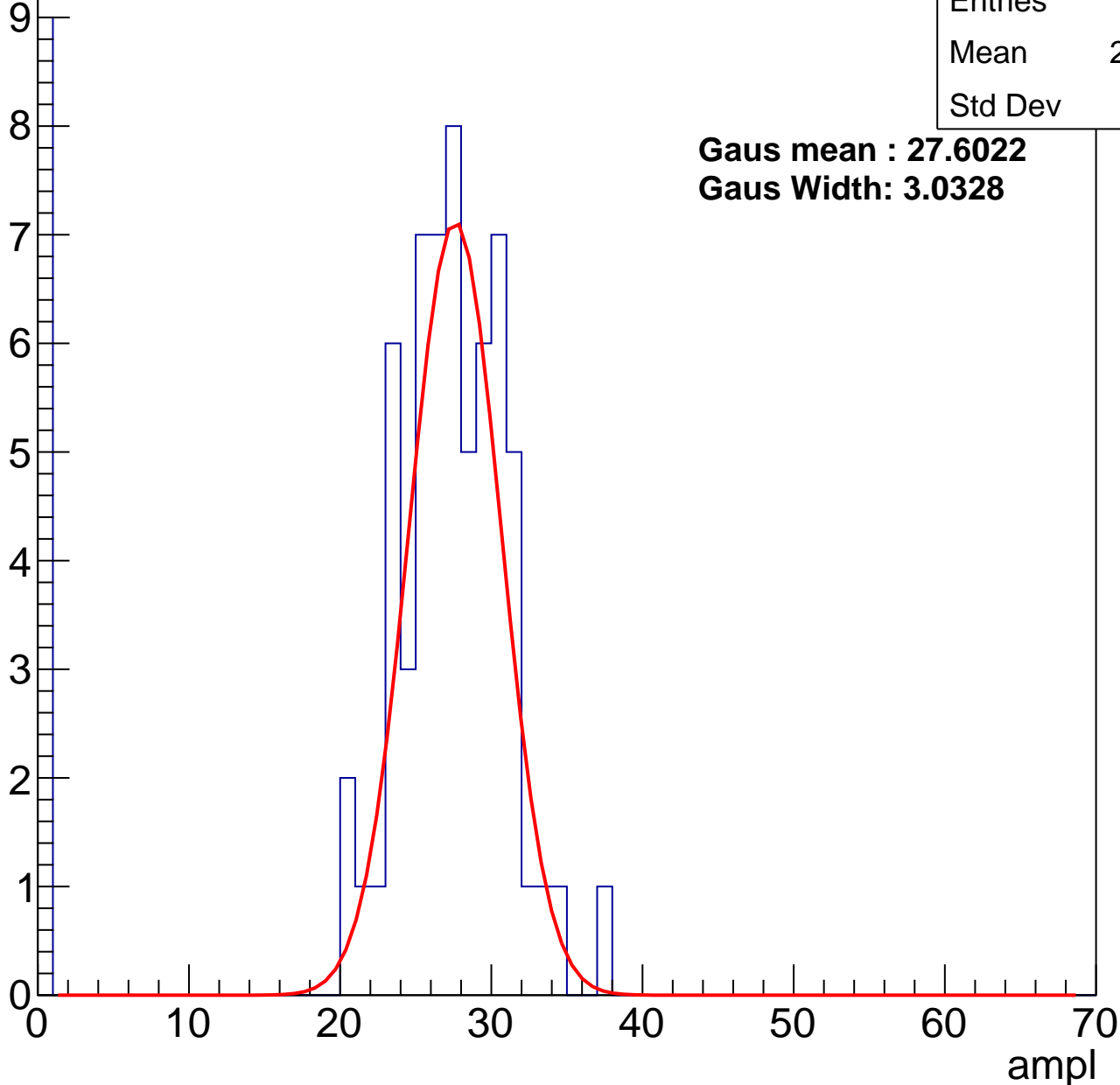
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	23.68
Std Dev	9.55

Gaus mean : 27.6022

Gaus Width: 3.0328



B1L103S, U18-ch64, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	34.04
Std Dev	3.969

Gaus mean : 34.1761

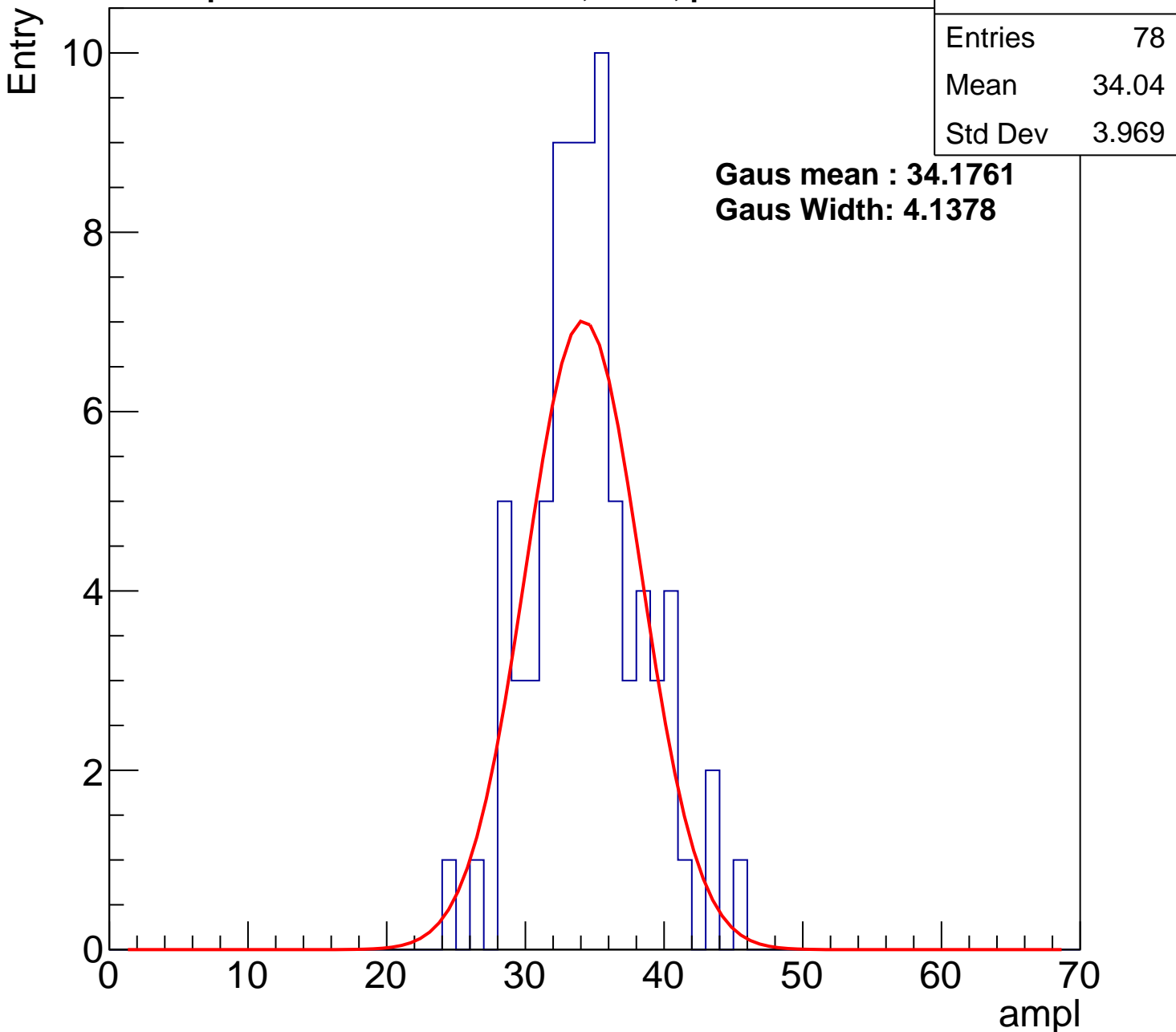
Gaus Width: 4.1378

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch64, adc2

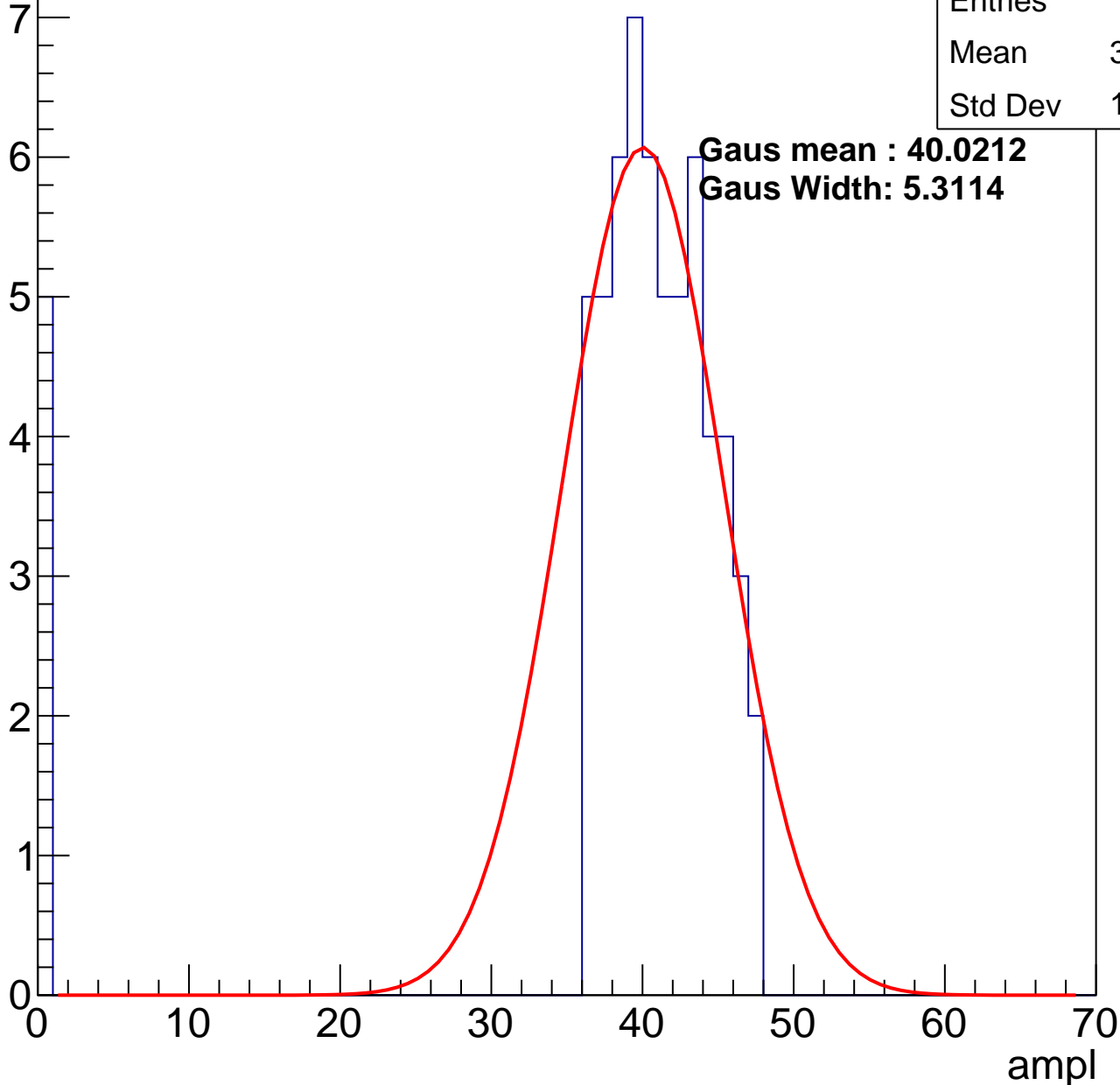
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	37.57
Std Dev	11.43

Gaus mean : 40.0212

Gaus Width: 5.3114

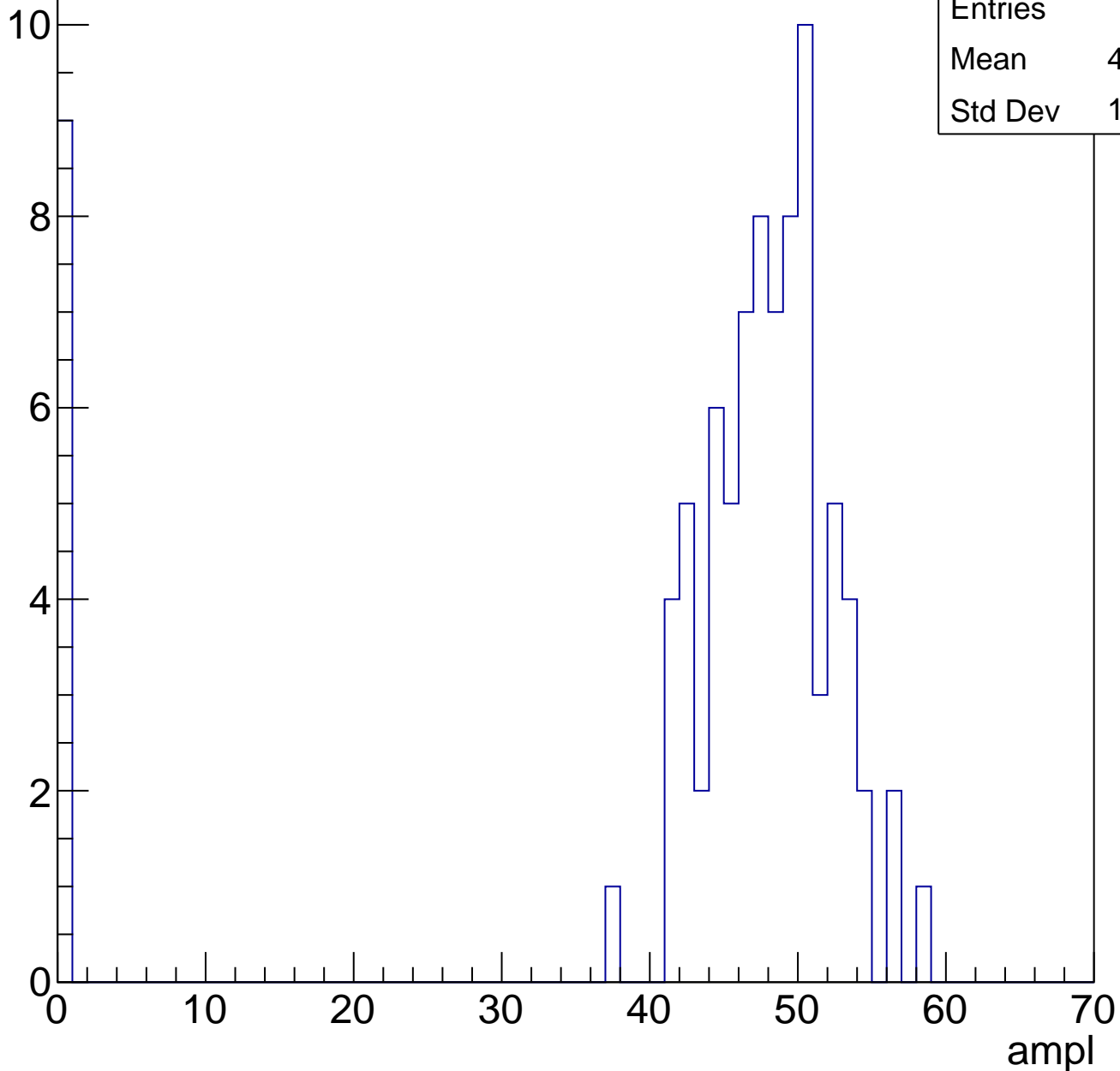


B1L103S, U18-ch64, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	42.87
Std Dev	14.87

Entry

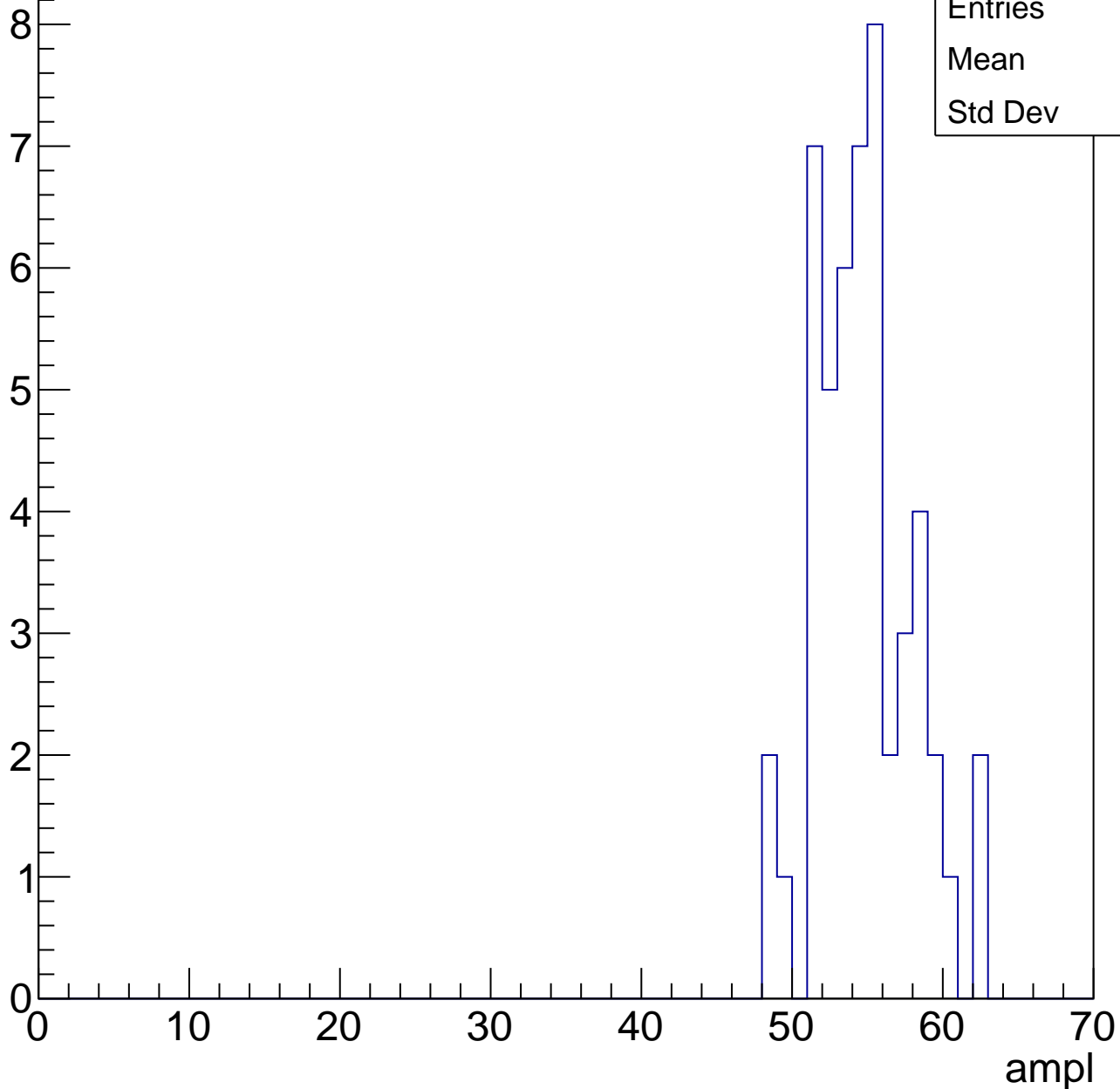


B1L103S, U18-ch64, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

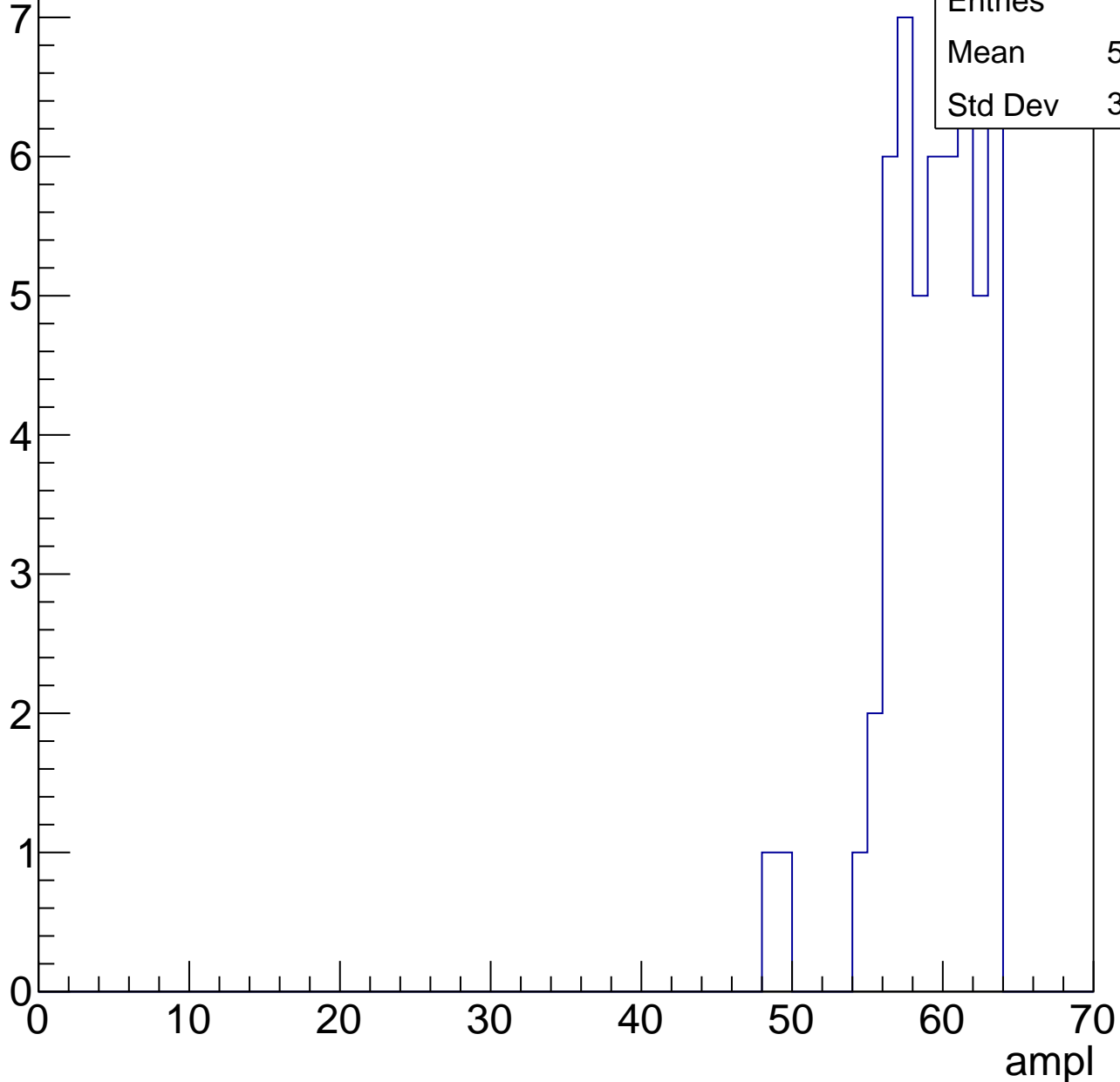
Entries	50
Mean	54.3
Std Dev	3.17



B1L103S, U18-ch64, adc5

calib_packv5_041523_1651.root, FC#0, port C2

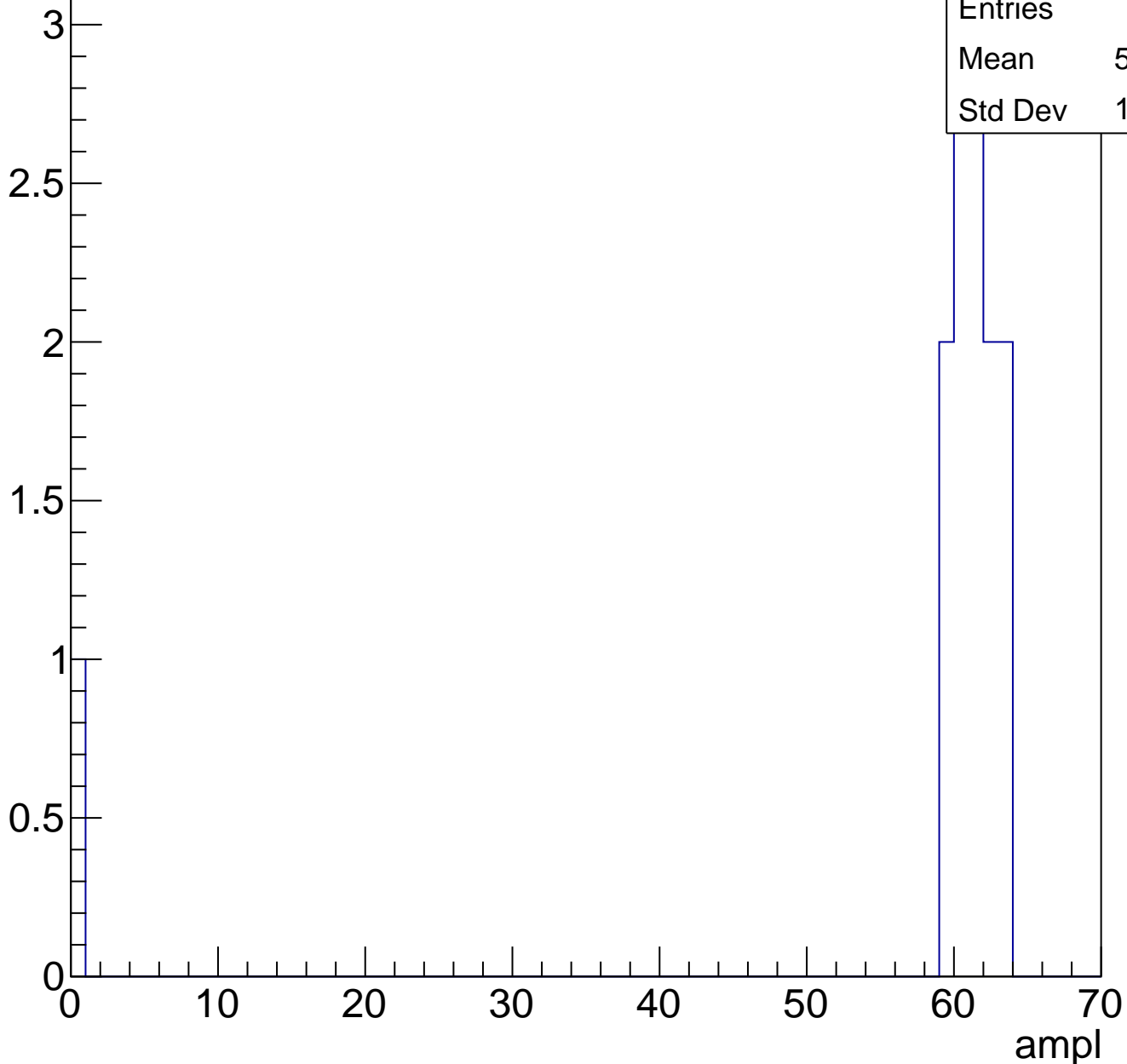
Entry



B1L103S, U18-ch64, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

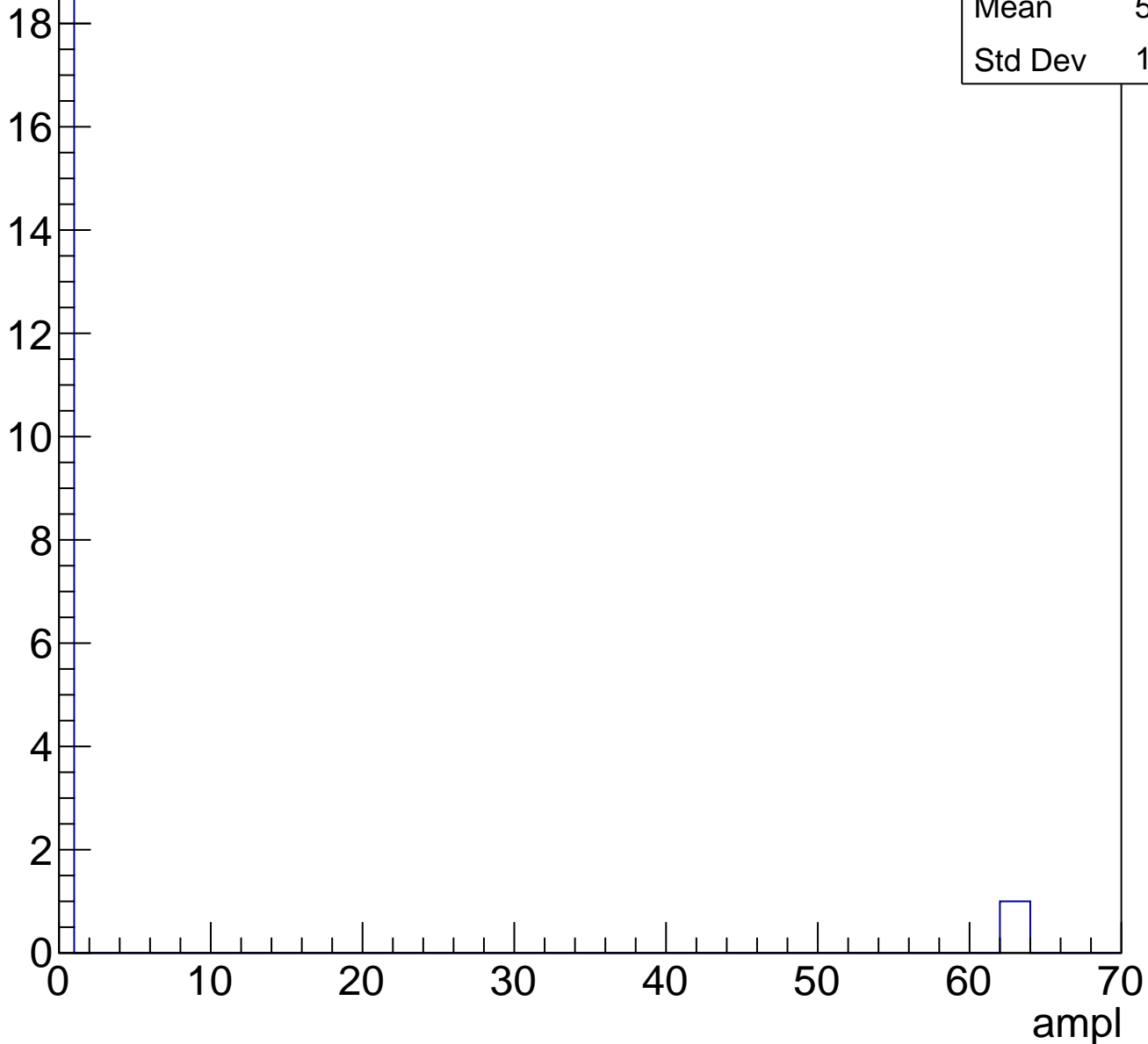


B1L103S, U18-ch64, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U18-ch65, adc0

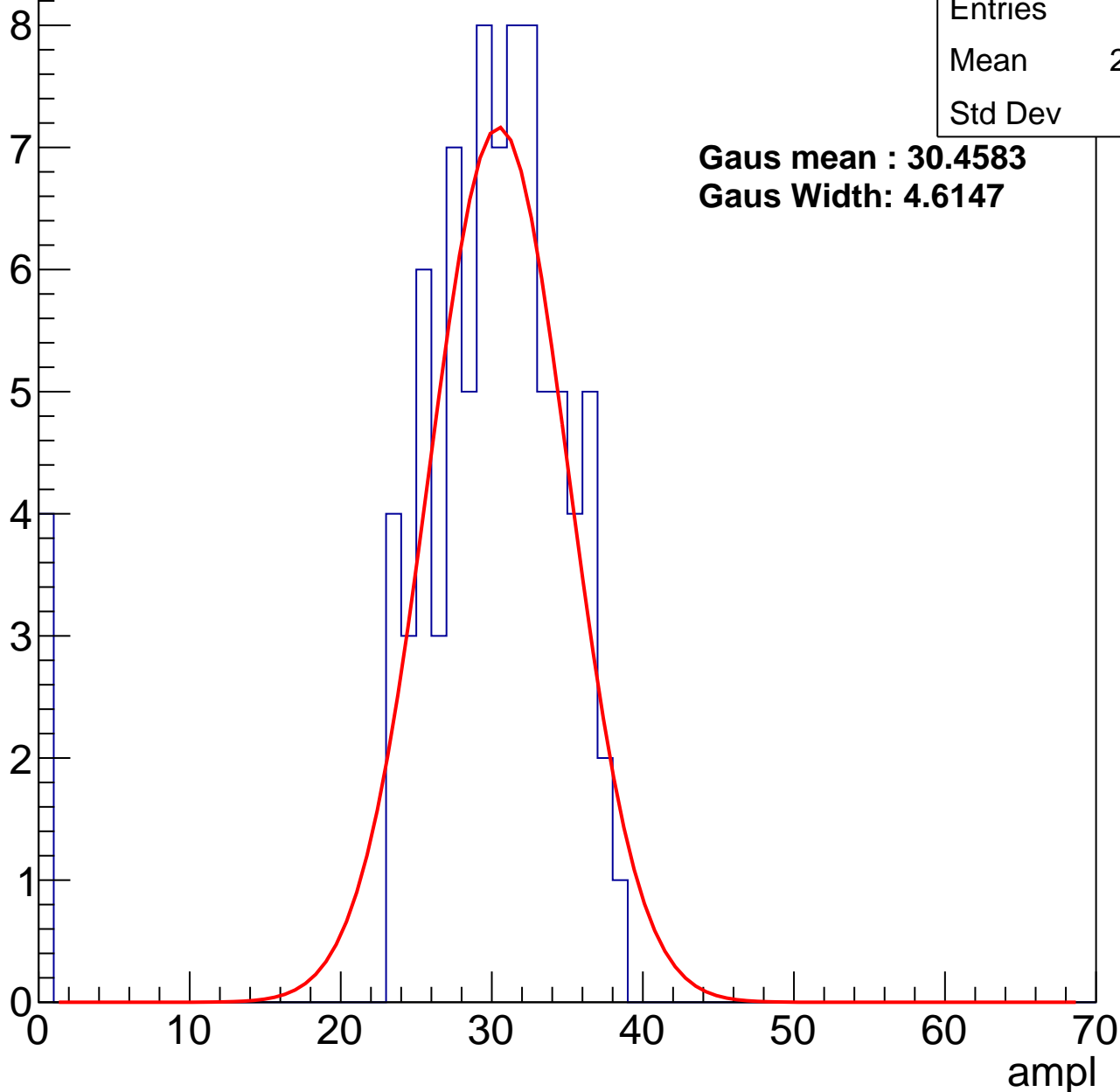
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	28.64
Std Dev	7.39

Gaus mean : 30.4583

Gaus Width: 4.6147



B1L103S, U18-ch65, adc1

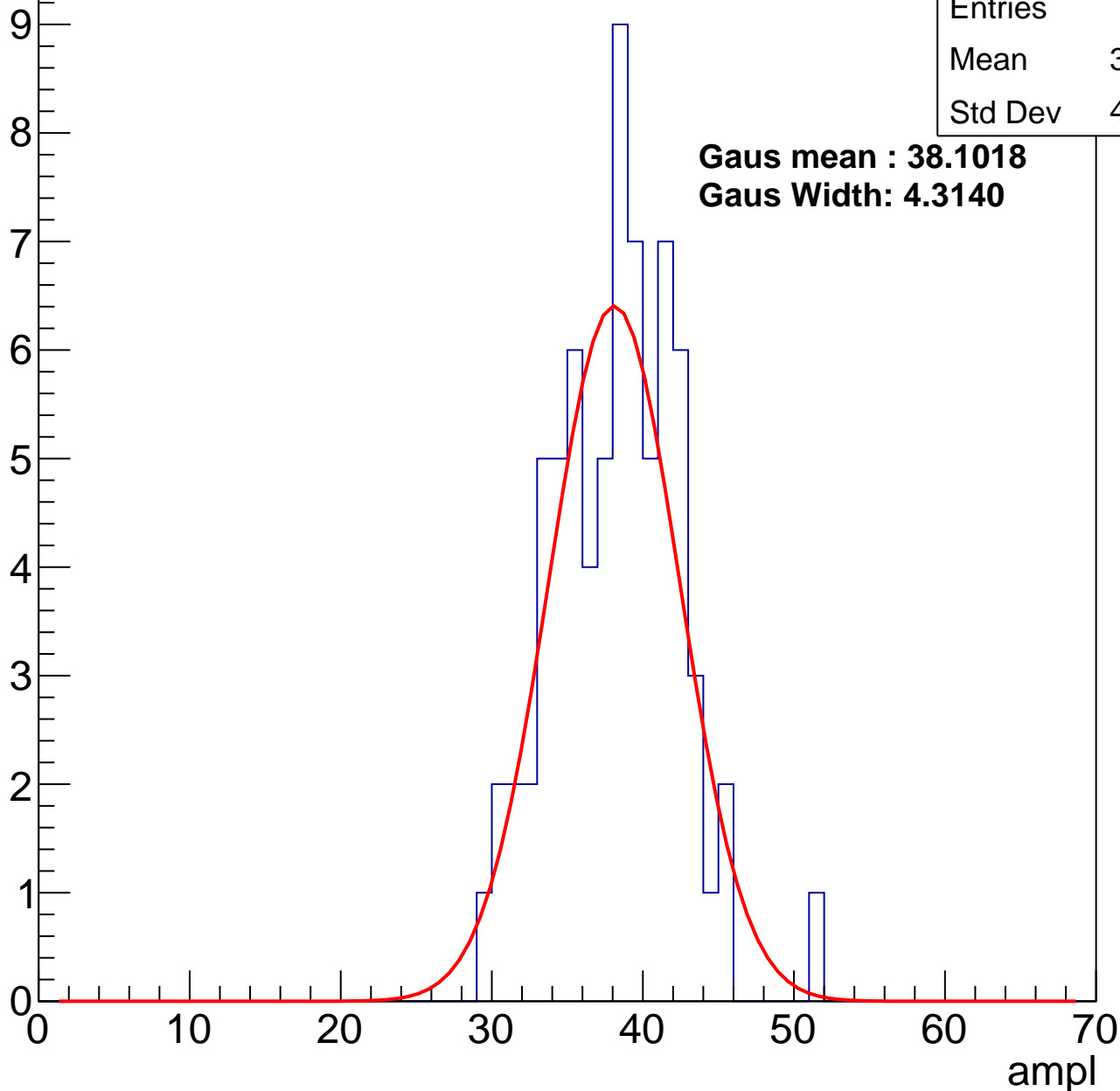
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	37.77
Std Dev	4.086

Gaus mean : 38.1018

Gaus Width: 4.3140



B1L103S, U18-ch65, adc2

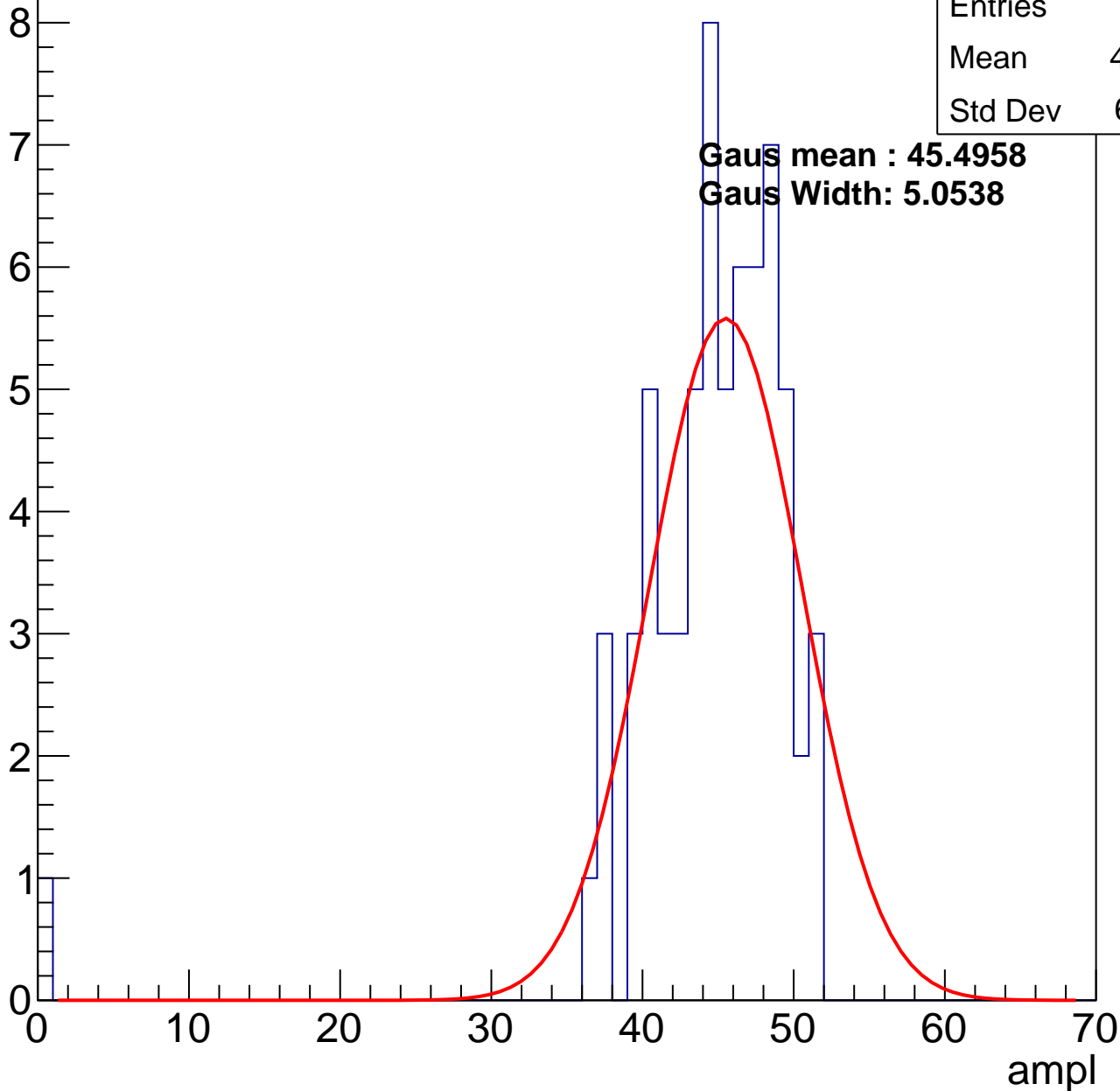
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	43.89
Std Dev	6.611

Gaus mean : 45.4958

Gaus Width: 5.0538

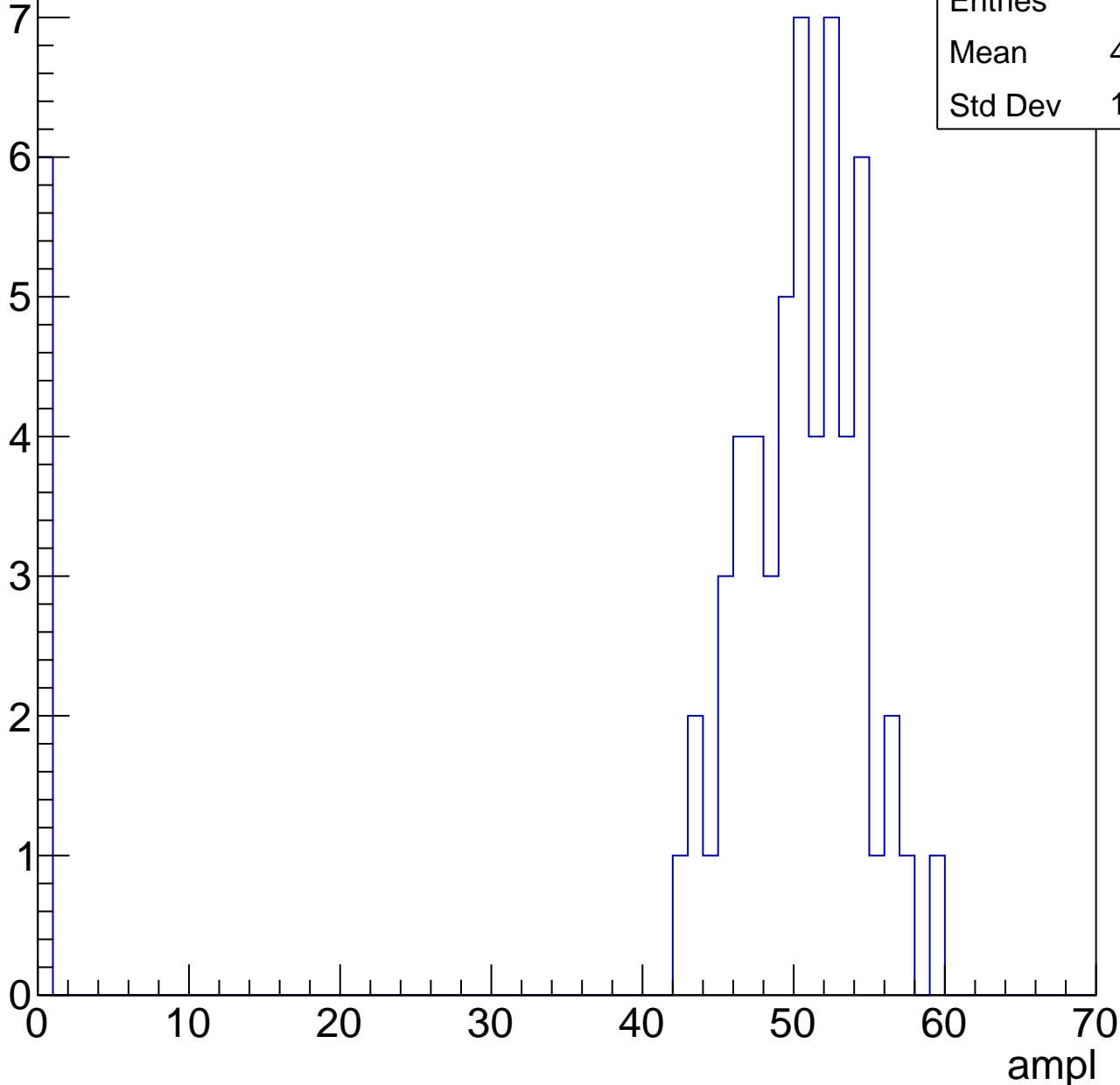


B1L103S, U18-ch65, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	45.24
Std Dev	15.23

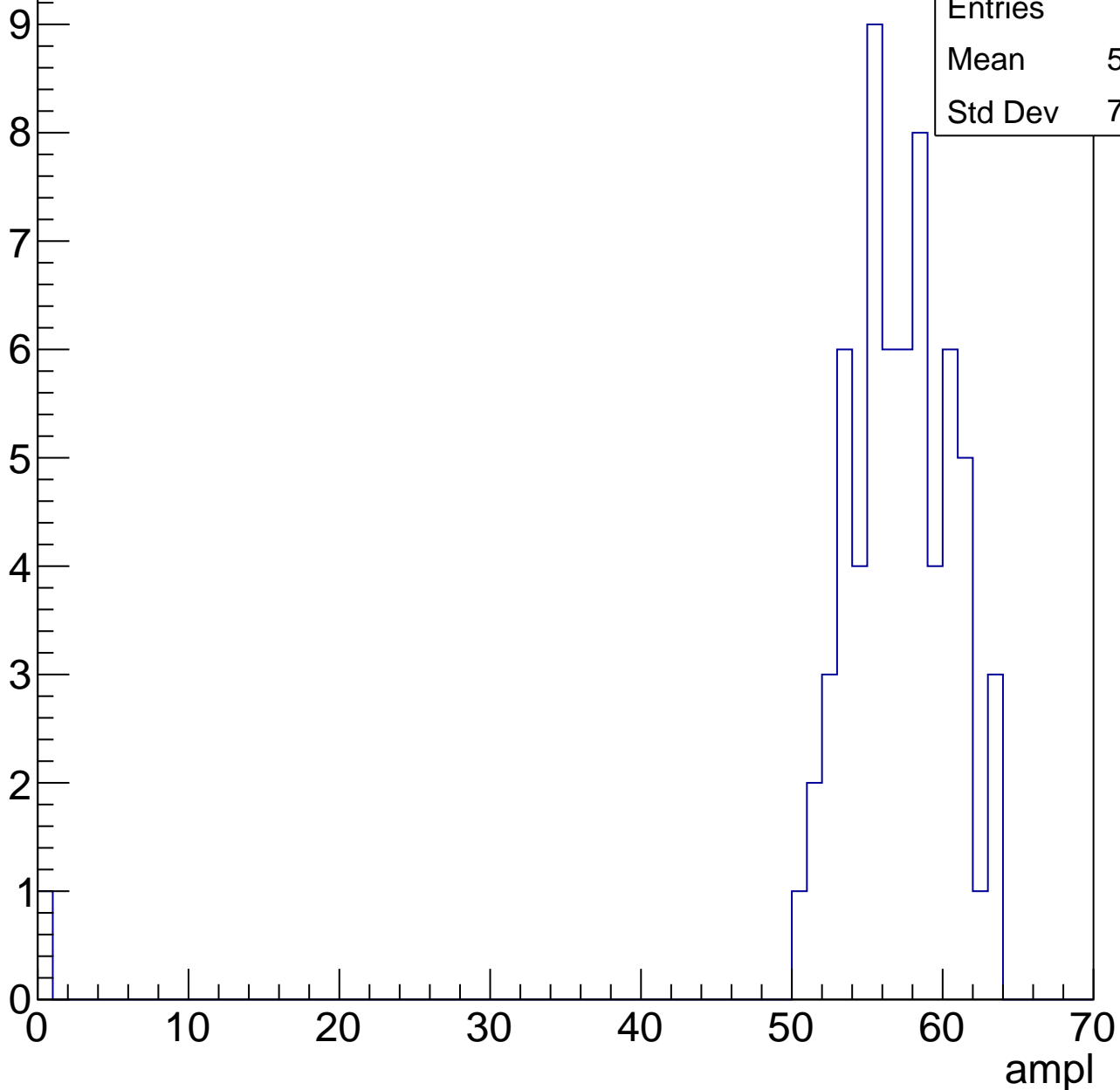


B1L103S, U18-ch65, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	55.86
Std Dev	7.672

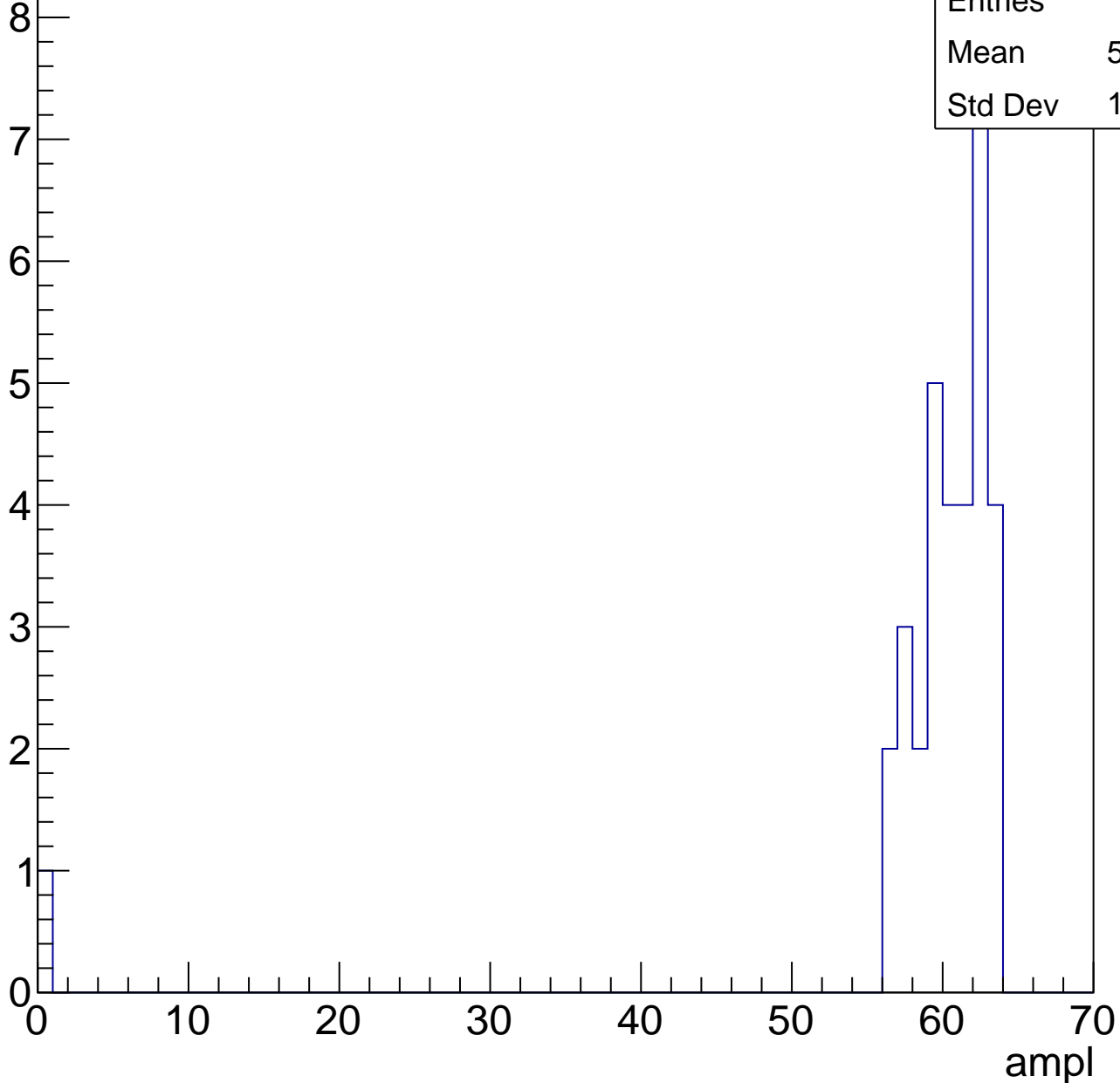


B1L103S, U18-ch65, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	58.36
Std Dev	10.53



B1L103S, U18-ch65, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch65, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch66, adc0

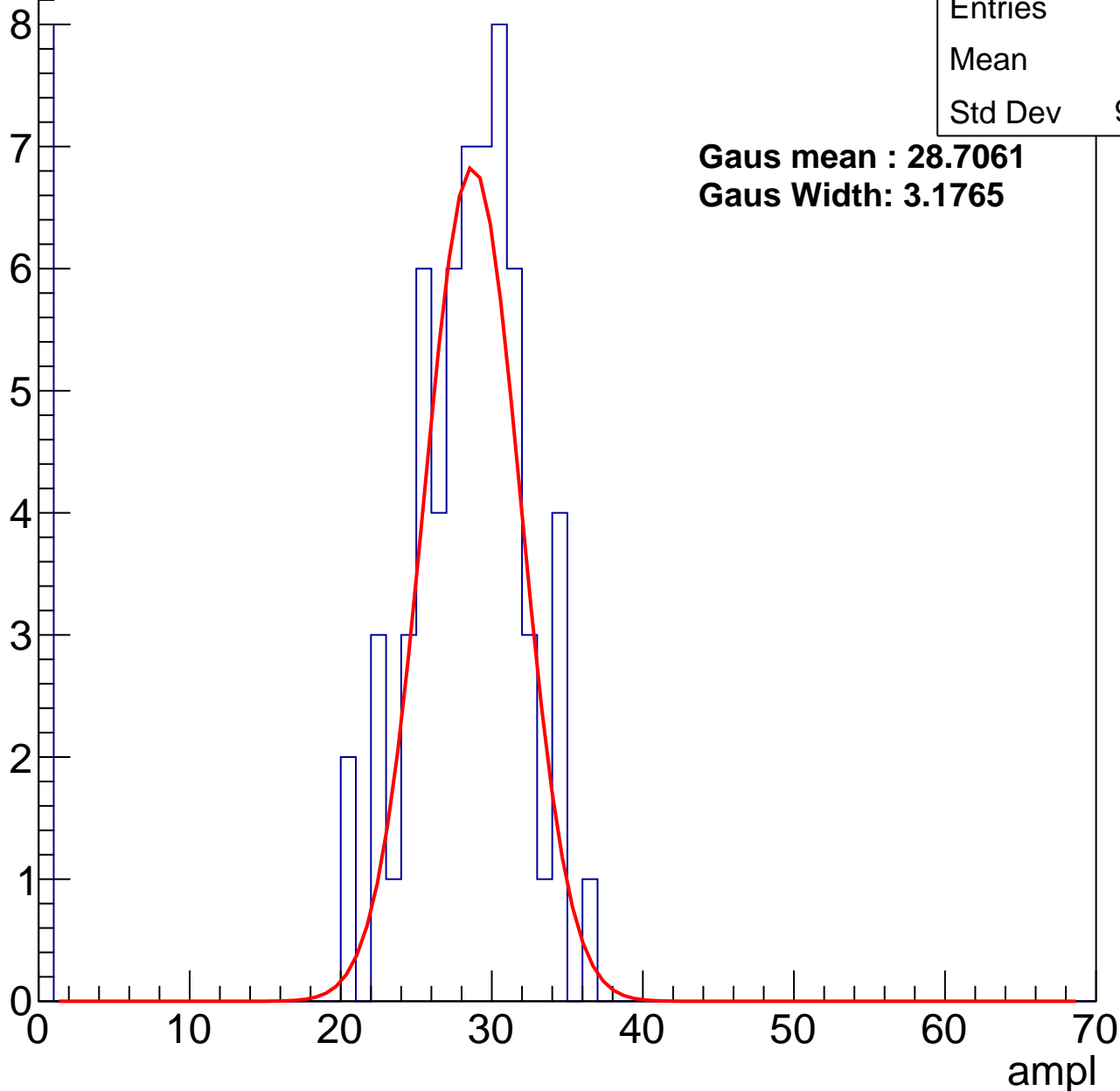
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	24.9
Std Dev	9.531

Gaus mean : 28.7061

Gaus Width: 3.1765



B1L103S, U18-ch66, adc1

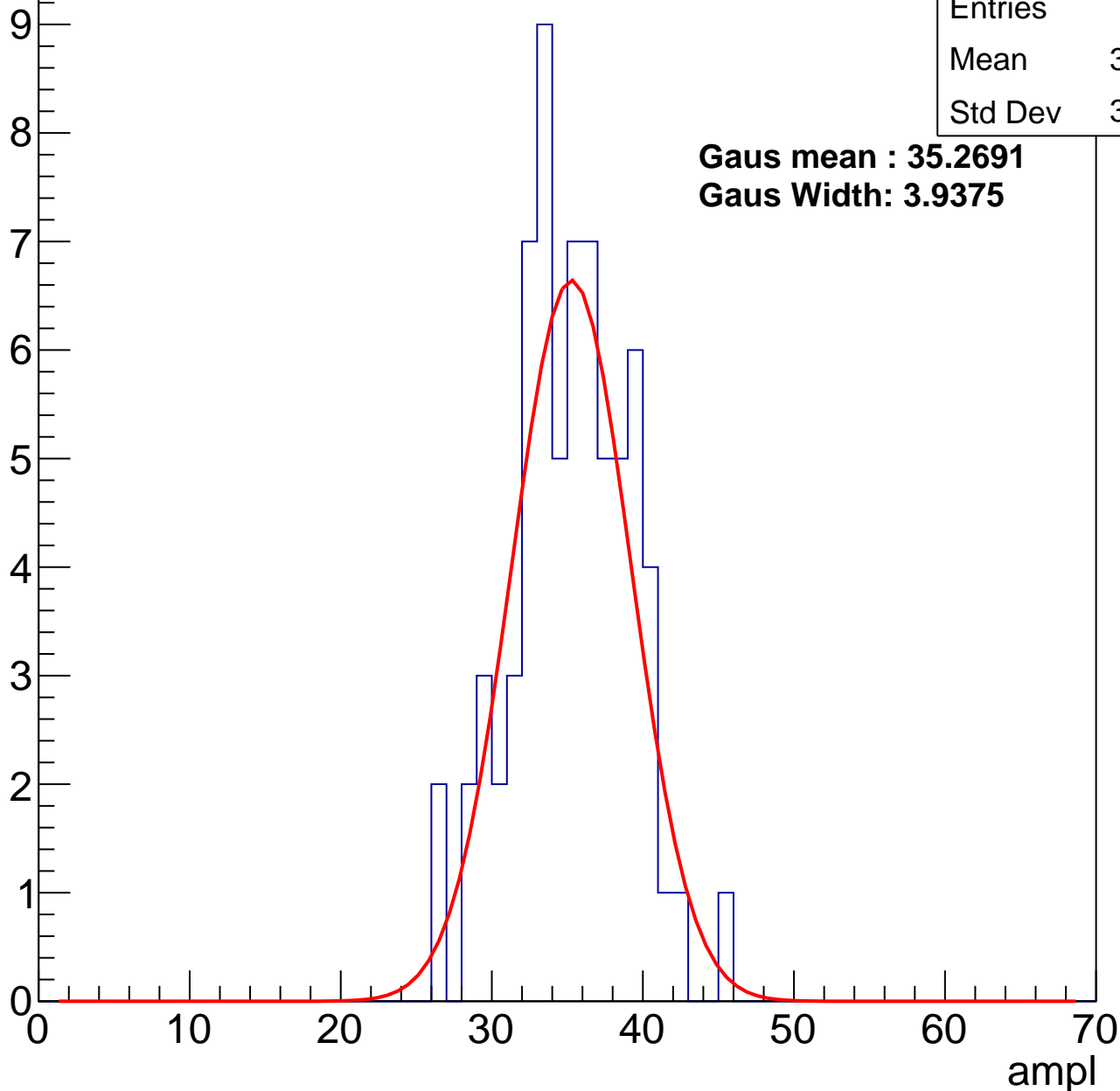
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.76
Std Dev	3.815

Gaus mean : 35.2691

Gaus Width: 3.9375



B1L103S, U18-ch66, adc2

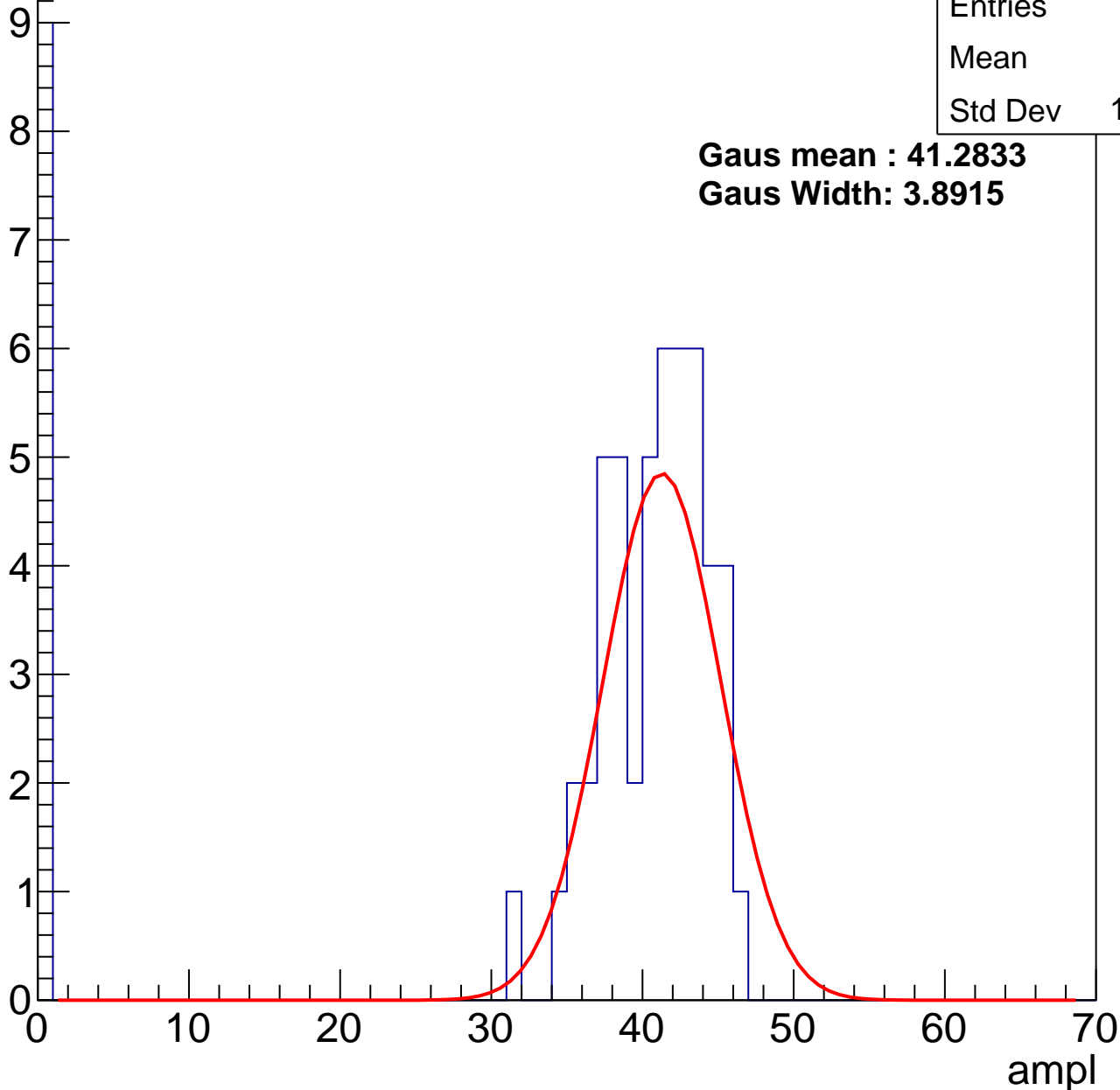
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	34.2
Std Dev	14.83

Gaus mean : 41.2833

Gaus Width: 3.8915

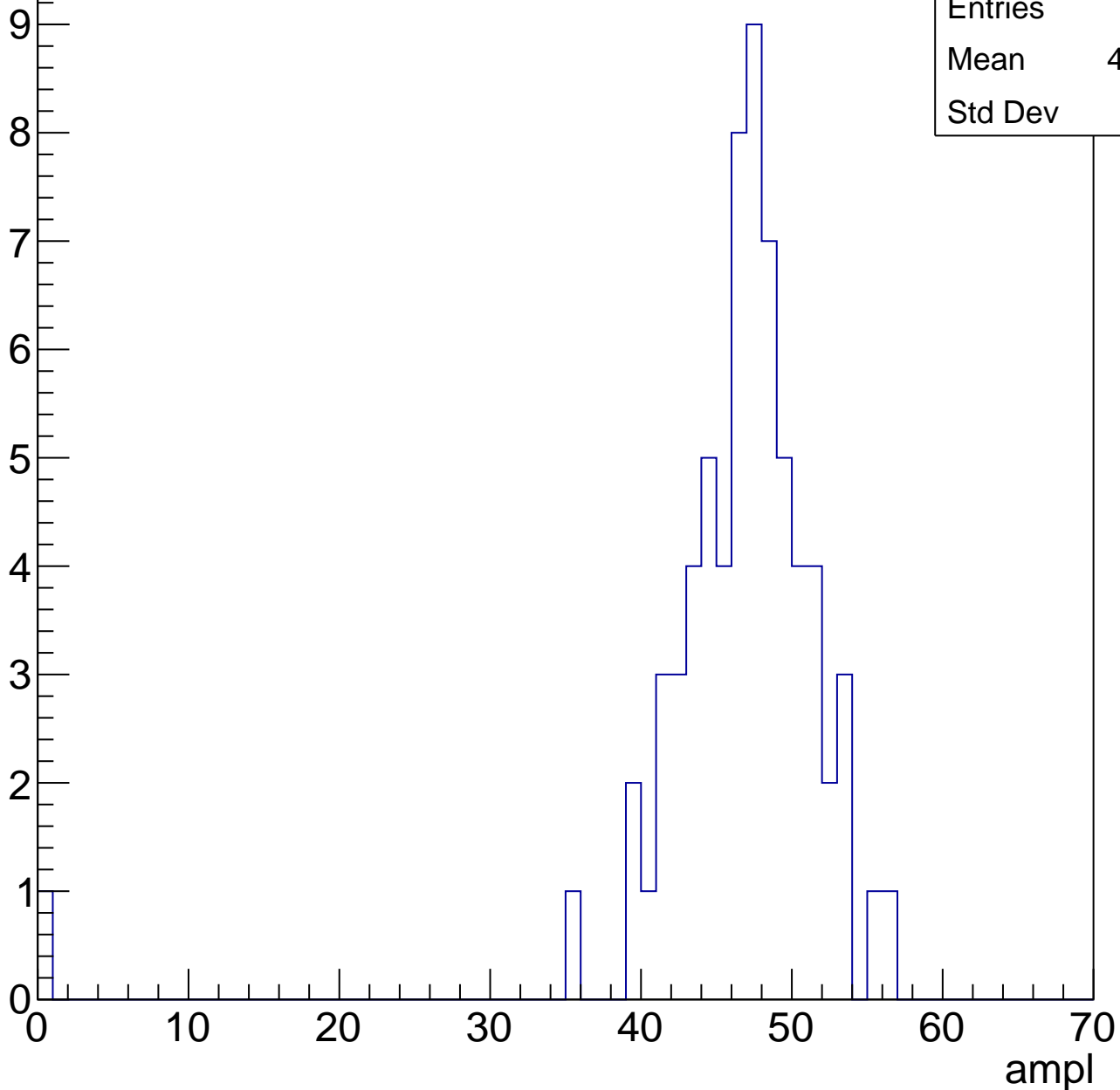


B1L103S, U18-ch66, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	45.94
Std Dev	6.86

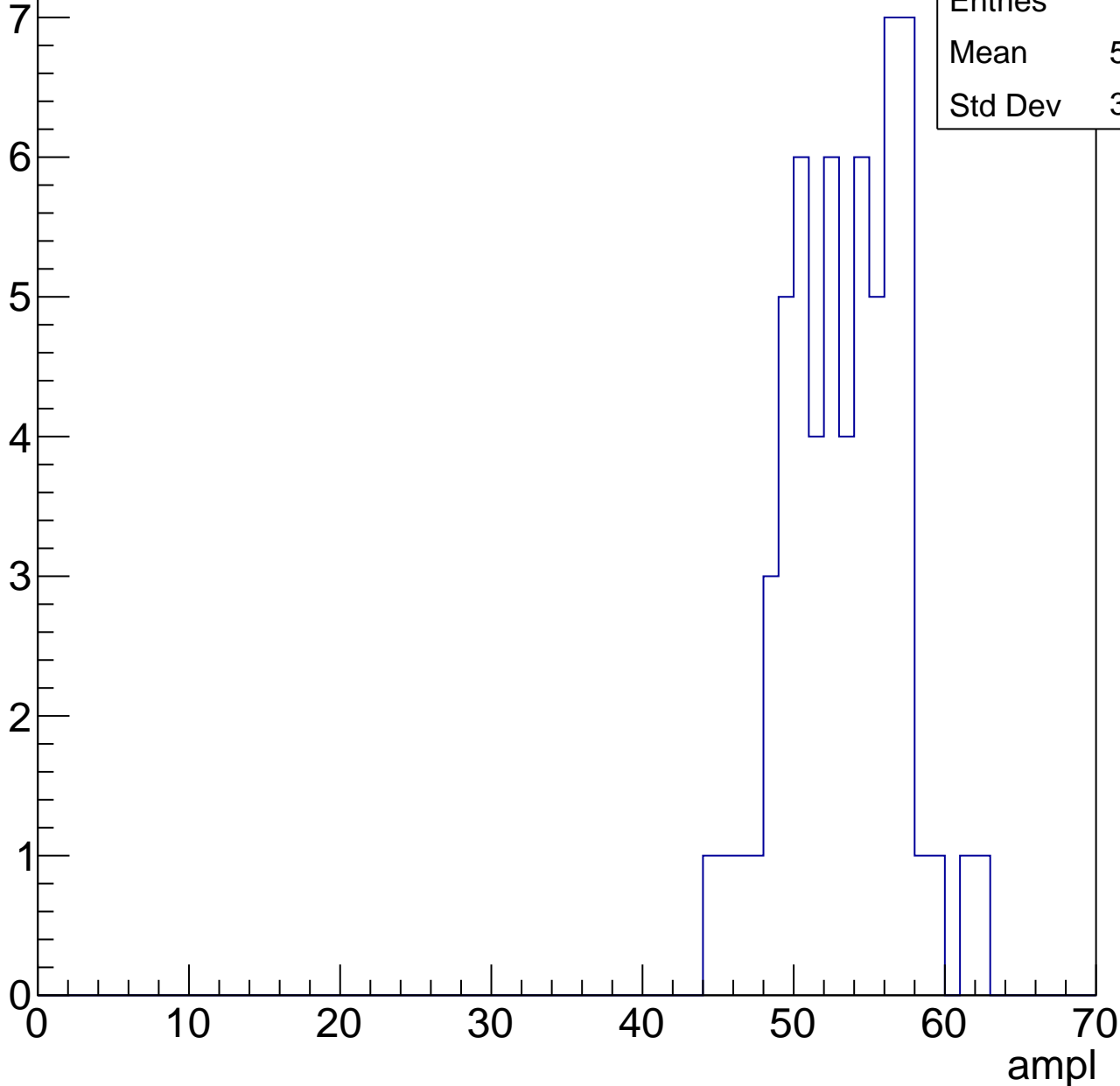


B1L103S, U18-ch66, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

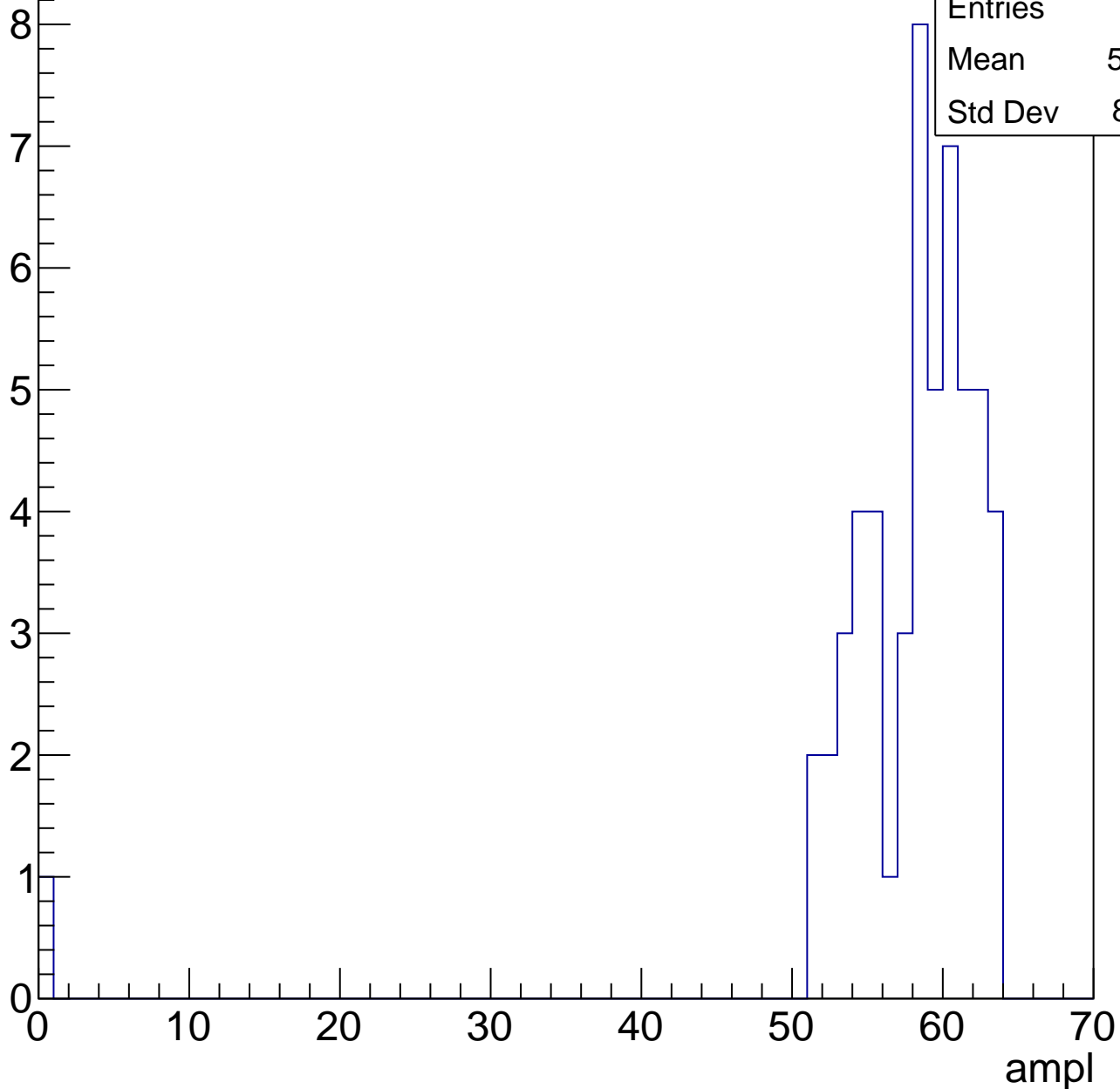
Entries	61
Mean	52.93
Std Dev	3.772



B1L103S, U18-ch66, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

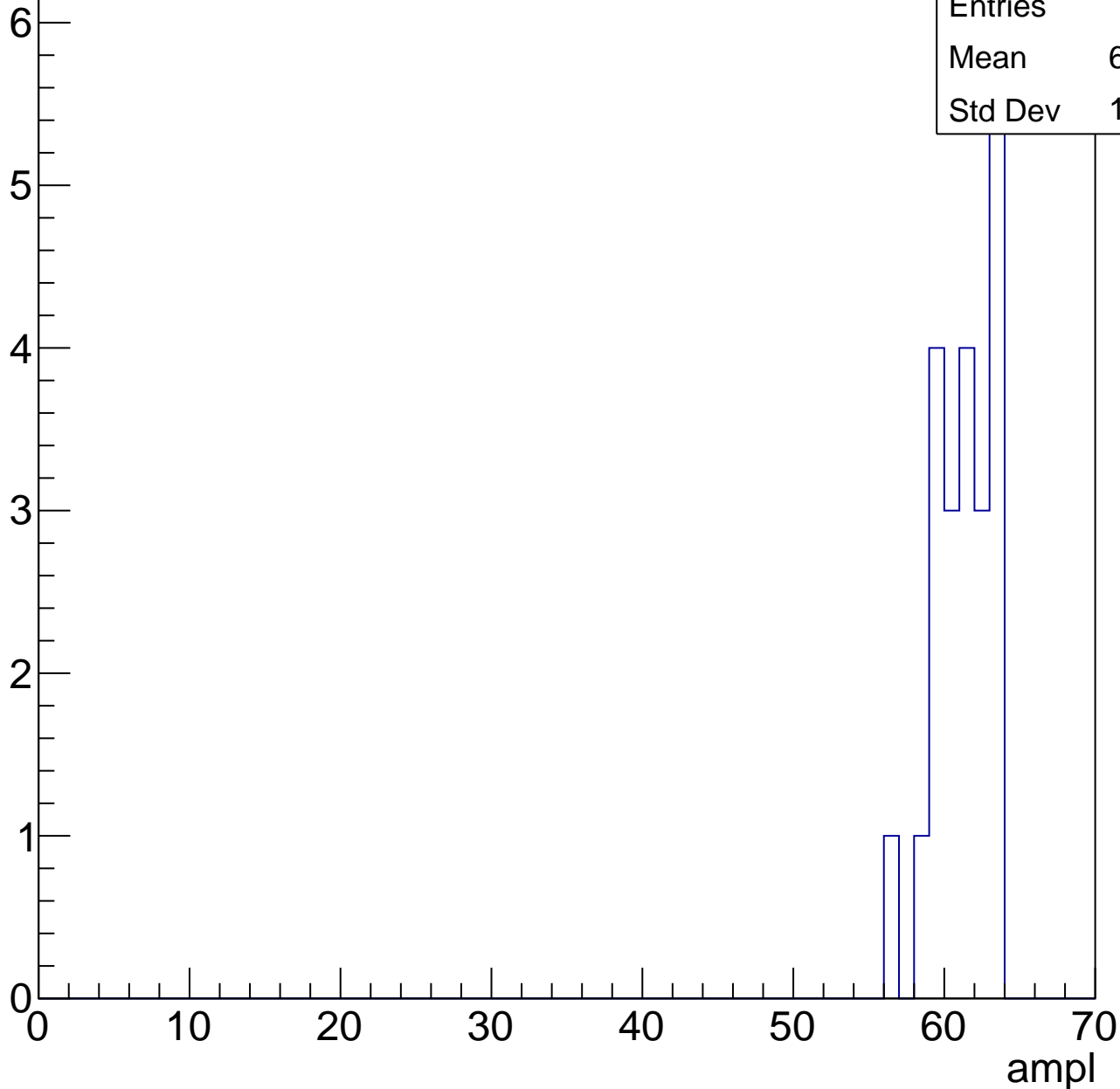


B1L103S, U18-ch66, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	22
Mean	60.82
Std Dev	1.898



B1L103S, U18-ch66, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U18-ch67, adc0

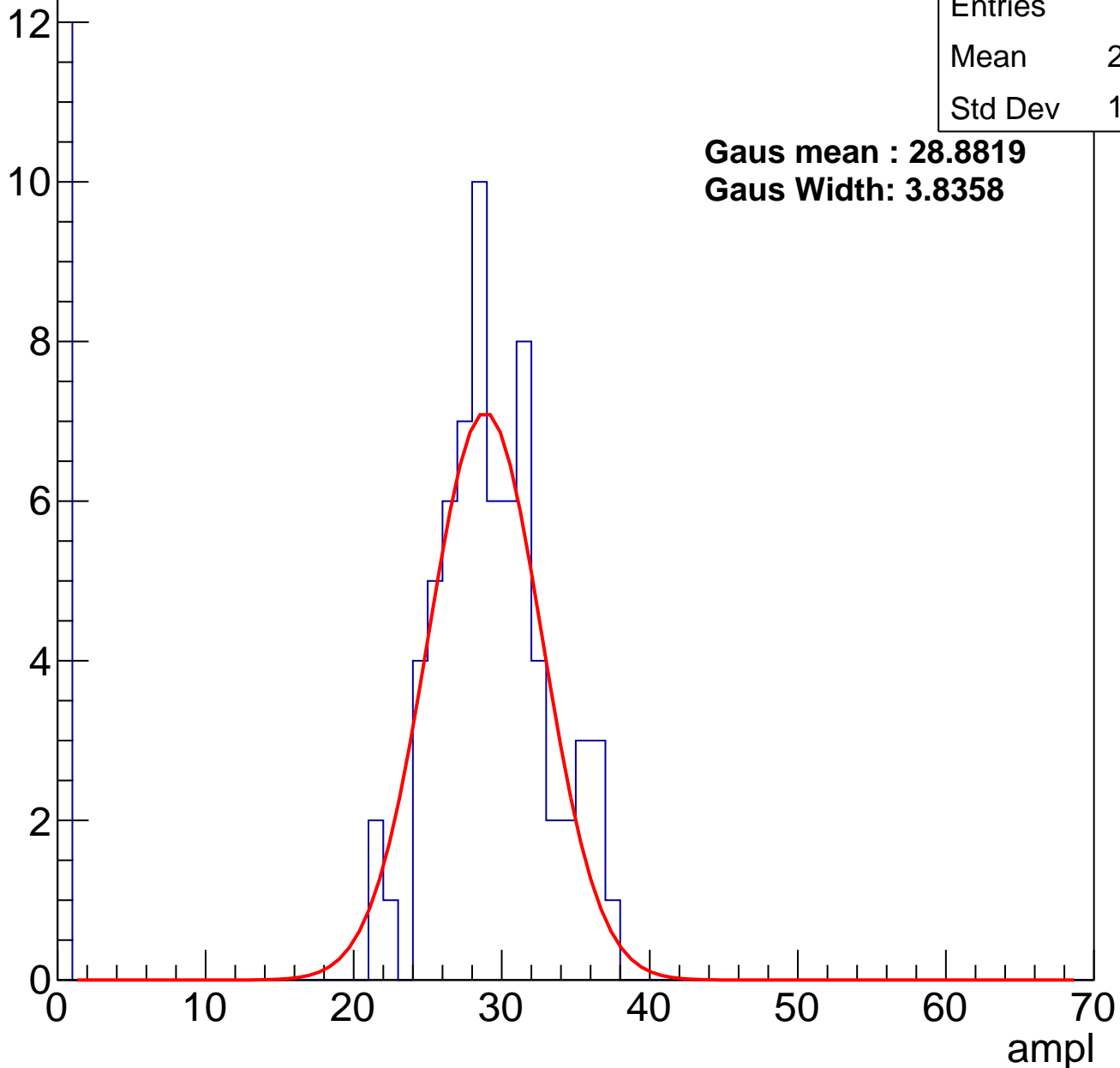
calib_packv5_041523_1651.root, FC#0, port C2

Entries	82
Mean	24.68
Std Dev	10.76

Gaus mean : 28.8819

Gaus Width: 3.8358

Entry



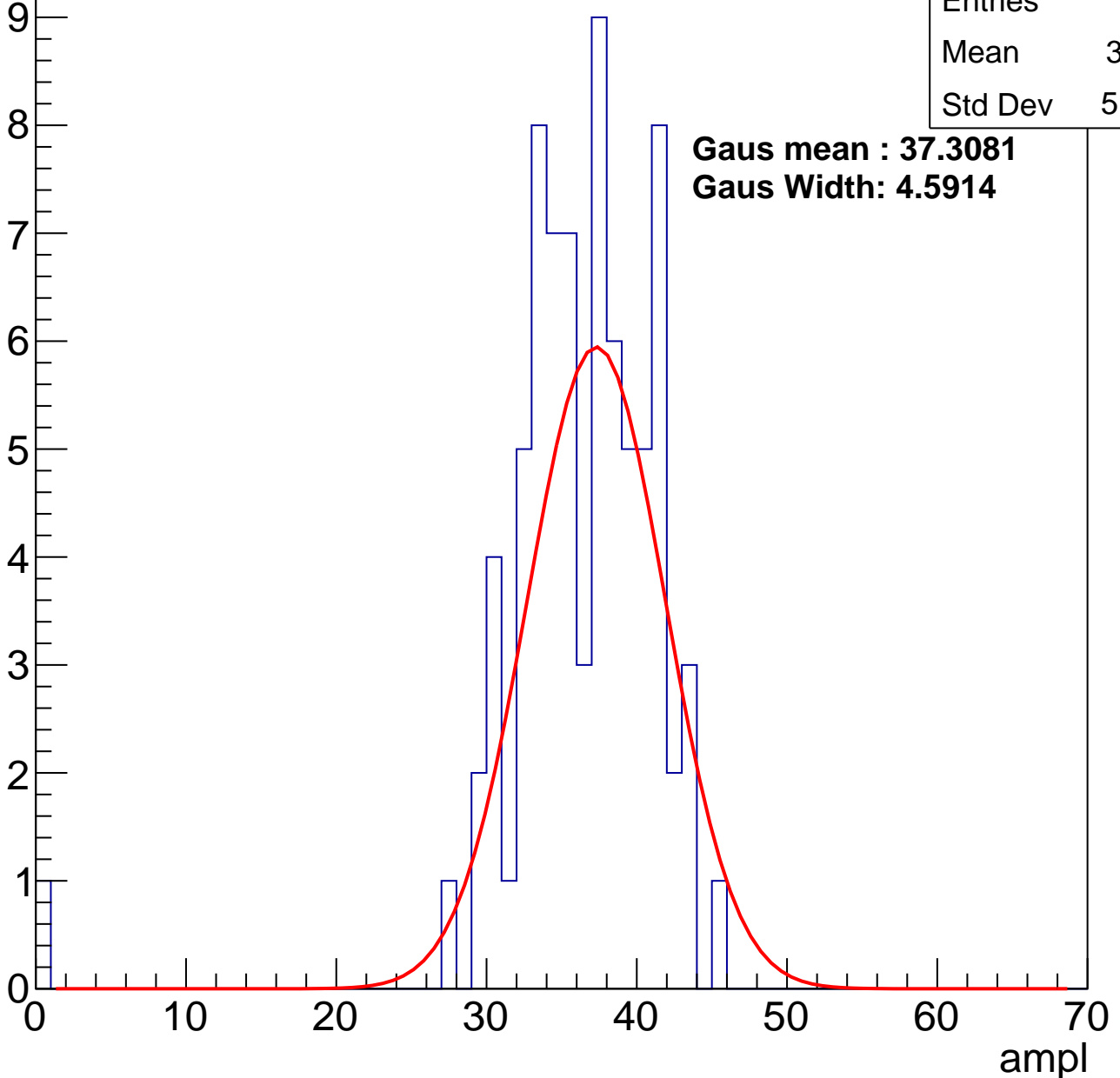
B1L103S, U18-ch67, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	35.81
Std Dev	5.646

Gaus mean : 37.3081
Gaus Width: 4.5914



B1L103S, U18-ch67, adc2

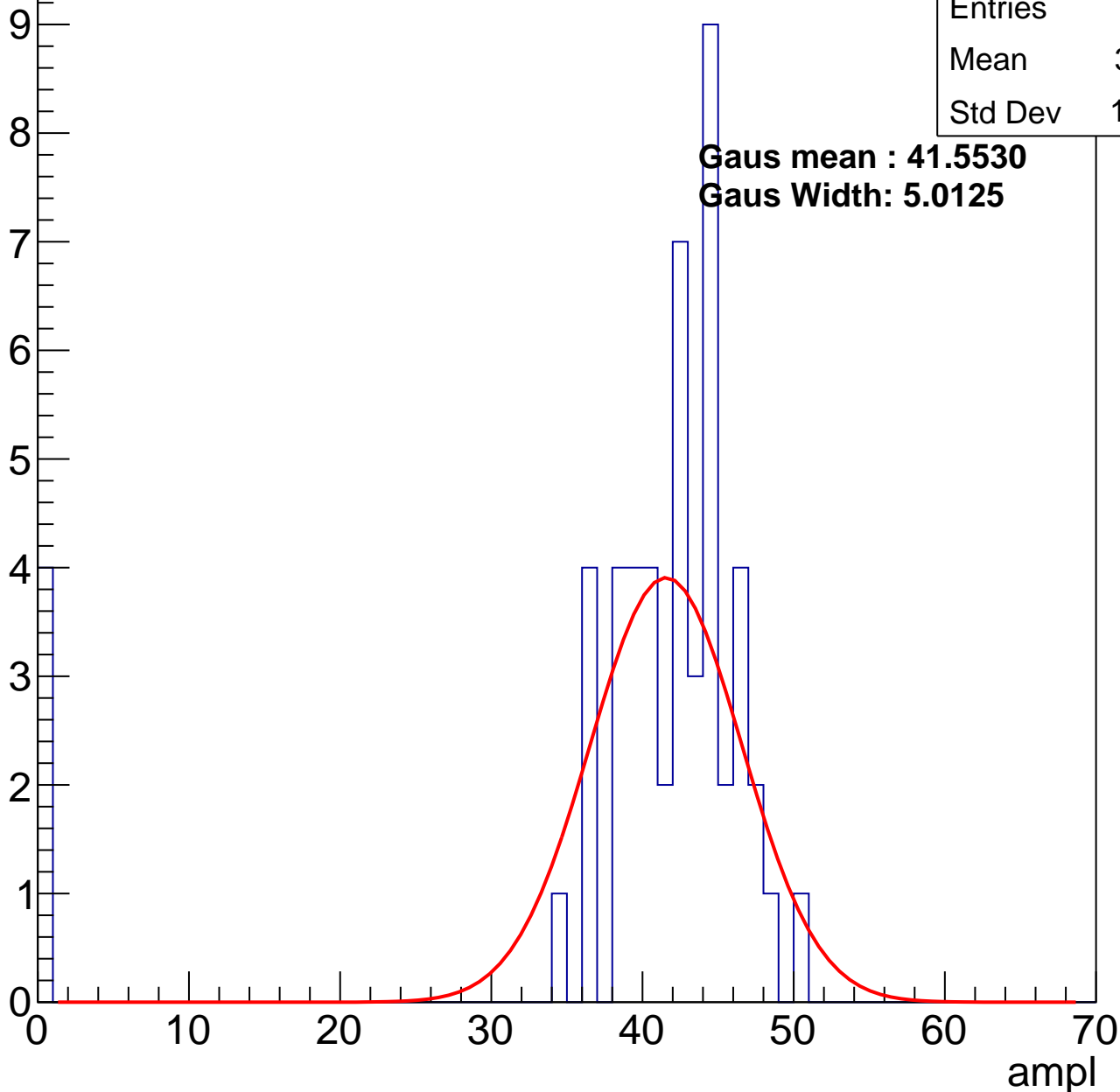
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	38.71
Std Dev	11.68

Gaus mean : 41.5530

Gaus Width: 5.0125

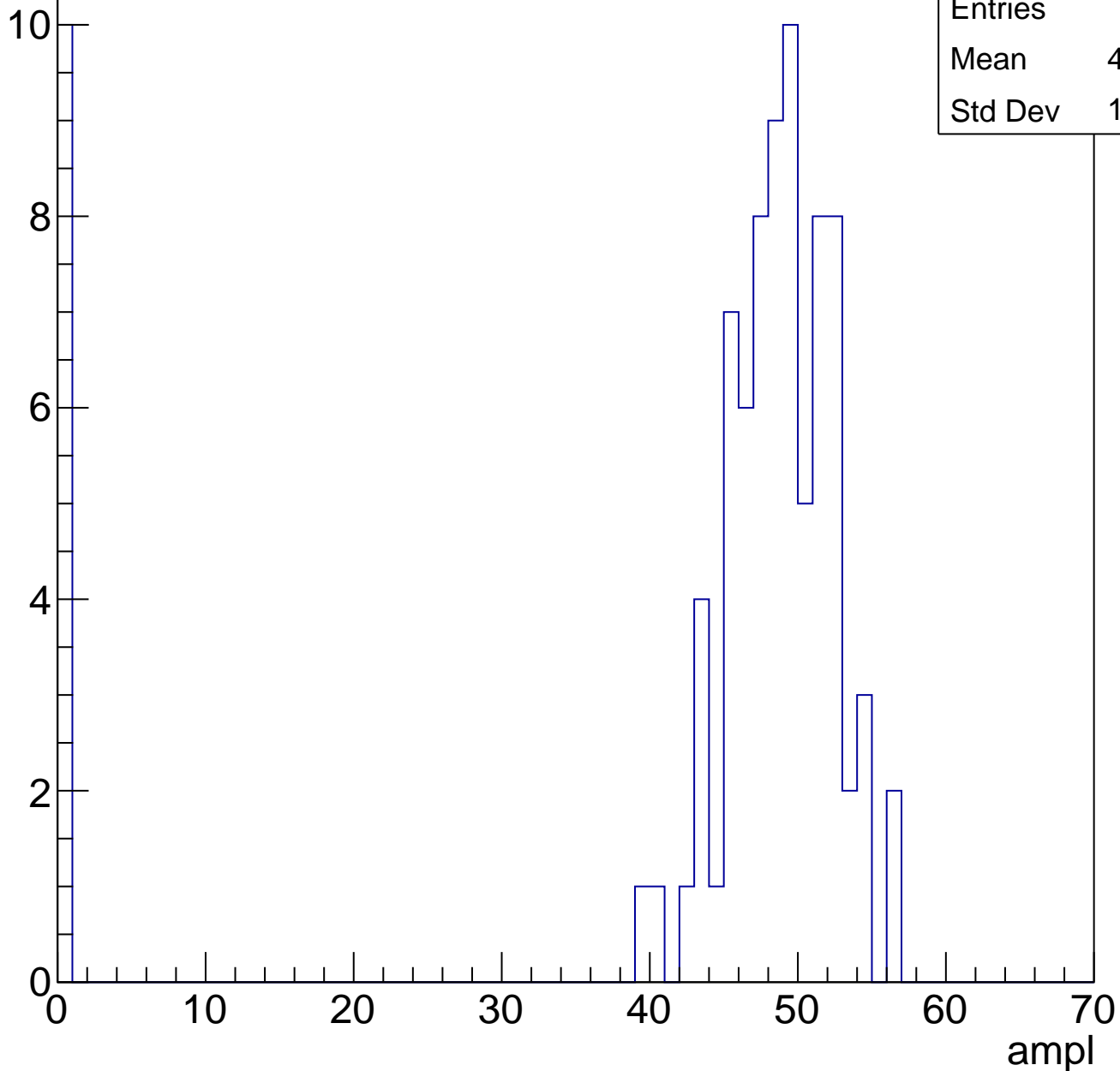


B1L103S, U18-ch67, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	42.79
Std Dev	15.85

Entry

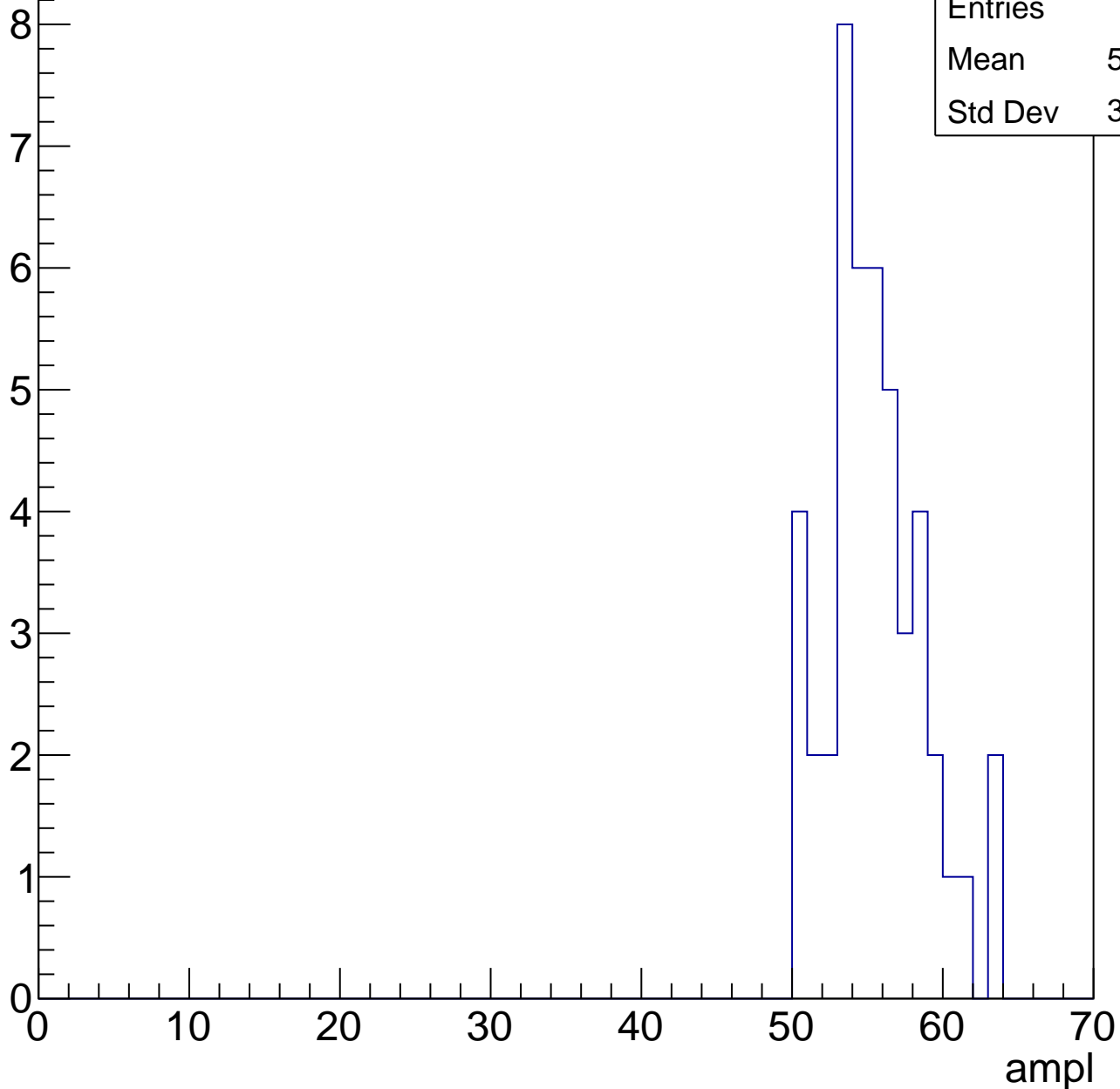


B1L103S, U18-ch67, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

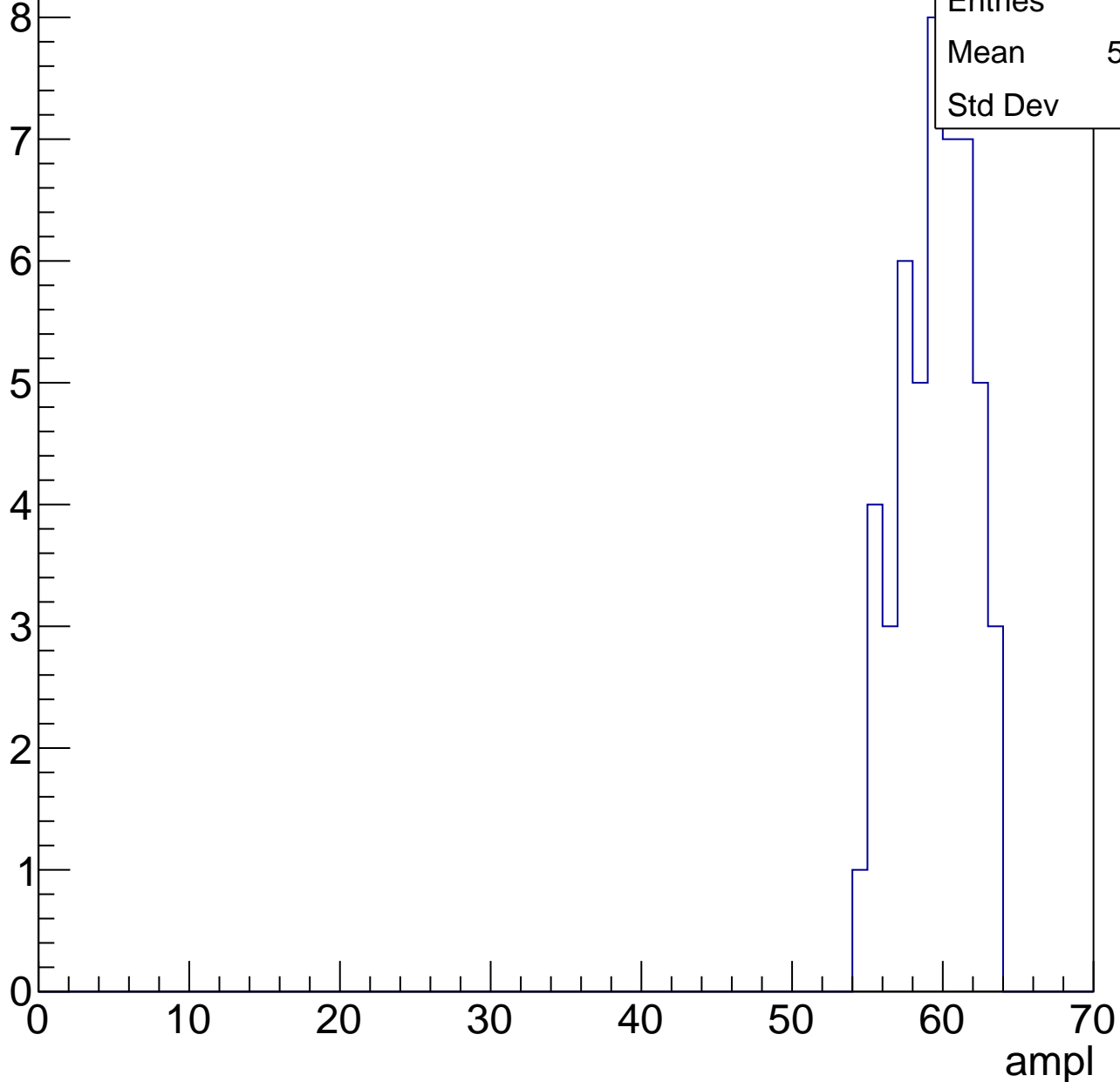
Entries	46
Mean	55.04
Std Dev	3.169



B1L103S, U18-ch67, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

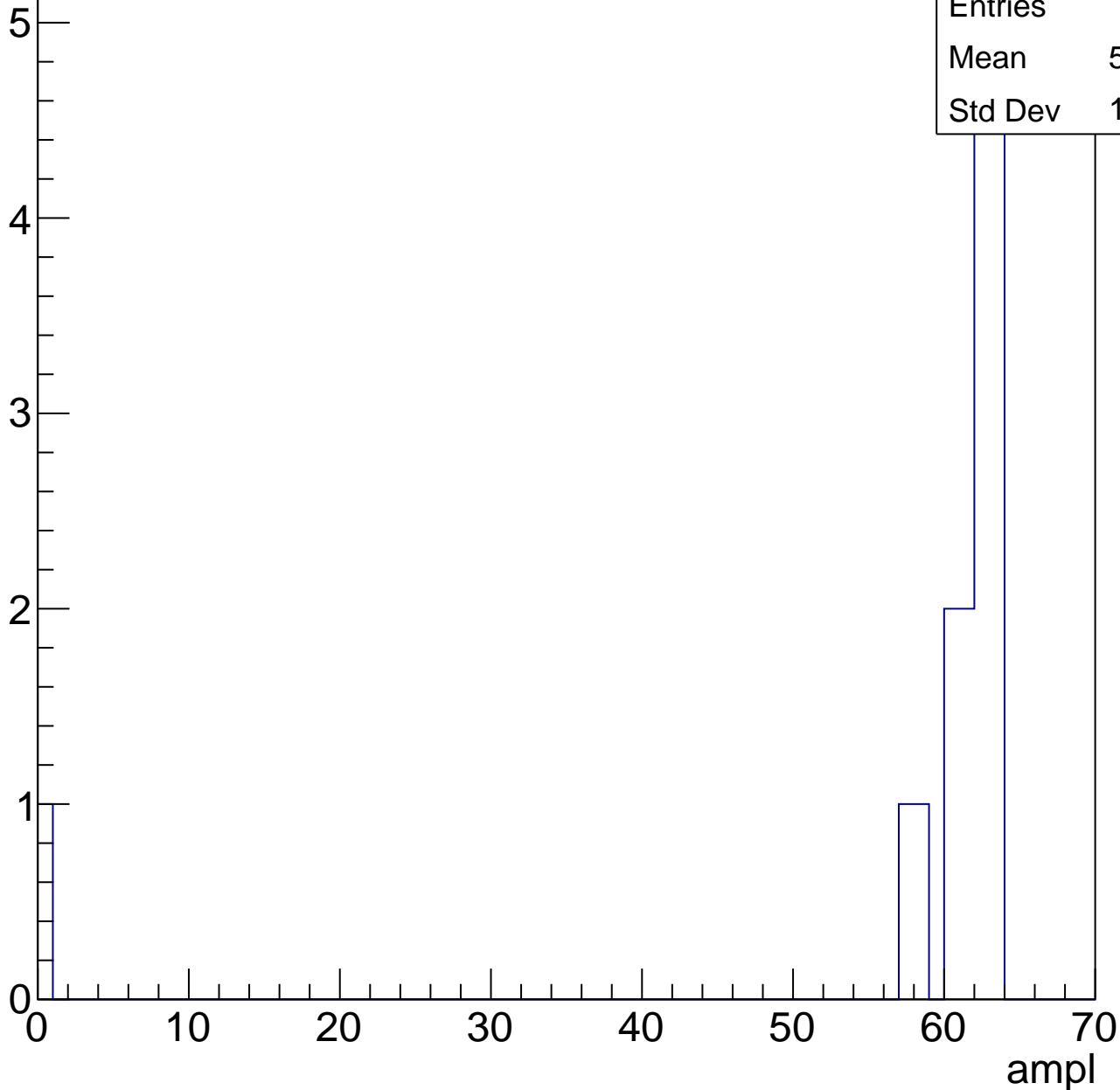


B1L103S, U18-ch67, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.76
Std Dev	14.54

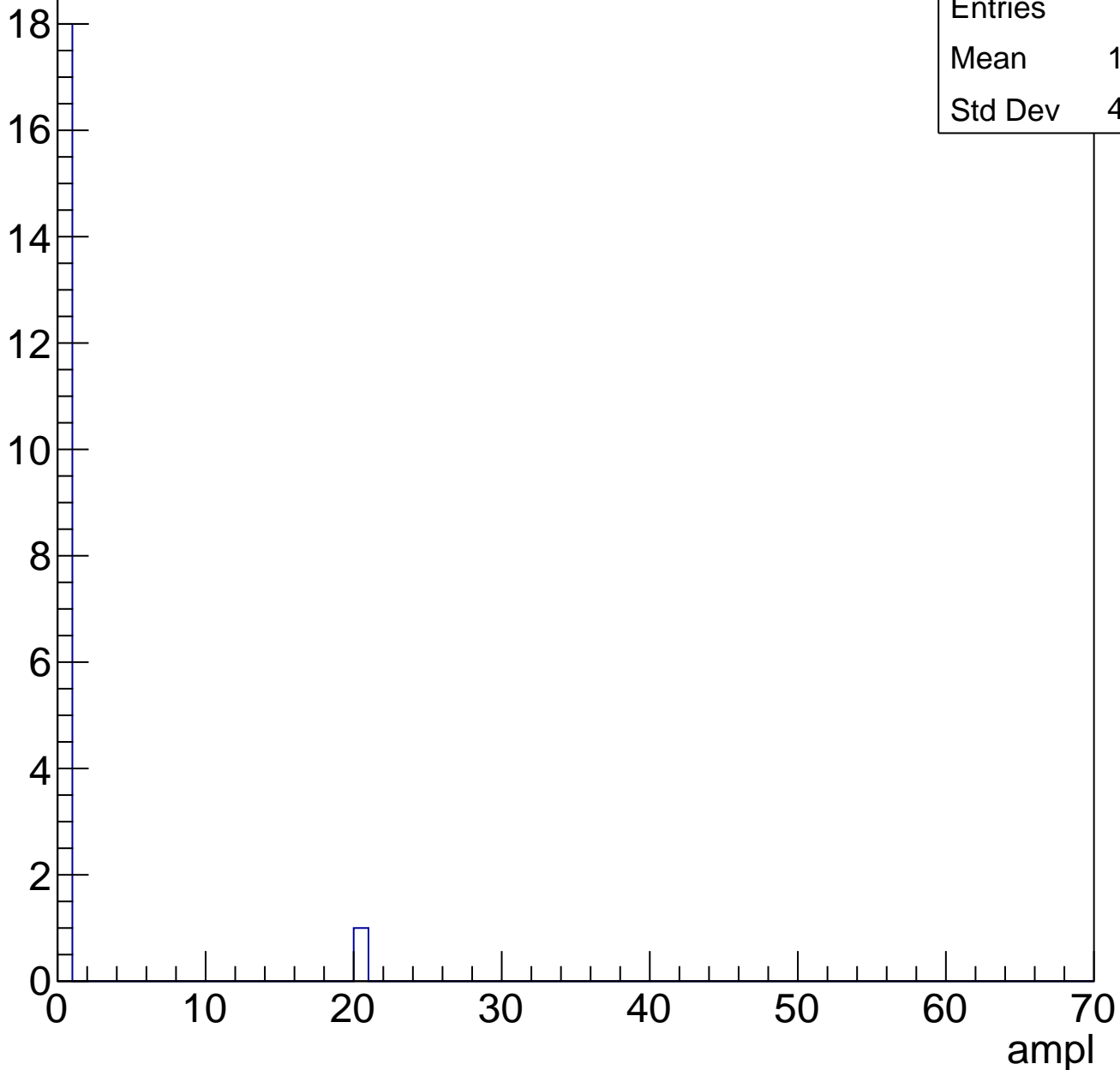


B1L103S, U18-ch67, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



B1L103S, U18-ch68, adc0

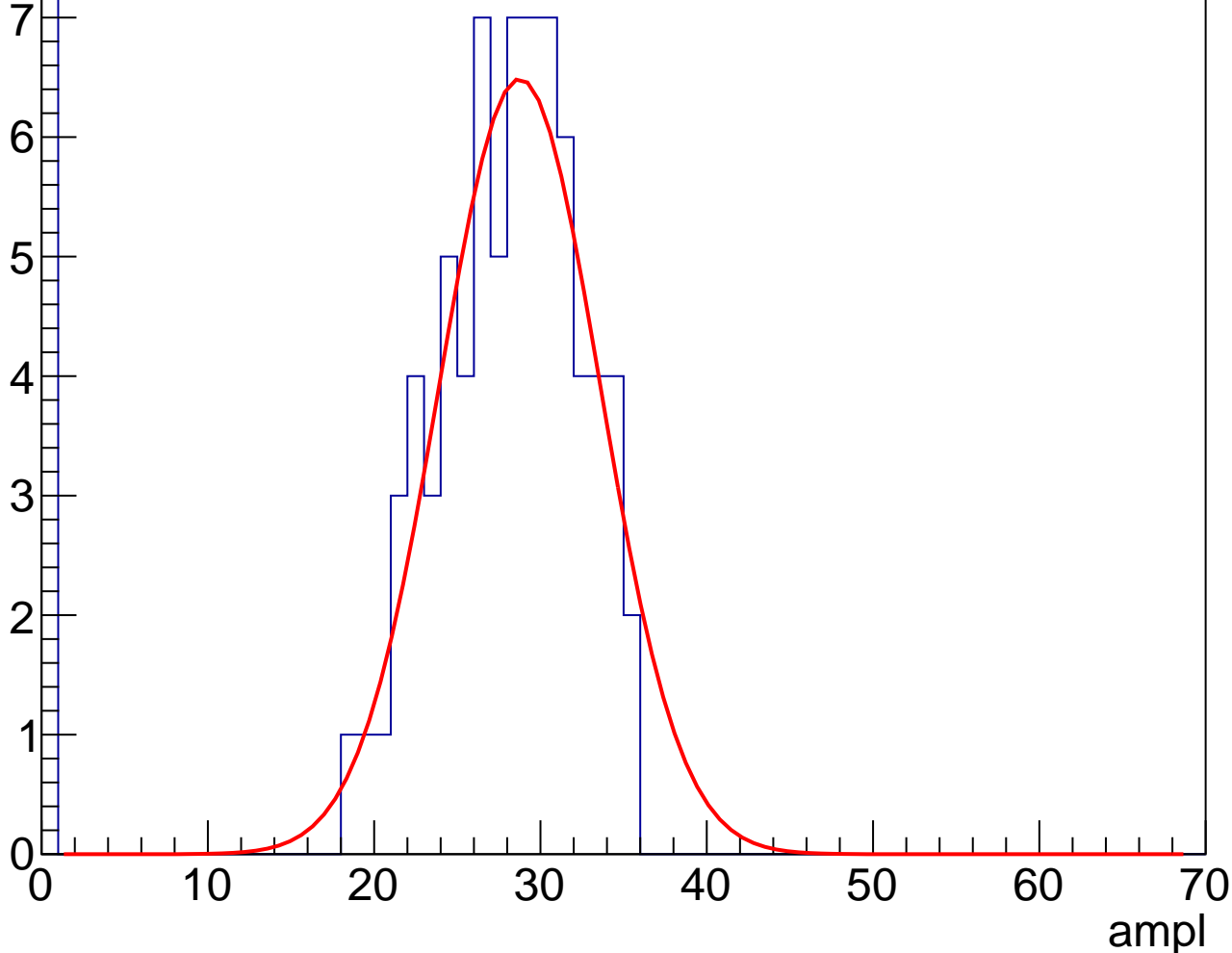
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	24.7
Std Dev	9.384

Gaus mean : 28.7541

Gaus Width: 4.8279



B1L103S, U18-ch68, adc1

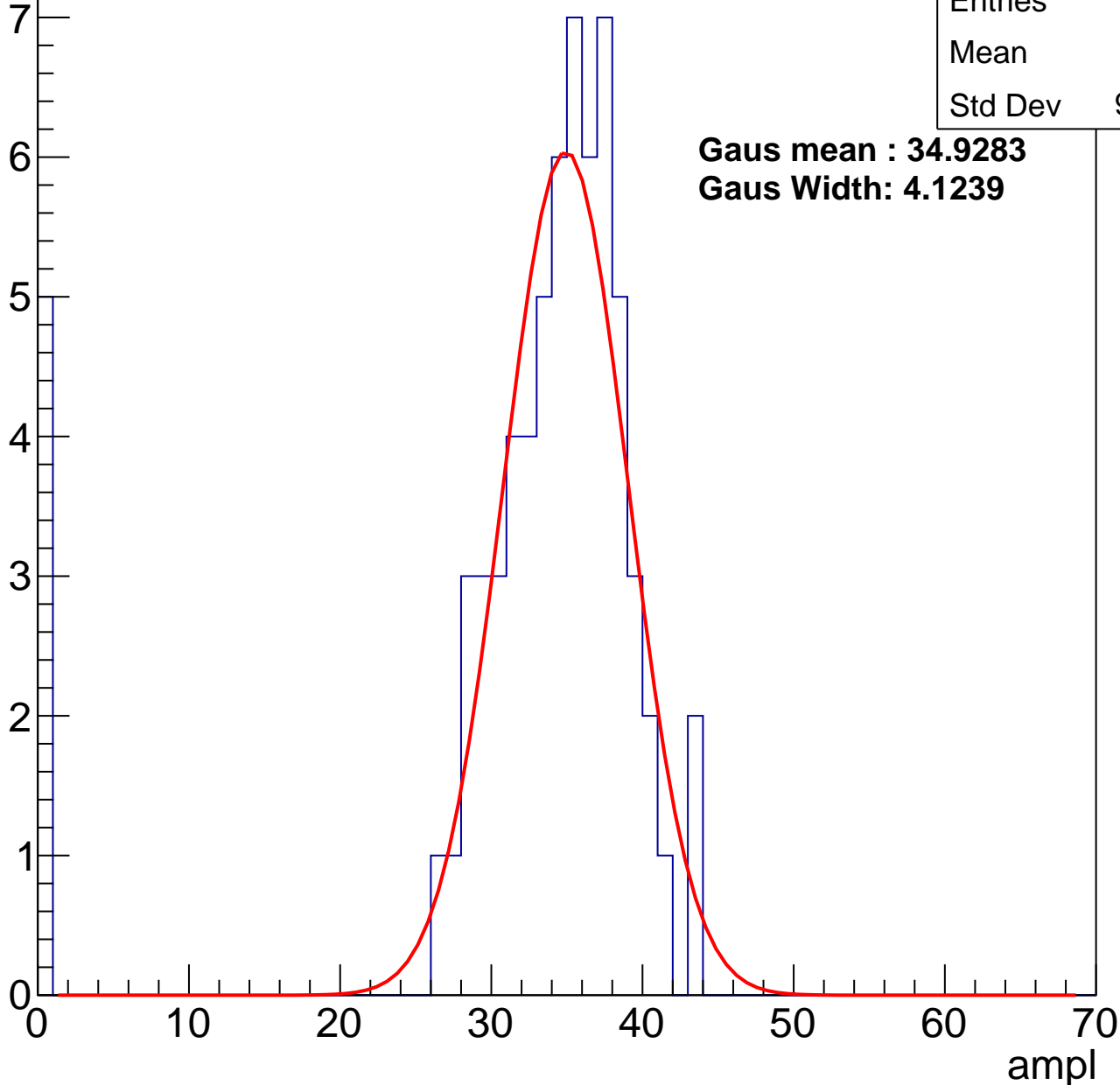
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	31.9
Std Dev	9.711

Gaus mean : 34.9283

Gaus Width: 4.1239



B1L103S, U18-ch68, adc2

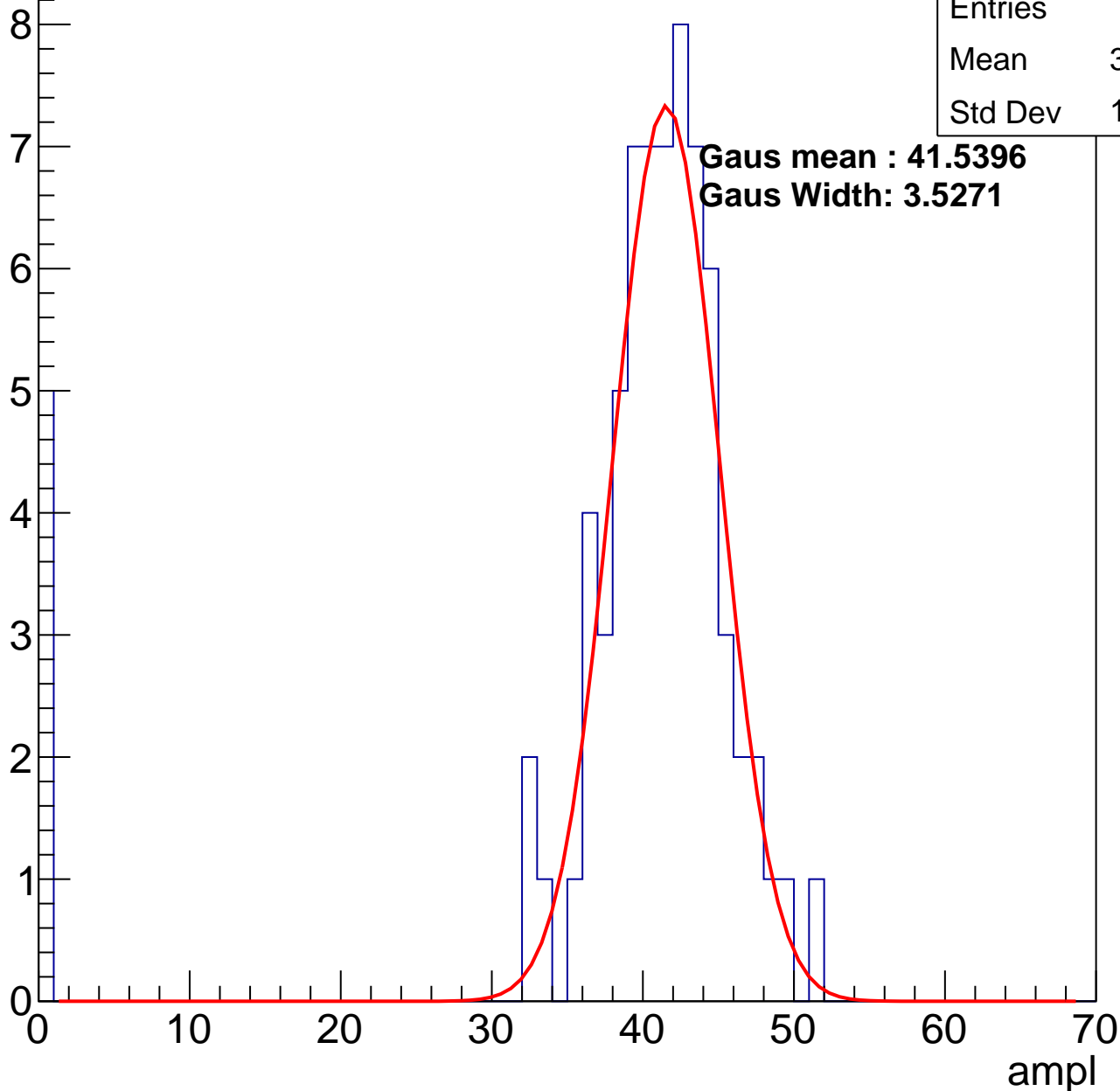
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	38.18
Std Dev	10.97

Gaus mean : 41.5396

Gaus Width: 3.5271

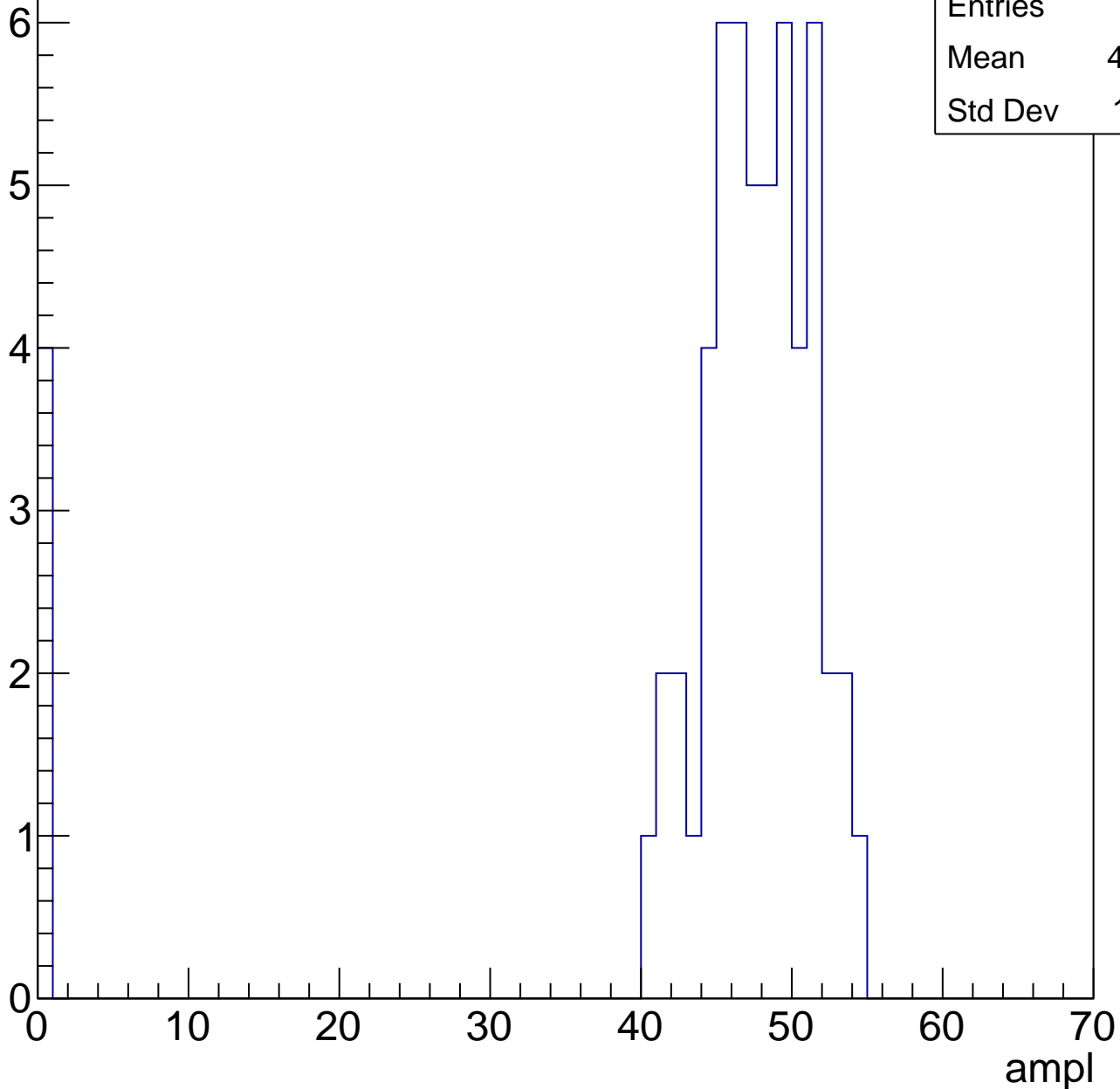


B1L103S, U18-ch68, adc3

calib_packv5_041523_1651.root, FC#0, port C2

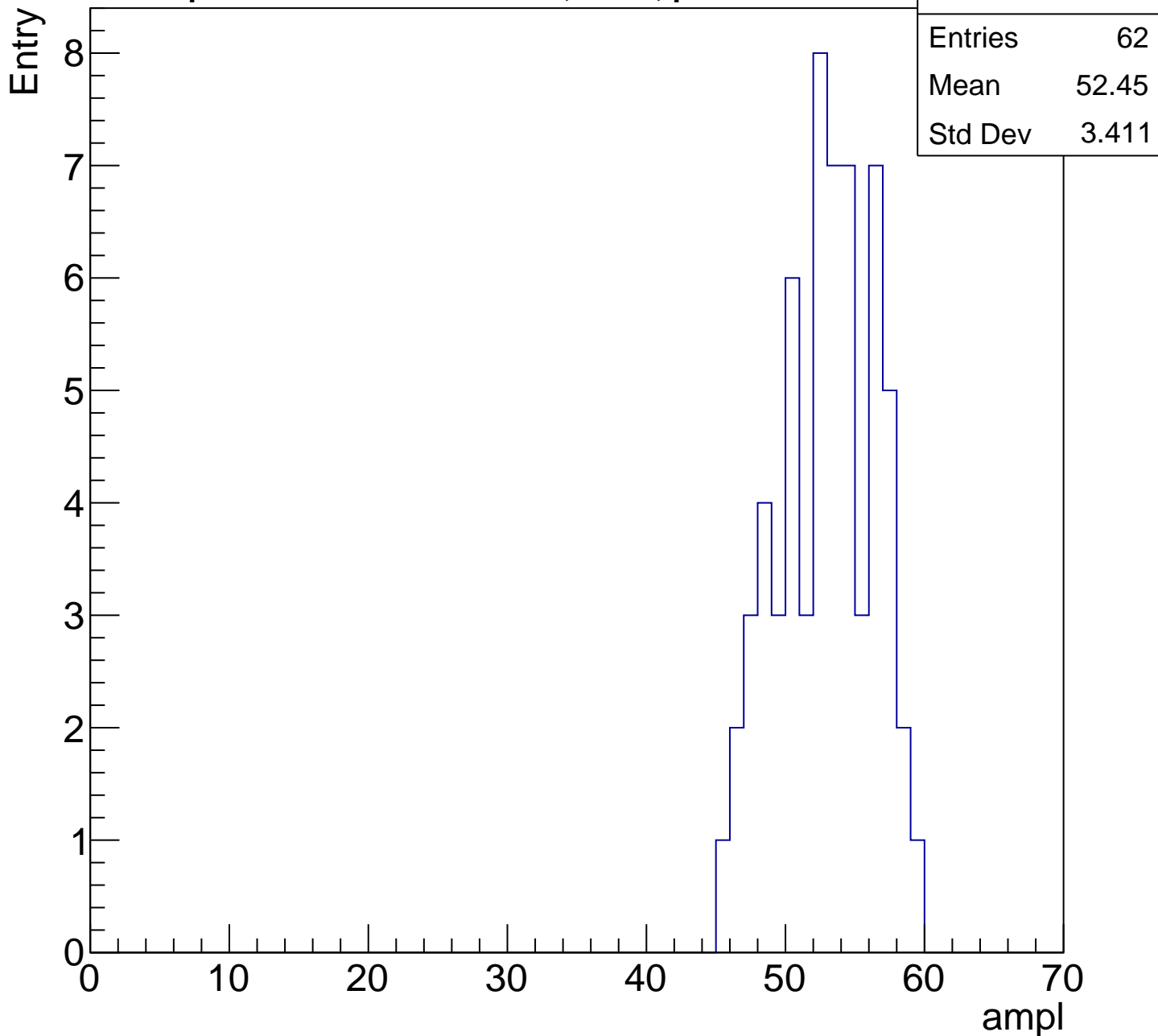
Entry

Entries	57
Mean	44.04
Std Dev	12.51



B1L103S, U18-ch68, adc4

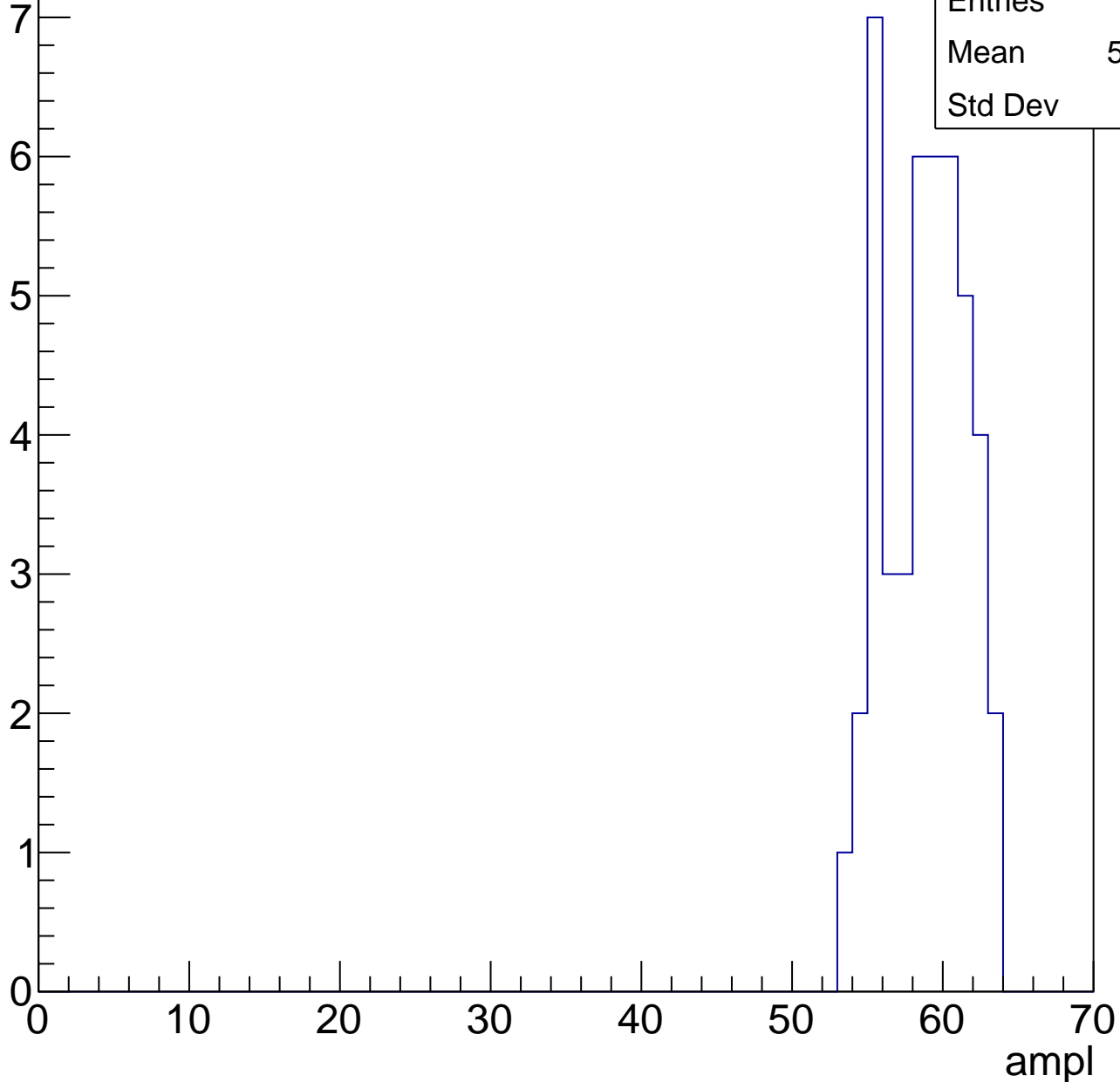
calib_packv5_041523_1651.root, FC#0, port C2



B1L103S, U18-ch68, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

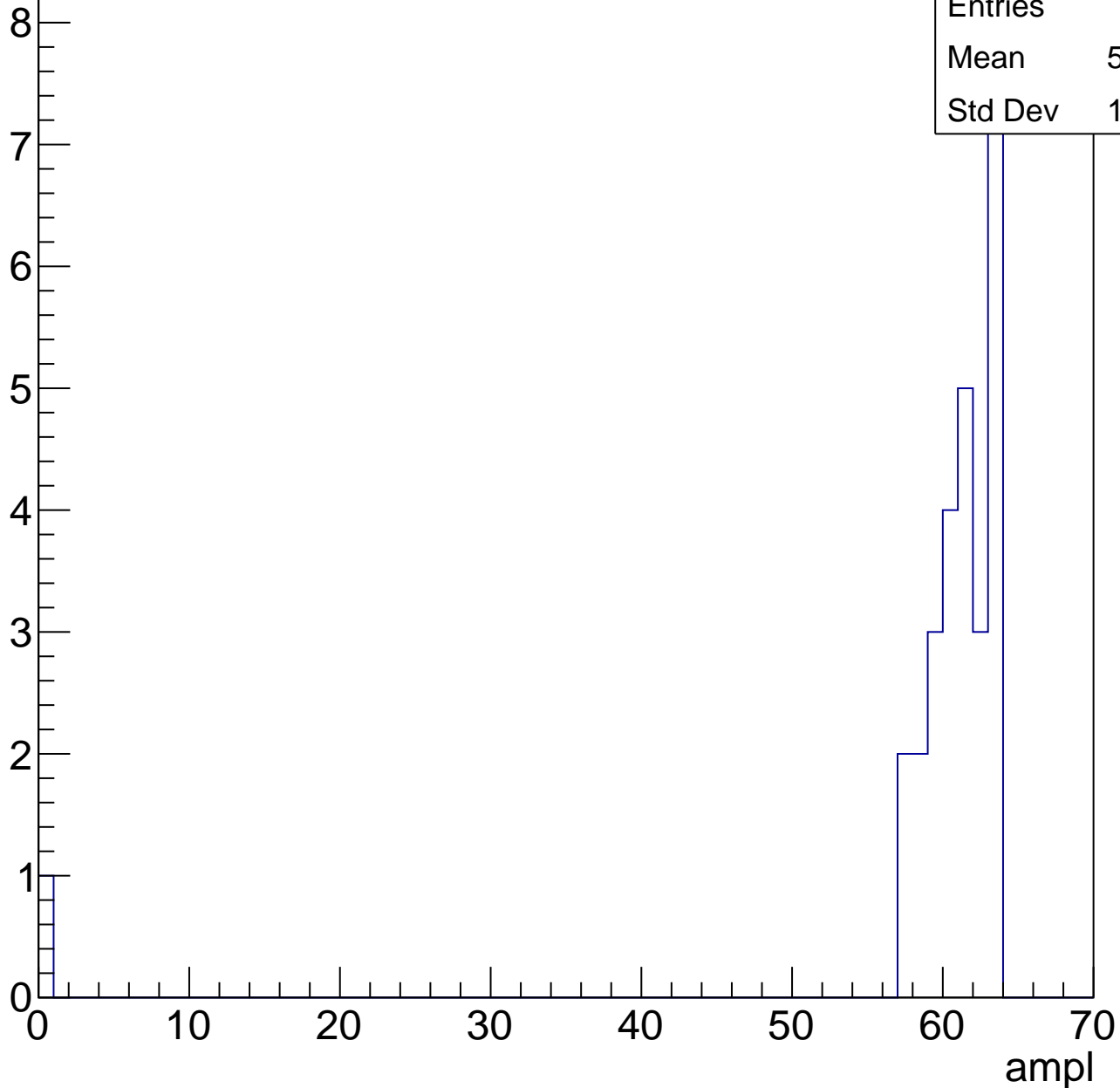


B1L103S, U18-ch68, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	28
Mean	58.64
Std Dev	11.44

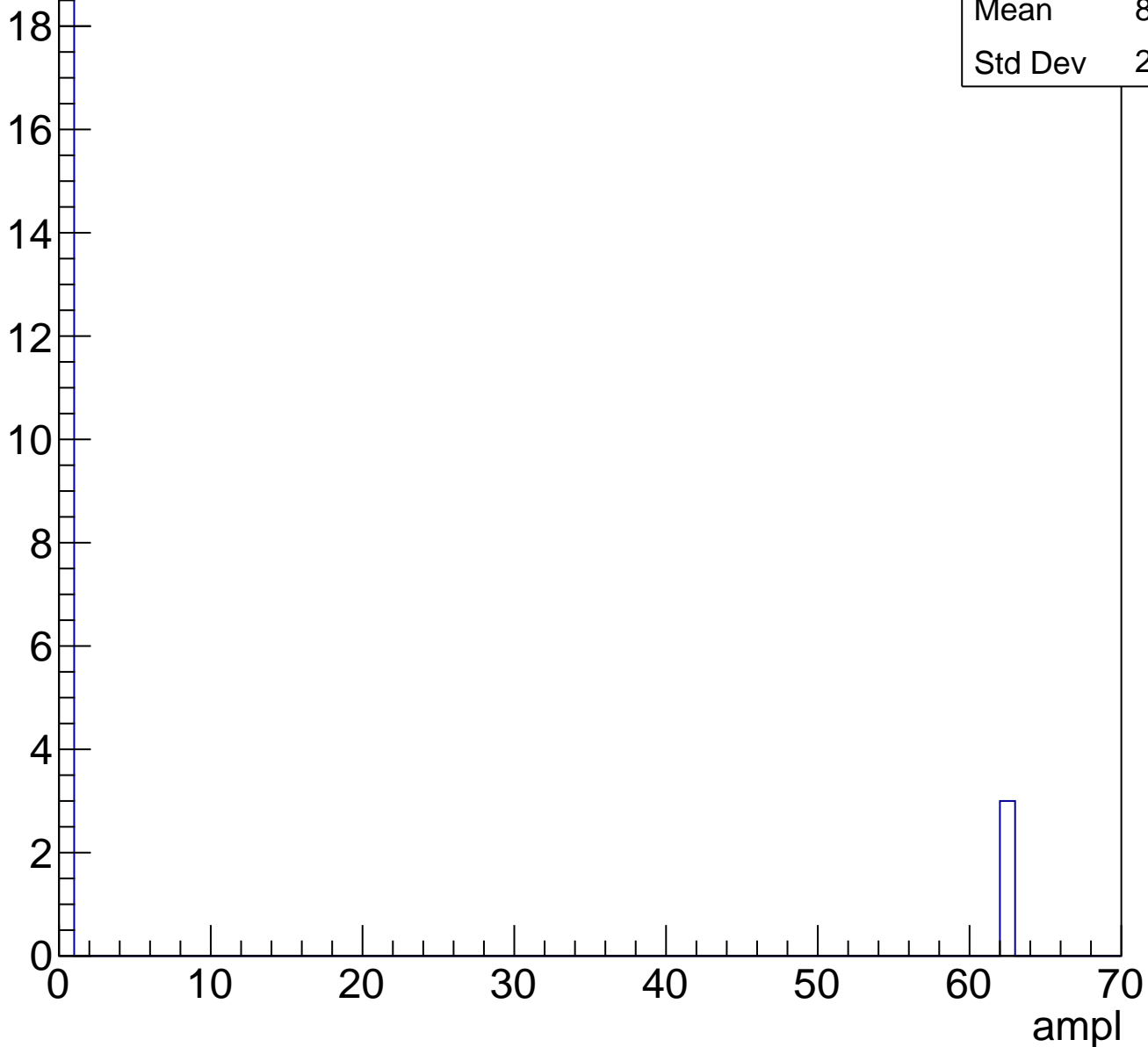


B1L103S, U18-ch68, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



B1L103S, U18-ch69, adc0

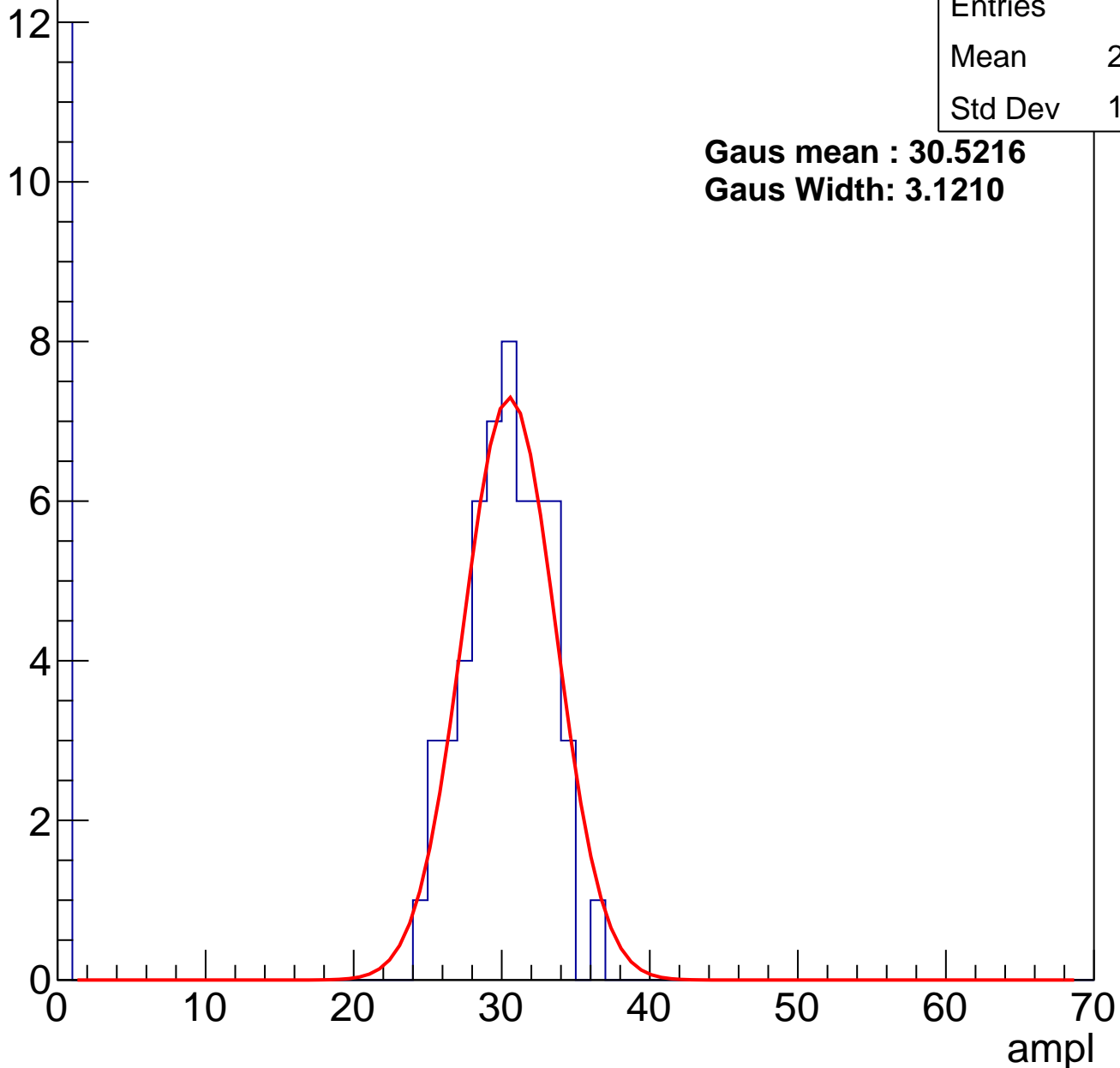
calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	24.39
Std Dev	11.75

Gaus mean : 30.5216

Gaus Width: 3.1210

Entry



B1L103S, U18-ch69, adc1

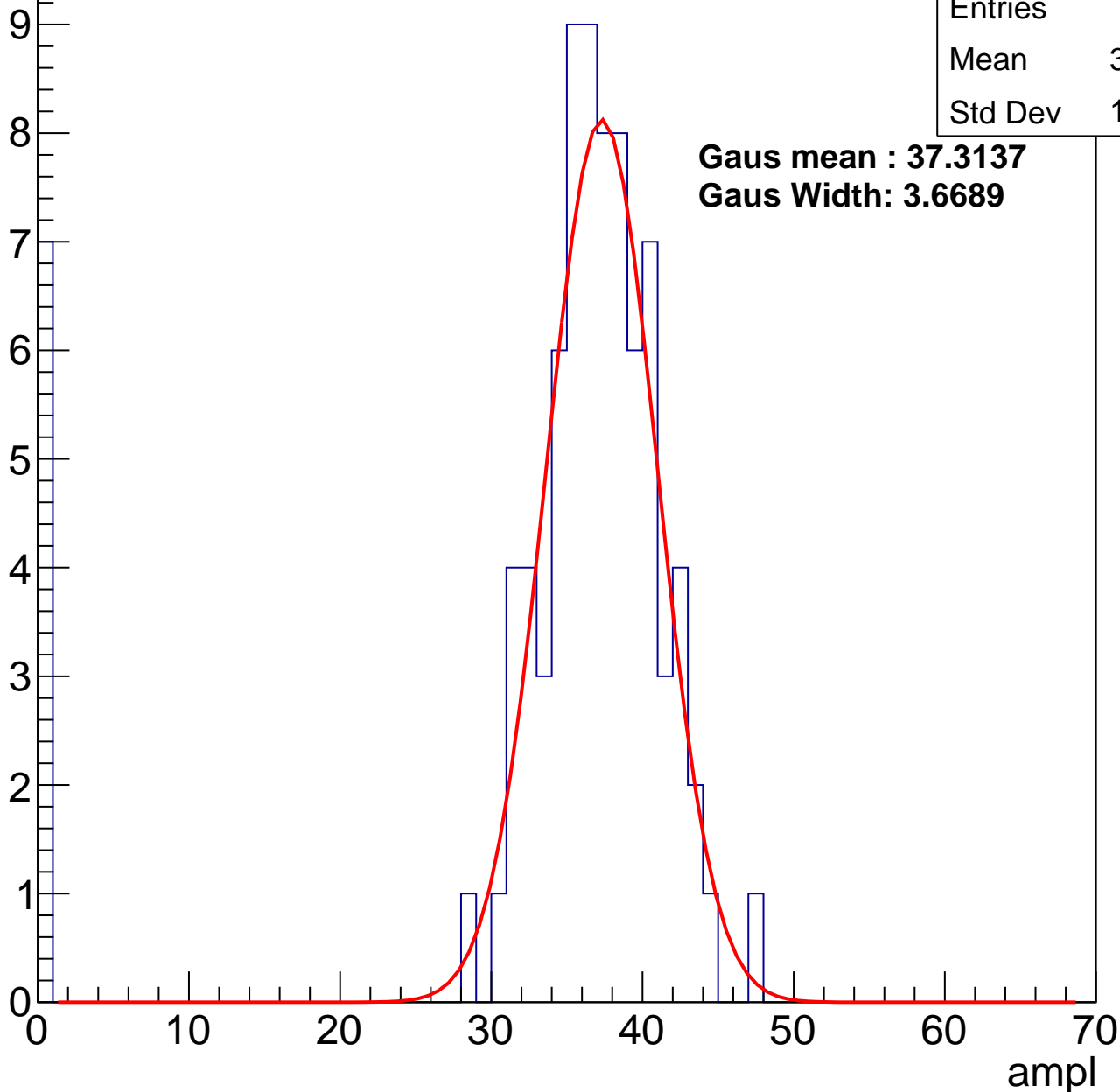
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	33.74
Std Dev	10.73

Gaus mean : 37.3137

Gaus Width: 3.6689



B1L103S, U18-ch69, adc2

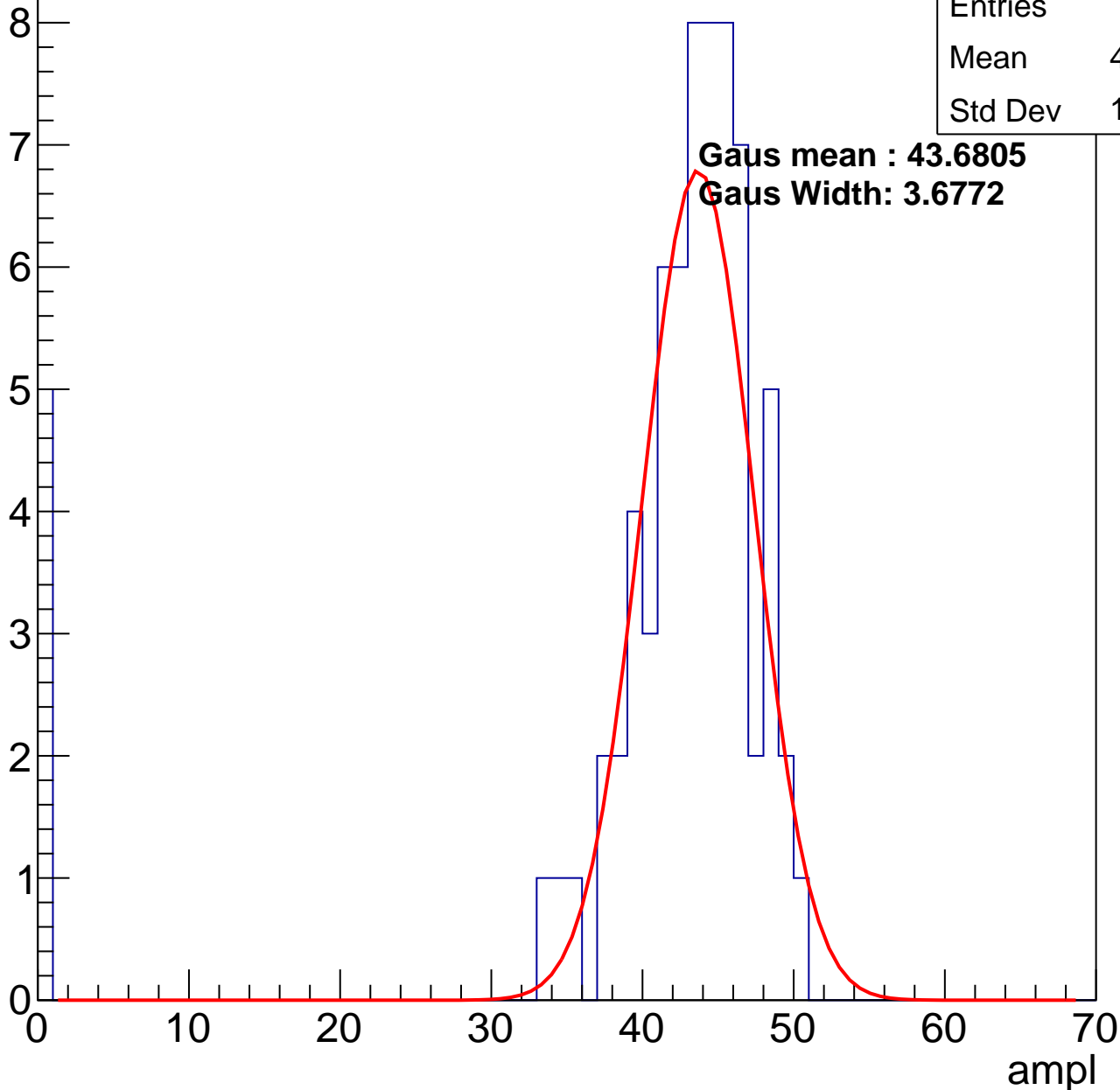
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	40.08
Std Dev	11.49

Gaus mean : 43.6805

Gaus Width: 3.6772

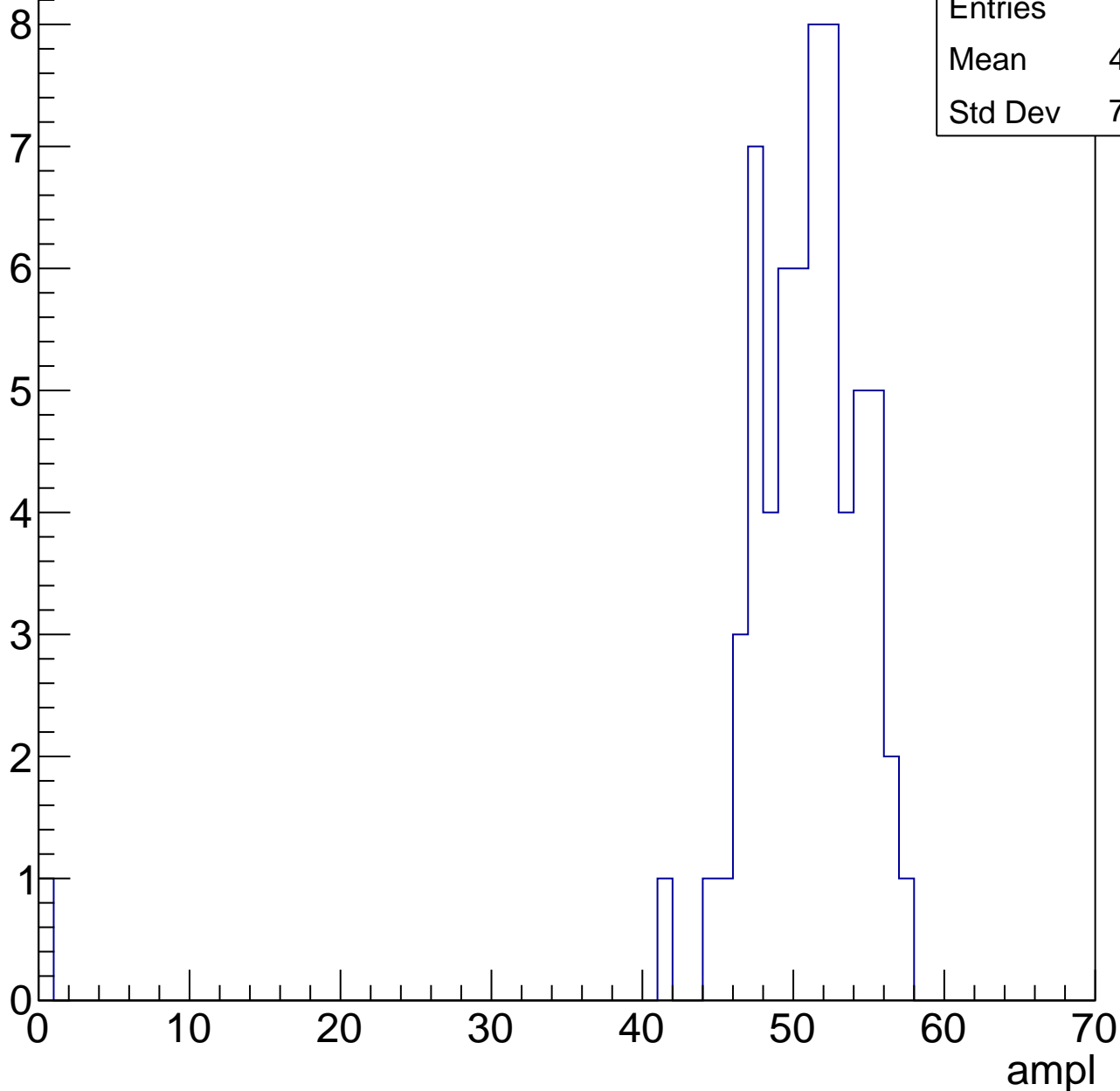


B1L103S, U18-ch69, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.73
Std Dev	7.096

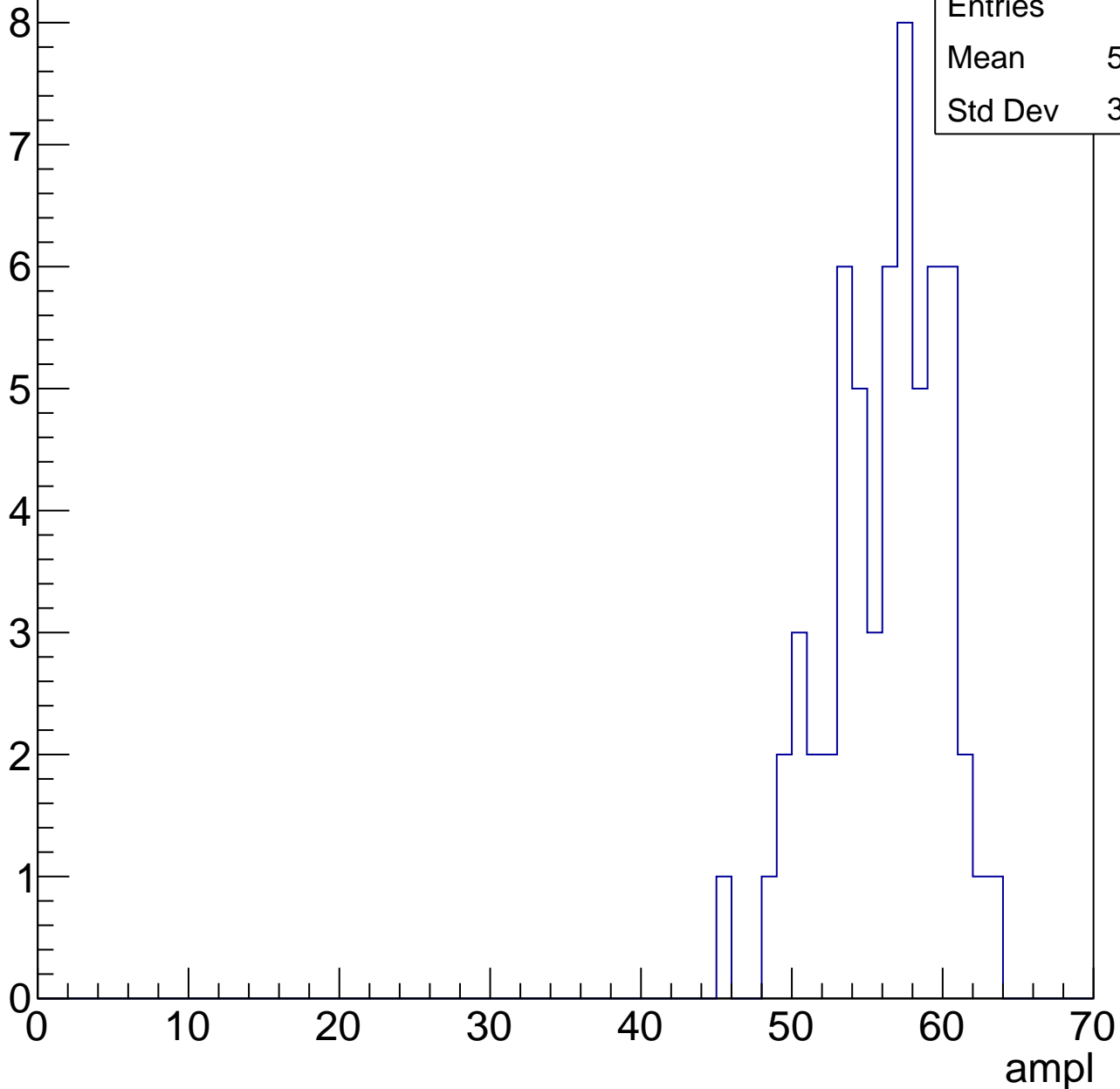


B1L103S, U18-ch69, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.72
Std Dev	3.782

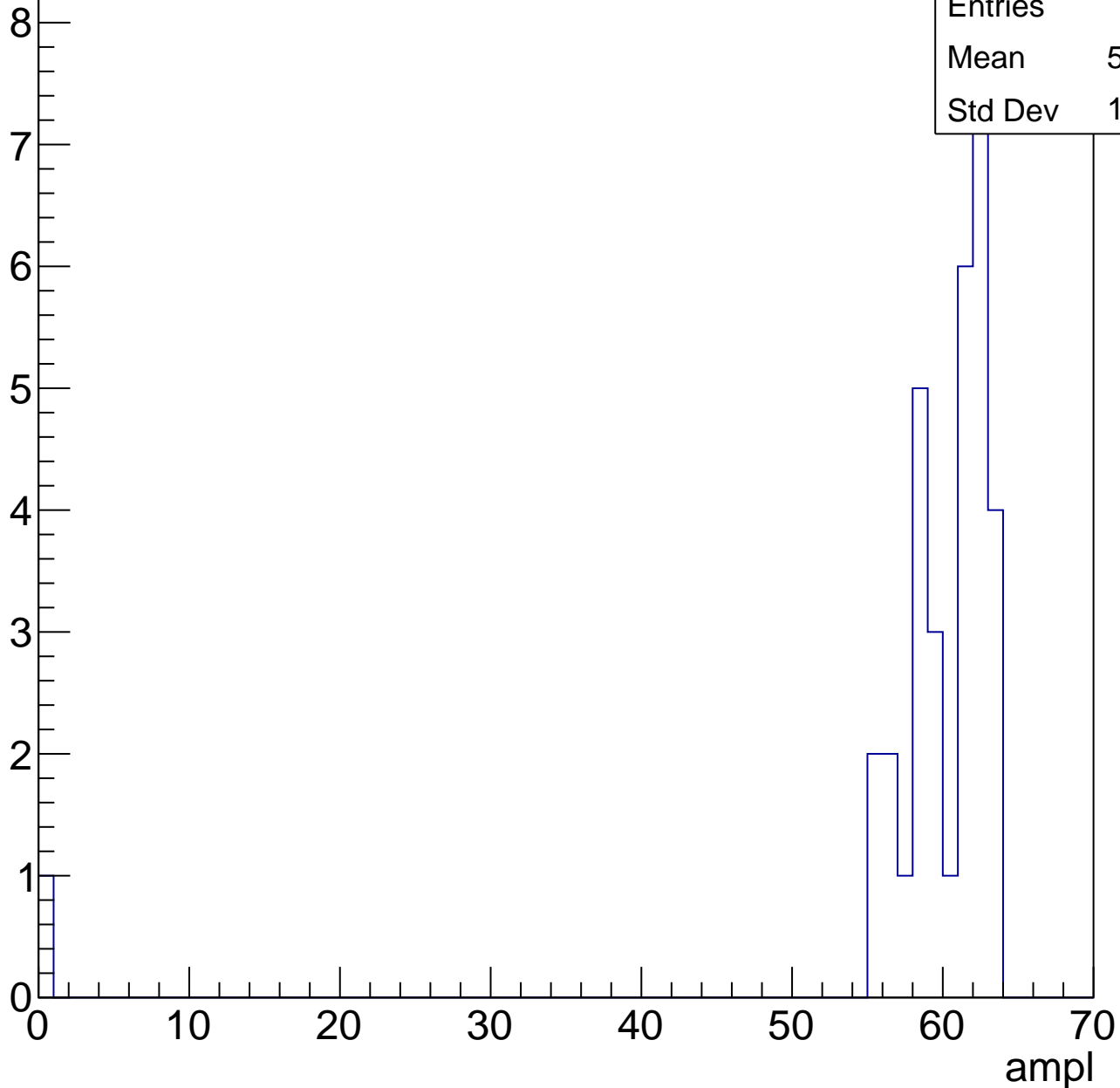


B1L103S, U18-ch69, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	33
Mean	58.18
Std Dev	10.56

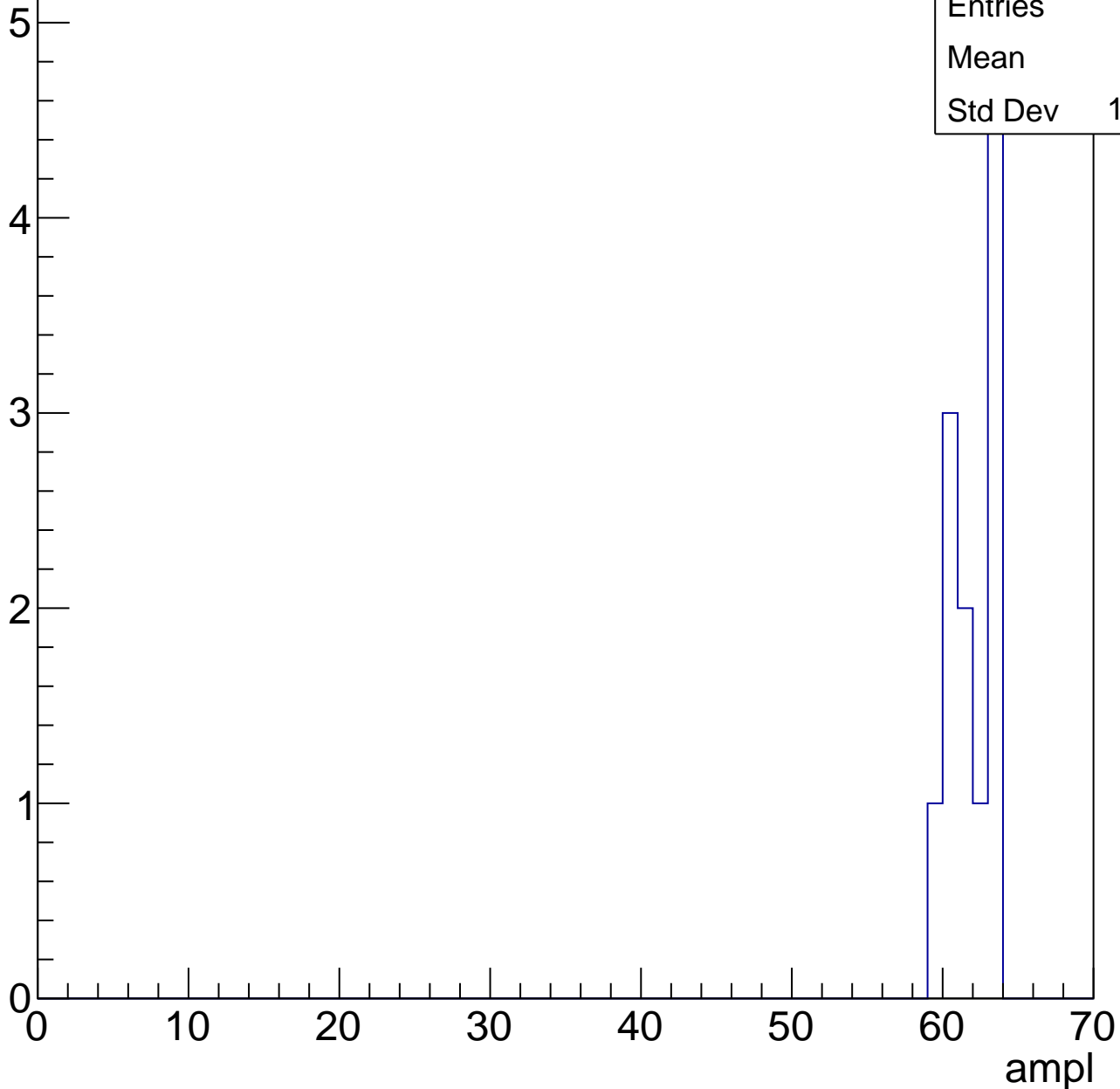


B1L103S, U18-ch69, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.5
Std Dev	1.443



B1L103S, U18-ch69, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch70, adc0

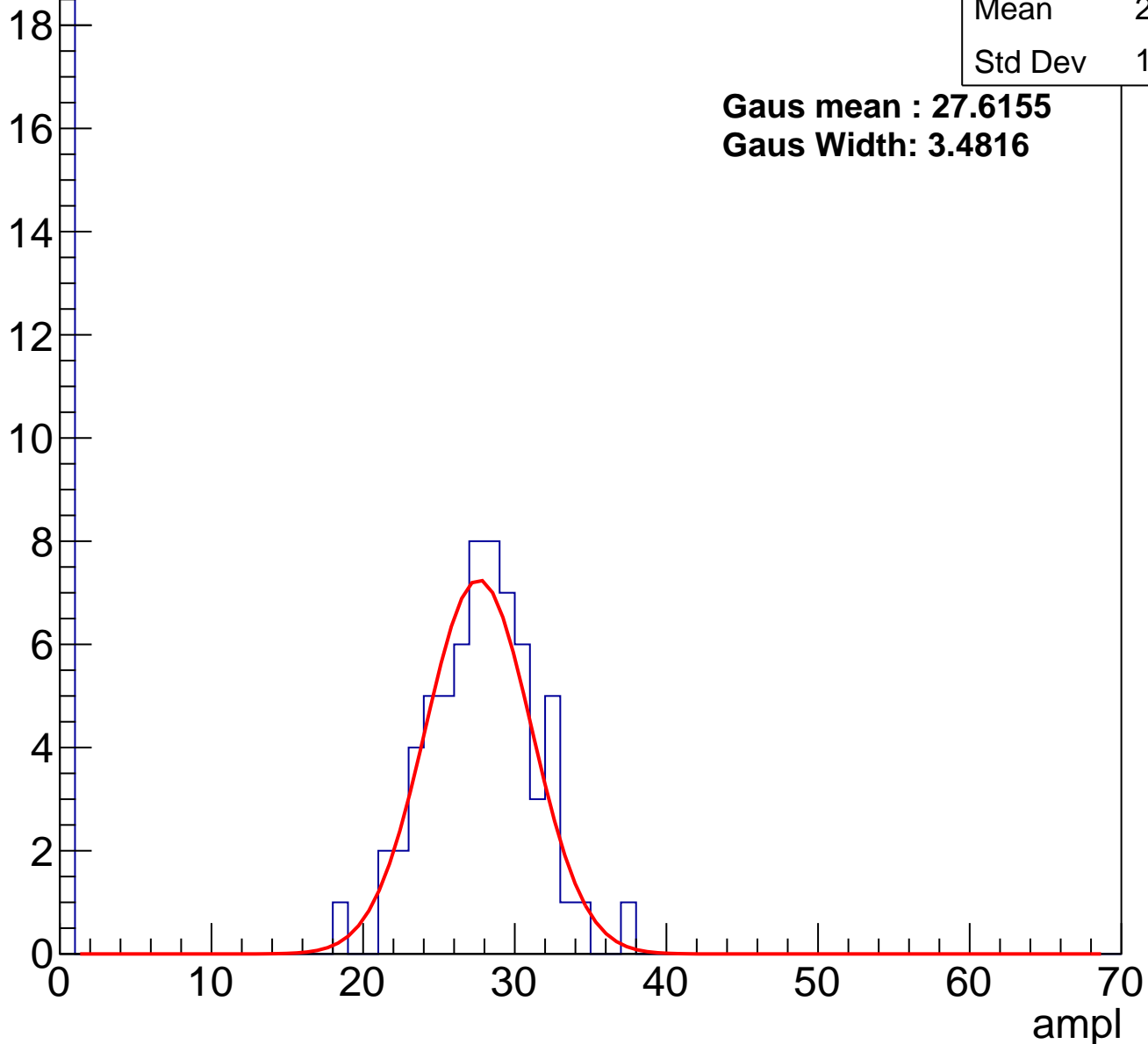
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	21.15
Std Dev	11.84

Gaus mean : 27.6155

Gaus Width: 3.4816

Entry



B1L103S, U18-ch70, adc1

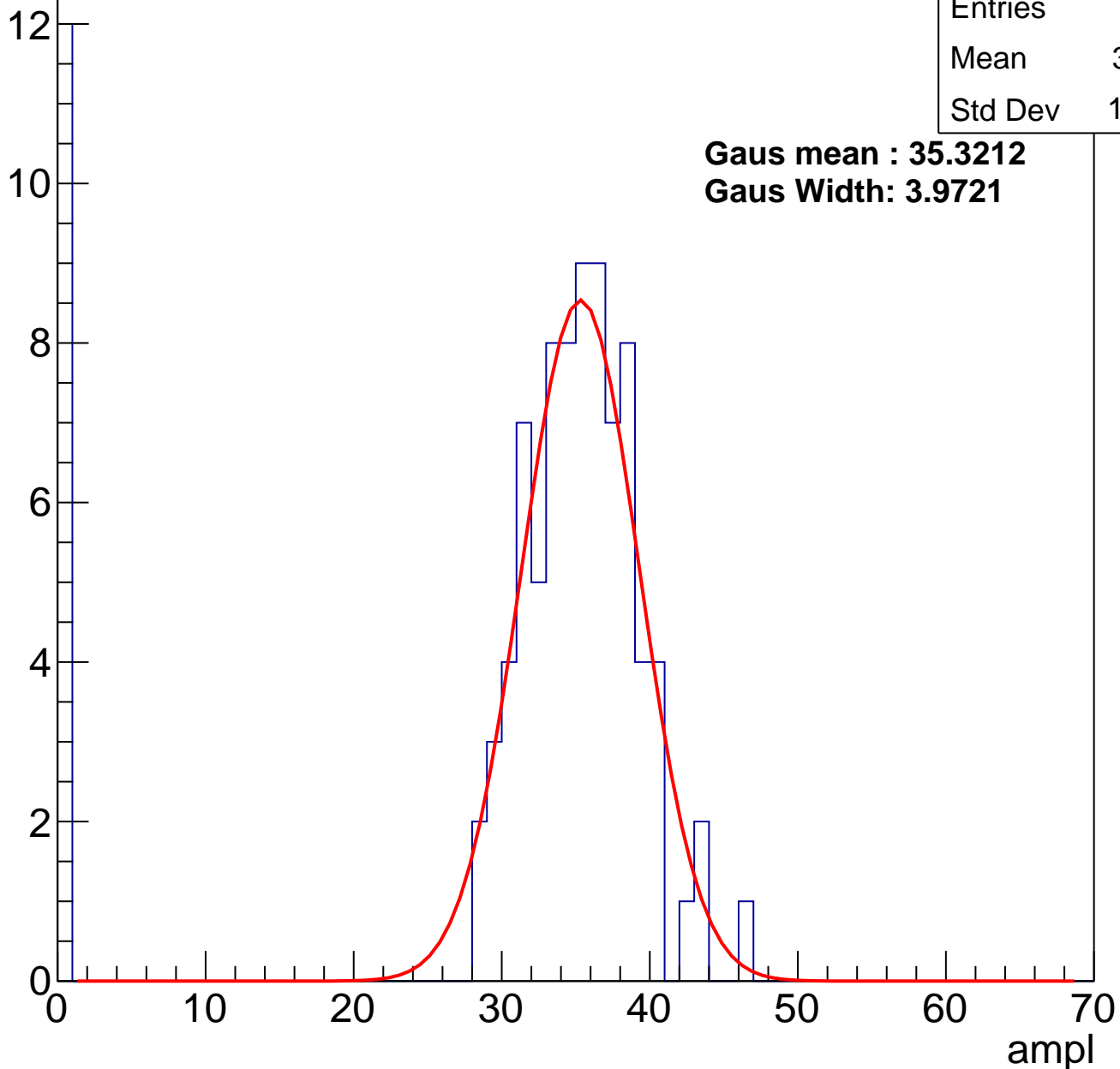
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	30.51
Std Dev	12.15

Gaus mean : 35.3212

Gaus Width: 3.9721

Entry



B1L103S, U18-ch70, adc2

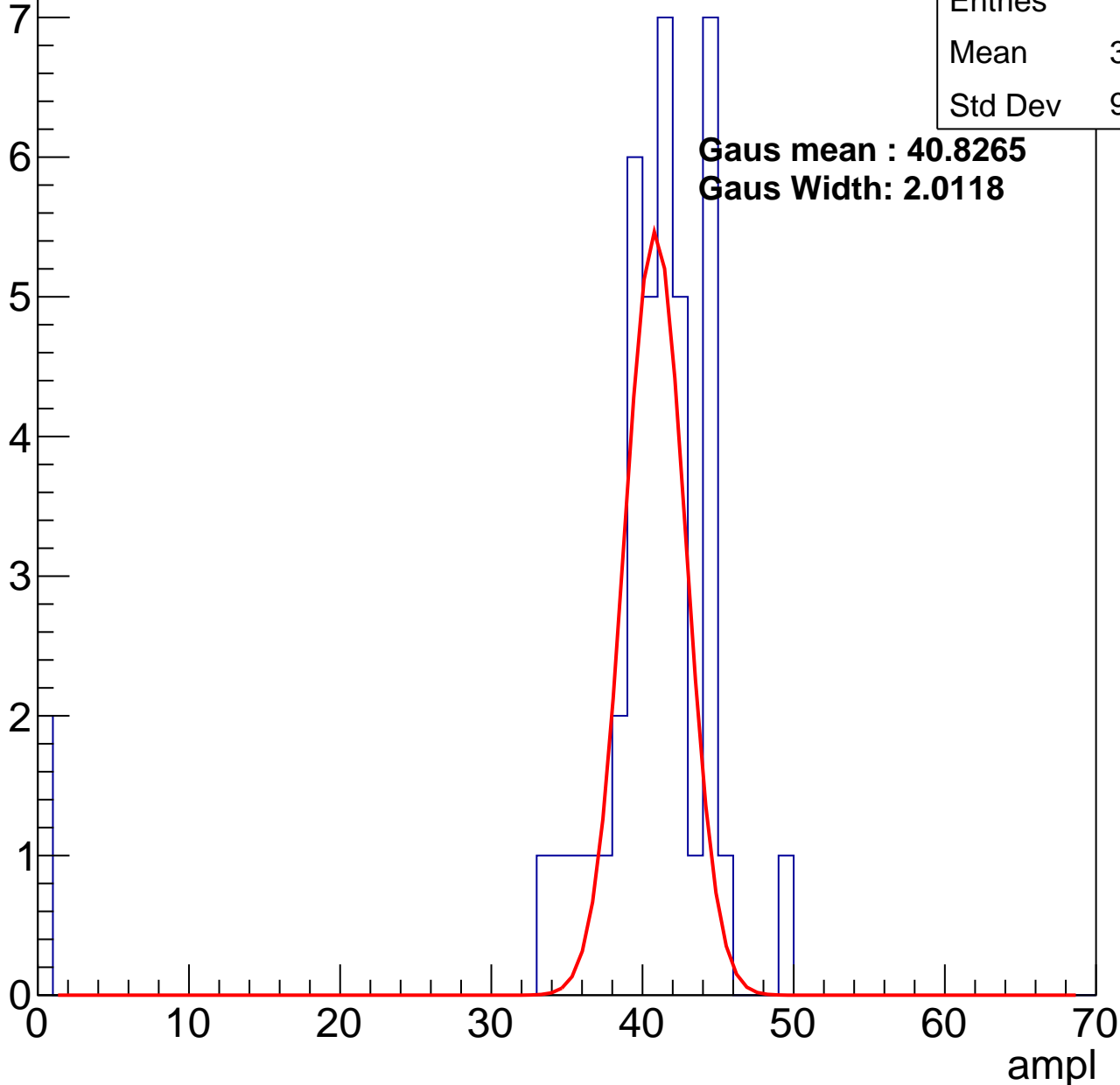
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	38.74
Std Dev	9.176

Gaus mean : 40.8265

Gaus Width: 2.0118



B1L103S, U18-ch70, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	46.63
Std Dev	6.219

Entry

10

8

6

4

2

0

0

10

20

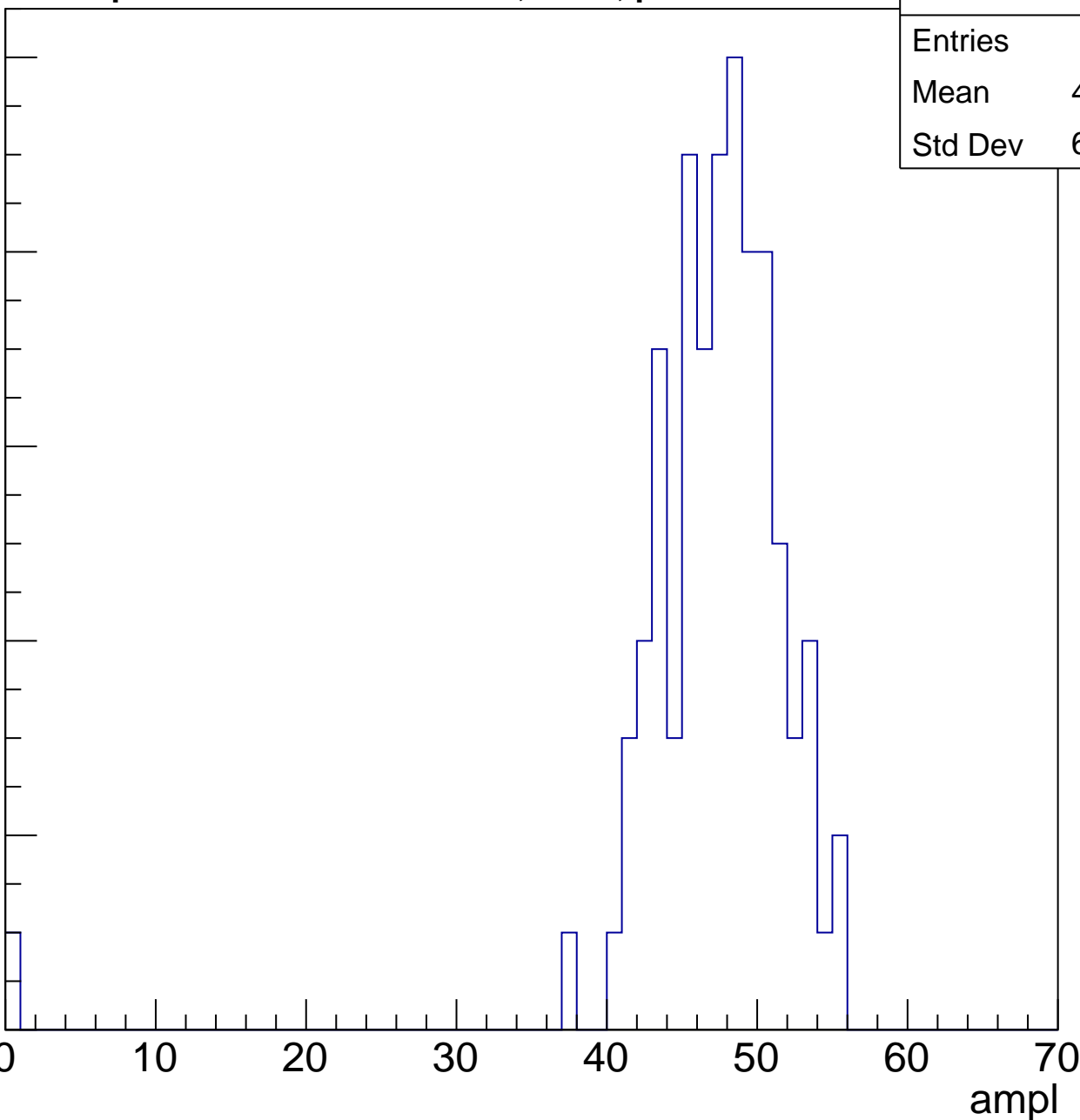
30

40

50

60

ampl

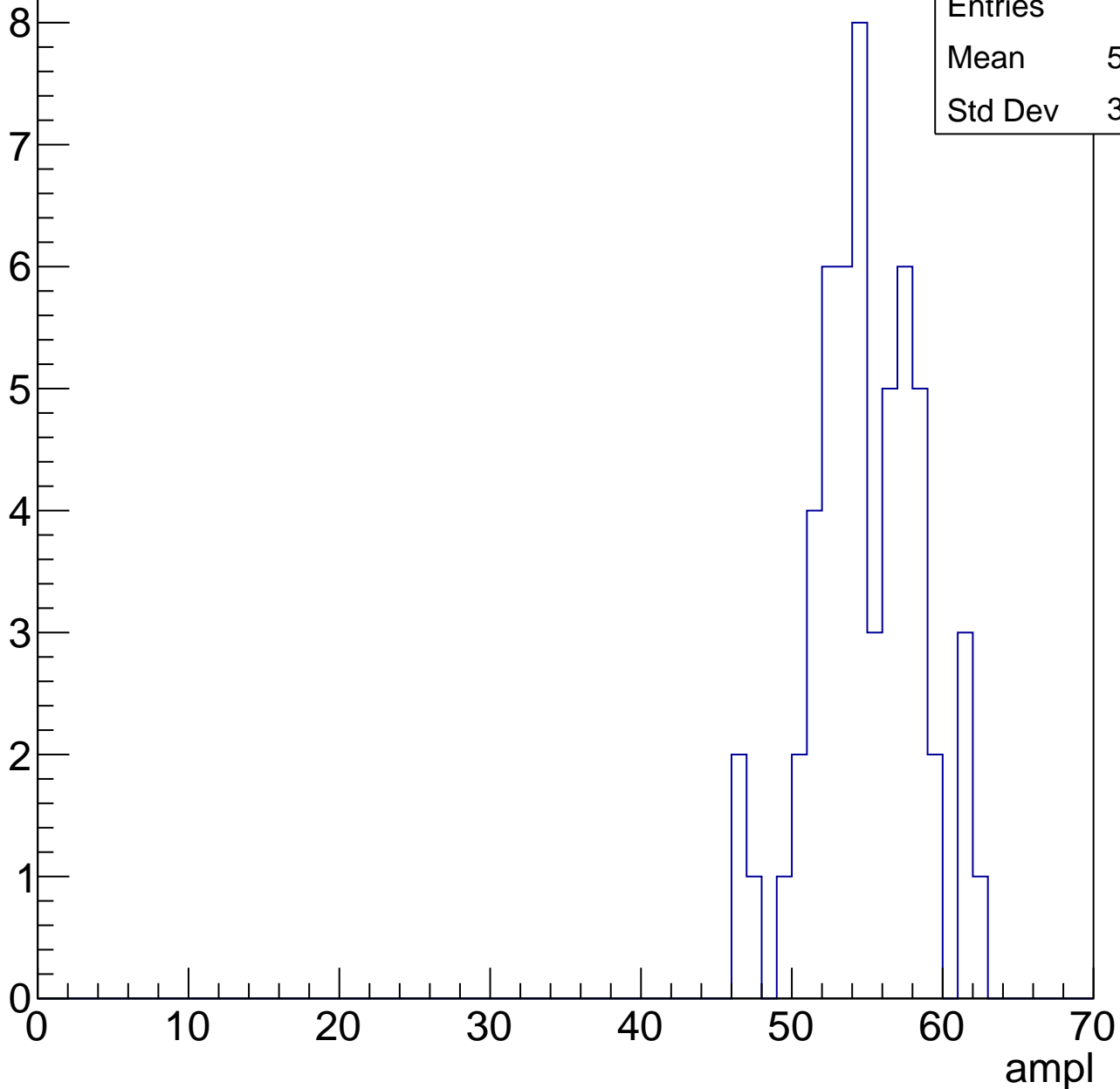


B1L103S, U18-ch70, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	54.44
Std Dev	3.587

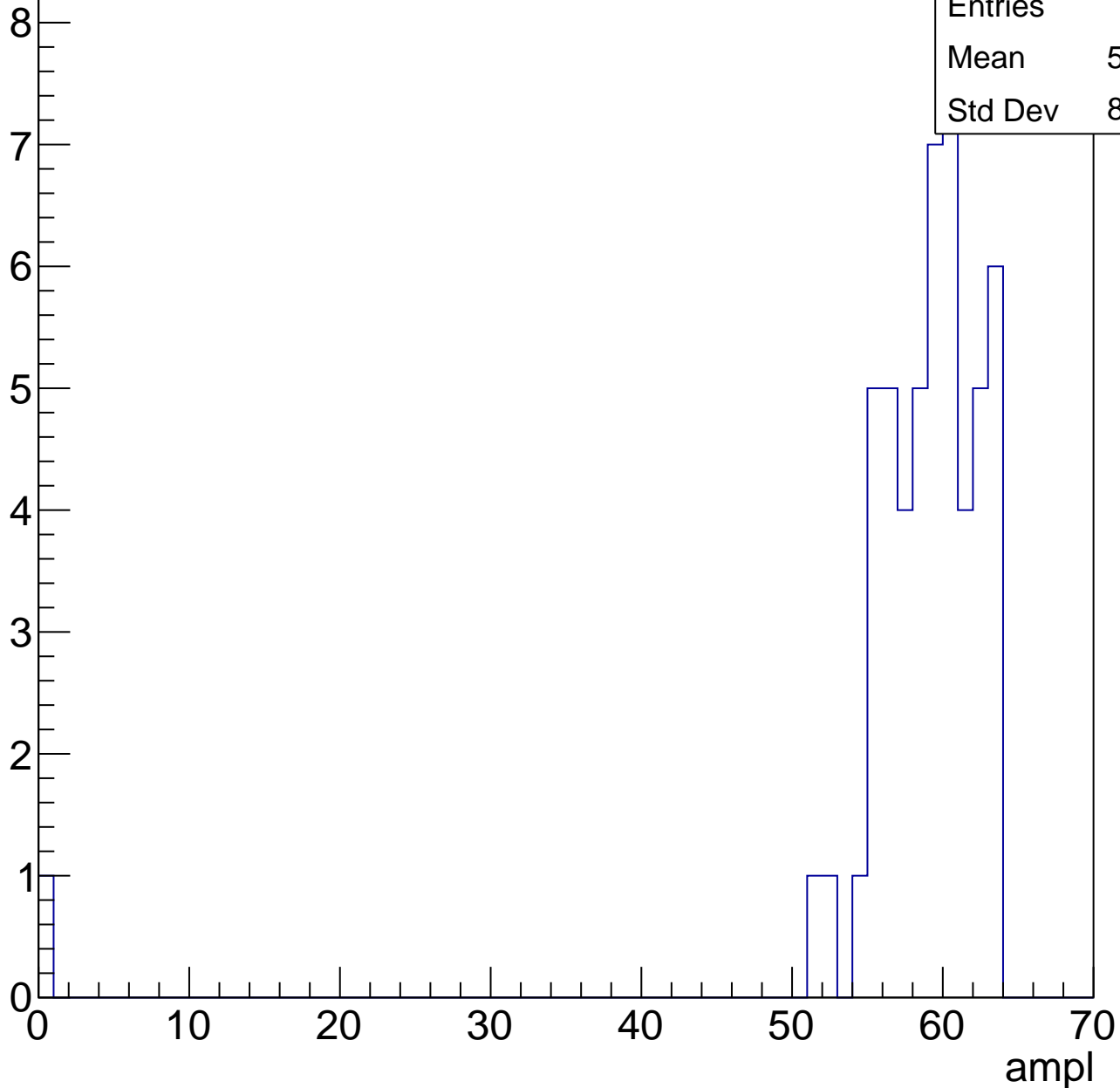


B1L103S, U18-ch70, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

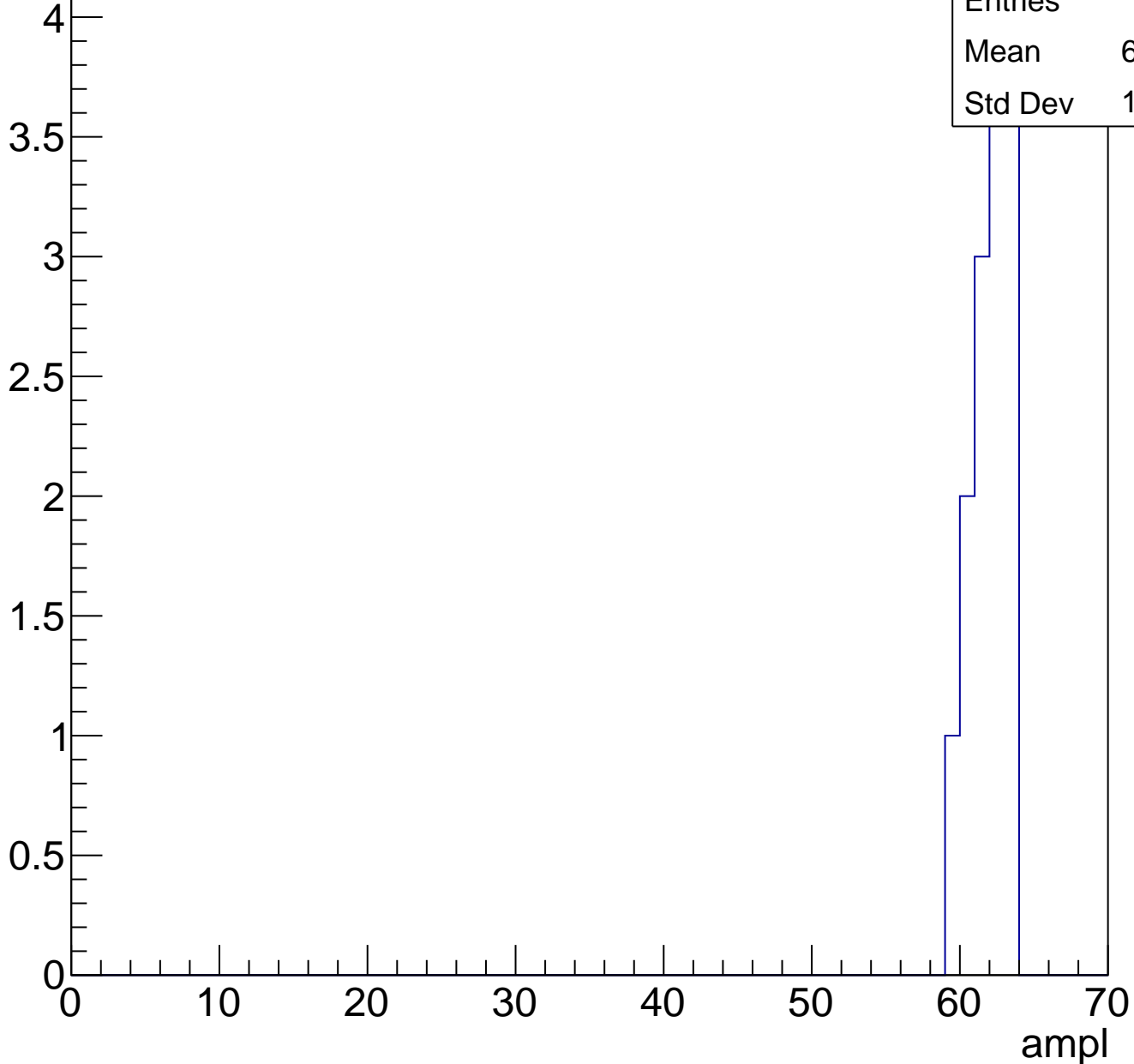
Entries	53
Mean	57.64
Std Dev	8.503



B1L103S, U18-ch70, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch70, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch71, adc0

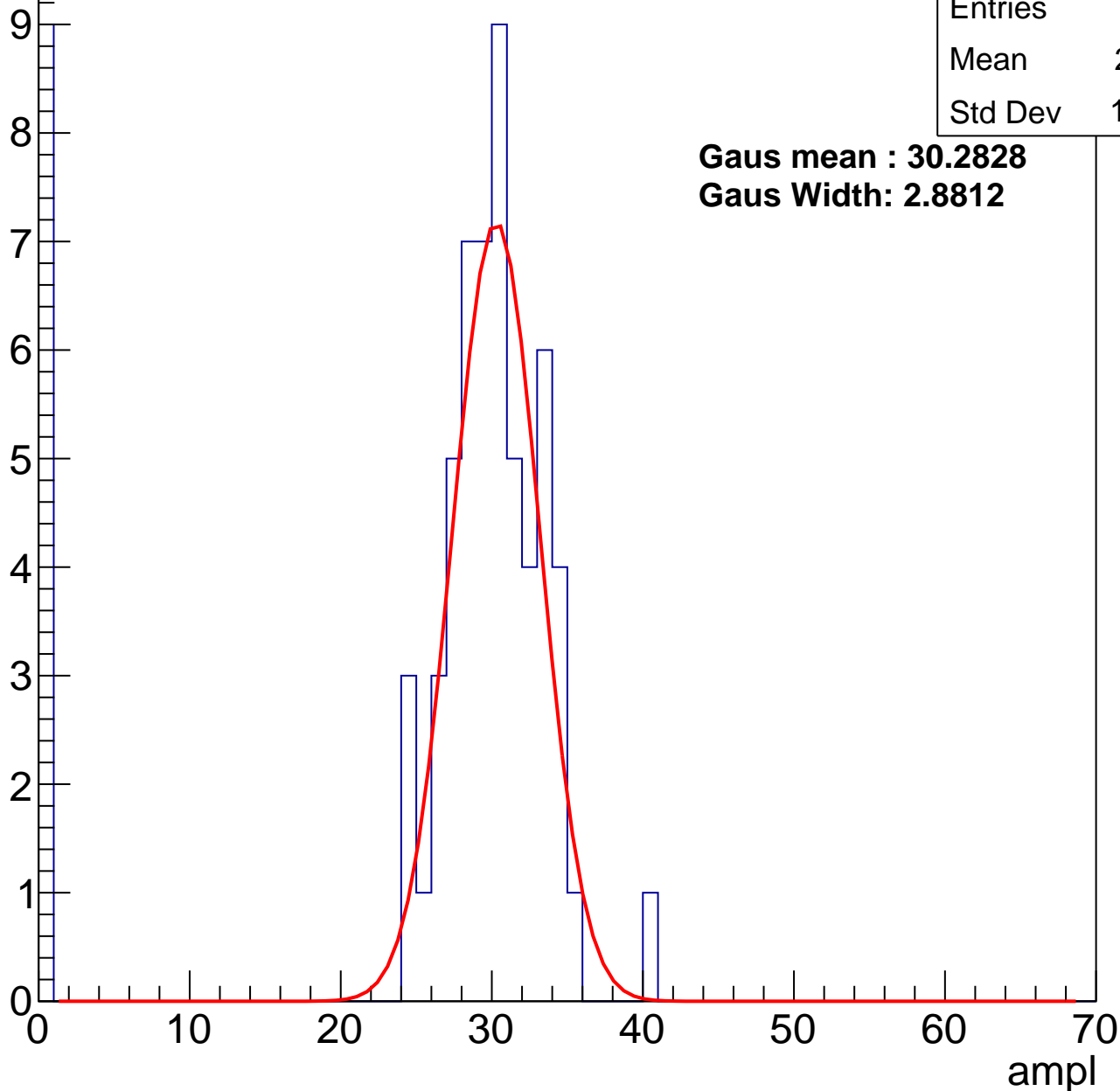
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	25.71
Std Dev	10.69

Gaus mean : 30.2828

Gaus Width: 2.8812



B1L103S, U18-ch71, adc1

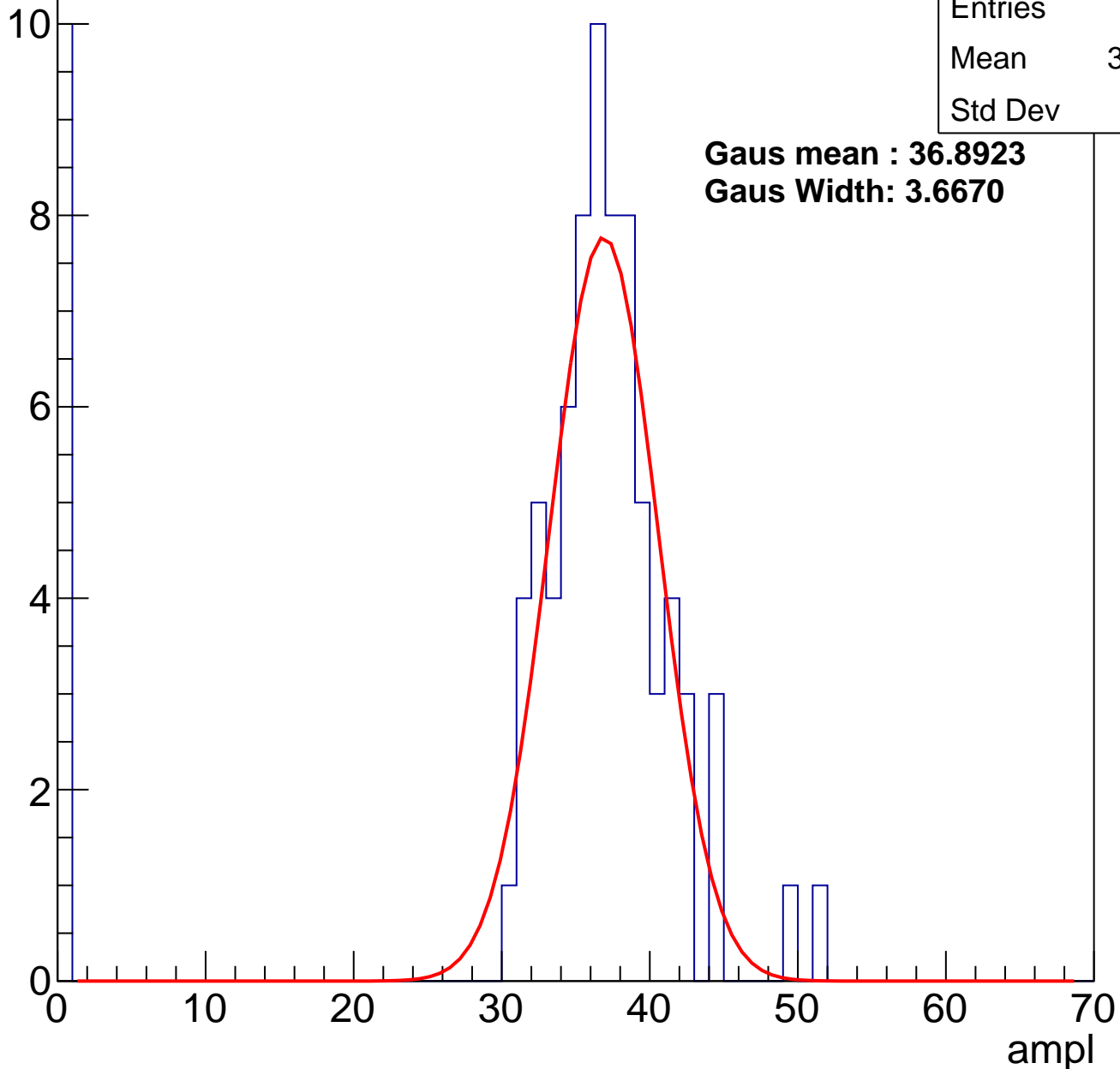
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	32.46
Std Dev	12.5

Gaus mean : 36.8923

Gaus Width: 3.6670

Entry



B1L103S, U18-ch71, adc2

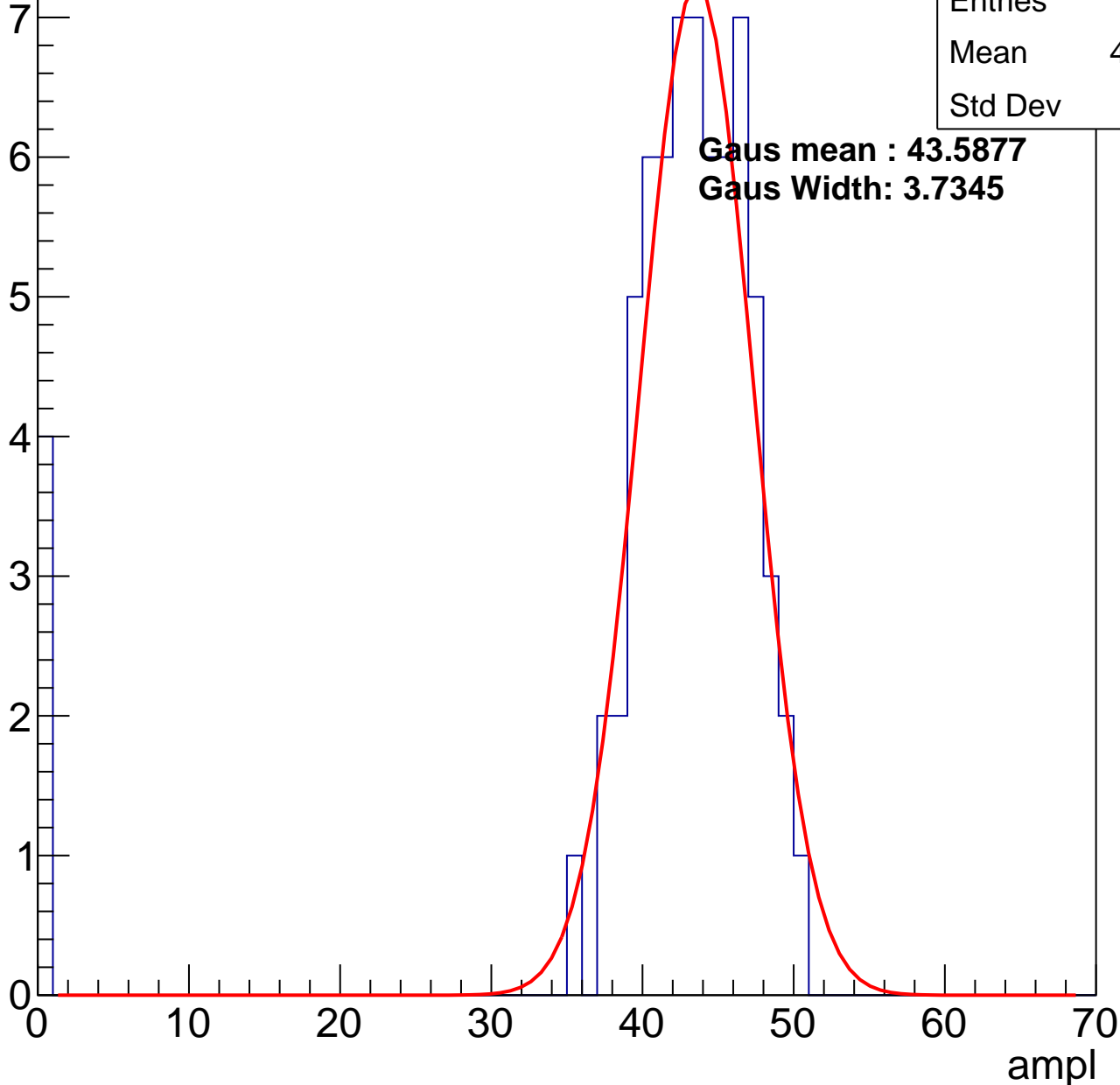
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	40.63
Std Dev	10.5

Gaus mean : 43.5877

Gaus Width: 3.7345

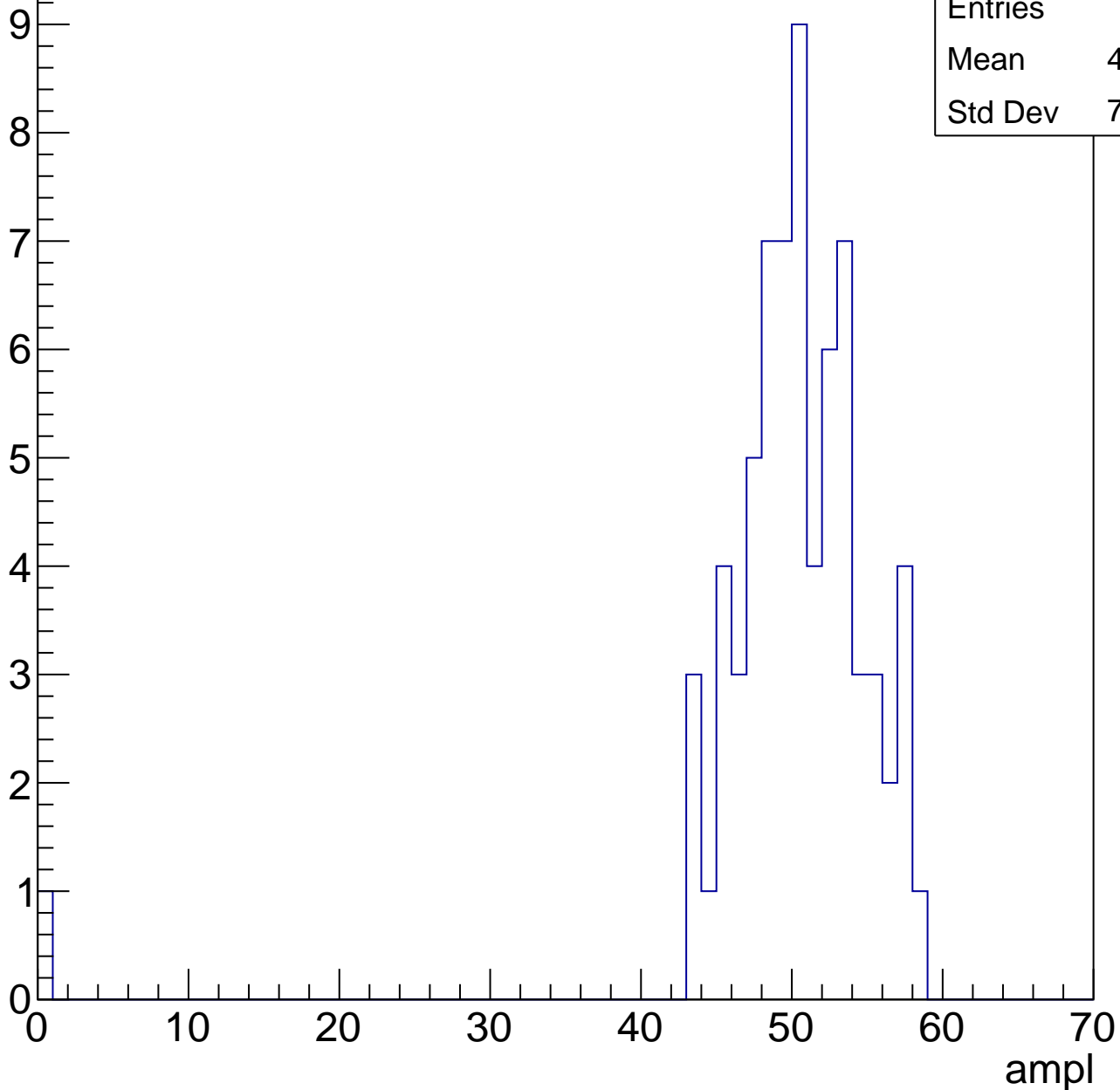


B1L103S, U18-ch71, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	49.53
Std Dev	7.012

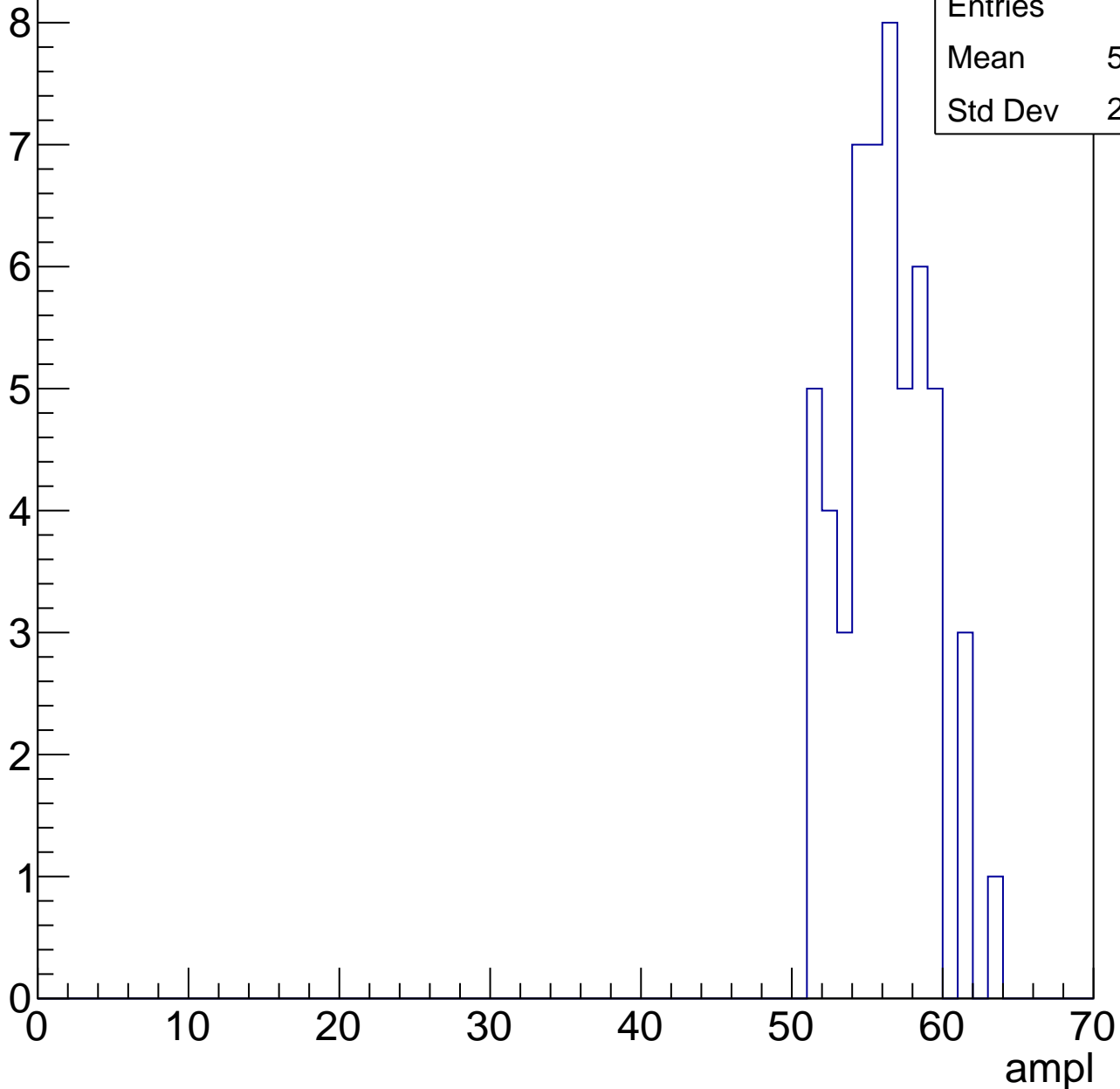


B1L103S, U18-ch71, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

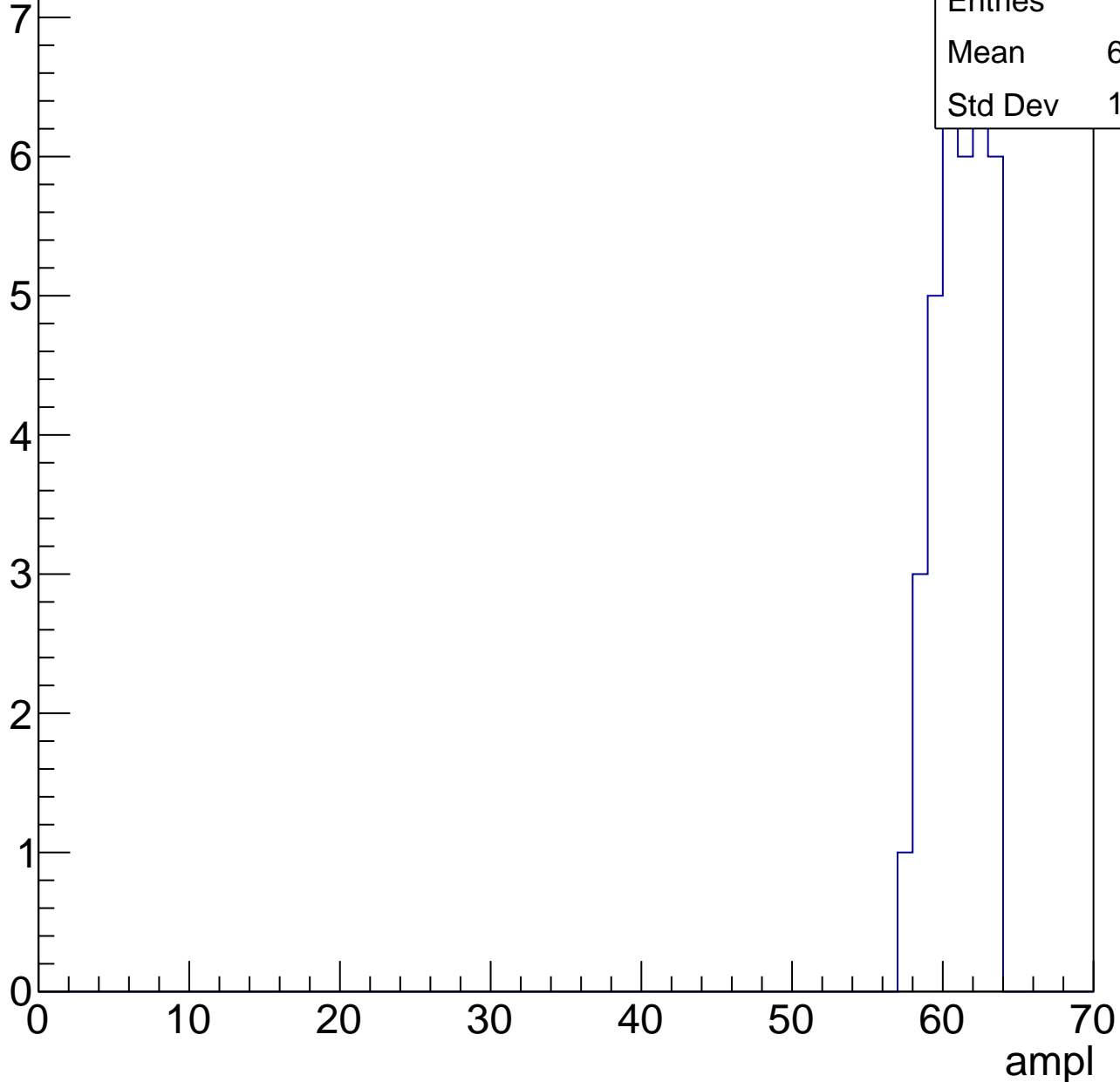
Entries	54
Mean	55.69
Std Dev	2.866



B1L103S, U18-ch71, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

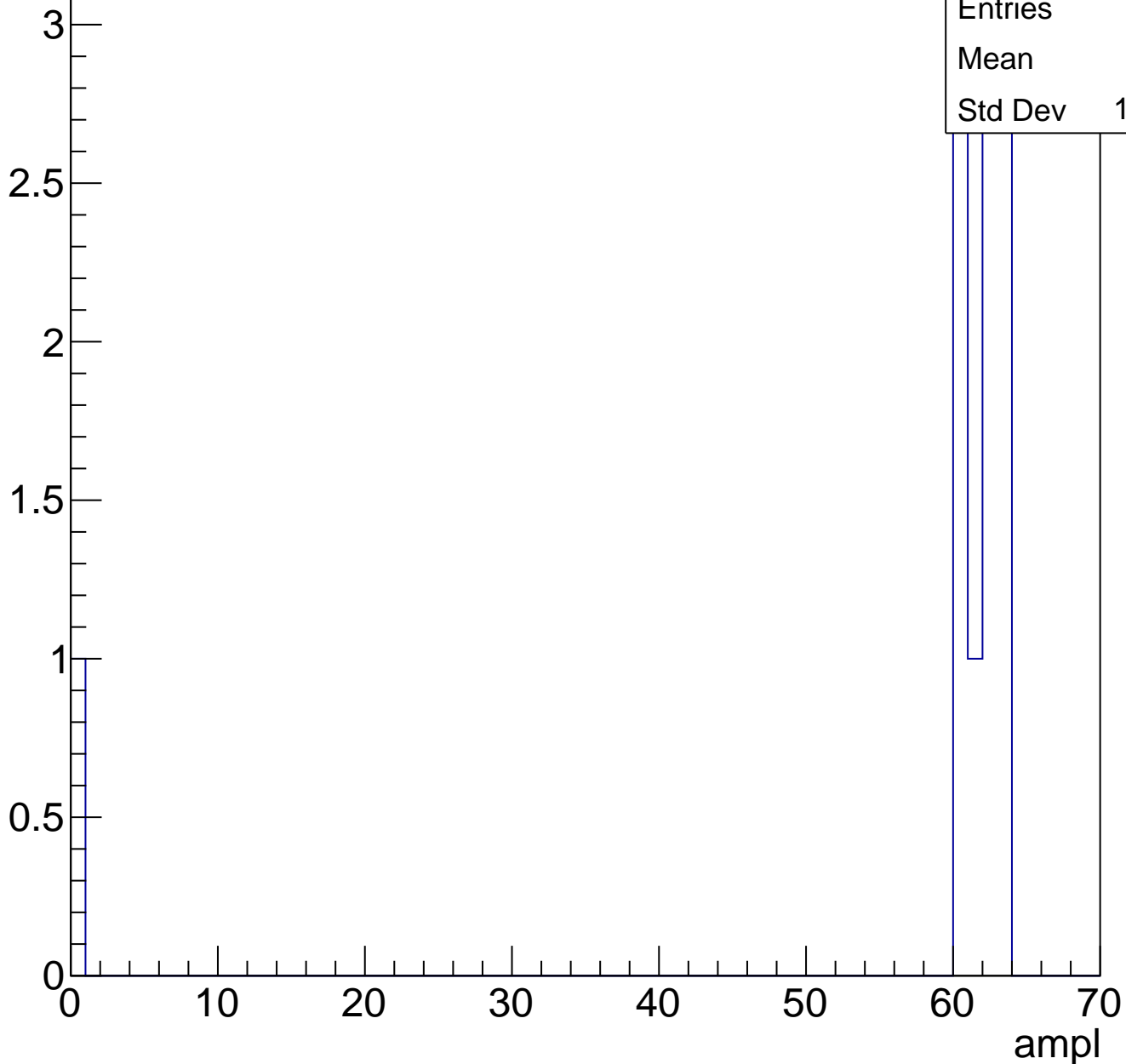


Entries	35
Mean	60.69
Std Dev	1.669

B1L103S, U18-ch71, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch71, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch72, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	66
Mean	22.42
Std Dev	11.47

Gaus mean : 28.5680

Gaus Width: 4.3024

Entry

12

10

8

6

4

2

0

0

10

20

30

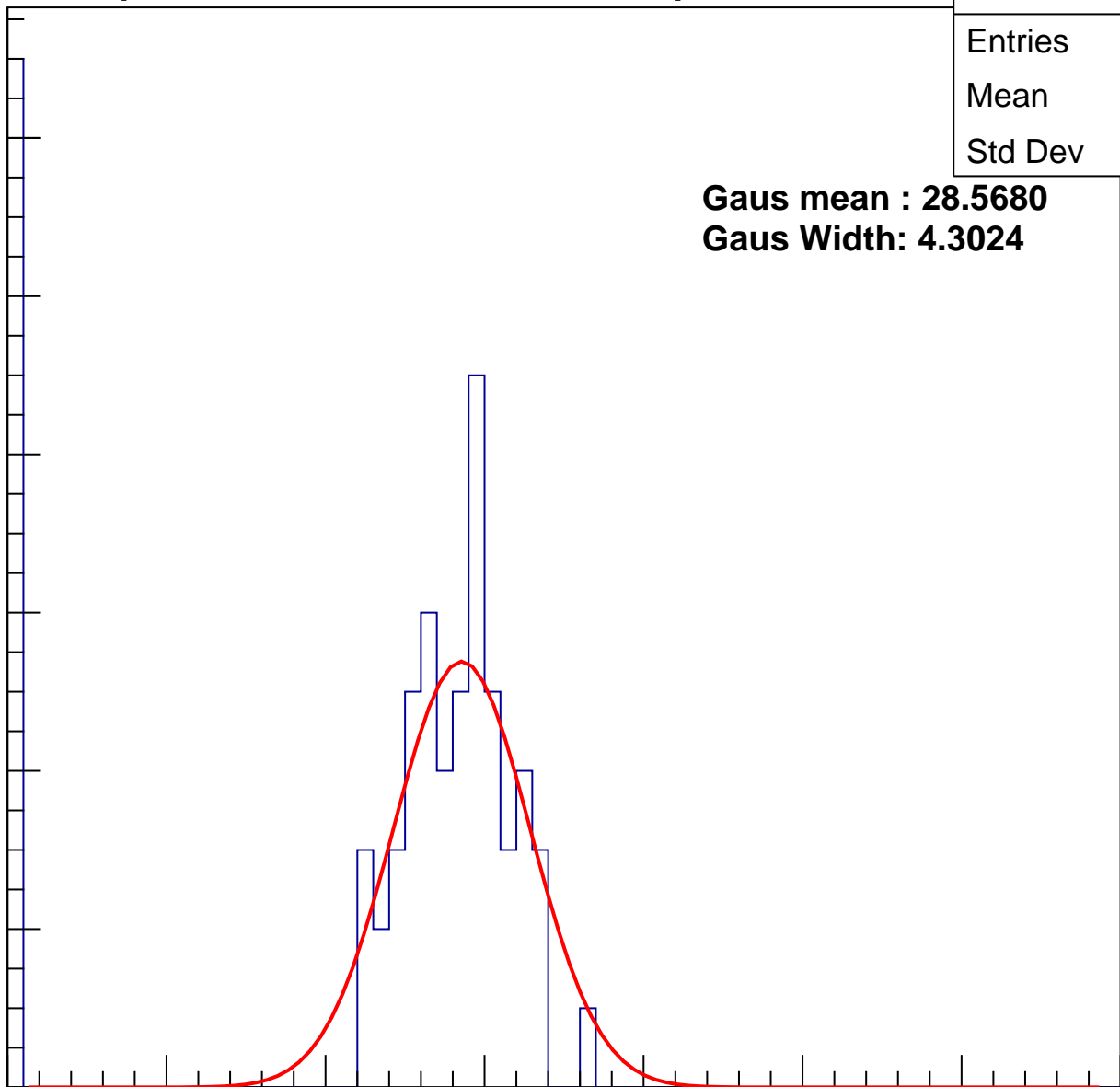
40

50

60

70

ampl



B1L103S, U18-ch72, adc1

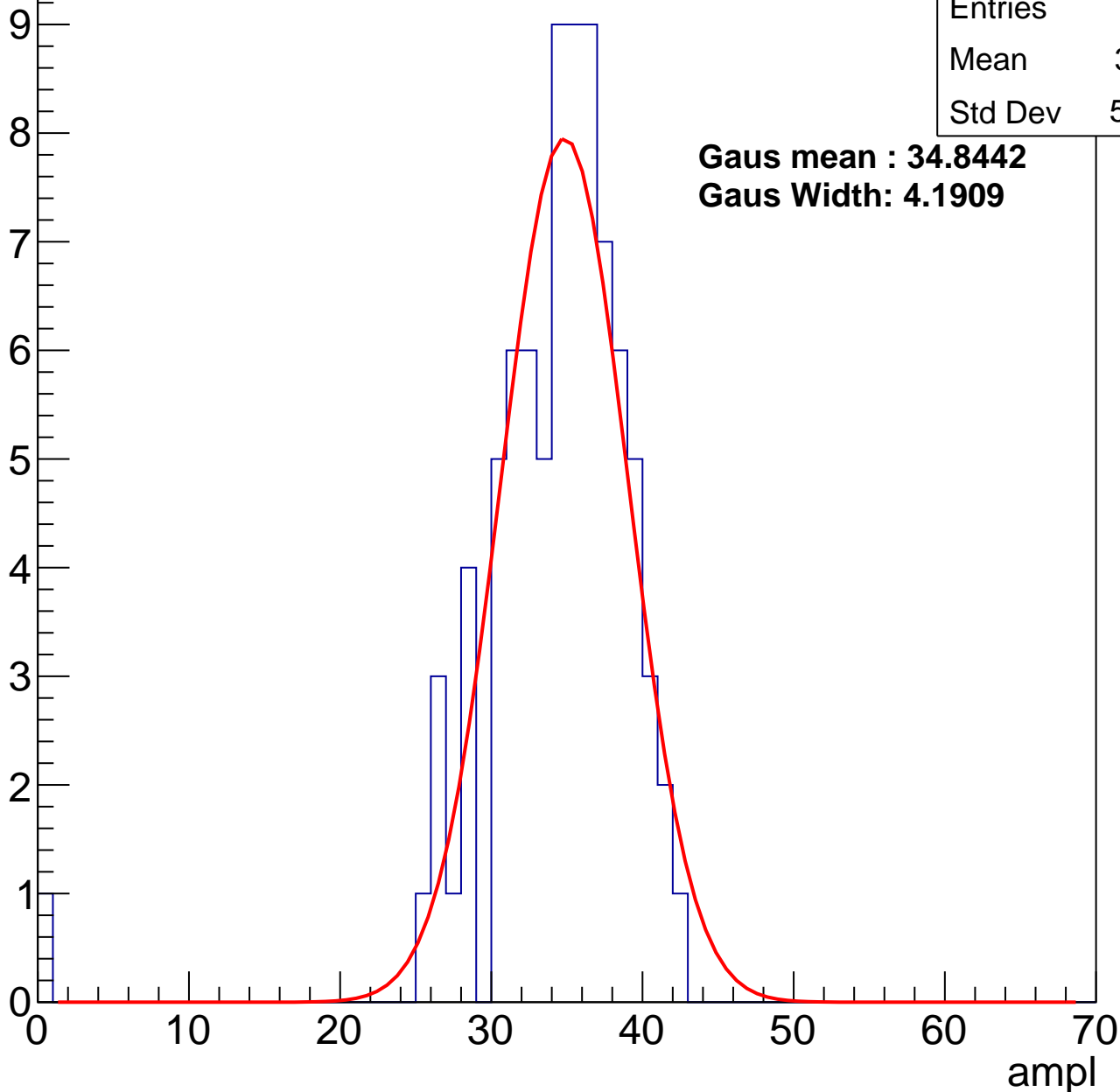
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	33.81
Std Dev	5.356

Gaus mean : 34.8442

Gaus Width: 4.1909



B1L103S, U18-ch72, adc2

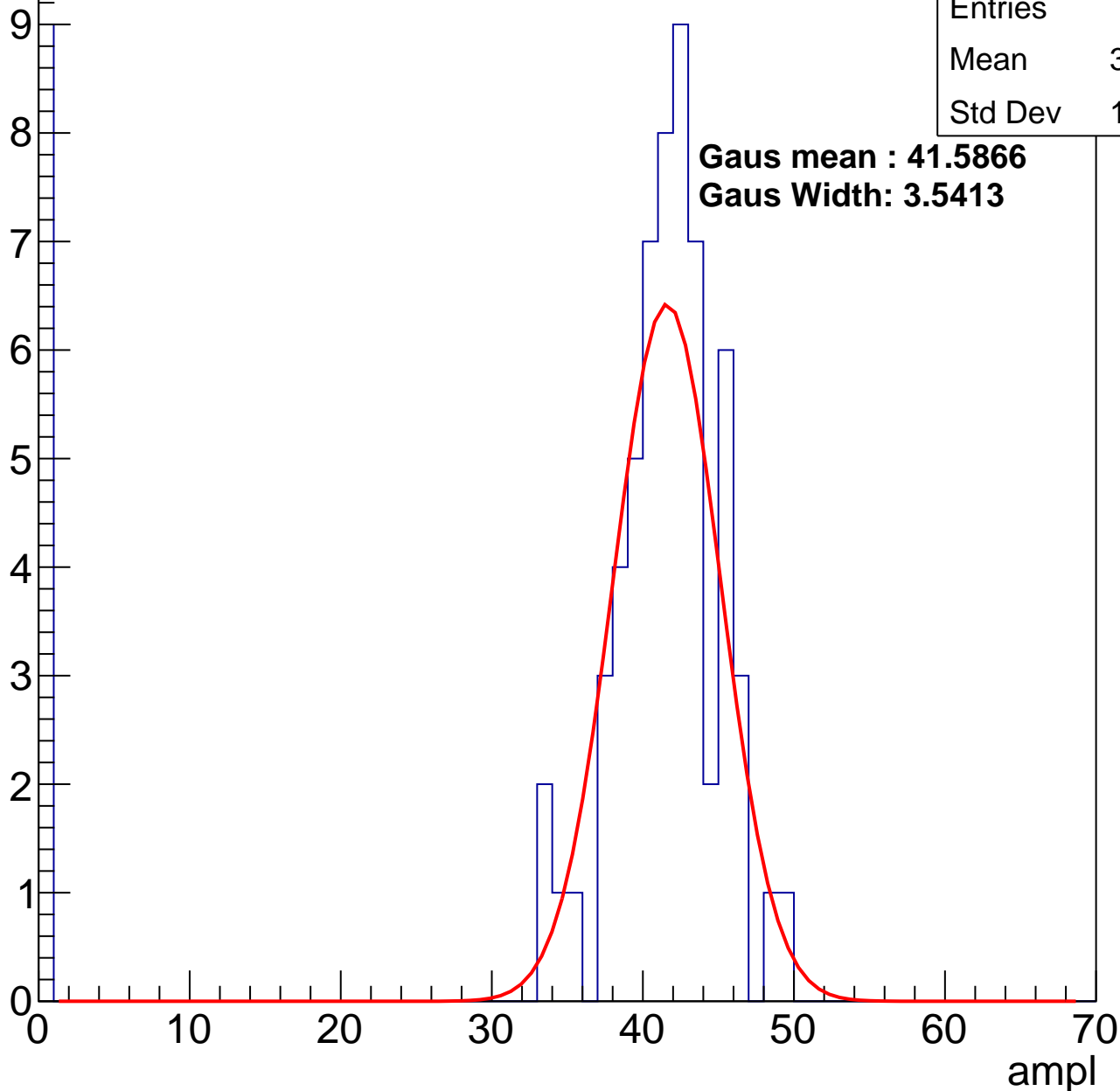
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	35.84
Std Dev	14.22

Gaus mean : 41.5866

Gaus Width: 3.5413

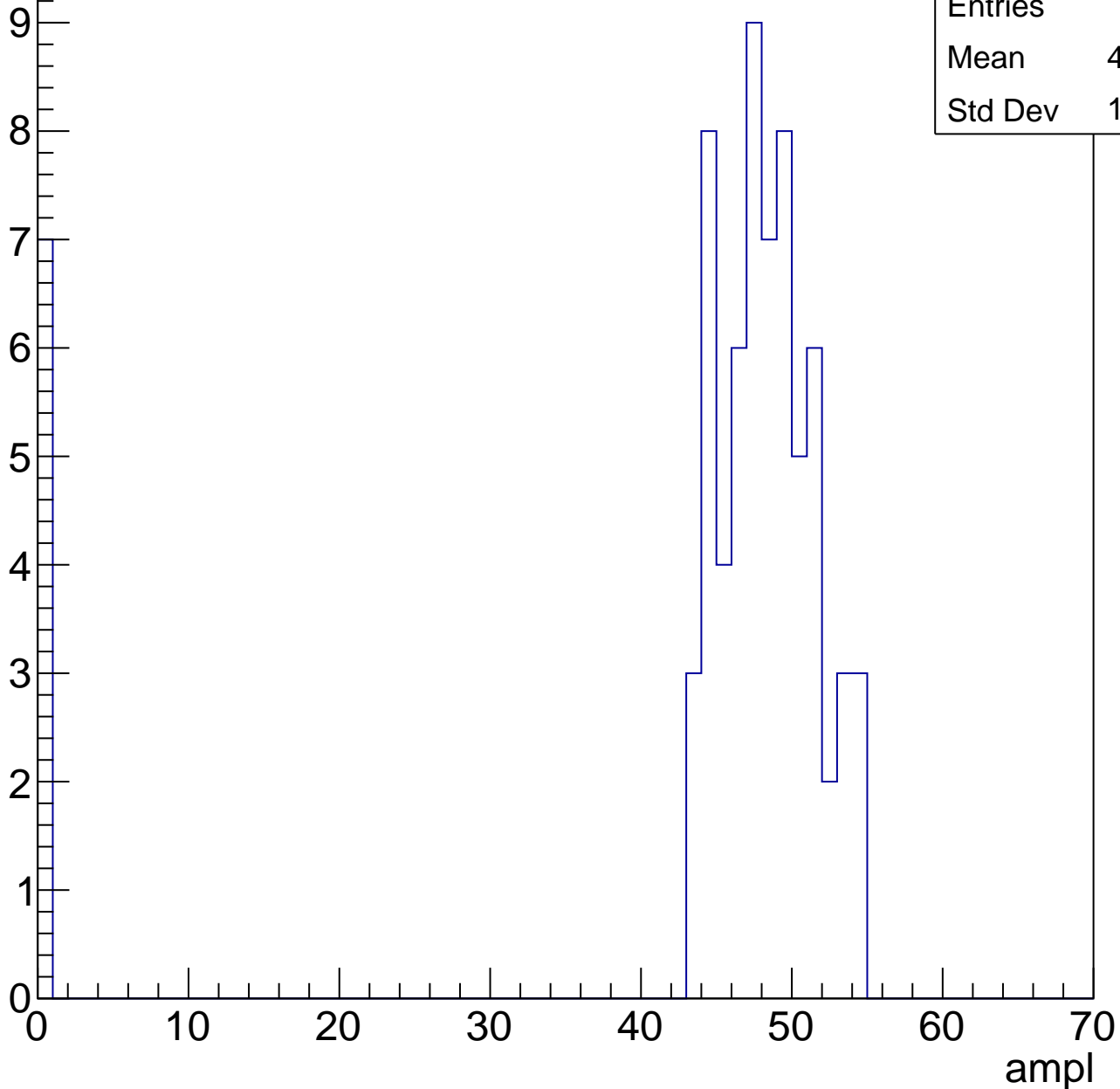


B1L103S, U18-ch72, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	43.23
Std Dev	14.57

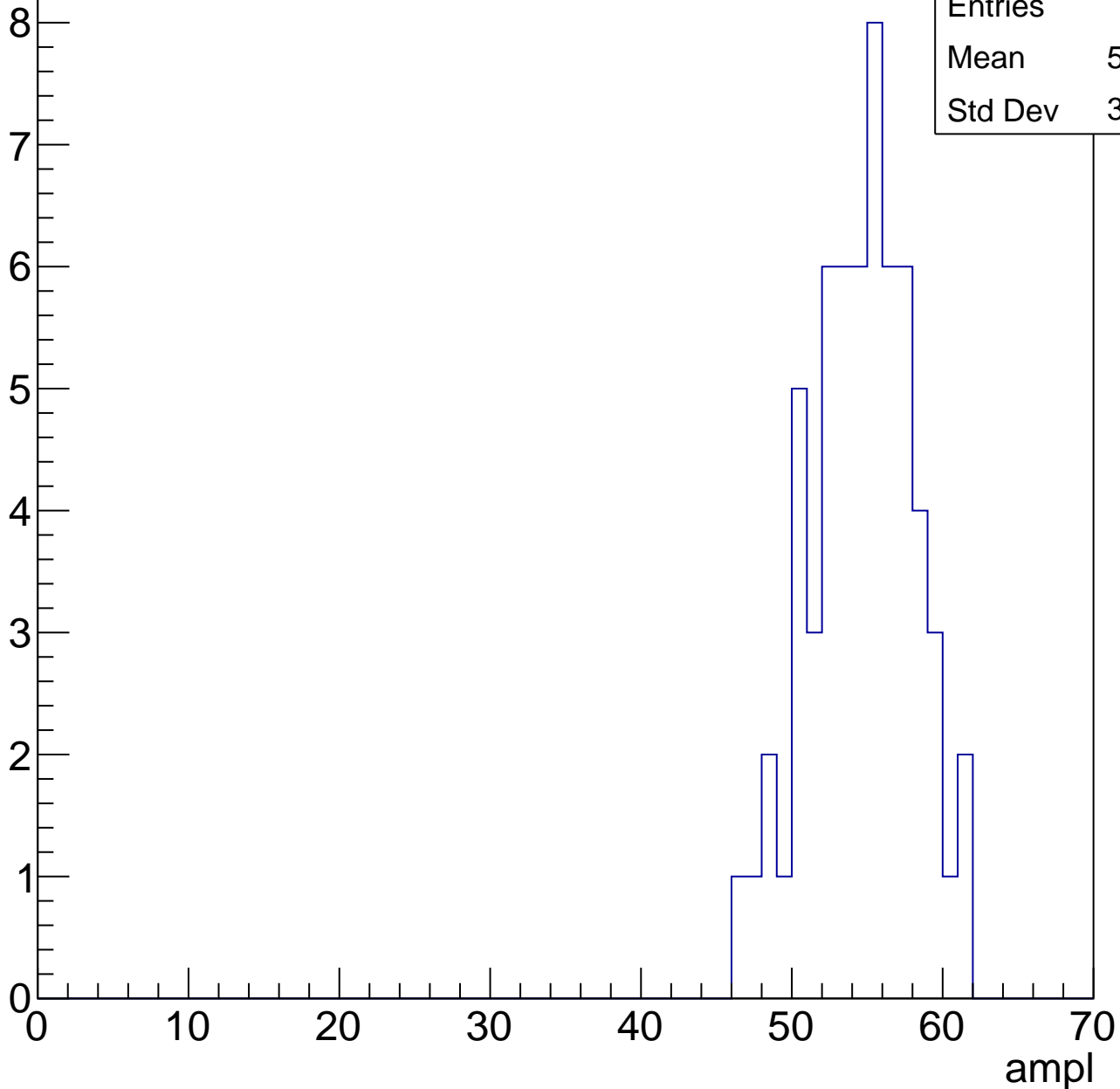


B1L103S, U18-ch72, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	54.16
Std Dev	3.398

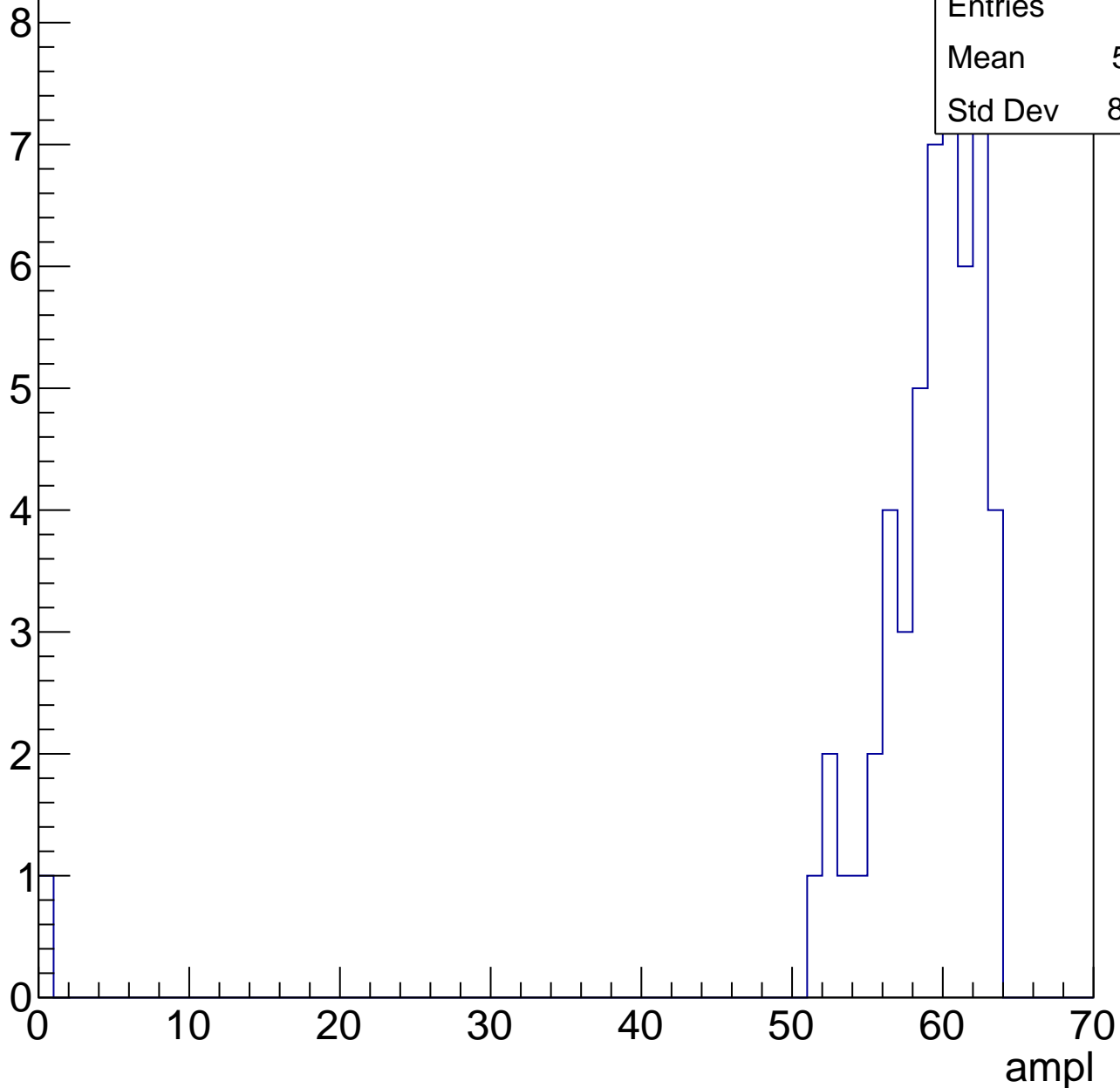


B1L103S, U18-ch72, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	57.81
Std Dev	8.558

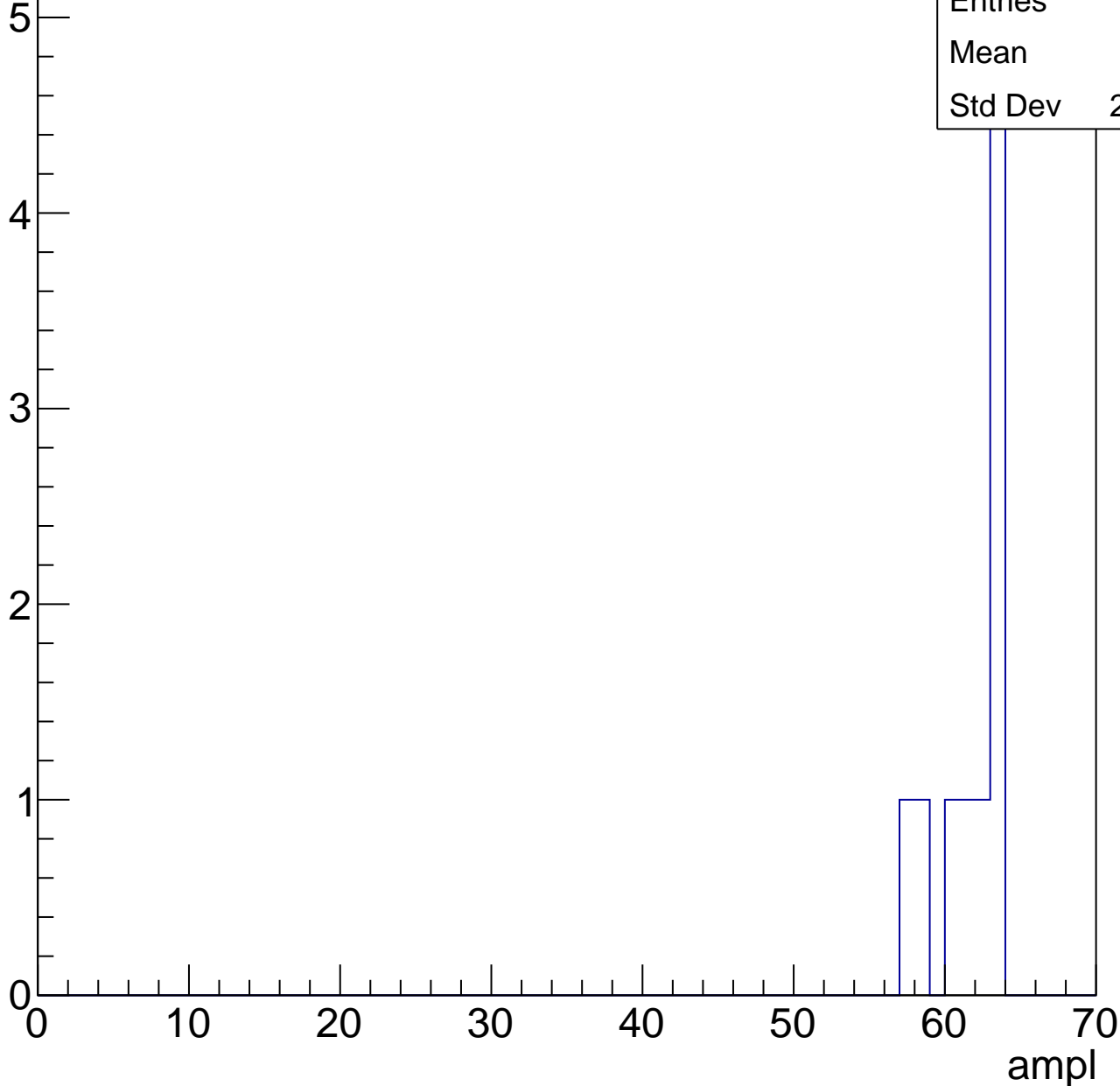


B1L103S, U18-ch72, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	61.3
Std Dev	2.147

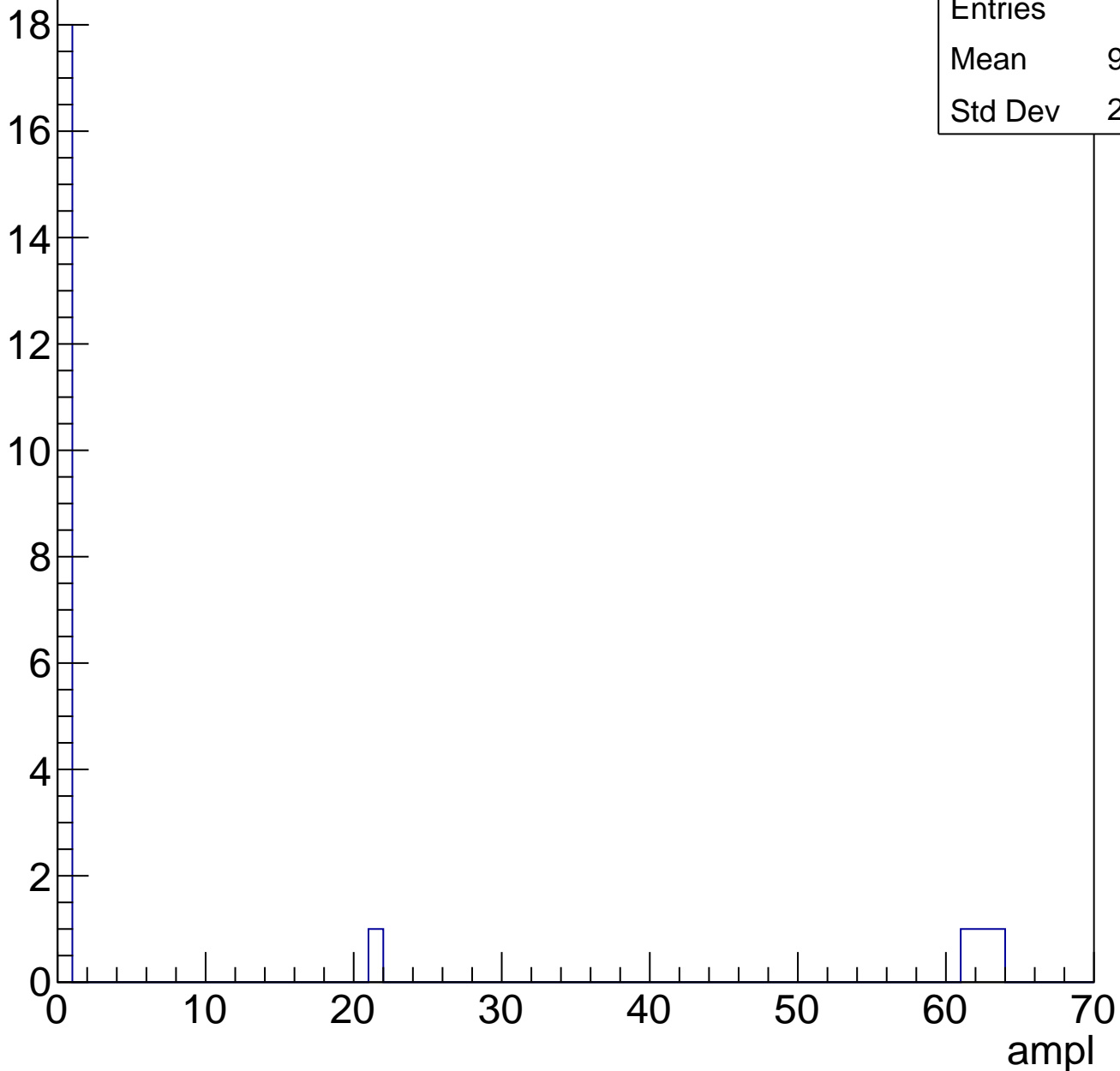


B1L103S, U18-ch72, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	9.409
Std Dev	21.35

Entry



B1L103S, U18-ch73, adc0

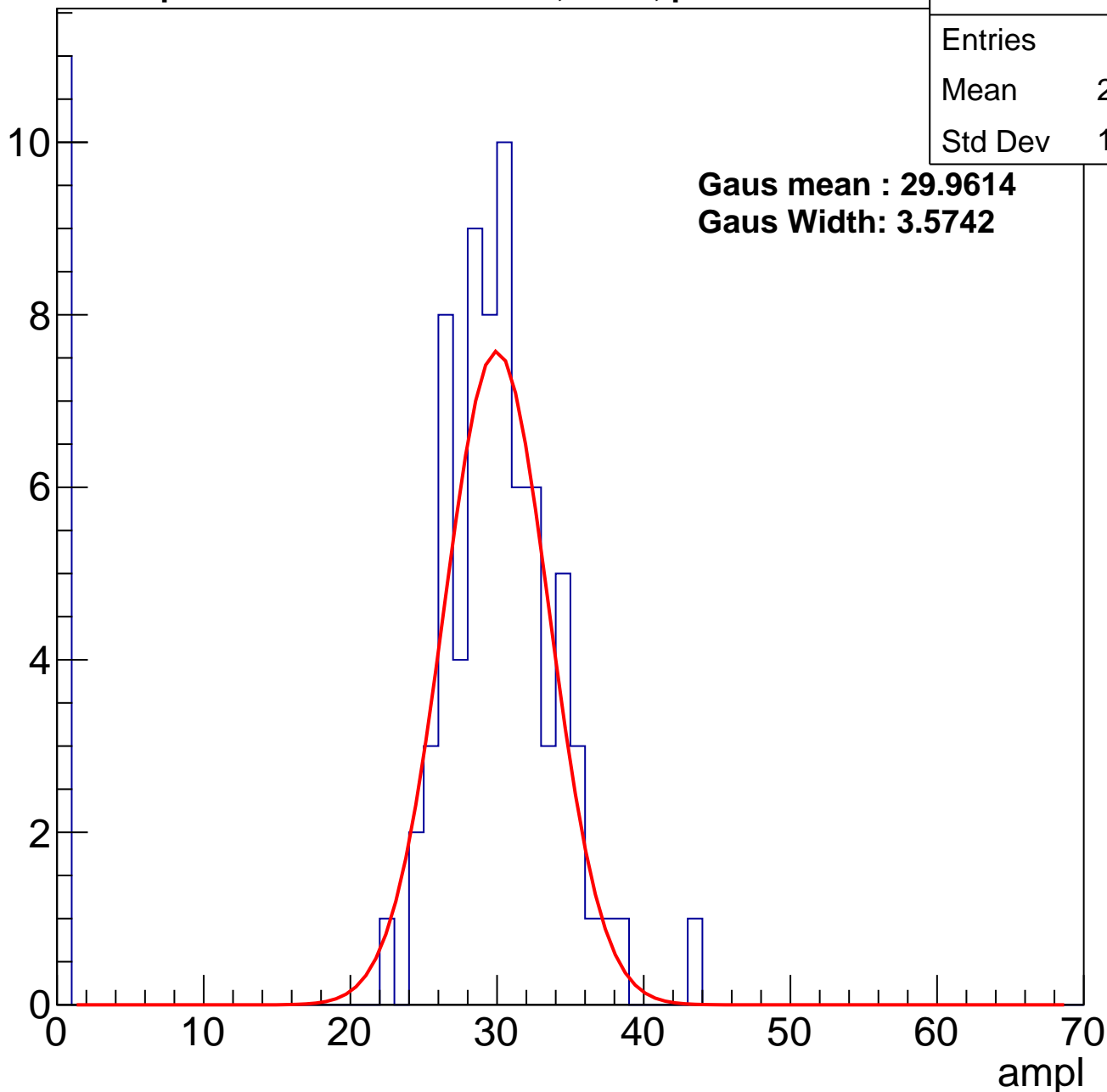
calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	25.92
Std Dev	10.68

Gaus mean : 29.9614

Gaus Width: 3.5742

Entry



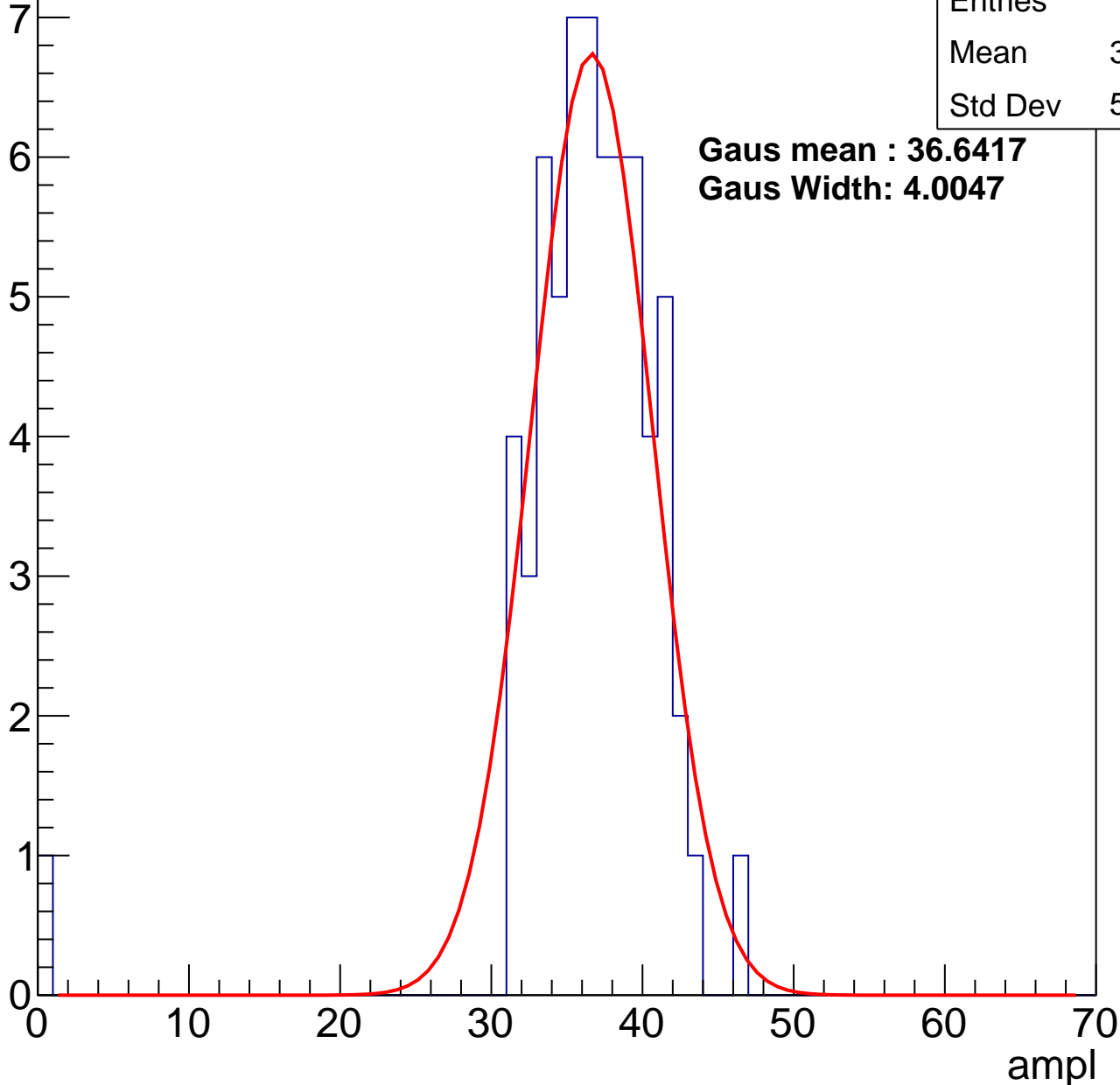
B1L103S, U18-ch73, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.05
Std Dev	5.616

Gaus mean : 36.6417
Gaus Width: 4.0047



B1L103S, U18-ch73, adc2

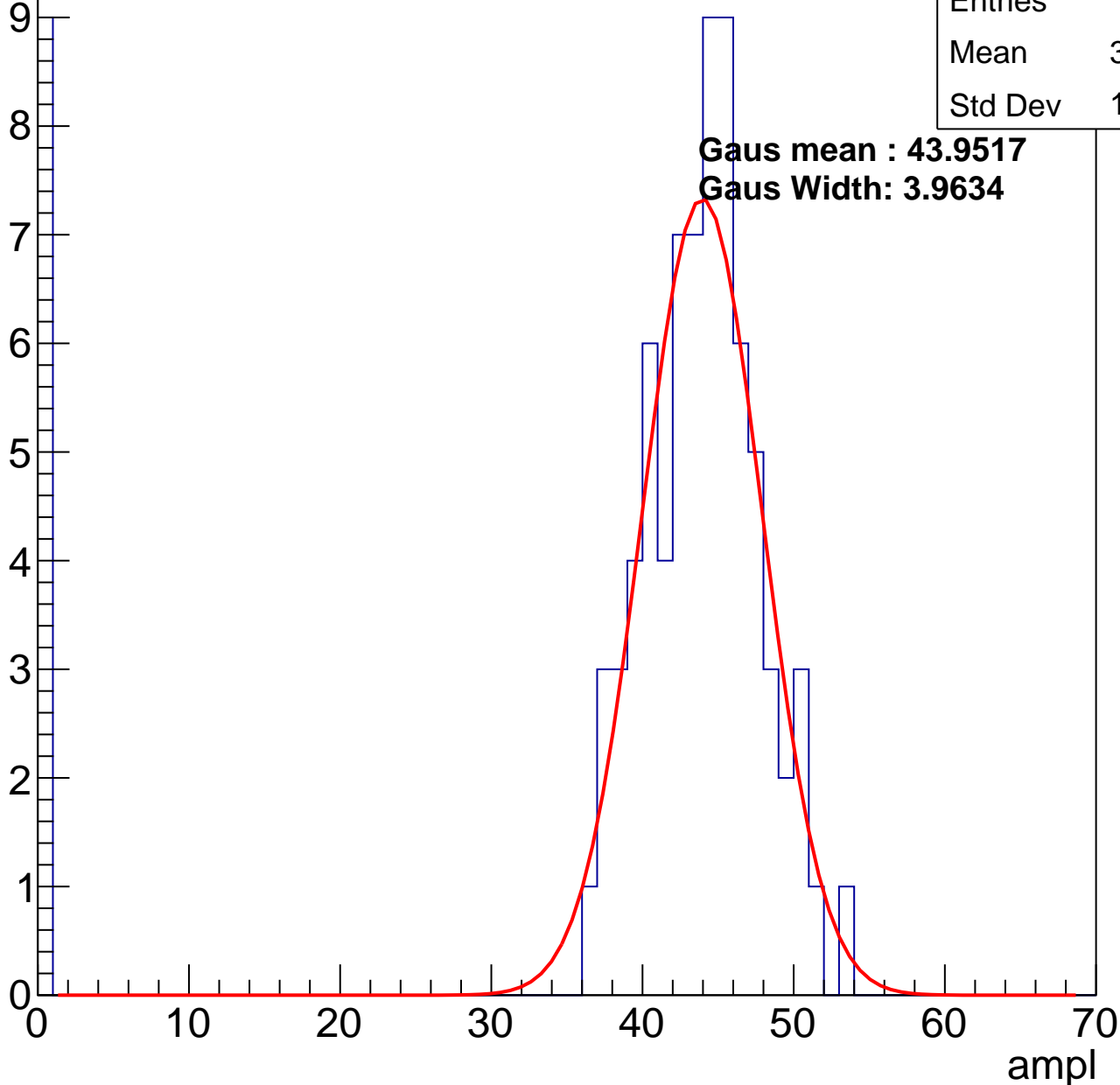
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	38.84
Std Dev	13.98

Gaus mean : 43.9517

Gaus Width: 3.9634

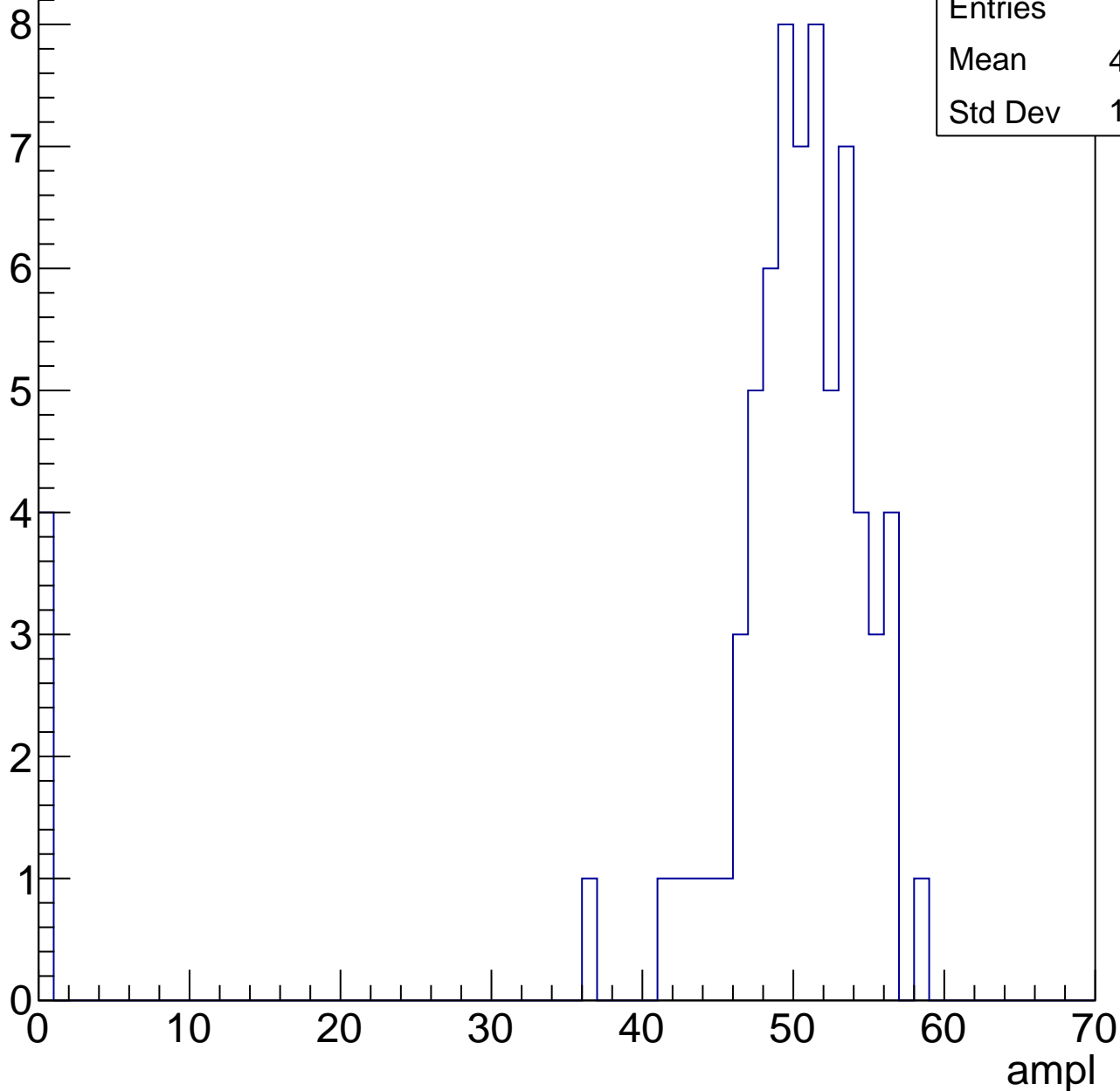


B1L103S, U18-ch73, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	47.27
Std Dev	12.15

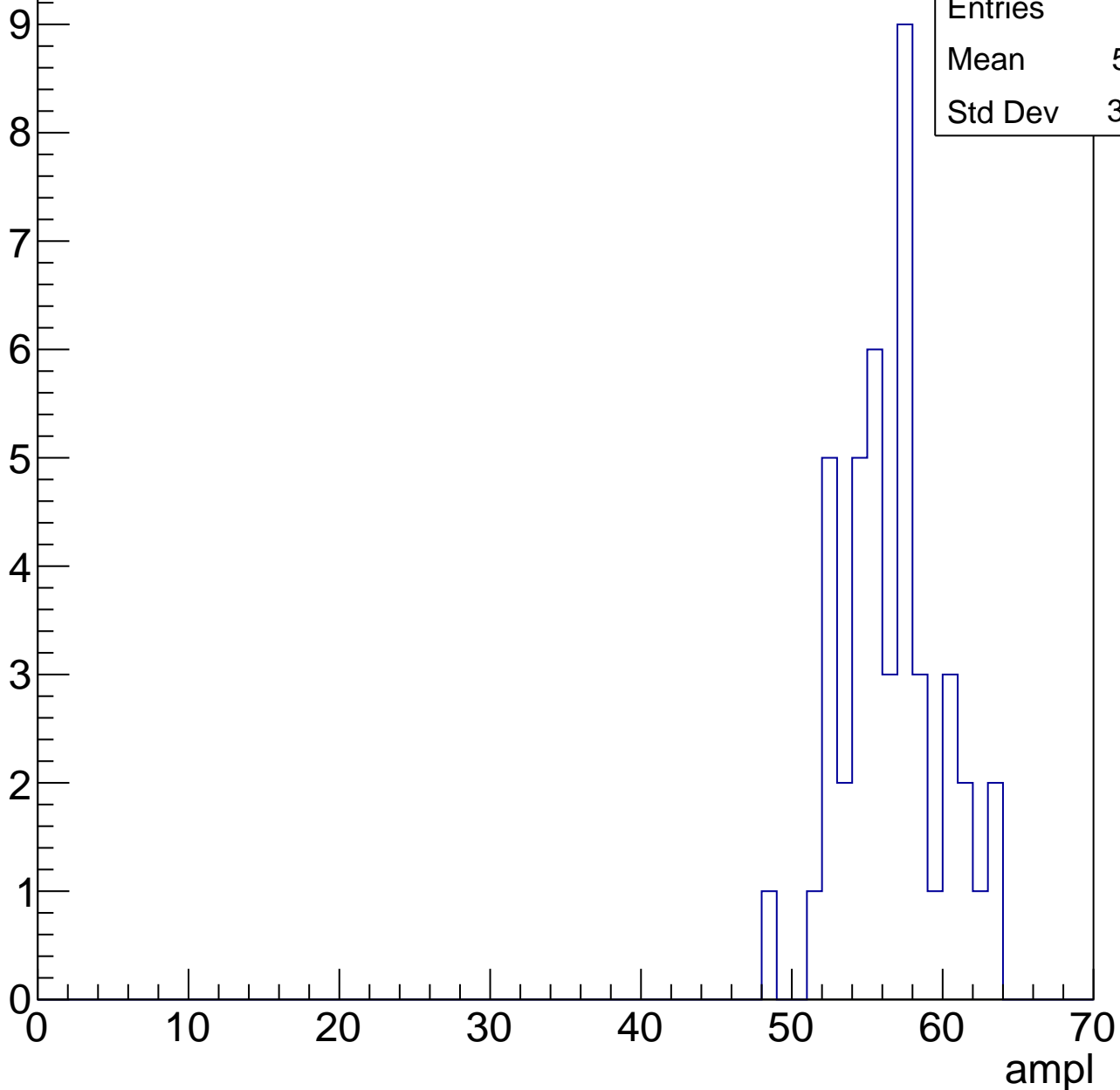


B1L103S, U18-ch73, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	56.11
Std Dev	3.284



B1L103S, U18-ch73, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries

38

Mean

59.47

Std Dev

2.256

ampl

0

10

20

30

40

50

60

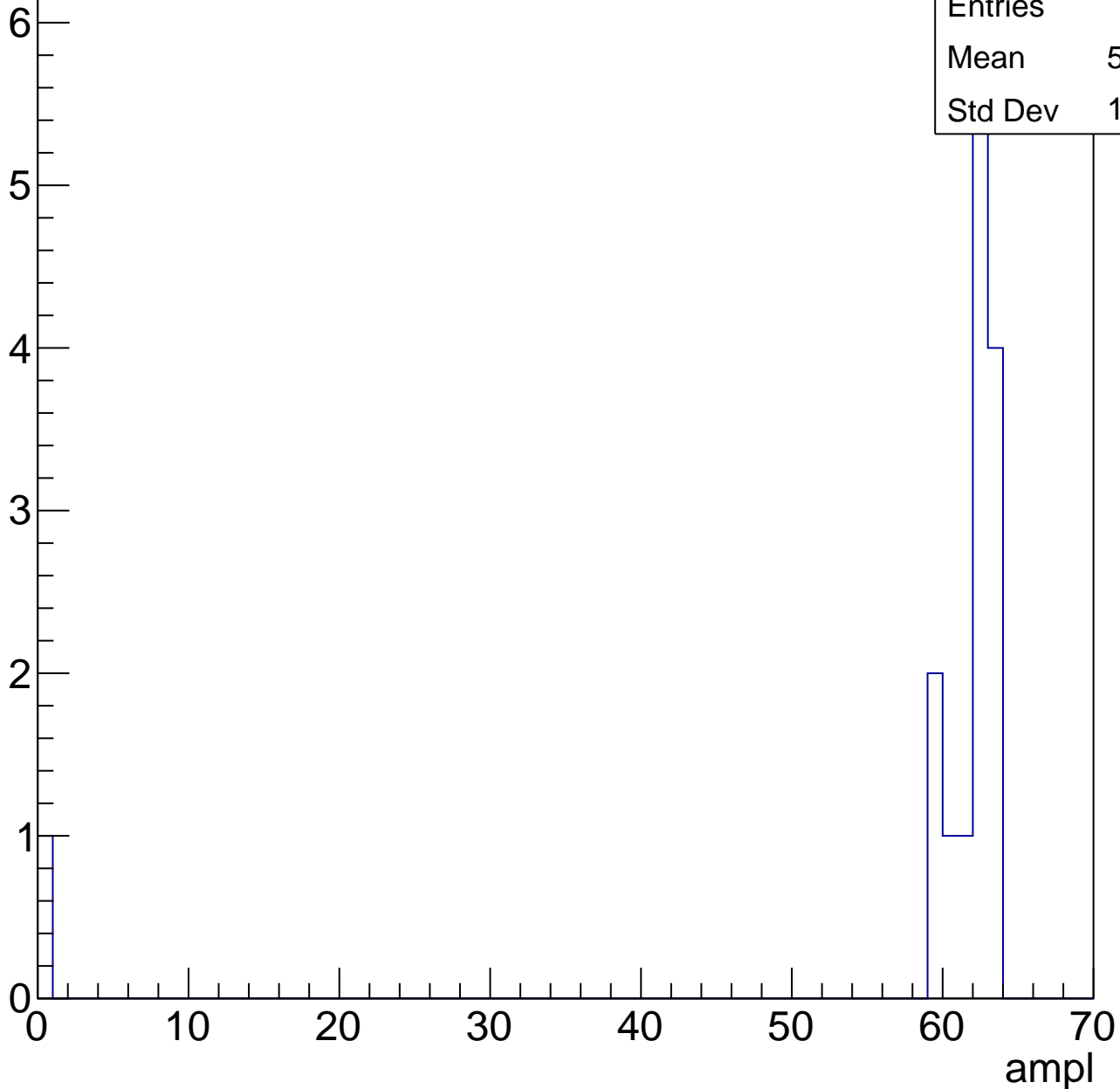
70

B1L103S, U18-ch73, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.53
Std Dev	15.43

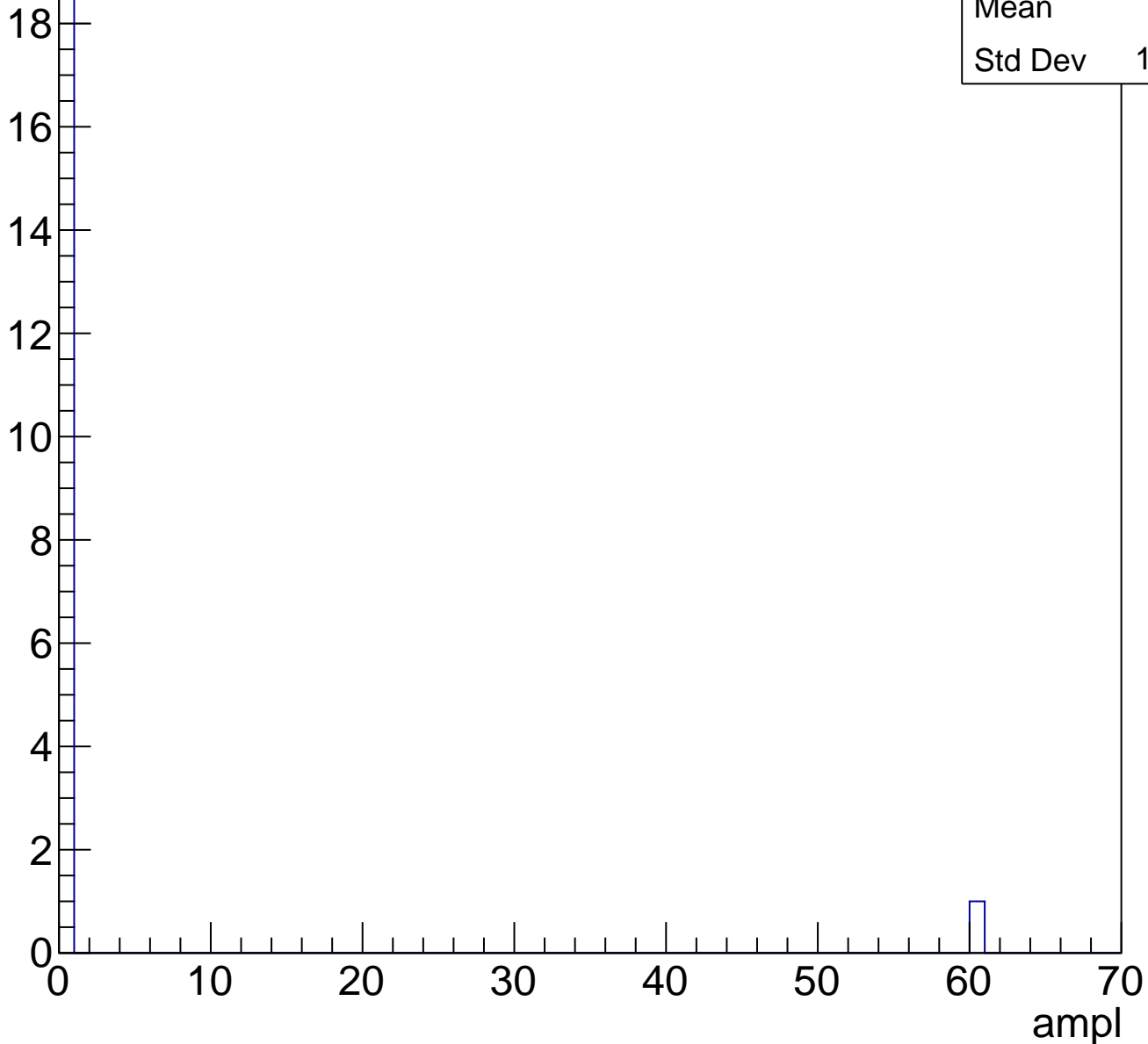


B1L103S, U18-ch73, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



B1L103S, U18-ch74, adc0

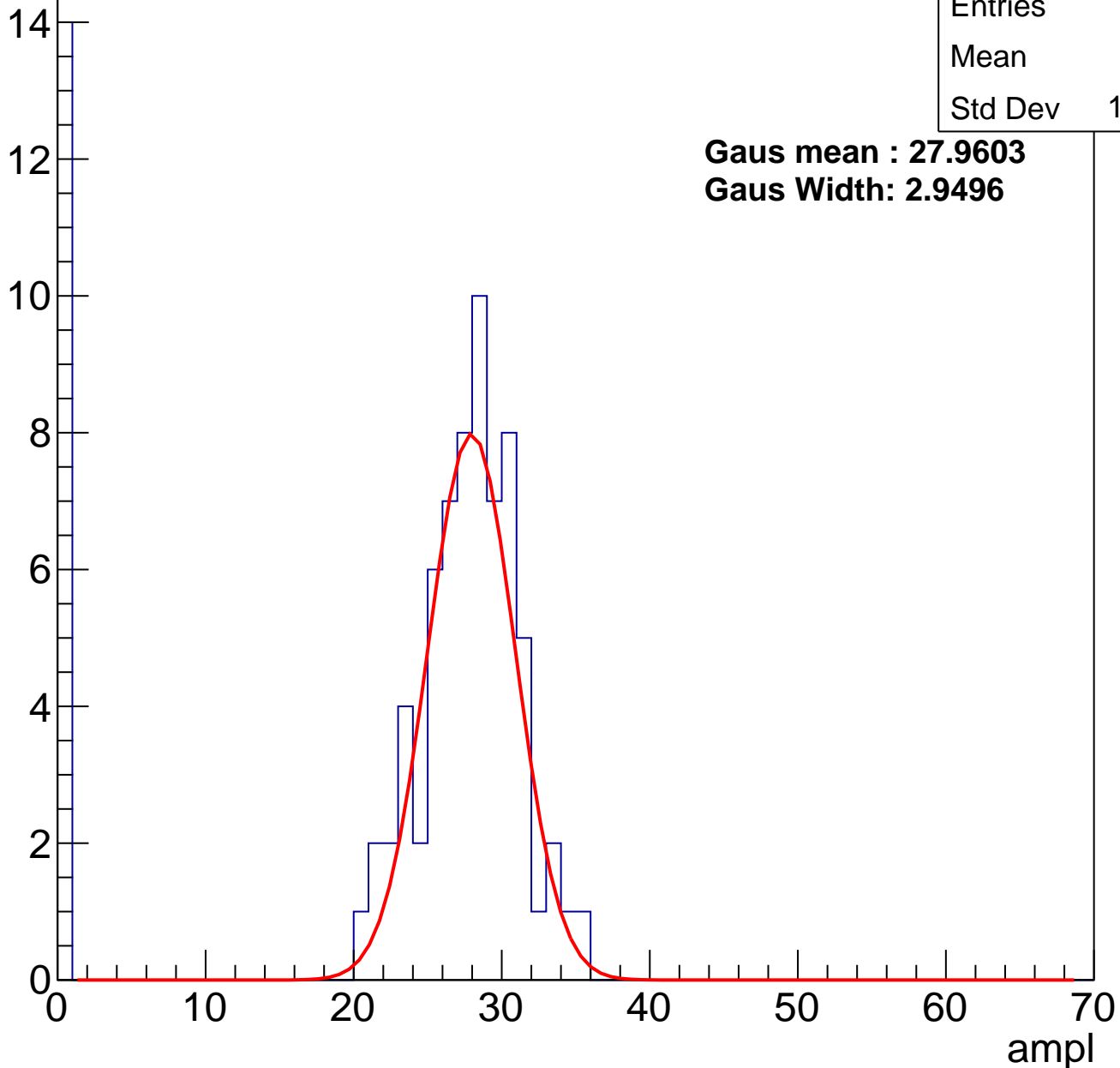
calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	22.7
Std Dev	10.77

Gaus mean : 27.9603

Gaus Width: 2.9496

Entry



B1L103S, U18-ch74, adc1

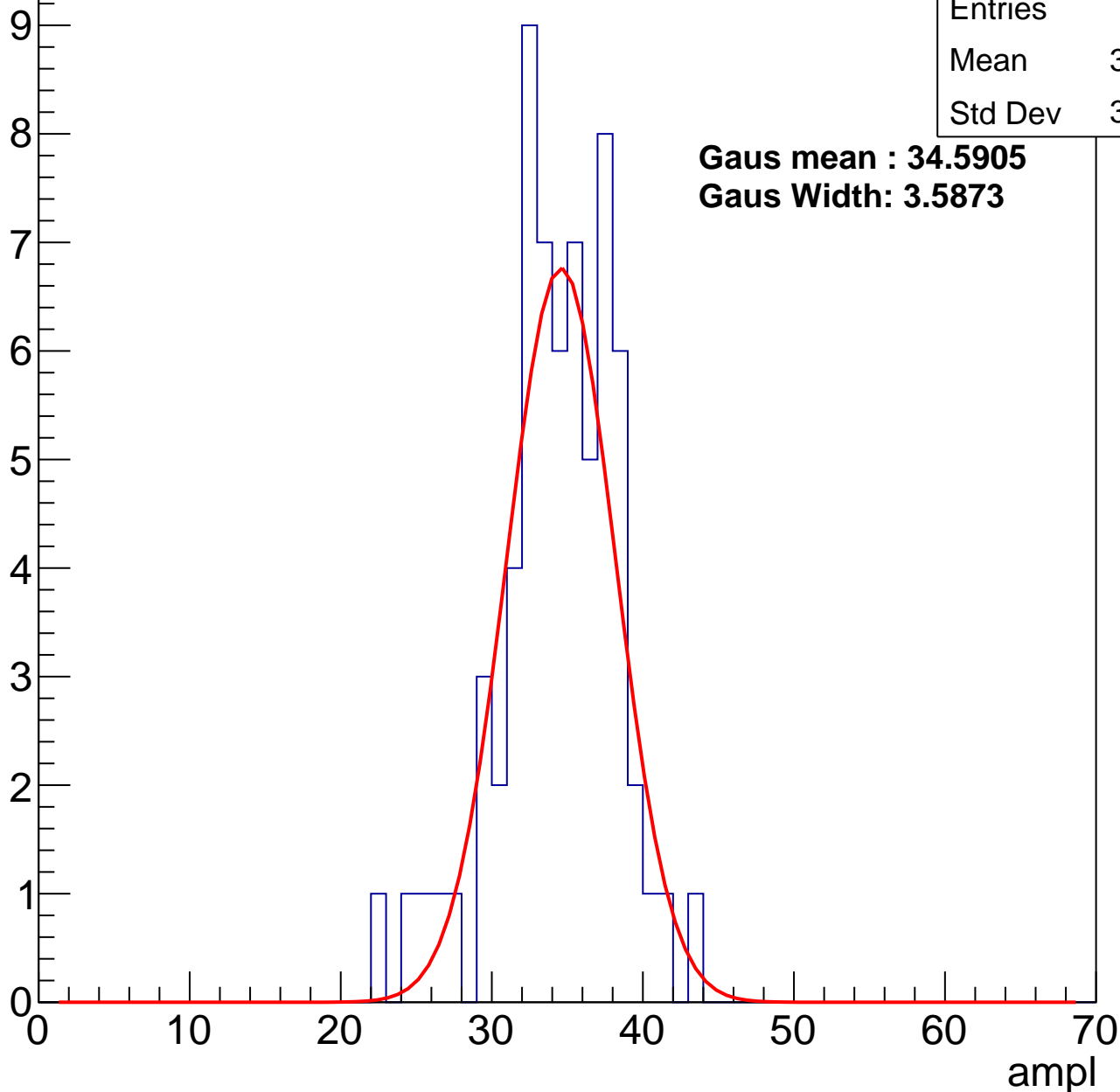
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.87
Std Dev	3.928

Gaus mean : 34.5905

Gaus Width: 3.5873



B1L103S, U18-ch74, adc2

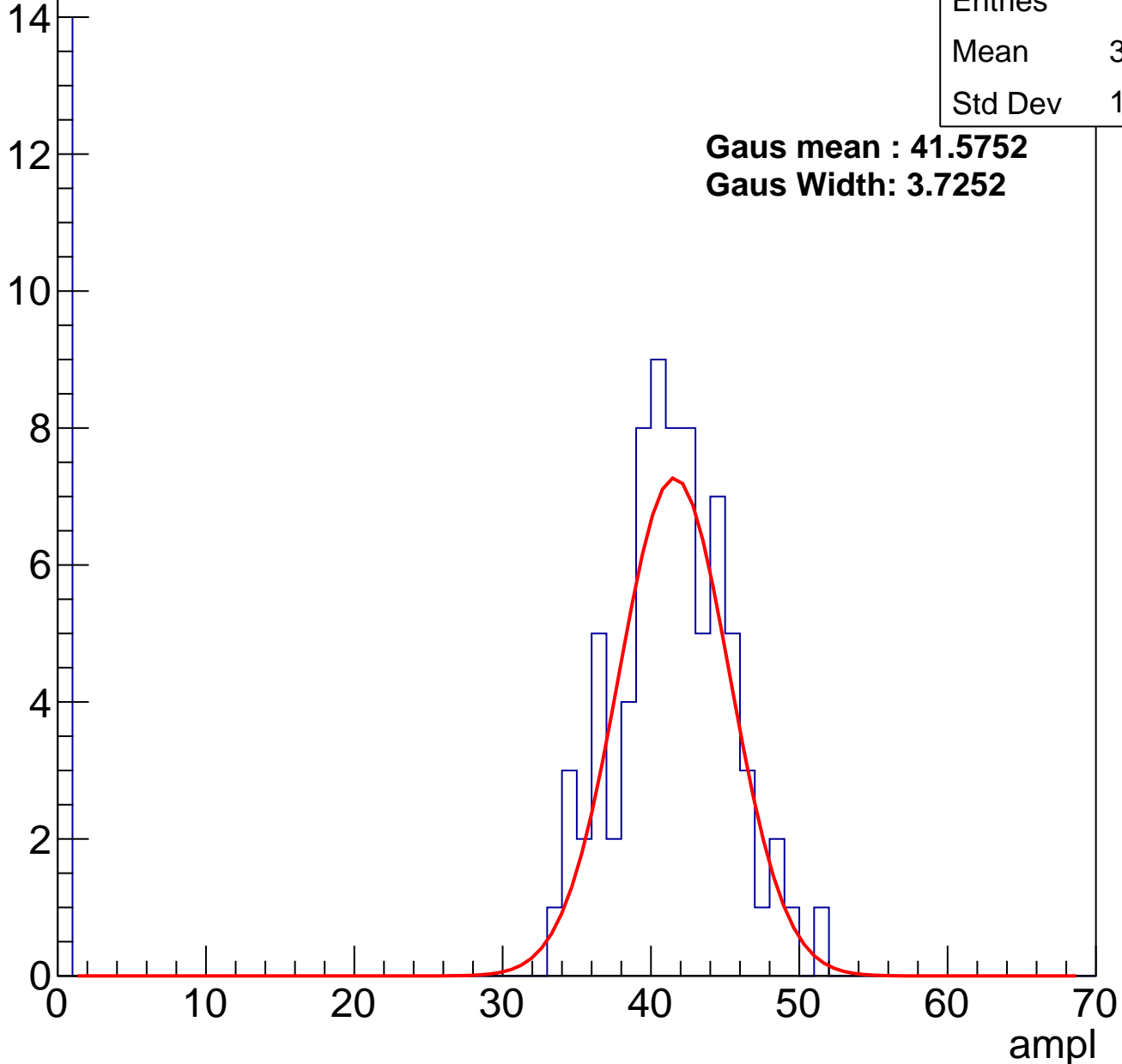
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	34.56
Std Dev	15.33

Gaus mean : 41.5752

Gaus Width: 3.7252

Entry

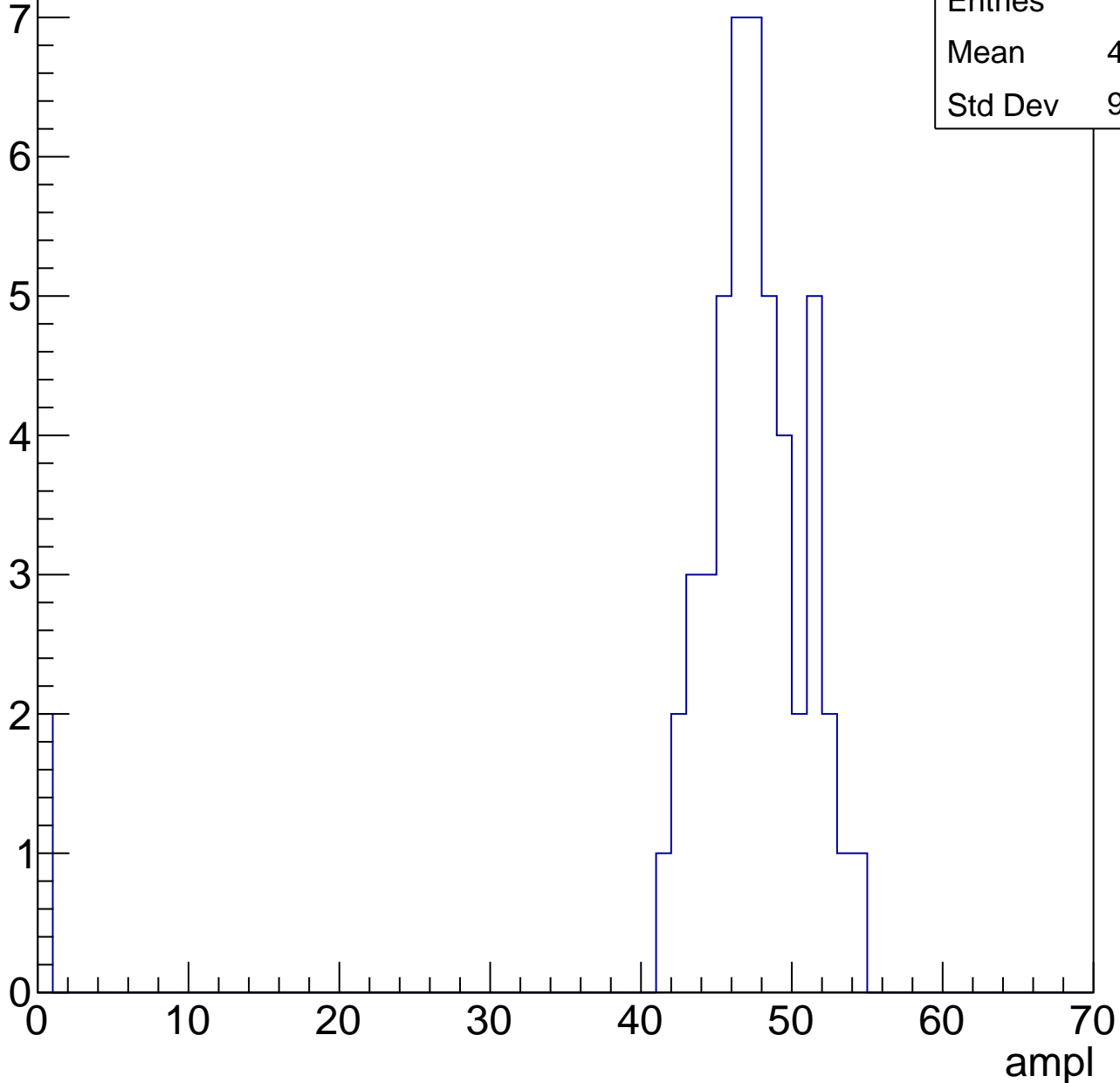


B1L103S, U18-ch74, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	45.28
Std Dev	9.708

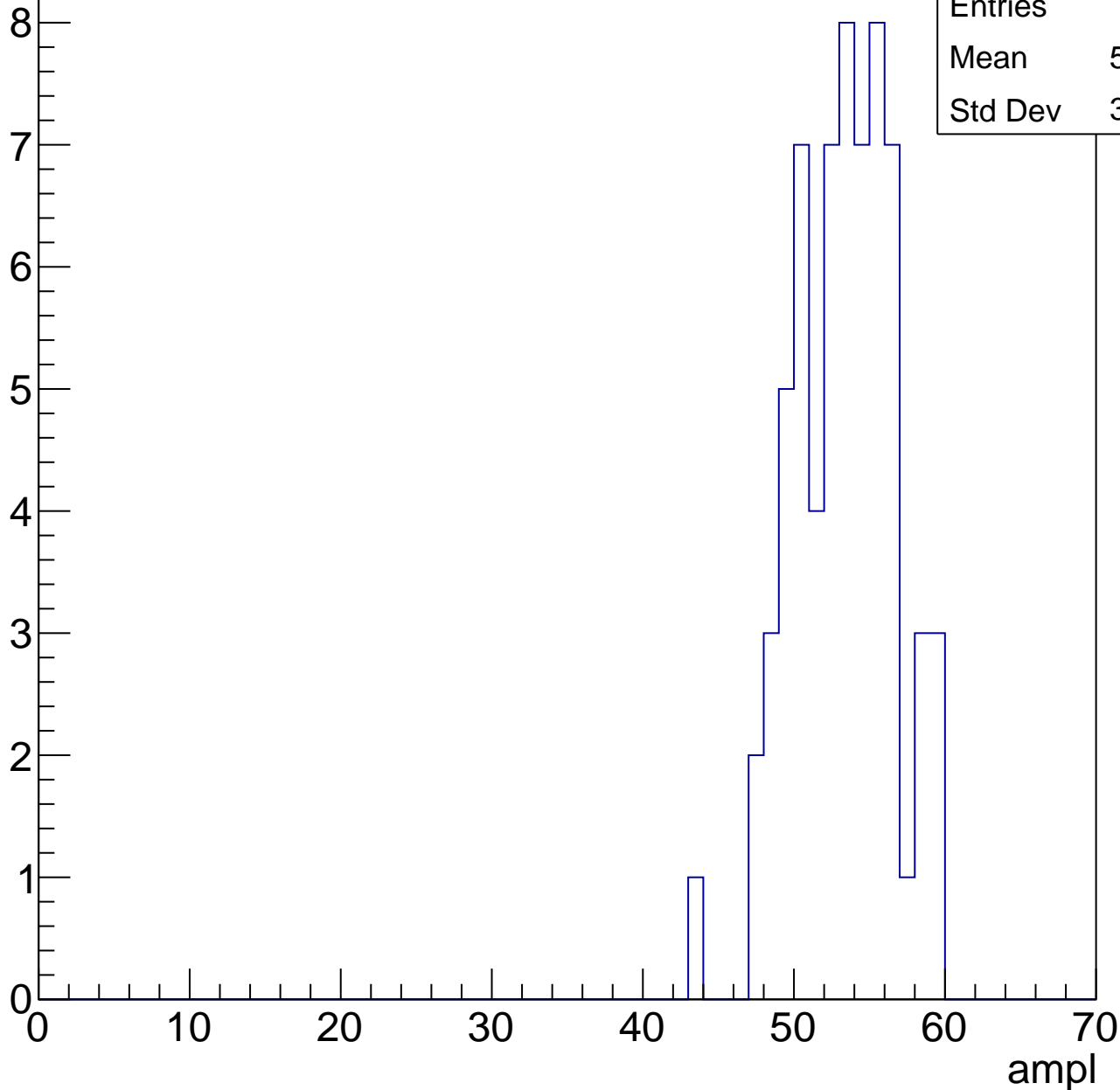


B1L103S, U18-ch74, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	52.82
Std Dev	3.284

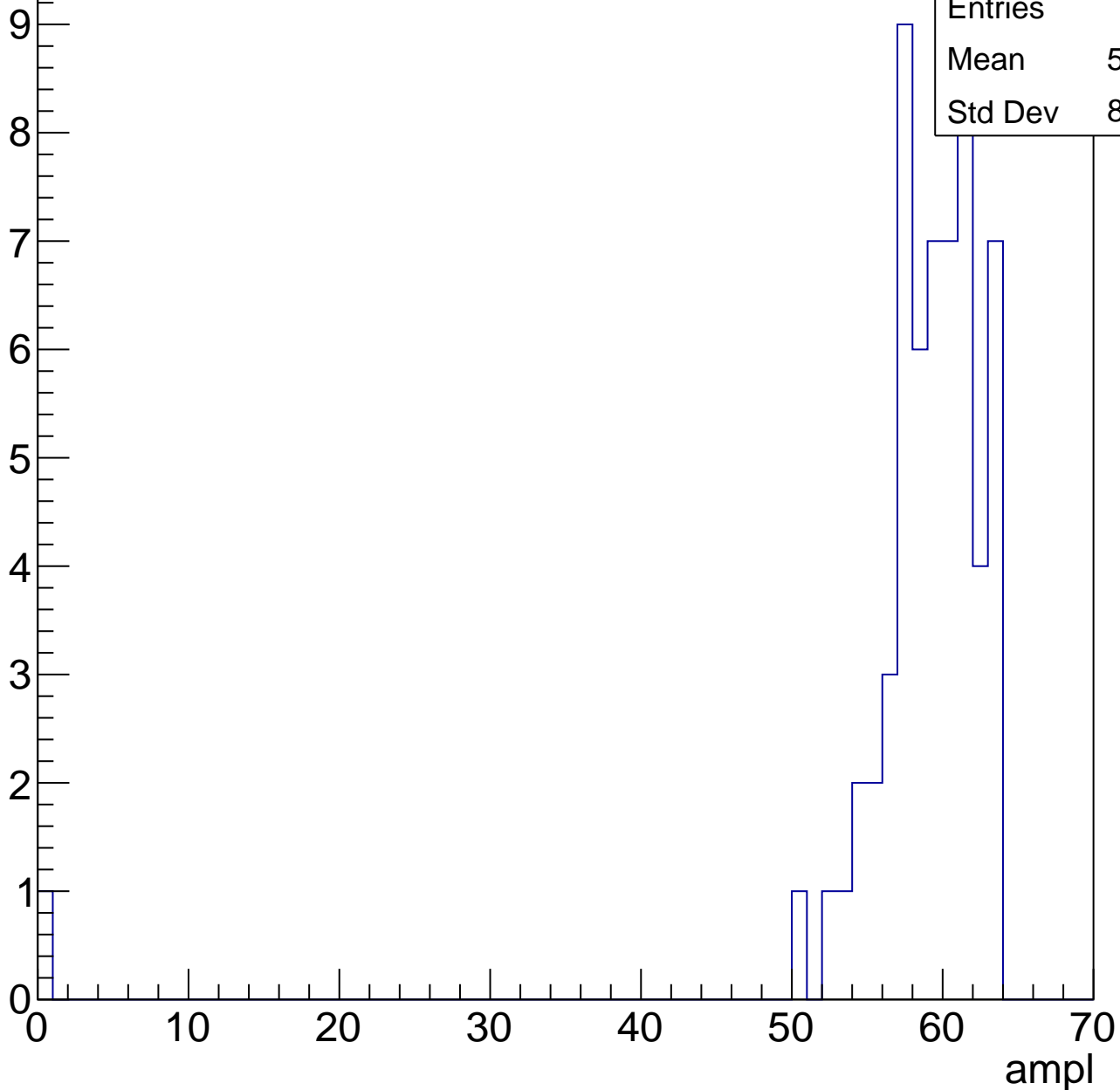


B1L103S, U18-ch74, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.88
Std Dev	8.077

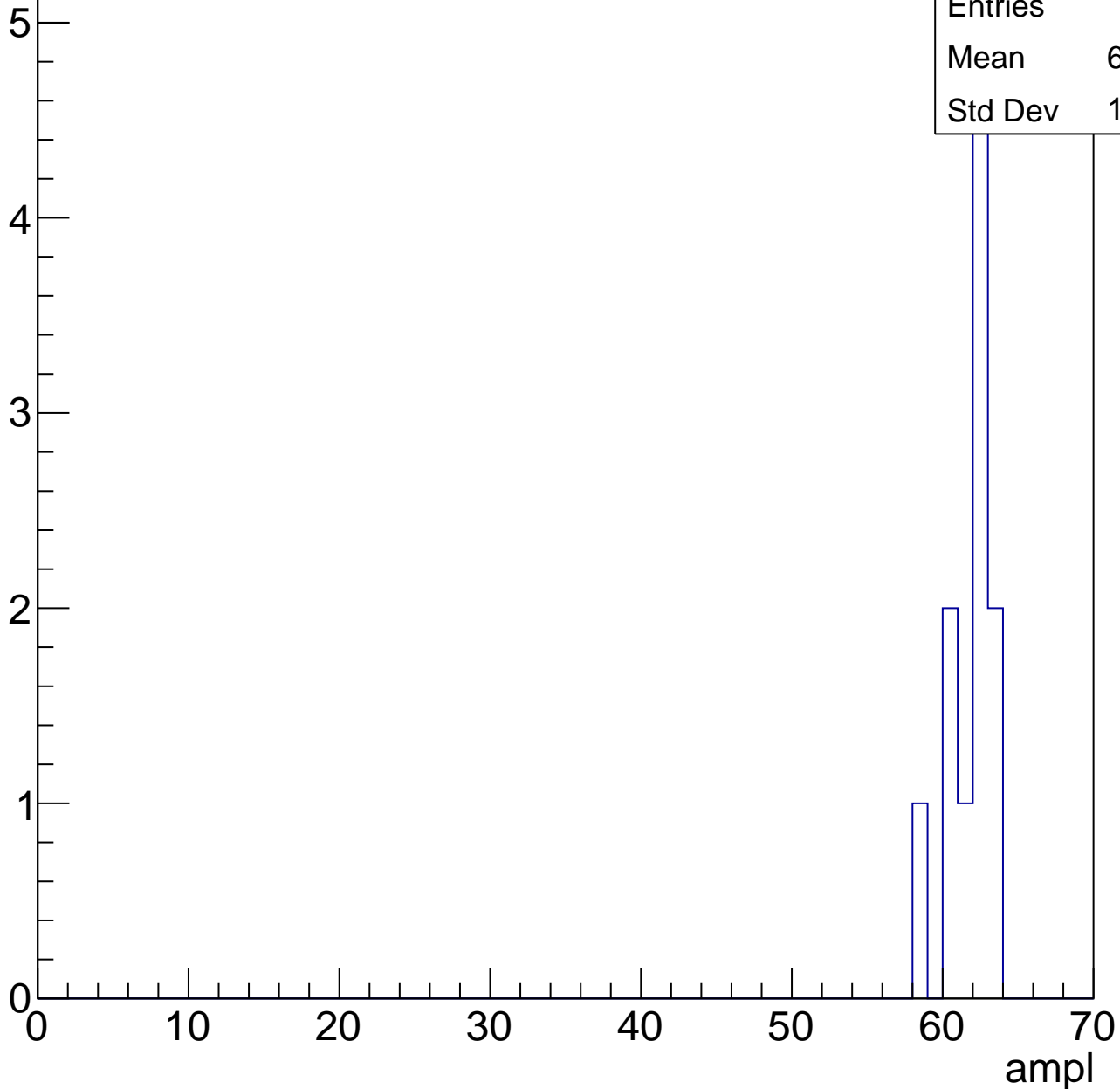


B1L103S, U18-ch74, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	11
Mean	61.36
Std Dev	1.432

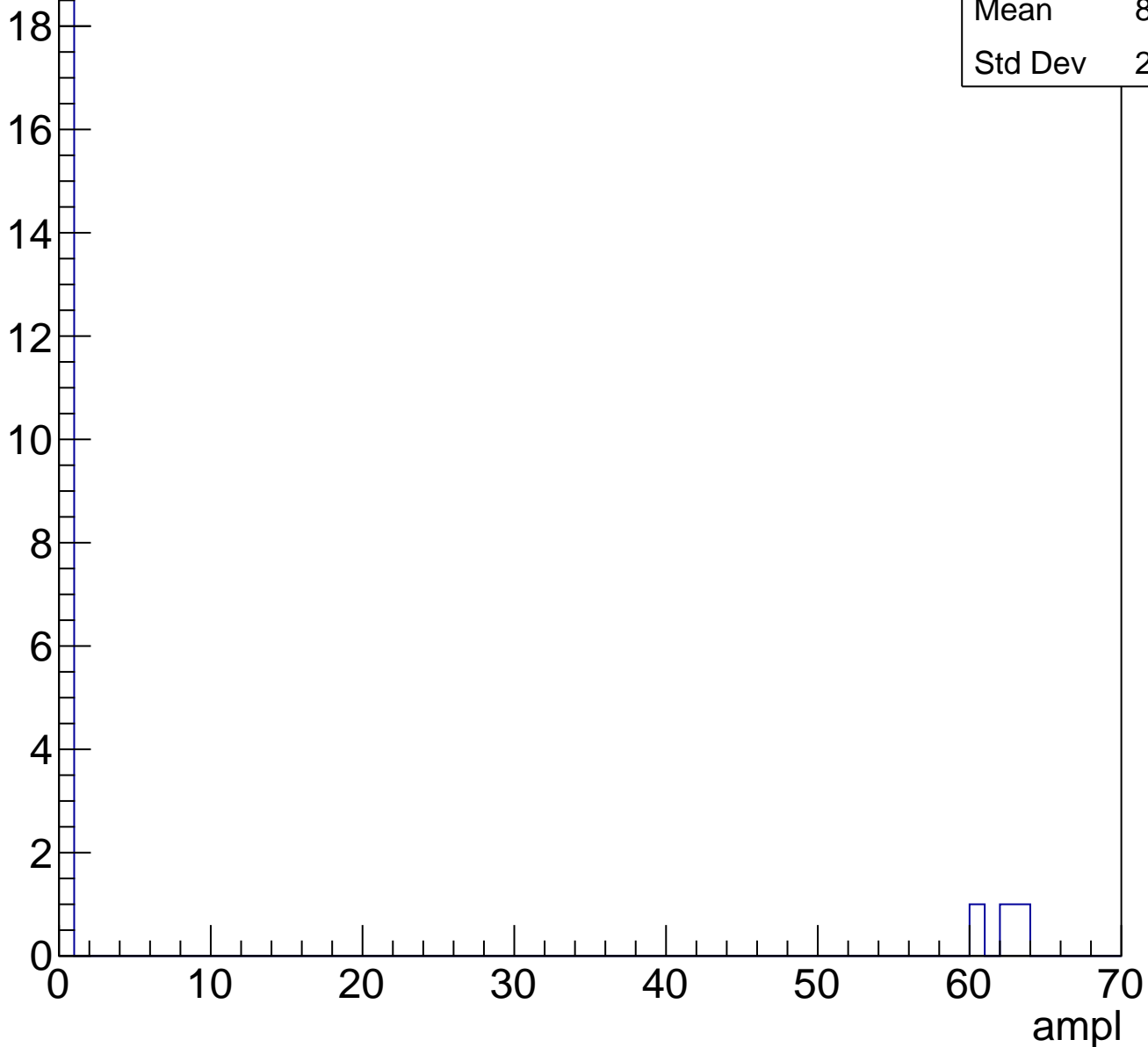


B1L103S, U18-ch74, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.409
Std Dev	21.17

Entry



B1L103S, U18-ch75, adc0

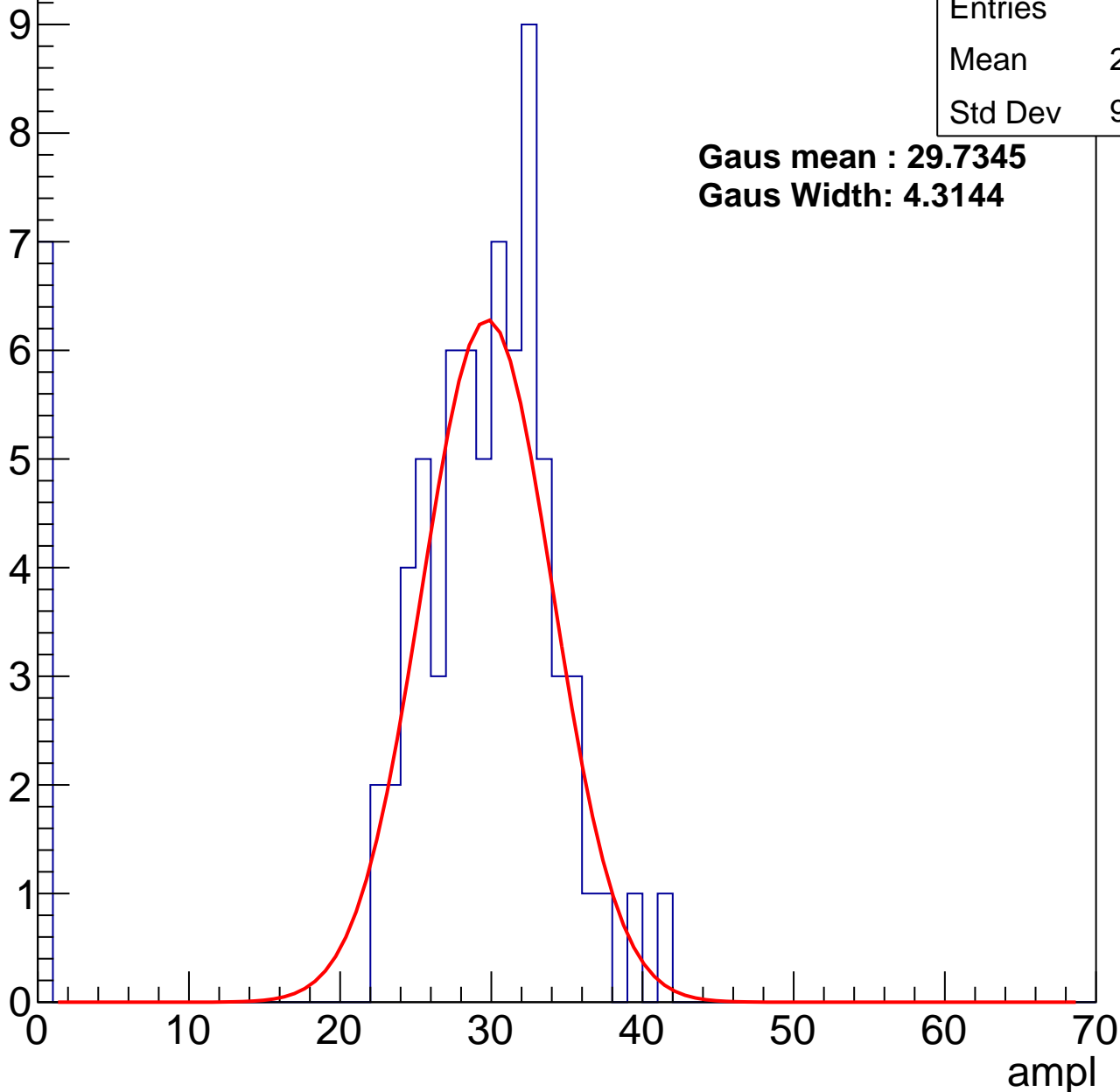
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	26.92
Std Dev	9.324

Gaus mean : 29.7345

Gaus Width: 4.3144



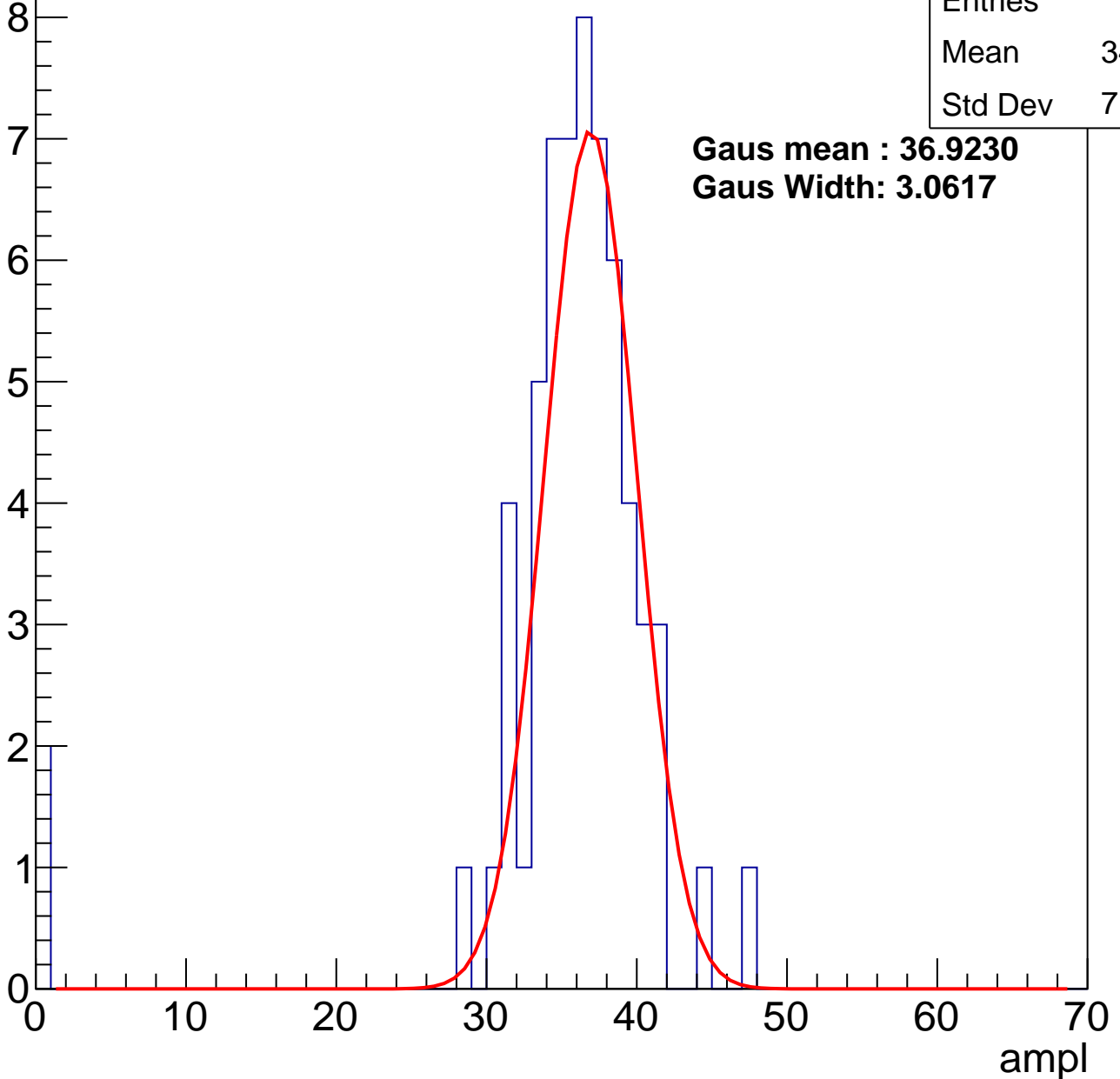
B1L103S, U18-ch75, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.87
Std Dev	7.226

Gaus mean : 36.9230
Gaus Width: 3.0617



B1L103S, U18-ch75, adc2

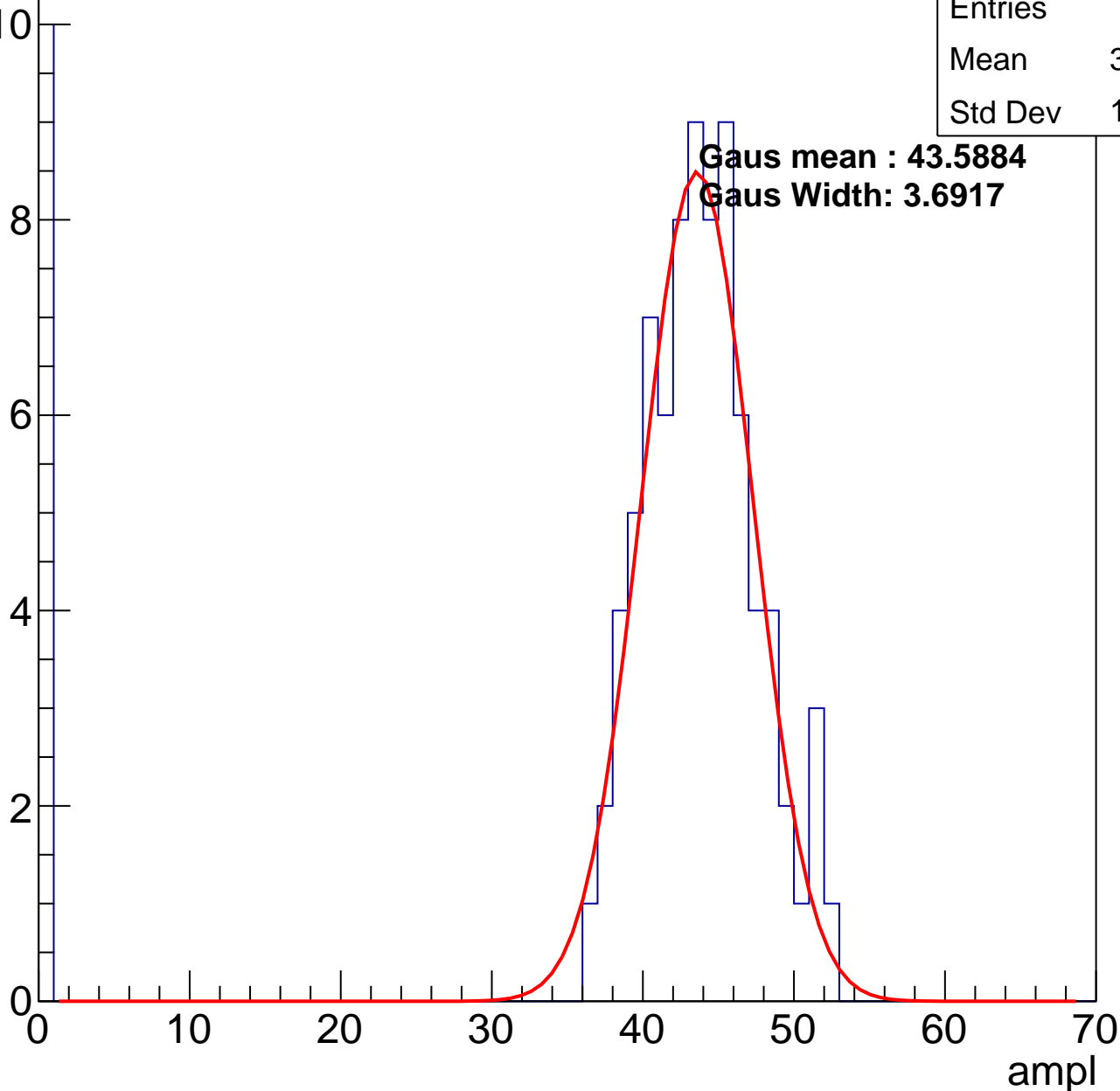
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	90
Mean	38.58
Std Dev	14.06

Gaus mean : 43.5884

Gaus Width: 3.6917

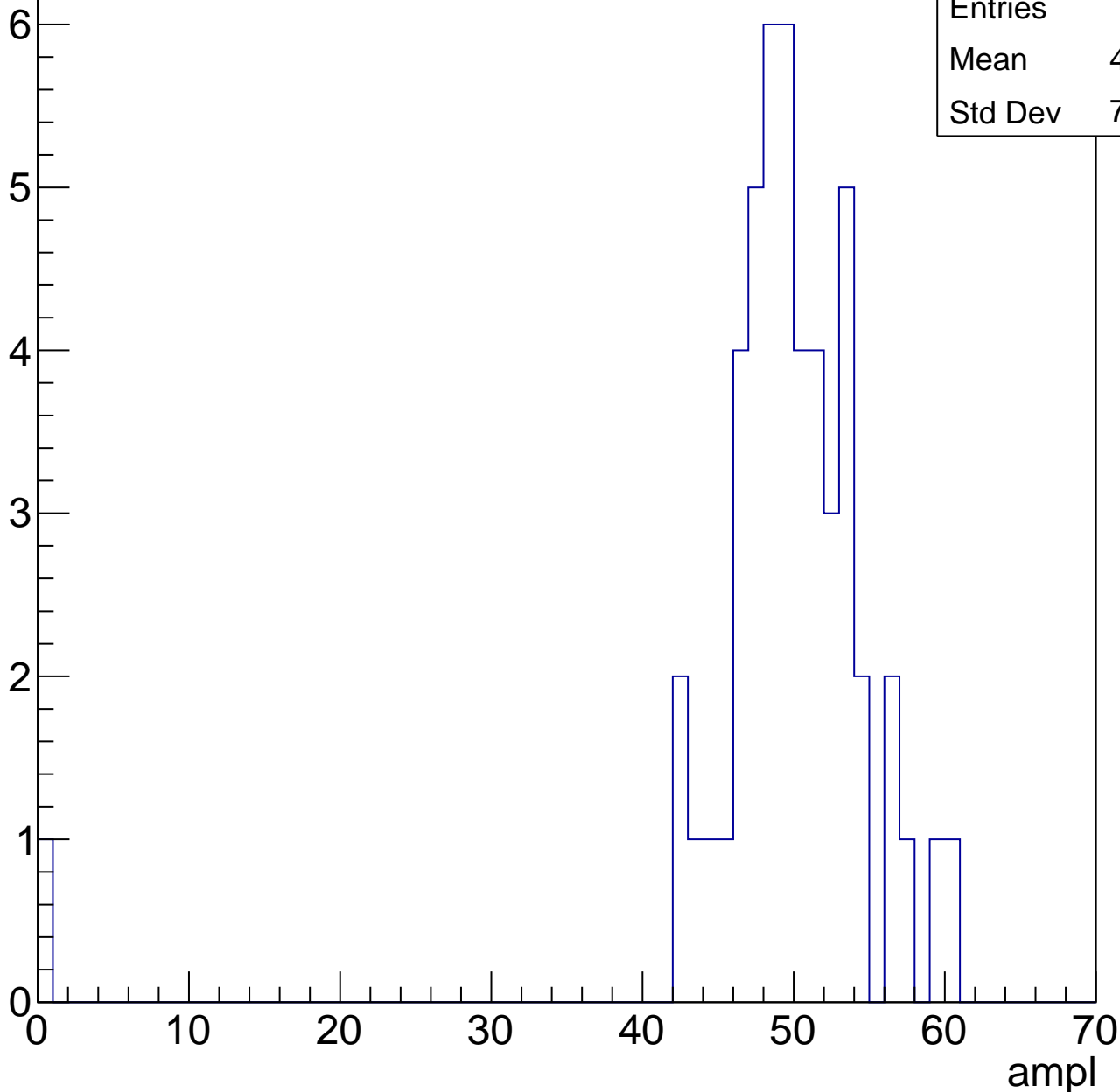


B1L103S, U18-ch75, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	48.76
Std Dev	7.994

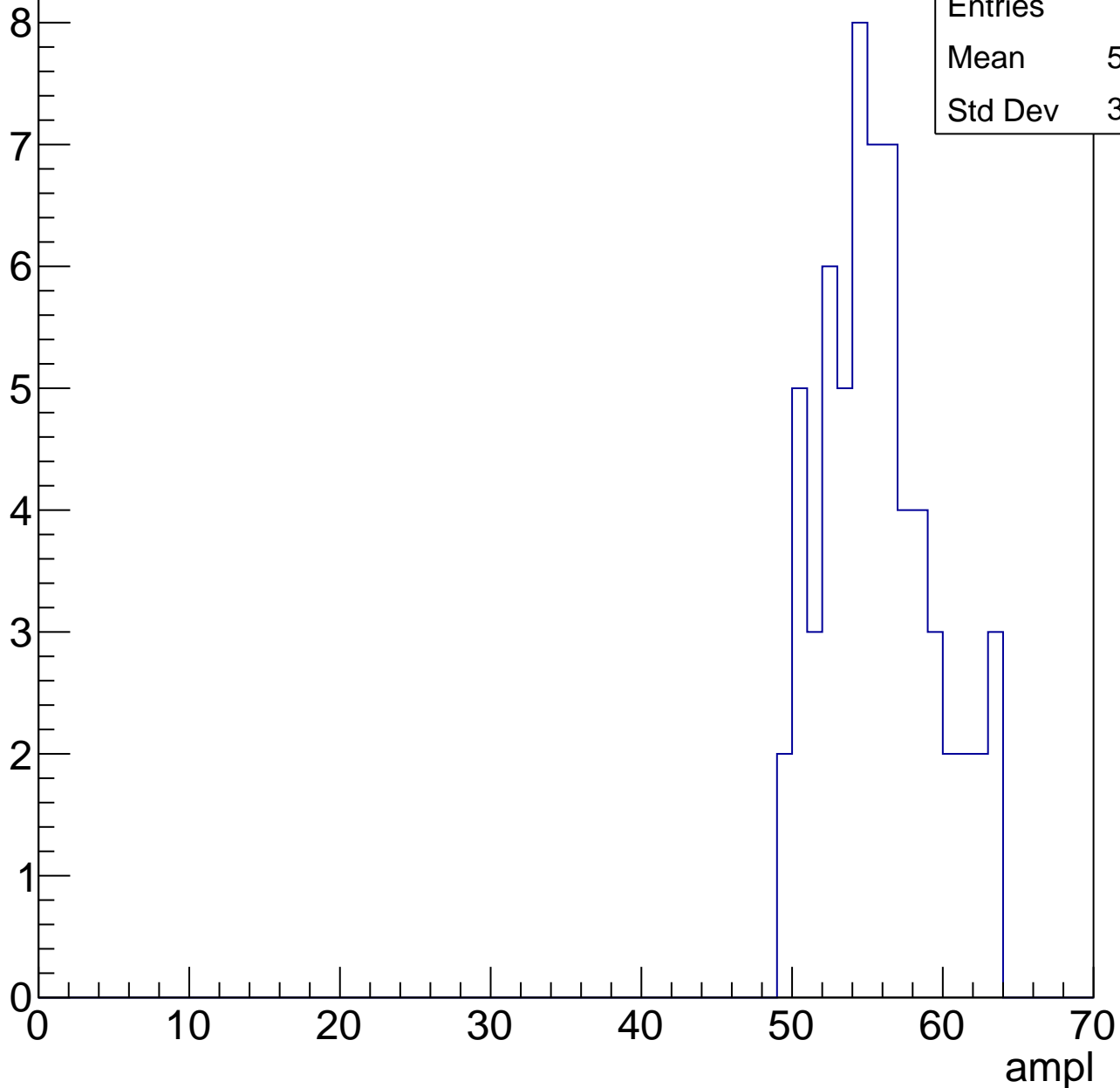


B1L103S, U18-ch75, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

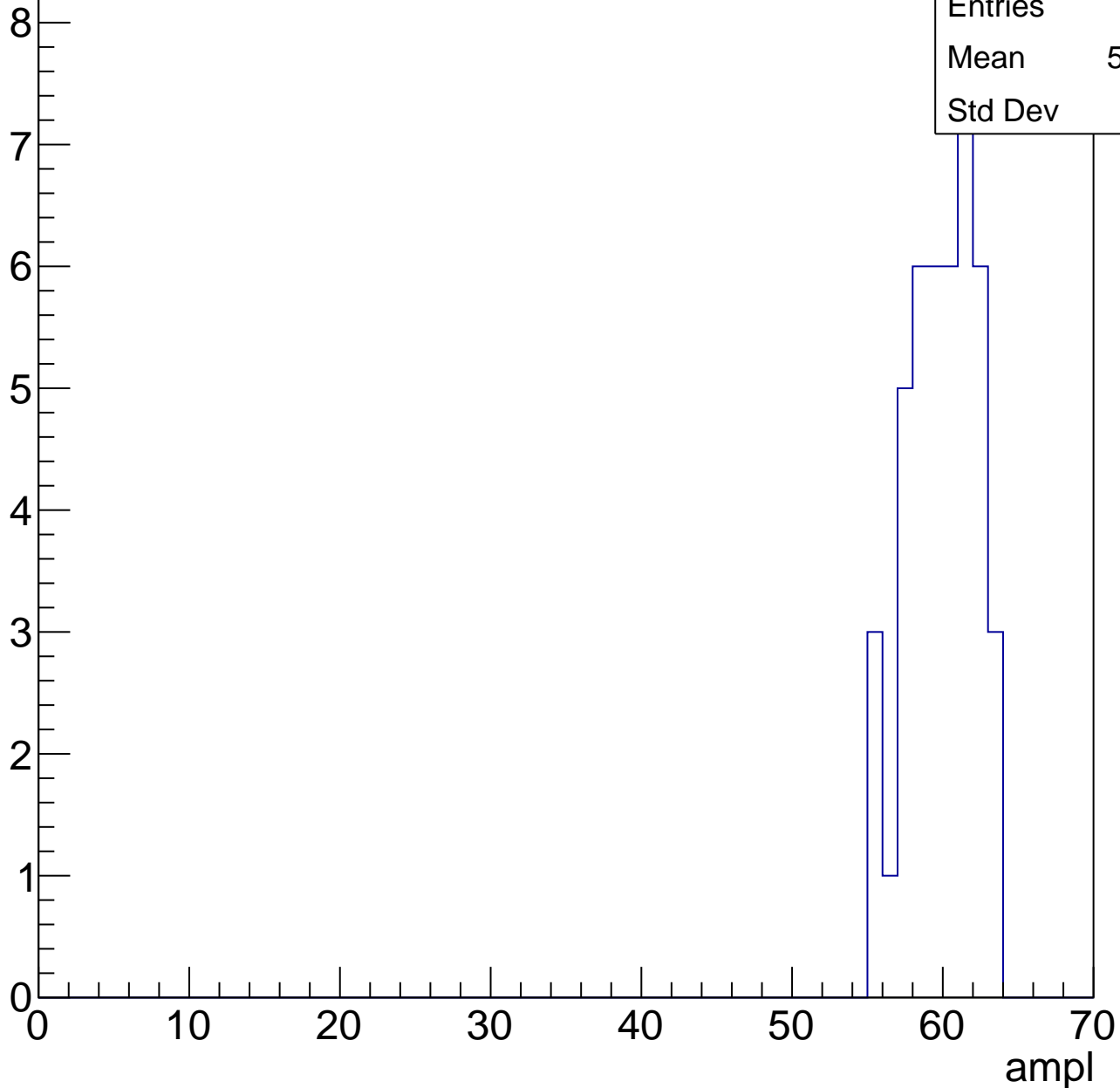
Entries	63
Mean	55.22
Std Dev	3.658



B1L103S, U18-ch75, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

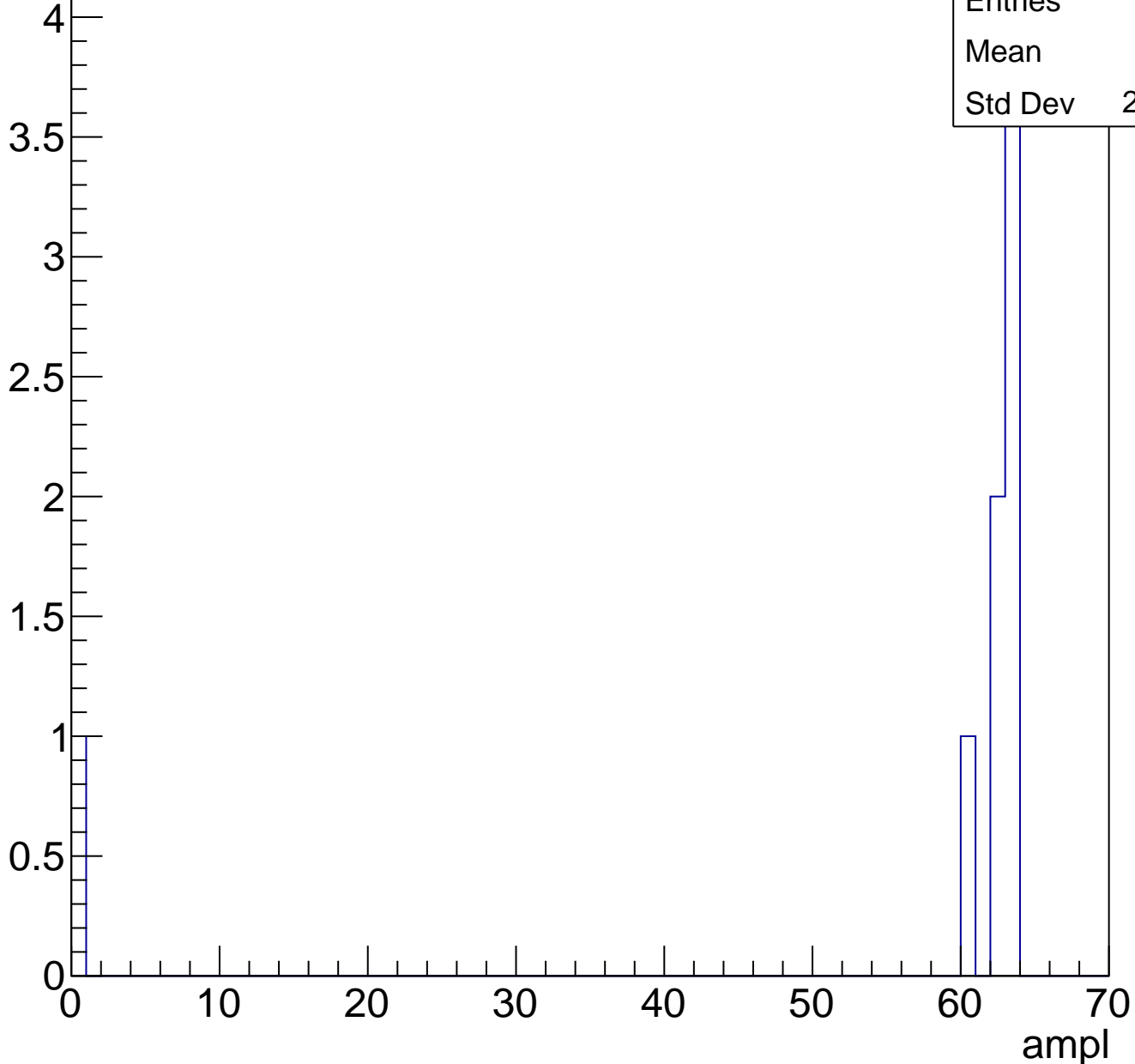


Entries	44
Mean	59.48
Std Dev	2.2

B1L103S, U18-ch75, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch75, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch76, adc0

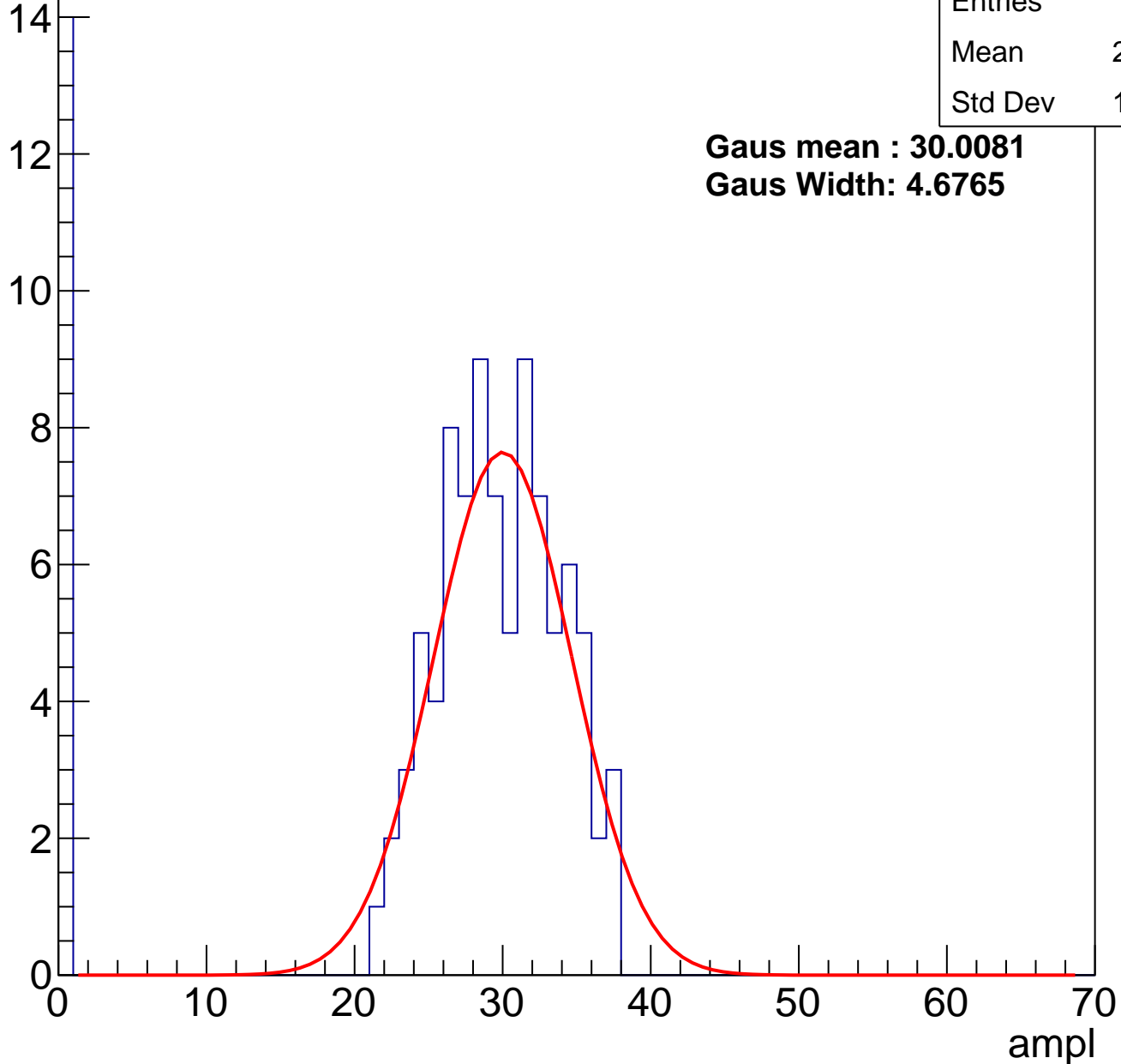
calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	25.35
Std Dev	10.75

Gaus mean : 30.0081

Gaus Width: 4.6765

Entry



B1L103S, U18-ch76, adc1

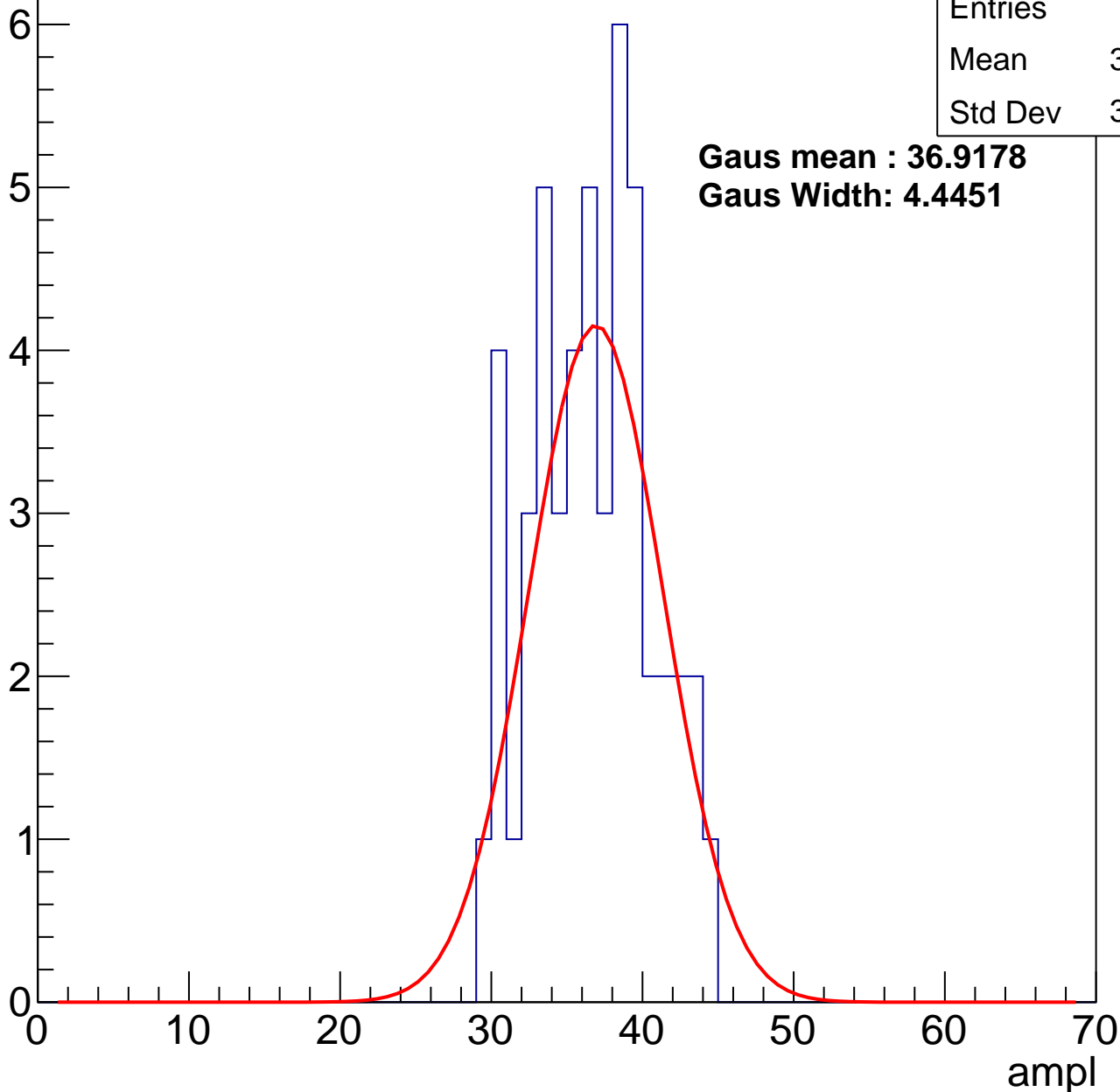
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	36.18
Std Dev	3.816

Gaus mean : 36.9178

Gaus Width: 4.4451



B1L103S, U18-ch76, adc2

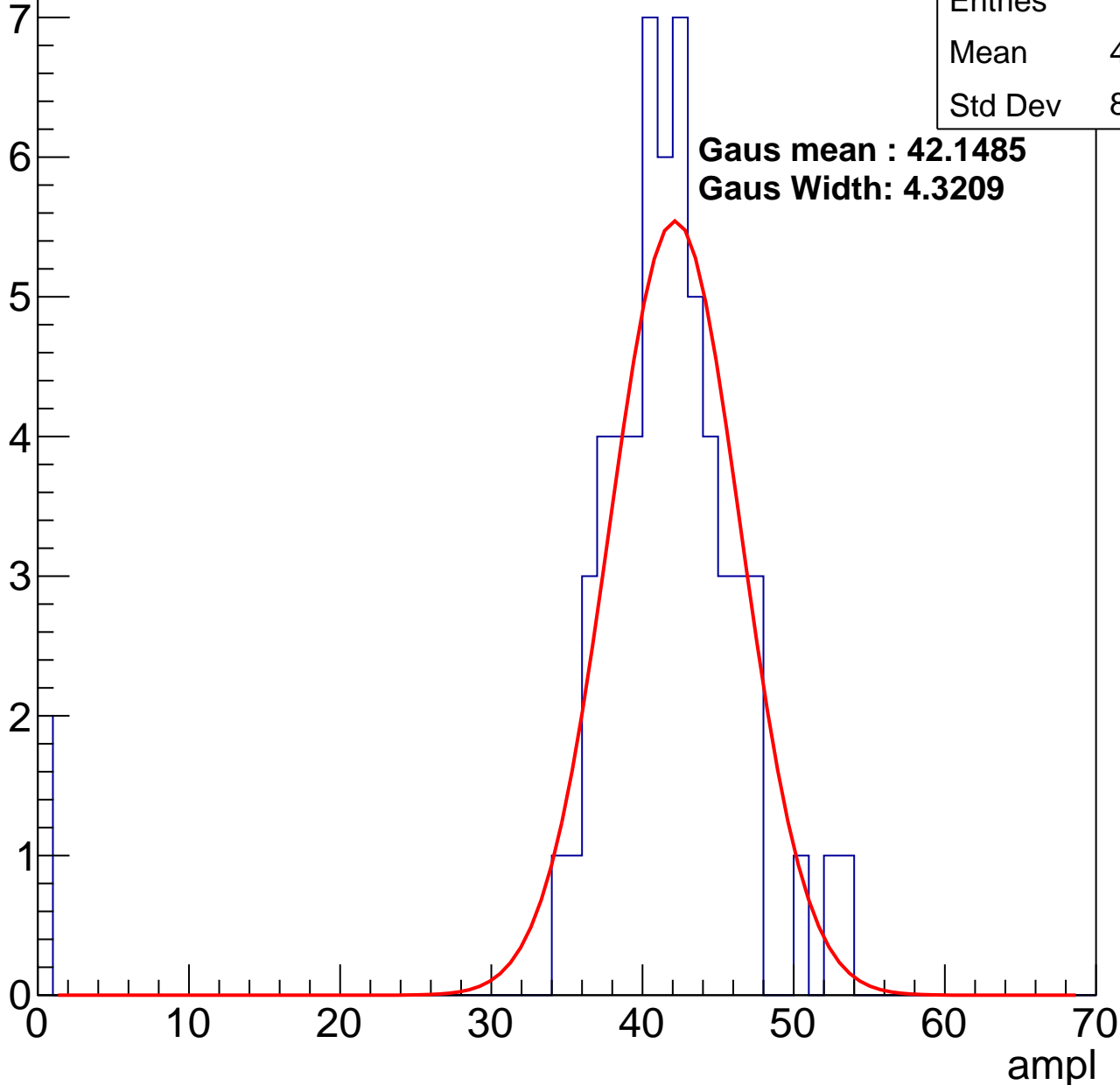
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	40.22
Std Dev	8.414

Gaus mean : 42.1485

Gaus Width: 4.3209



B1L103S, U18-ch76, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

Entries 80

Mean 41.29

Std Dev 16.86

ampl

0

10

20

30

40

50

60

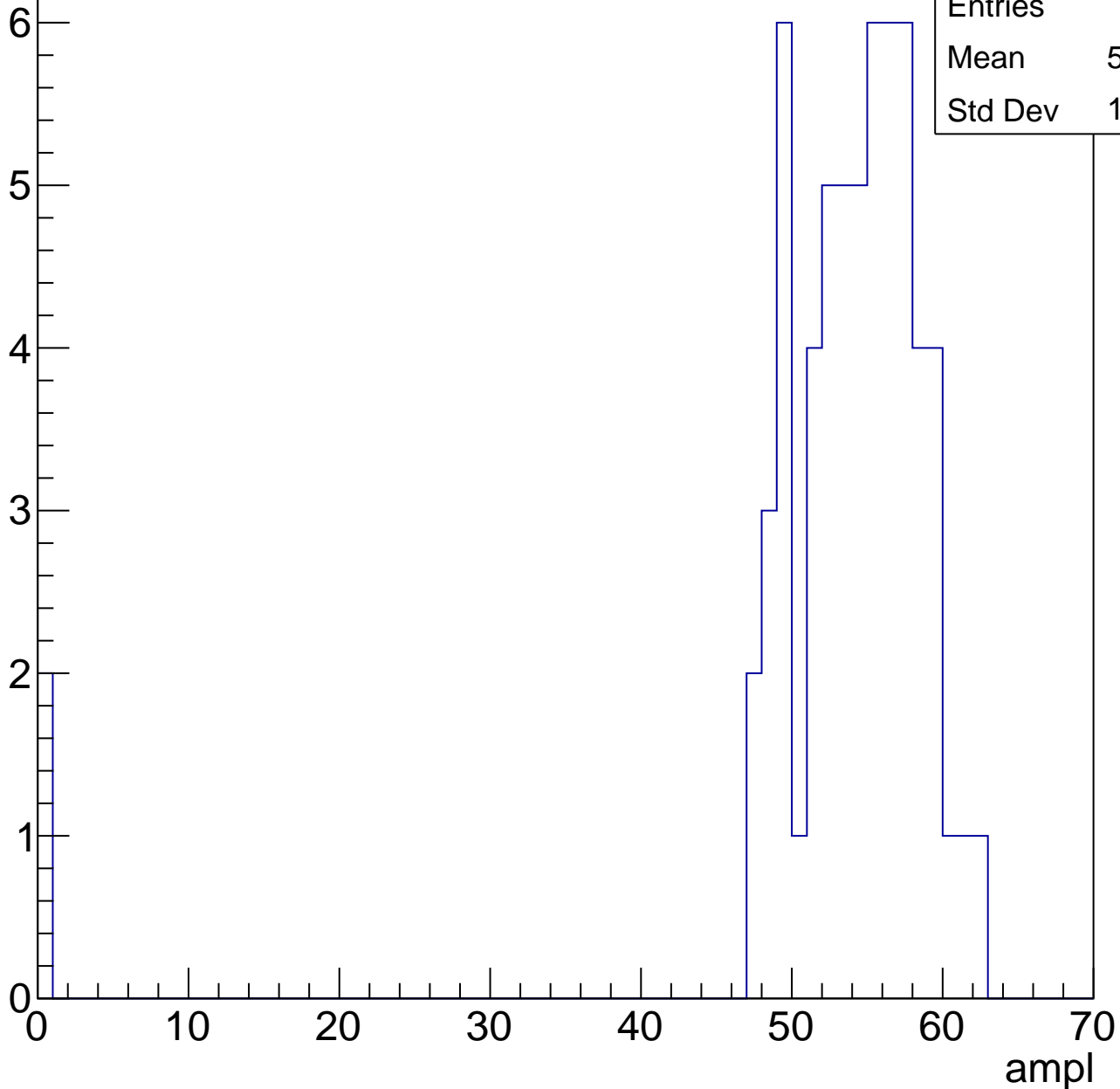
70

B1L103S, U18-ch76, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

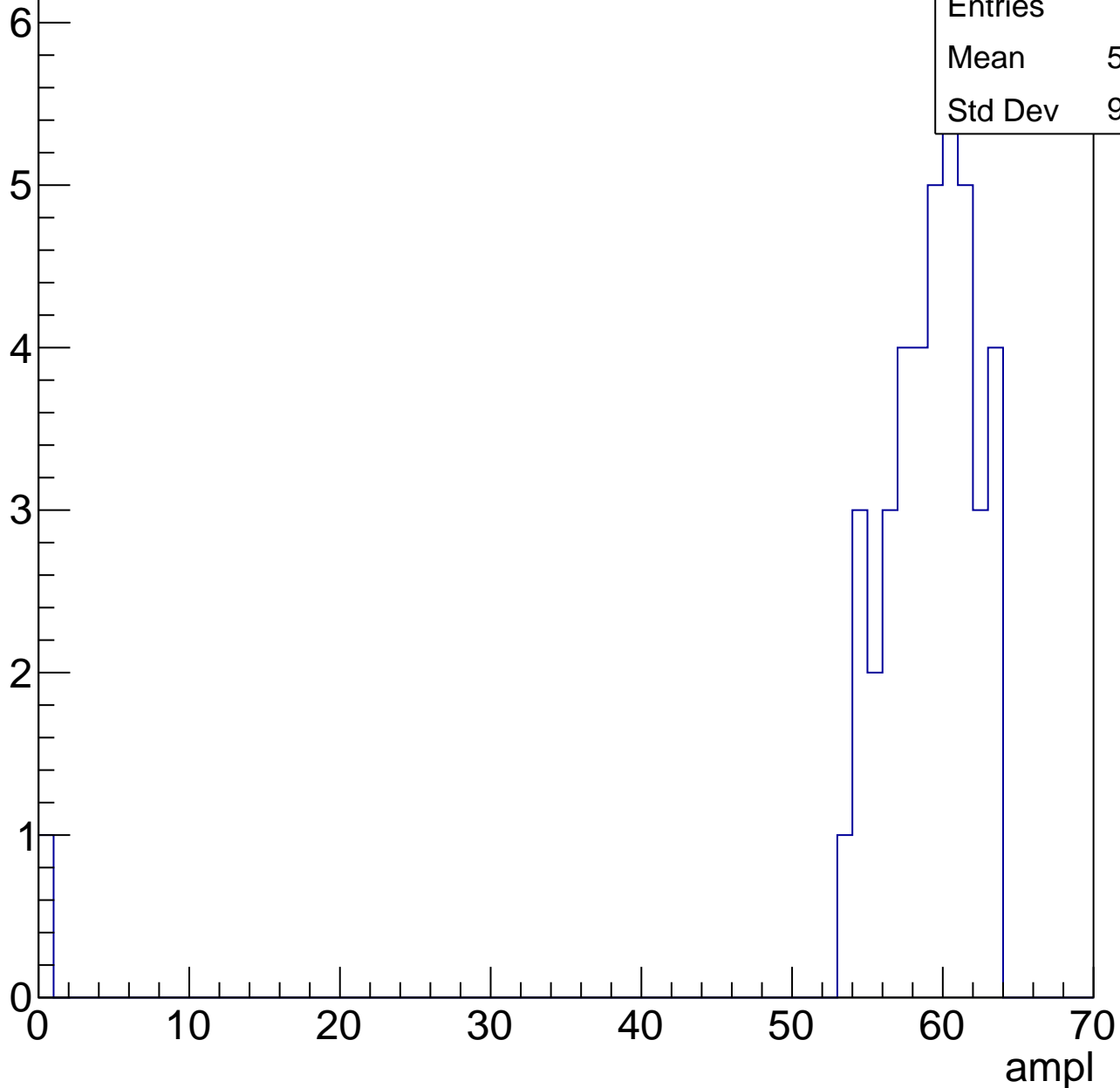
Entries	62
Mean	52.26
Std Dev	10.22



B1L103S, U18-ch76, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

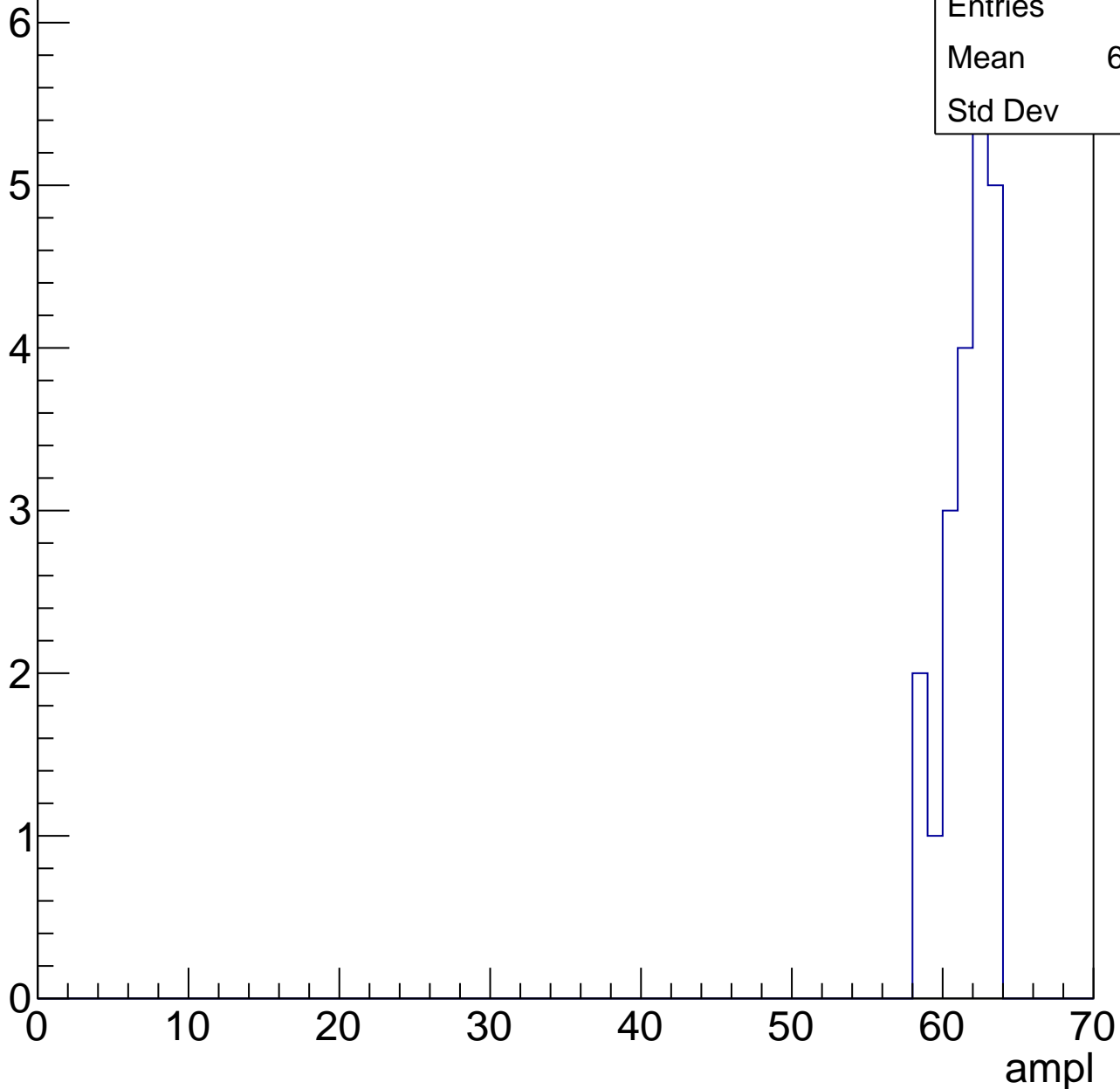


B1L103S, U18-ch76, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	21
Mean	61.24
Std Dev	1.54



B1L103S, U18-ch76, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



B1L103S, U18-ch77, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	23.22
Std Dev	11.53

Gaus mean : 29.5658

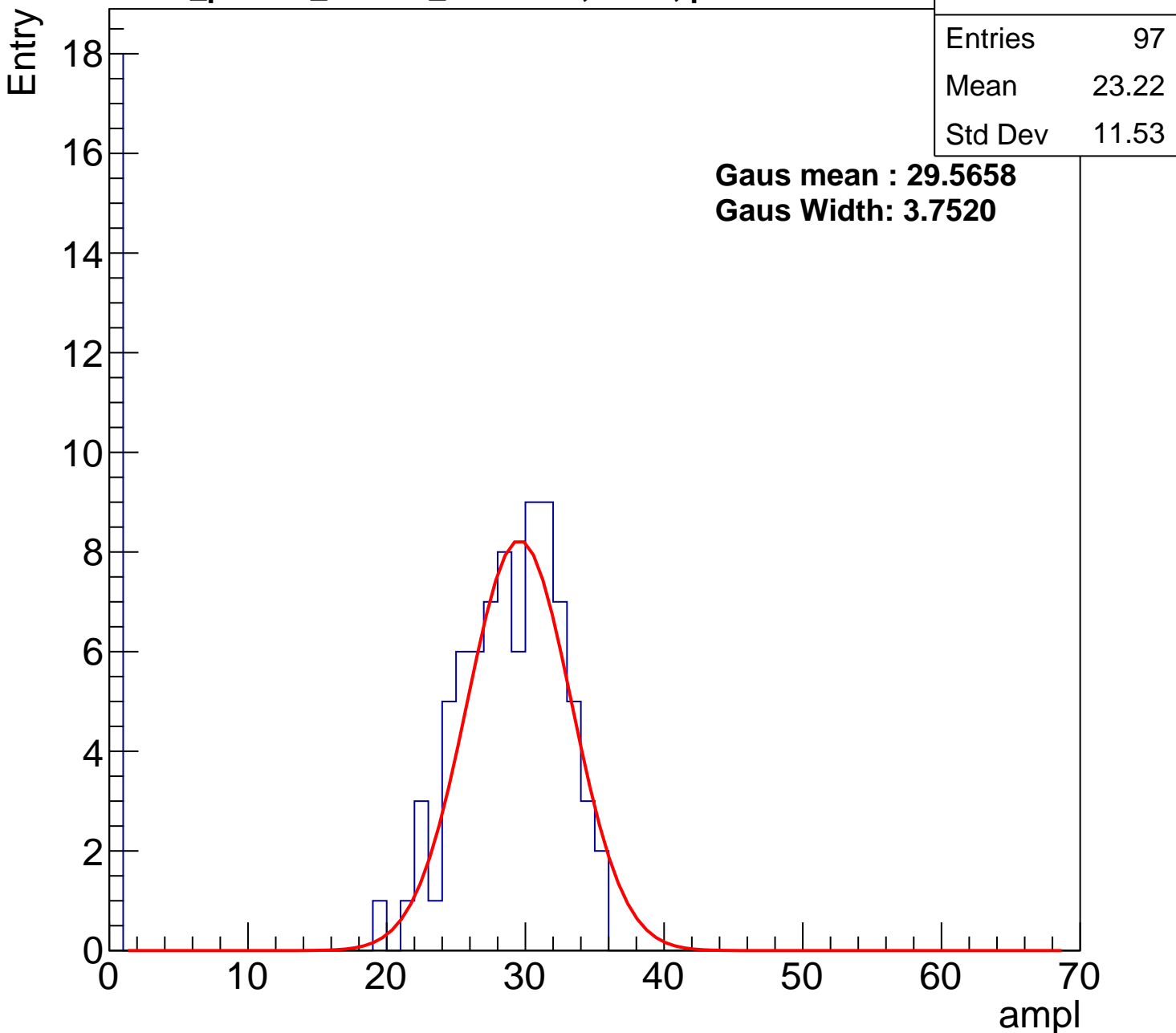
Gaus Width: 3.7520

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U18-ch77, adc1

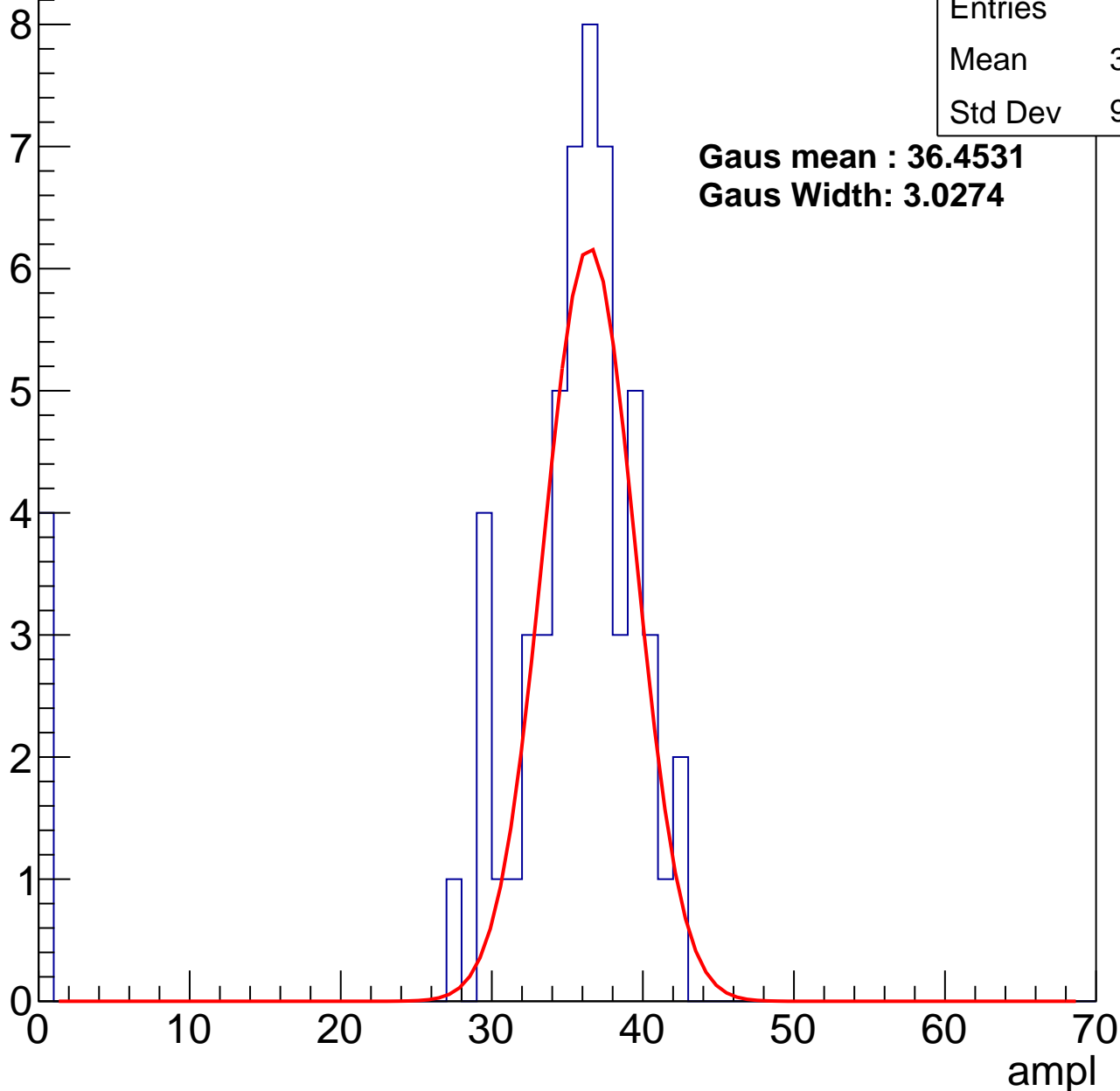
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	33.02
Std Dev	9.575

Gaus mean : 36.4531

Gaus Width: 3.0274



B1L103S, U18-ch77, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	37.29
Std Dev	14.1

Gaus mean : 42.6549

Gaus Width: 4.1929

Entry

10

8

6

4

2

0

0

10

20

30

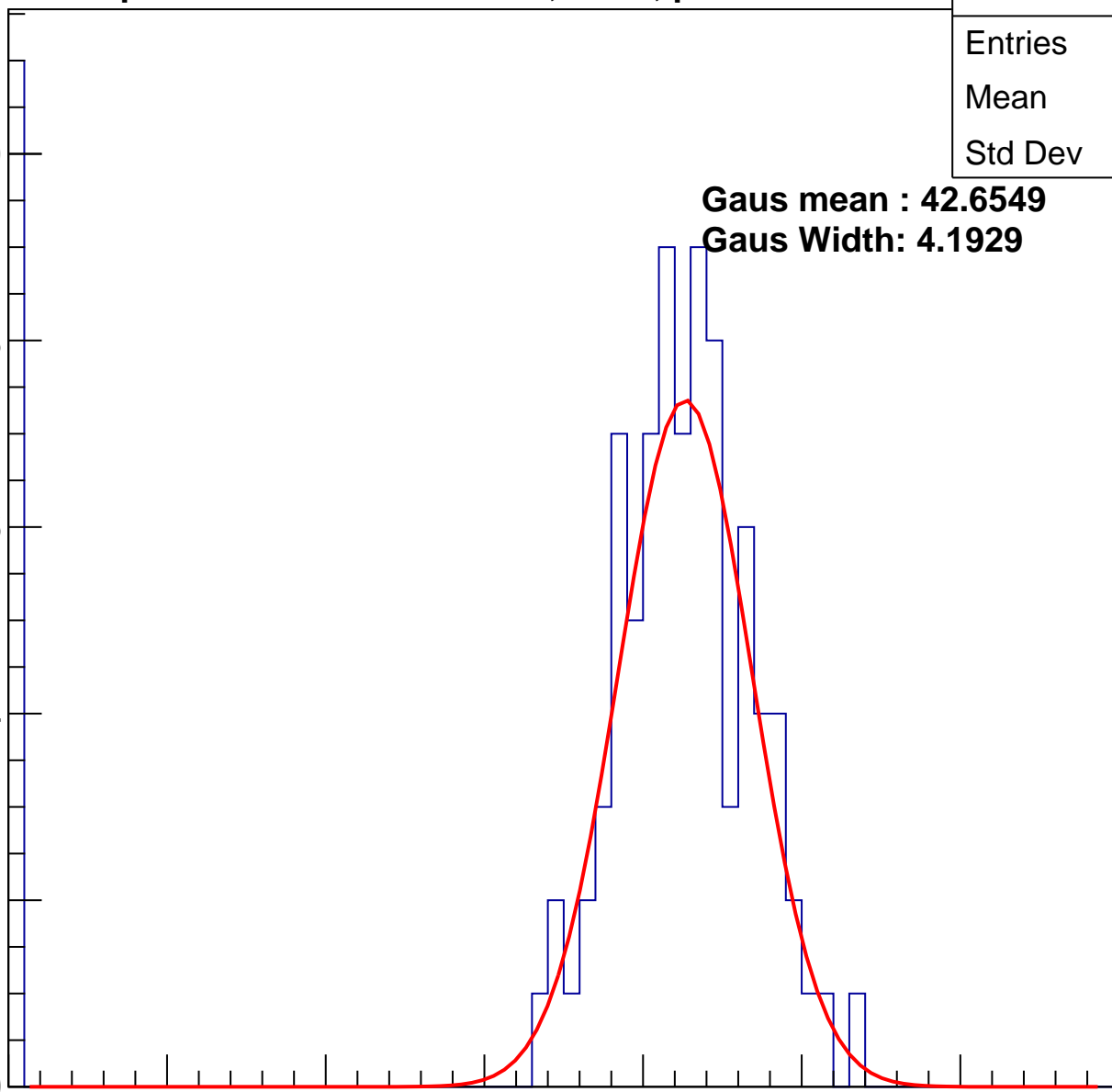
40

50

60

70

ampl

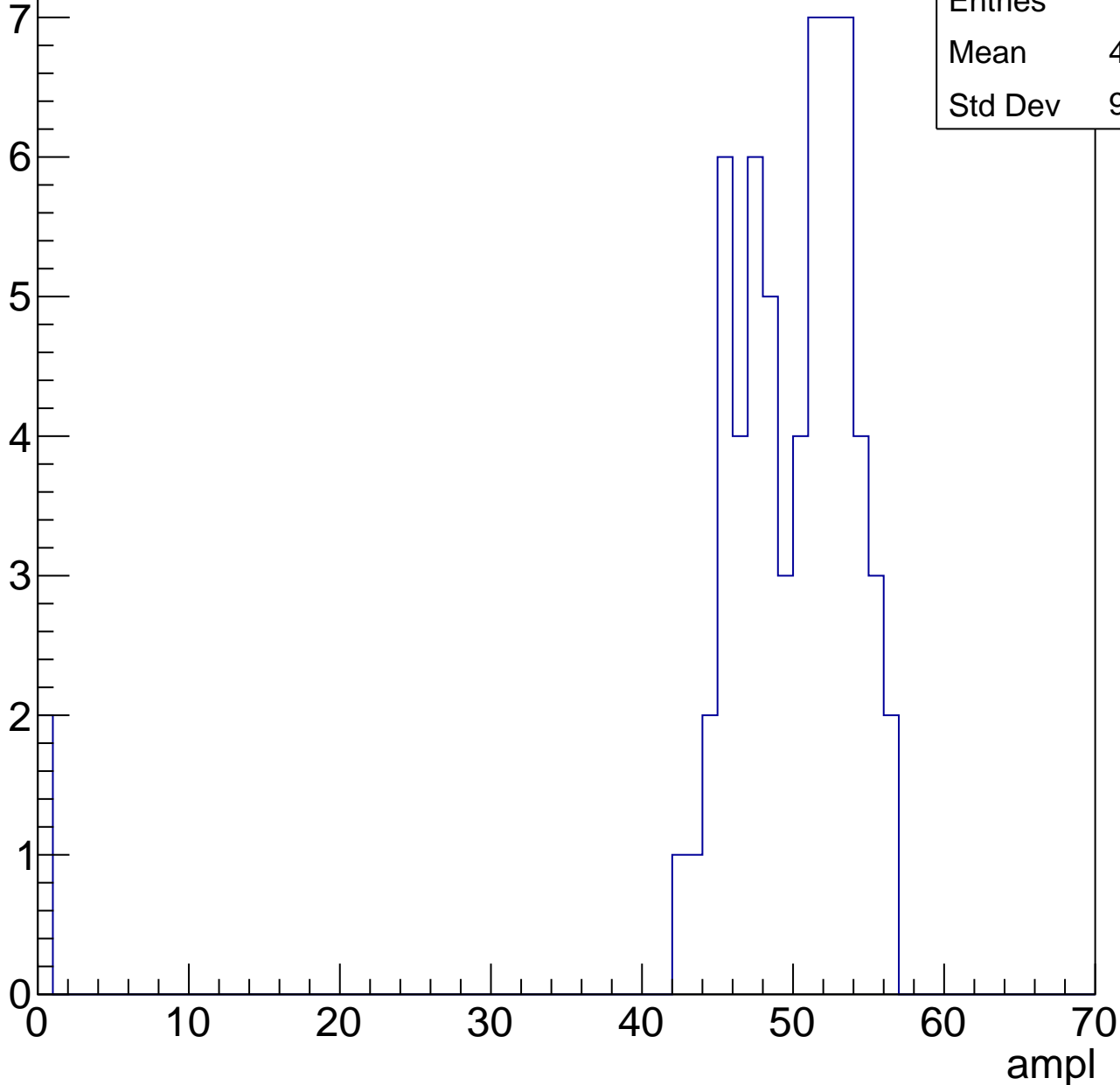


B1L103S, U18-ch77, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	48.14
Std Dev	9.324

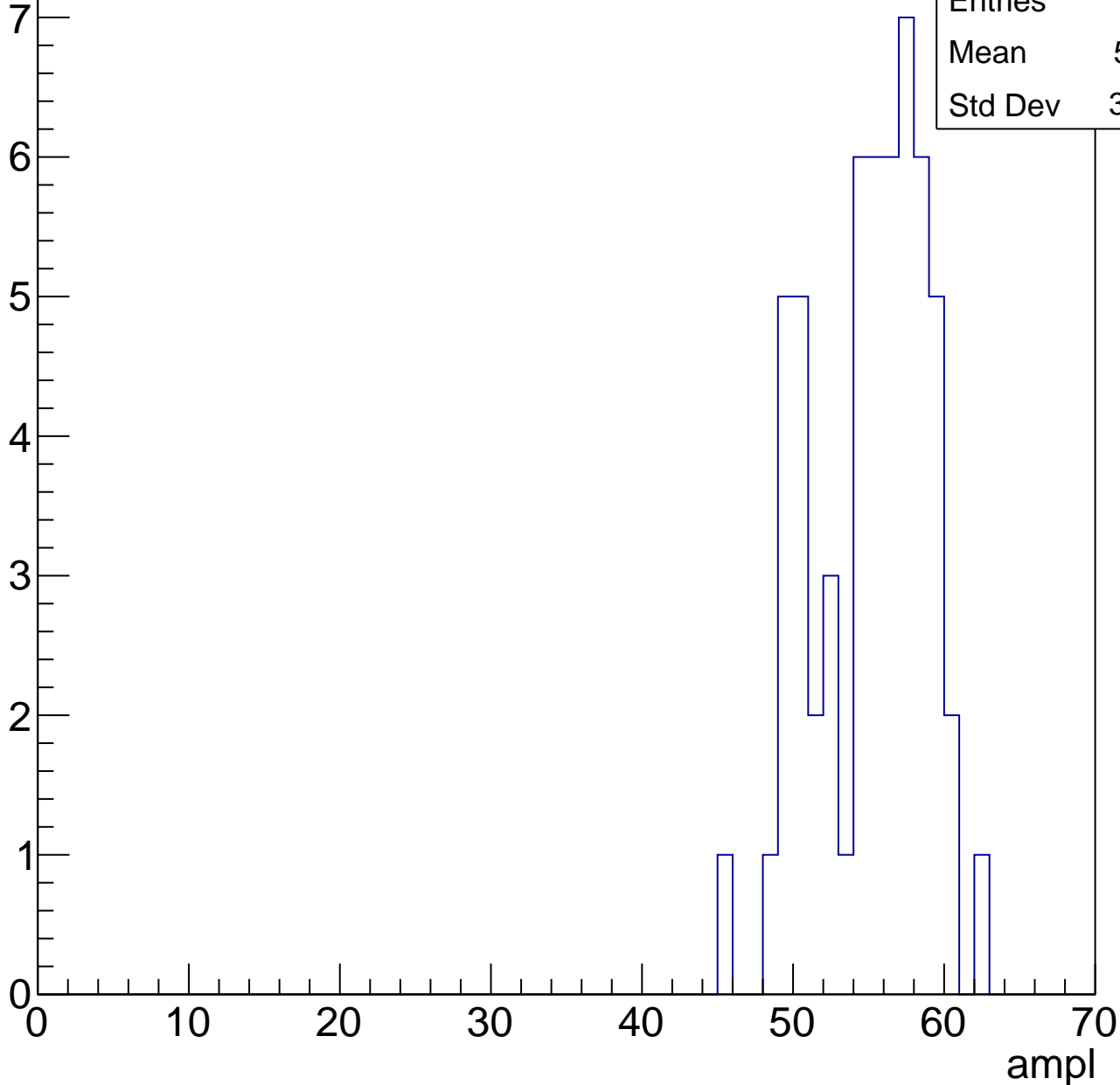


B1L103S, U18-ch77, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.61
Std Dev	3.703

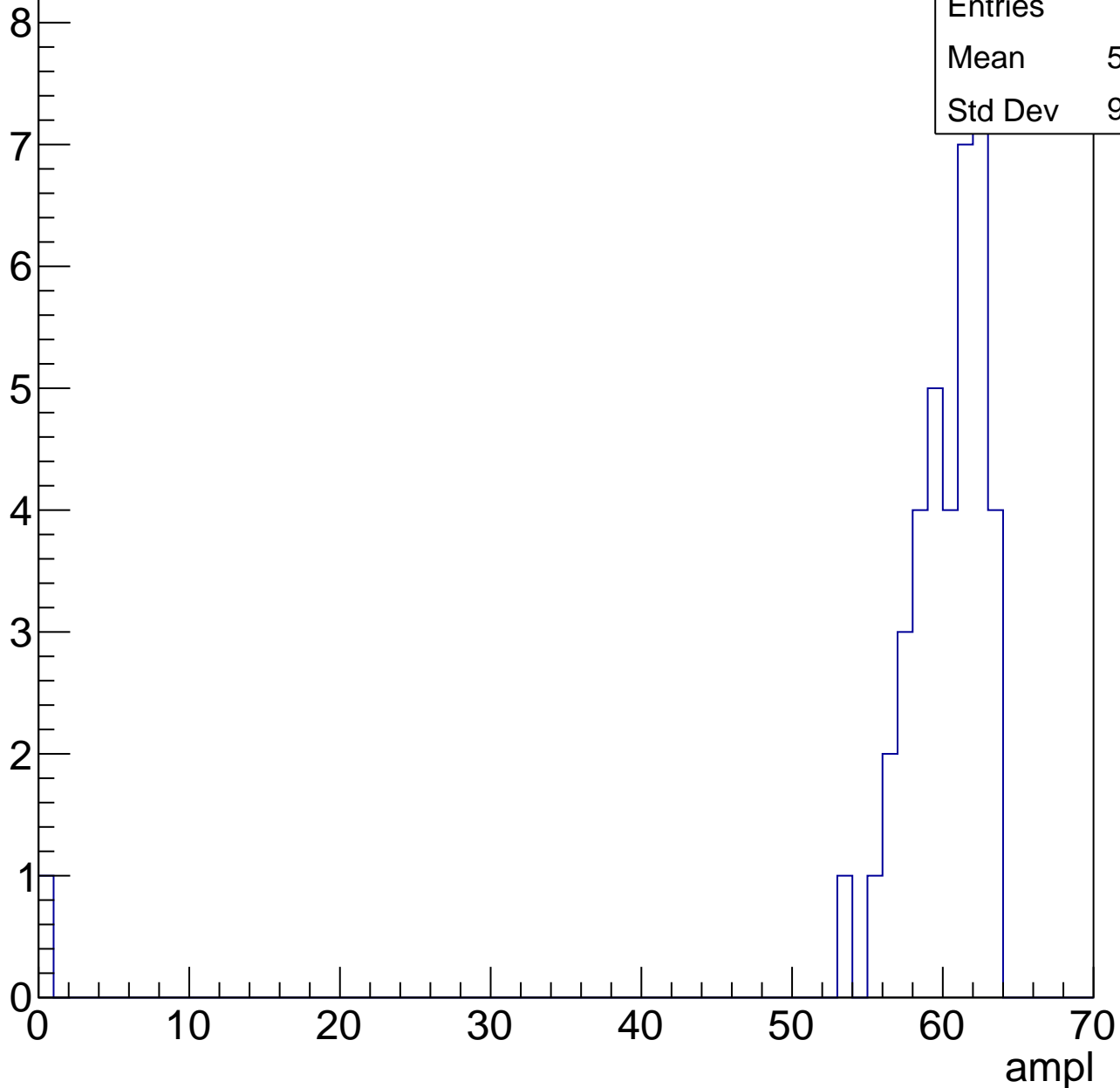


B1L103S, U18-ch77, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	58.33
Std Dev	9.639

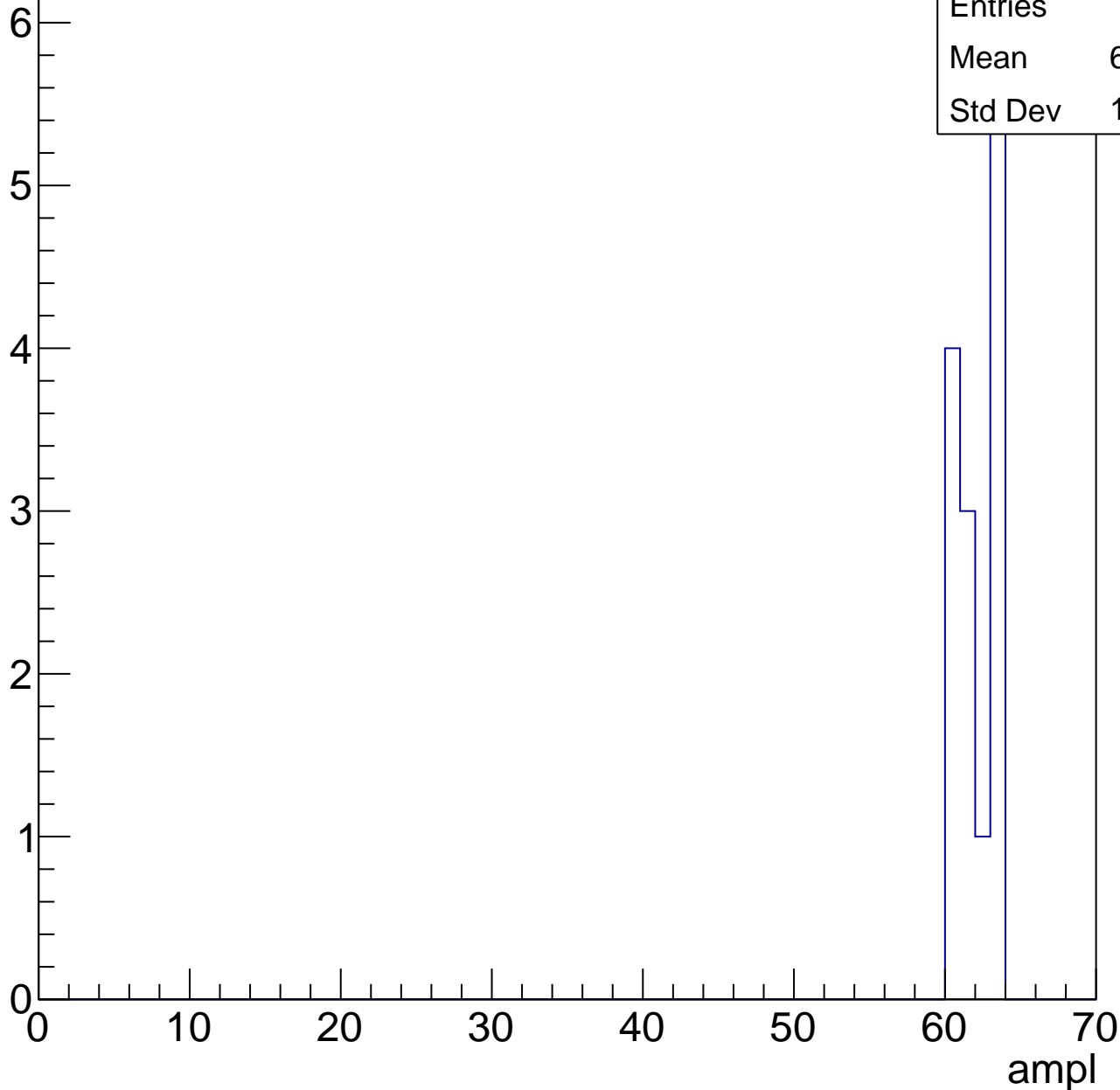


B1L103S, U18-ch77, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.64
Std Dev	1.288



B1L103S, U18-ch77, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch78, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	22.03
Std Dev	9.041

Gaus mean : 25.9418

Gaus Width: 4.1338

Entry

10

8

6

4

2

0

0

10

20

30

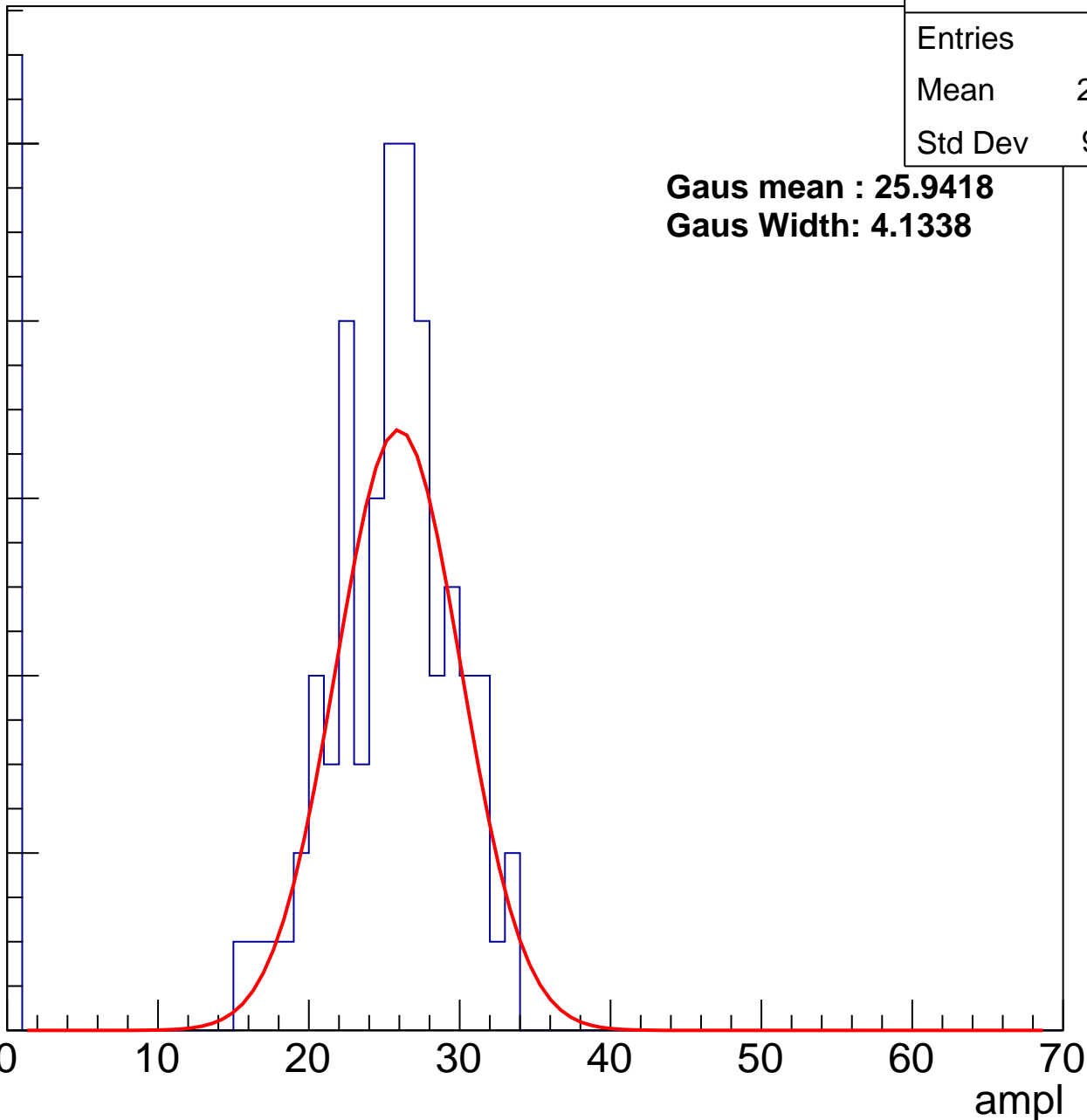
40

50

60

70

ampl



B1L103S, U18-ch78, adc1

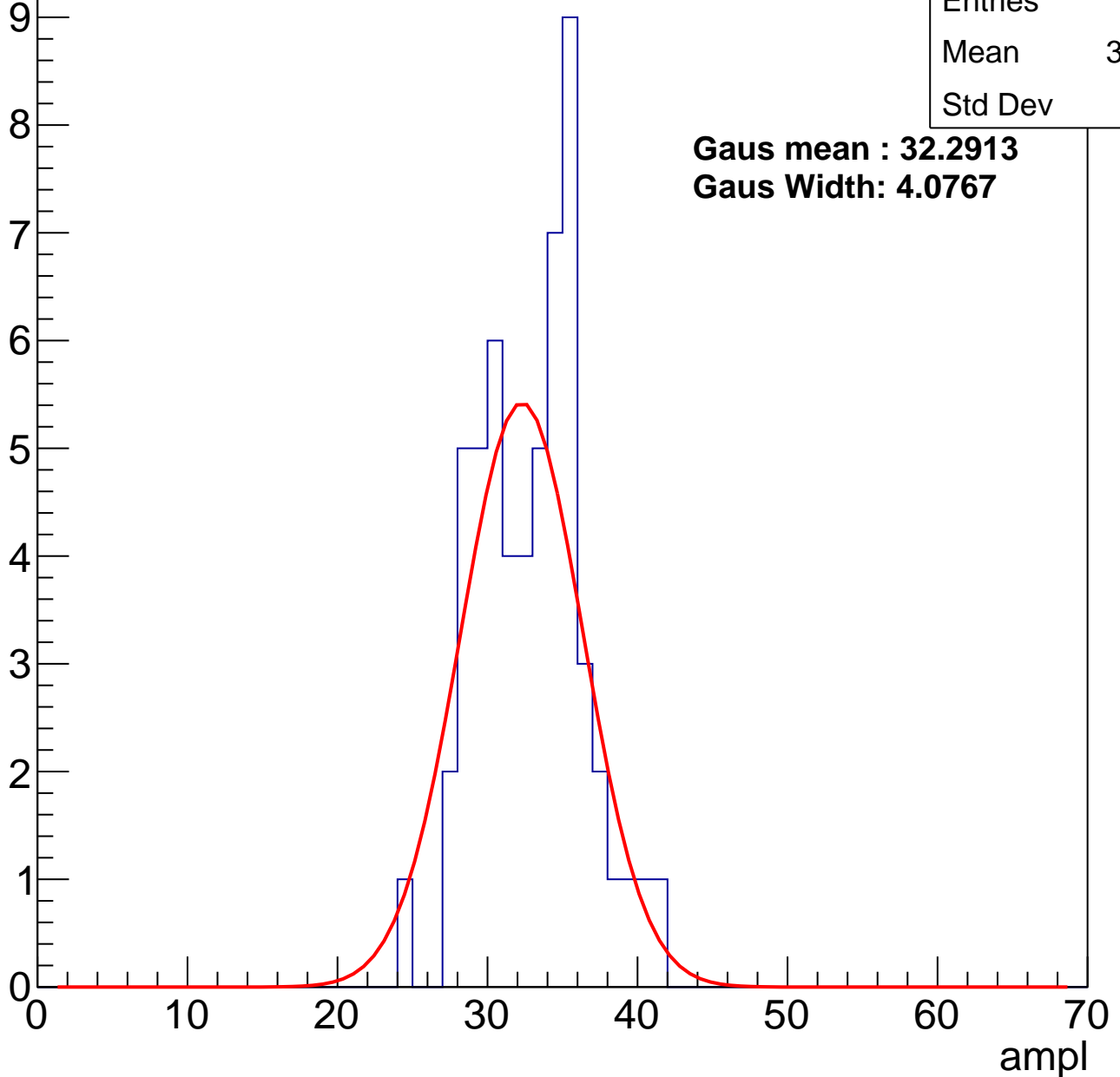
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	32.51
Std Dev	3.49

Gaus mean : 32.2913

Gaus Width: 4.0767



B1L103S, U18-ch78, adc2

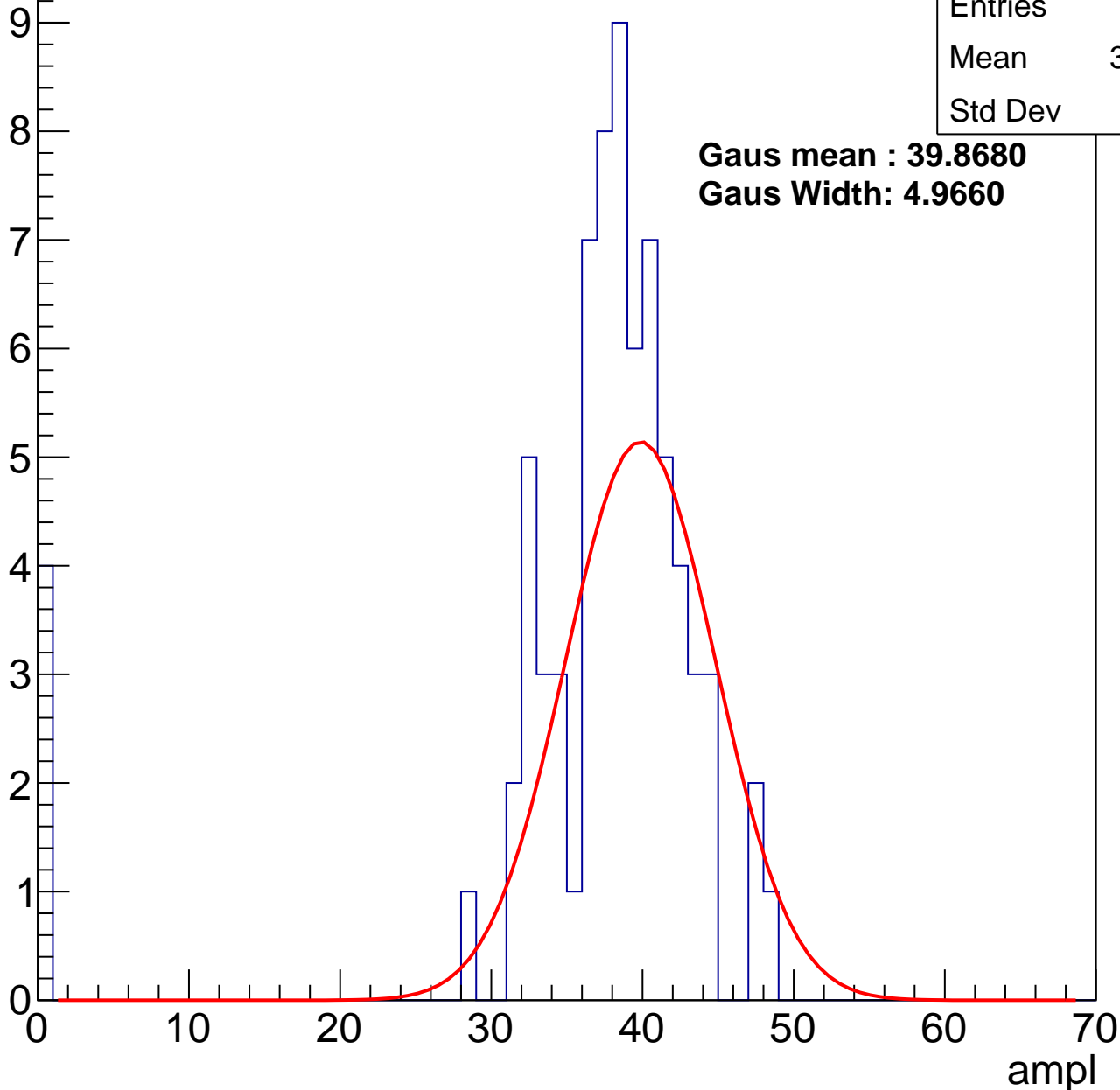
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	36.03
Std Dev	9.46

Gaus mean : 39.8680

Gaus Width: 4.9660

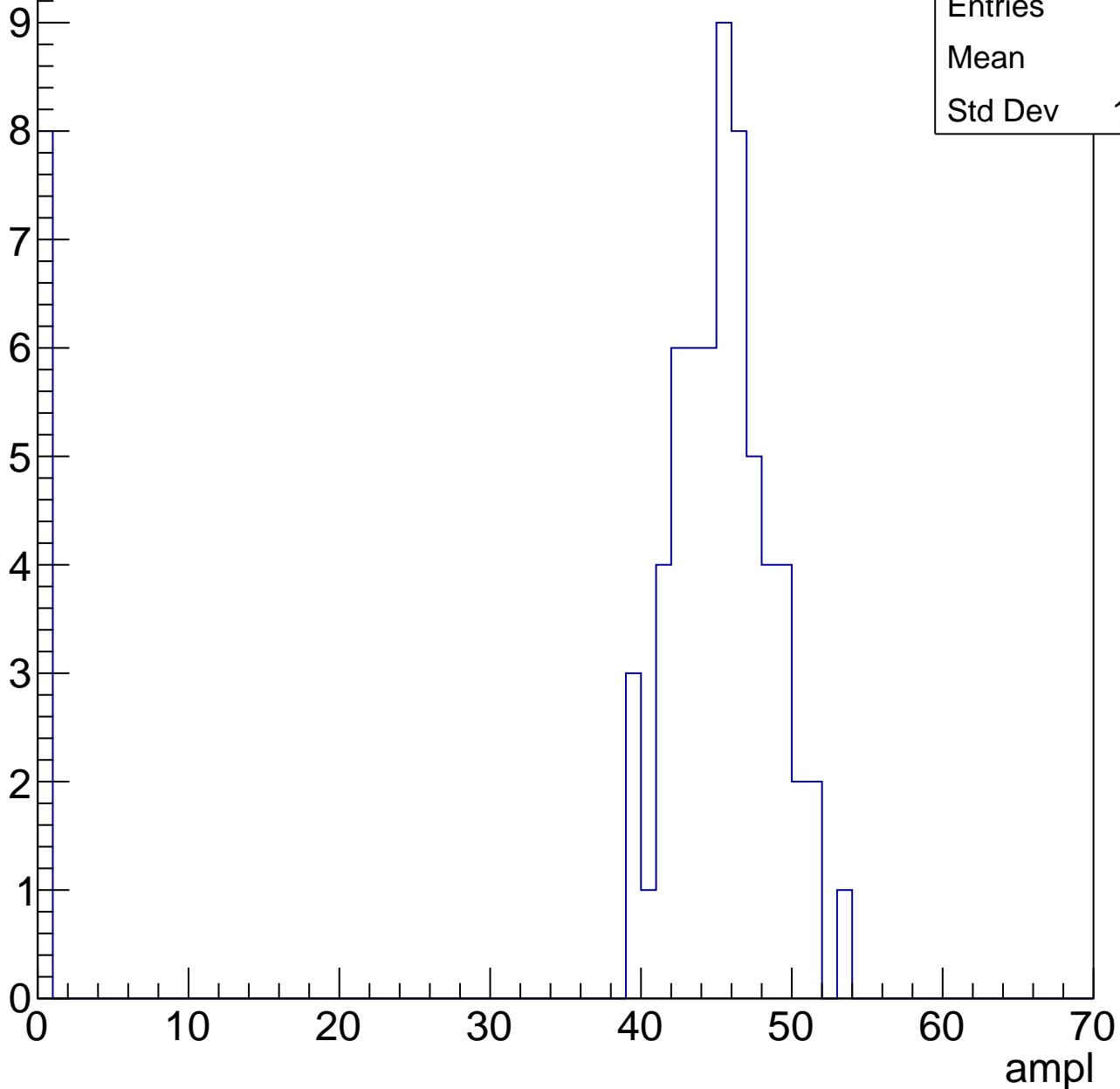


B1L103S, U18-ch78, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	39.8
Std Dev	14.71

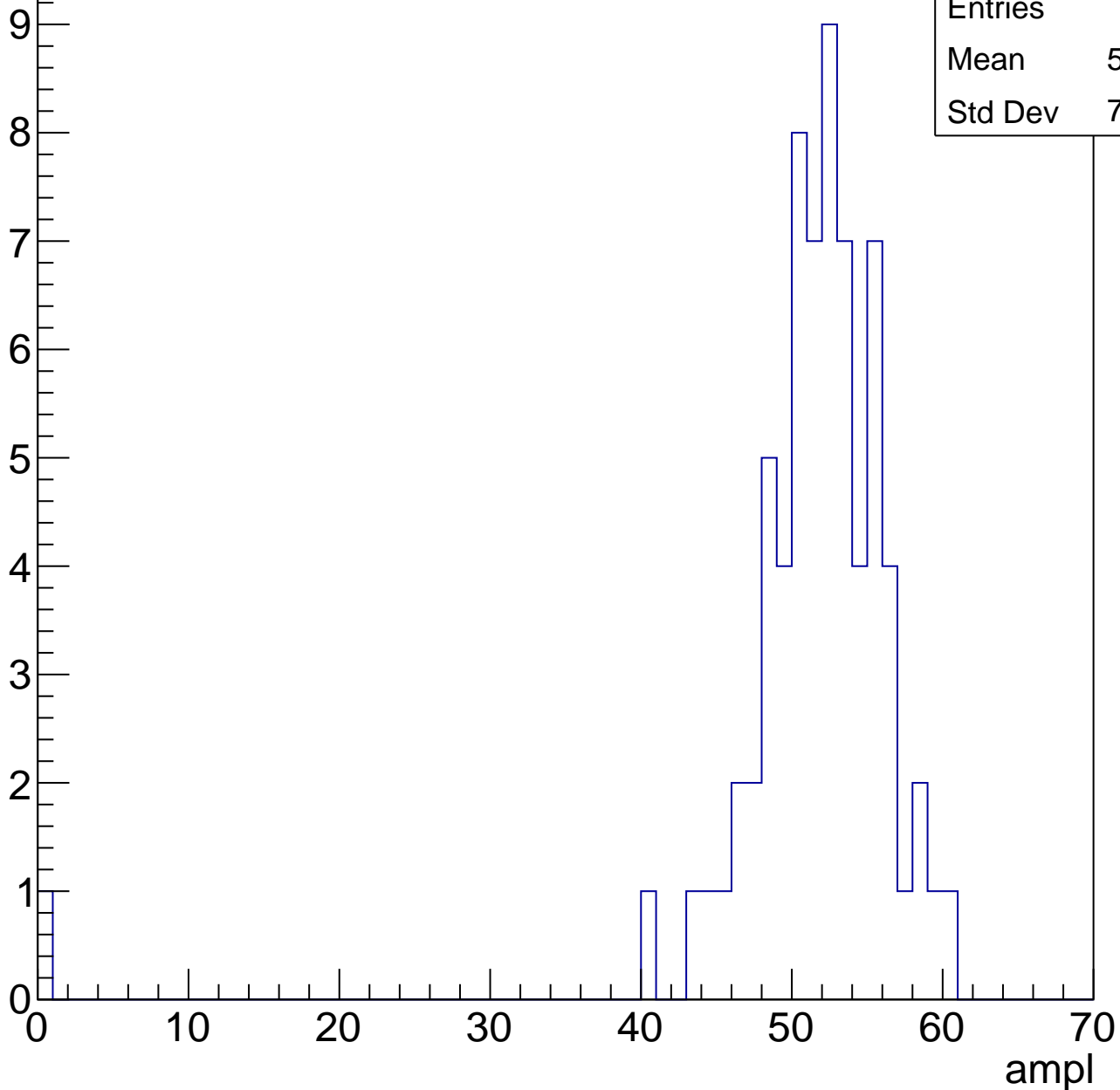


B1L103S, U18-ch78, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	50.83
Std Dev	7.207

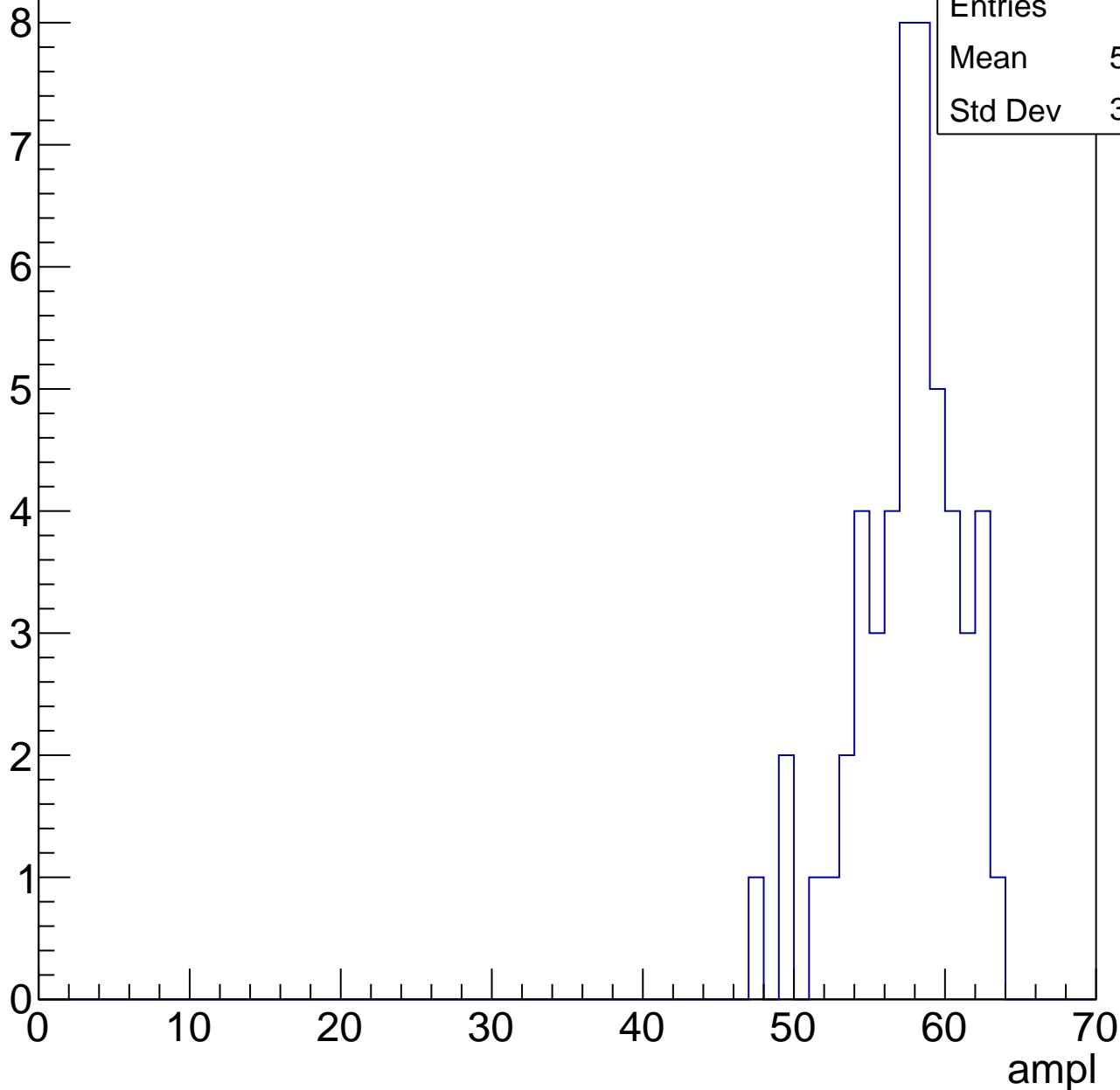


B1L103S, U18-ch78, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.02
Std Dev	3.489

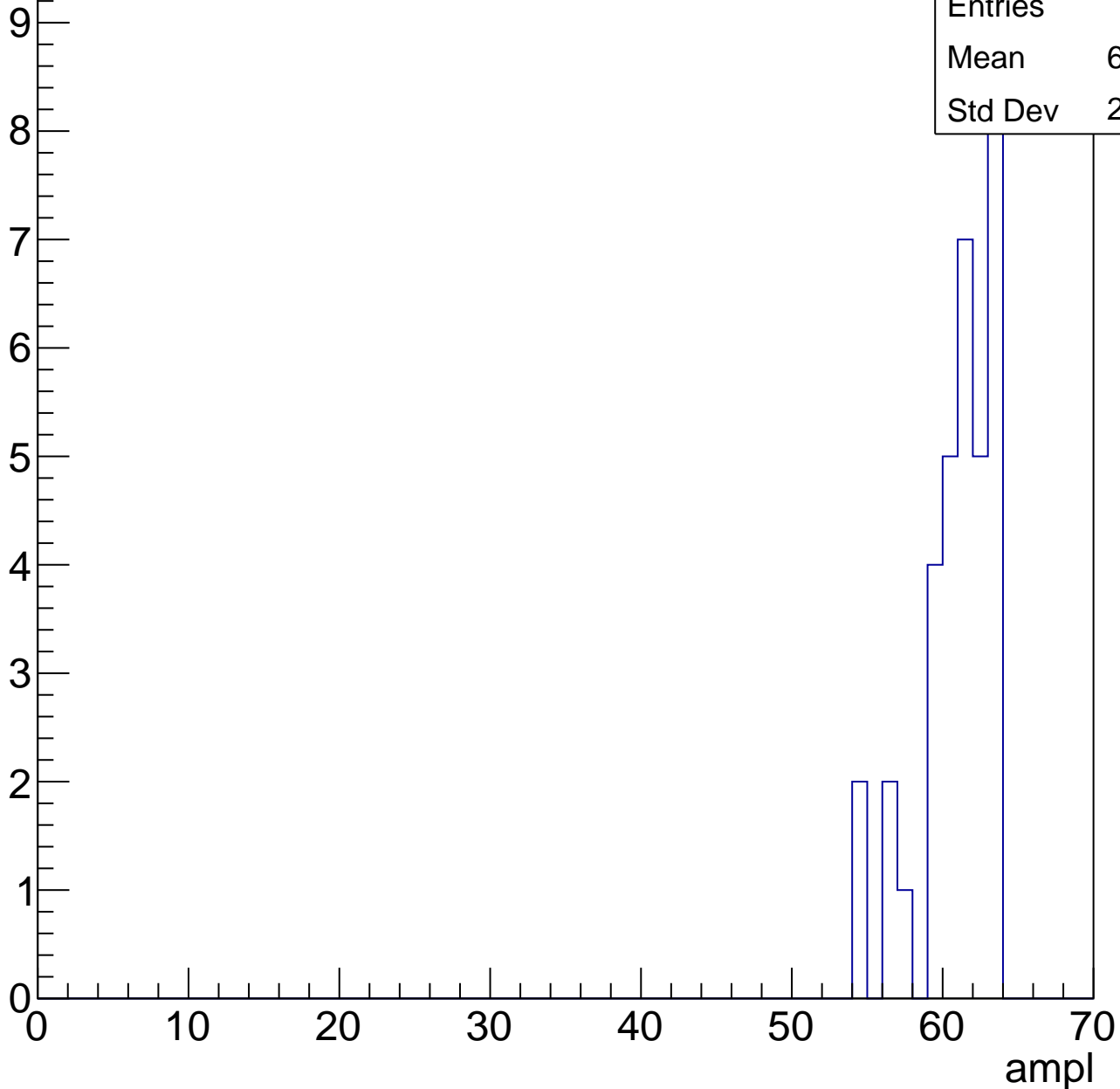


B1L103S, U18-ch78, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	35
Mean	60.49
Std Dev	2.489

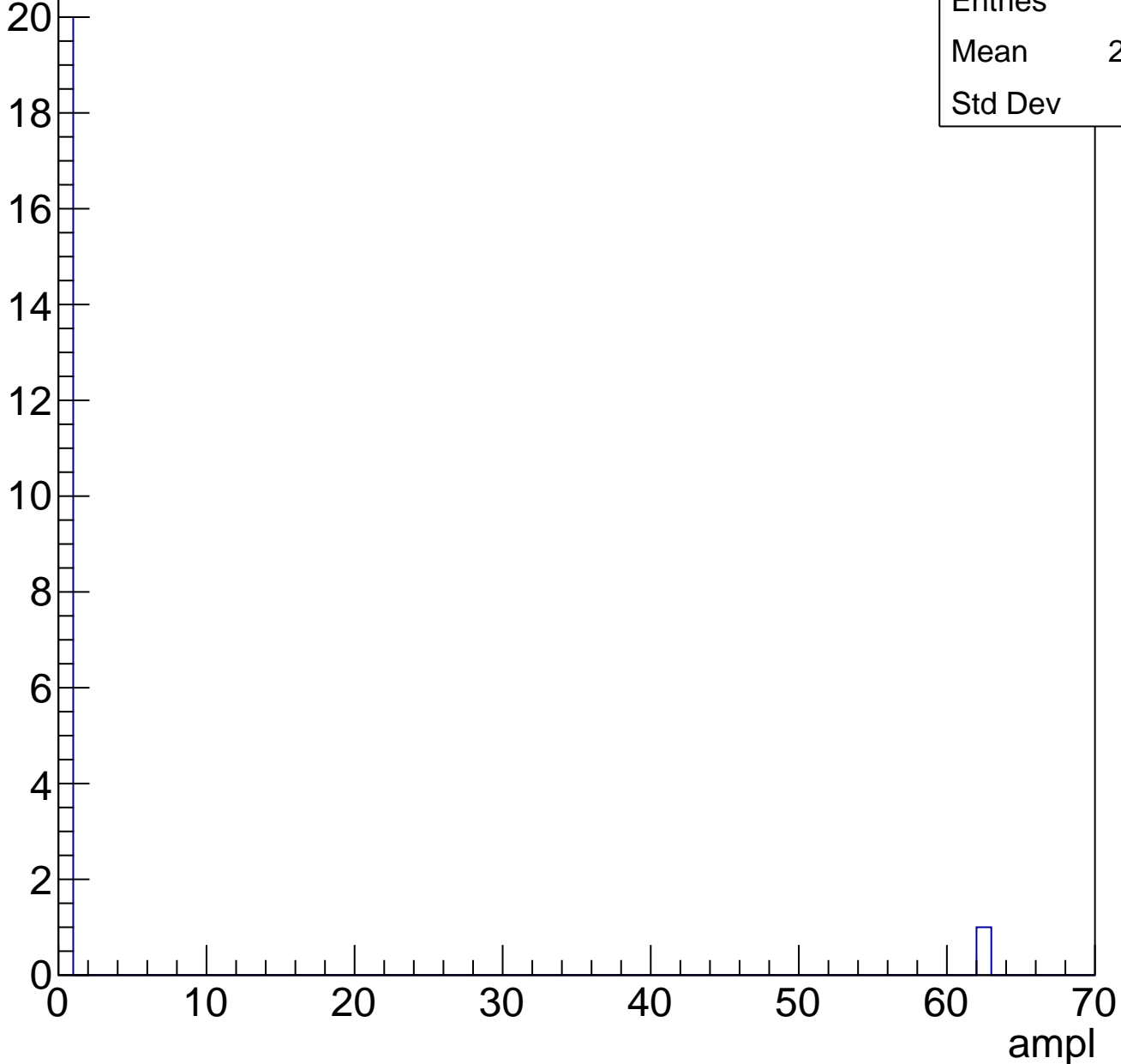


B1L103S, U18-ch78, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	2.952
Std Dev	13.2

Entry



B1L103S, U18-ch79, adc0

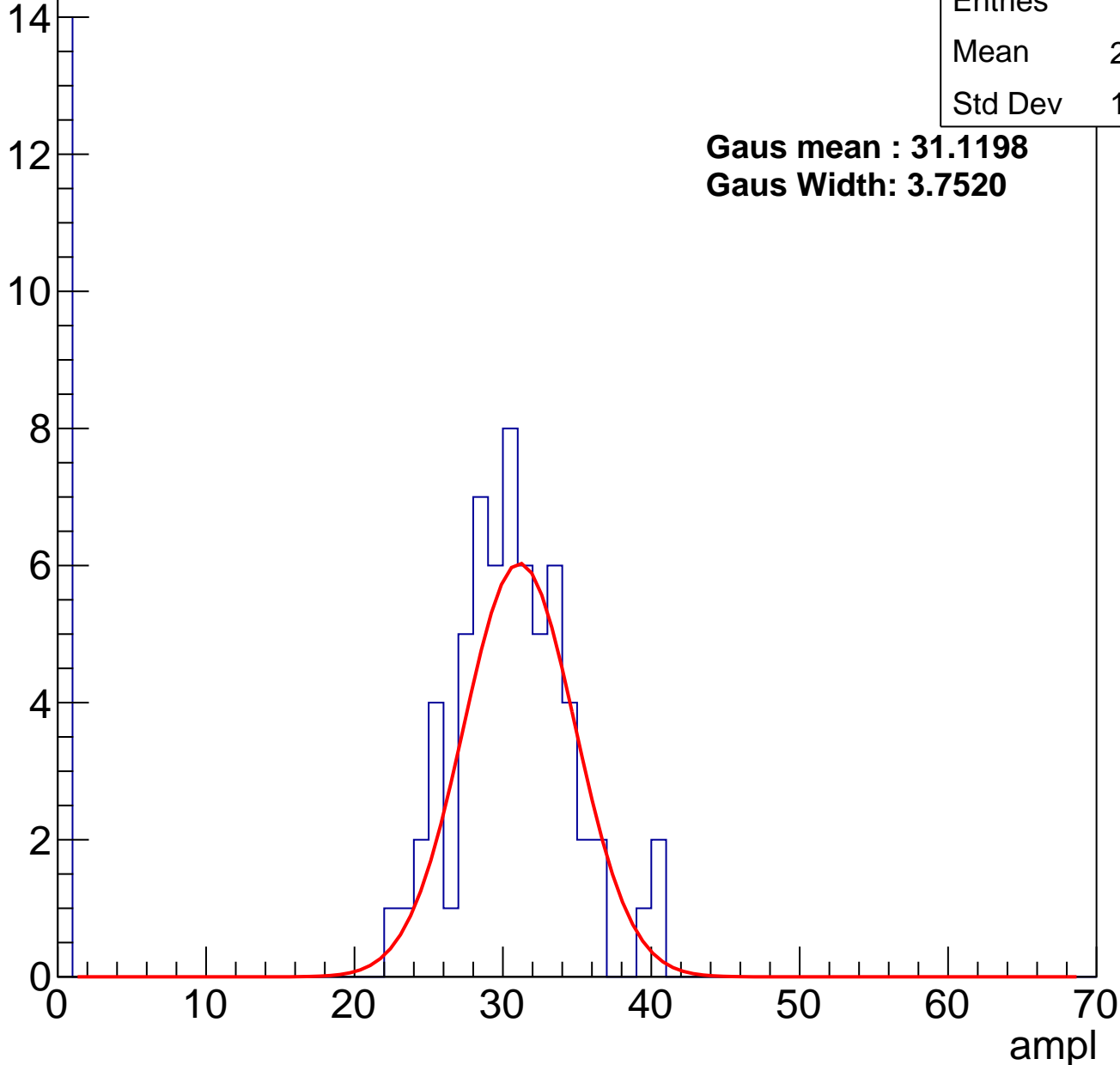
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	24.74
Std Dev	12.17

Gaus mean : 31.1198

Gaus Width: 3.7520

Entry



B1L103S, U18-ch79, adc1

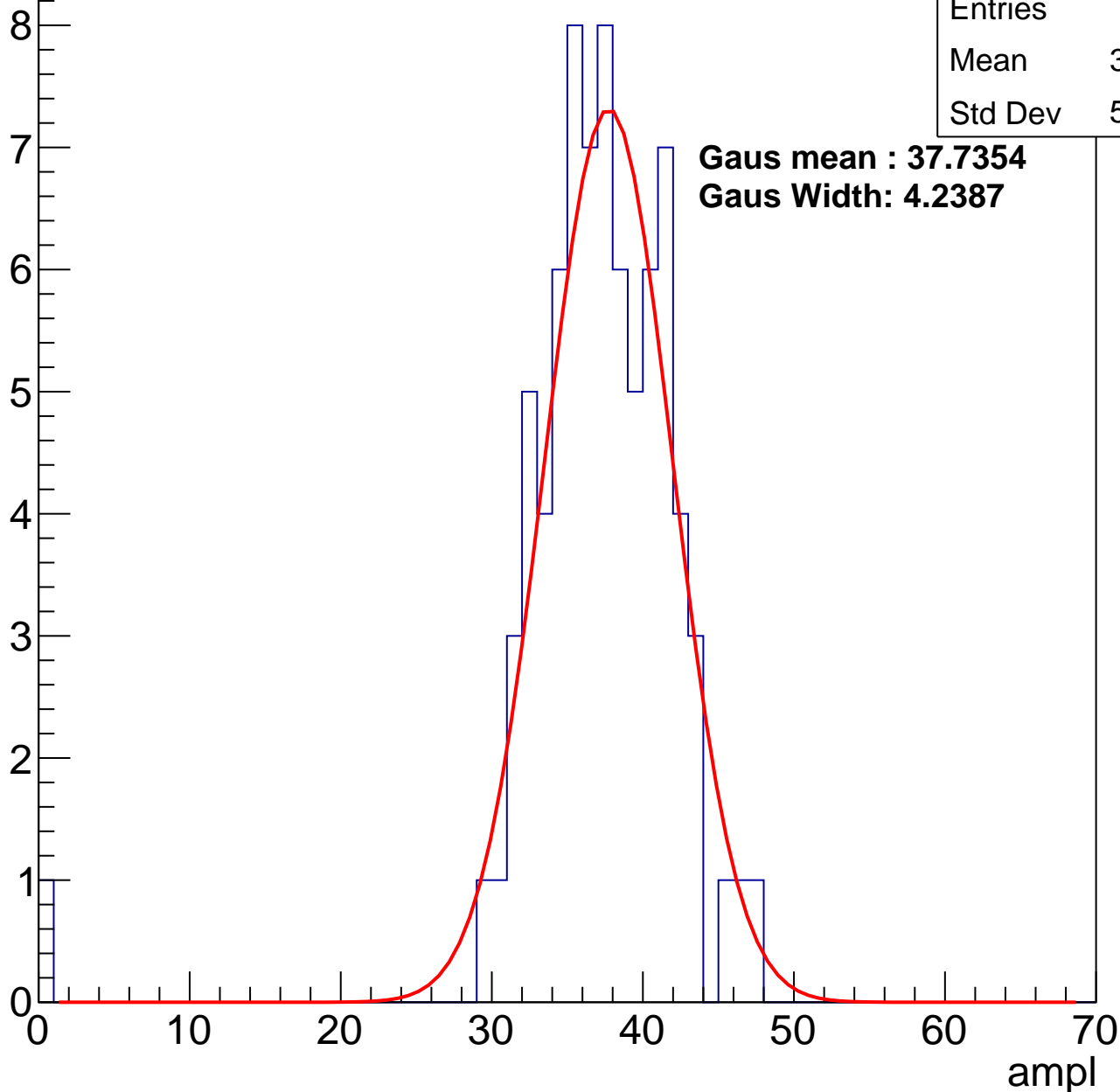
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	36.68
Std Dev	5.672

Gaus mean : 37.7354

Gaus Width: 4.2387



B1L103S, U18-ch79, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	63
Mean	35.78
Std Dev	16.8

Gaus mean : 44.3001
Gaus Width: 3.5957

Entry

10

8

6

4

2

0

0

10

20

30

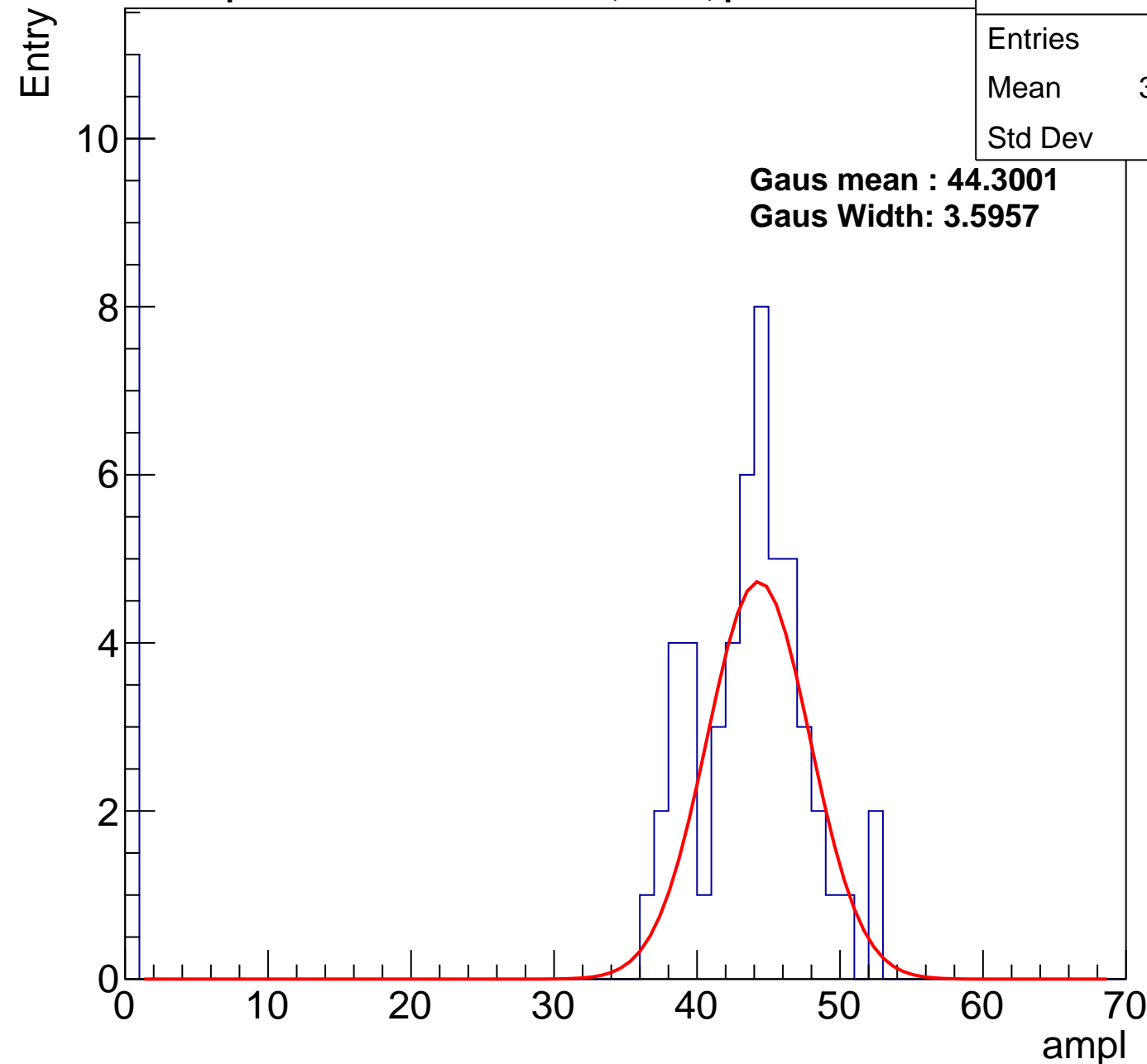
40

50

60

70

ampl

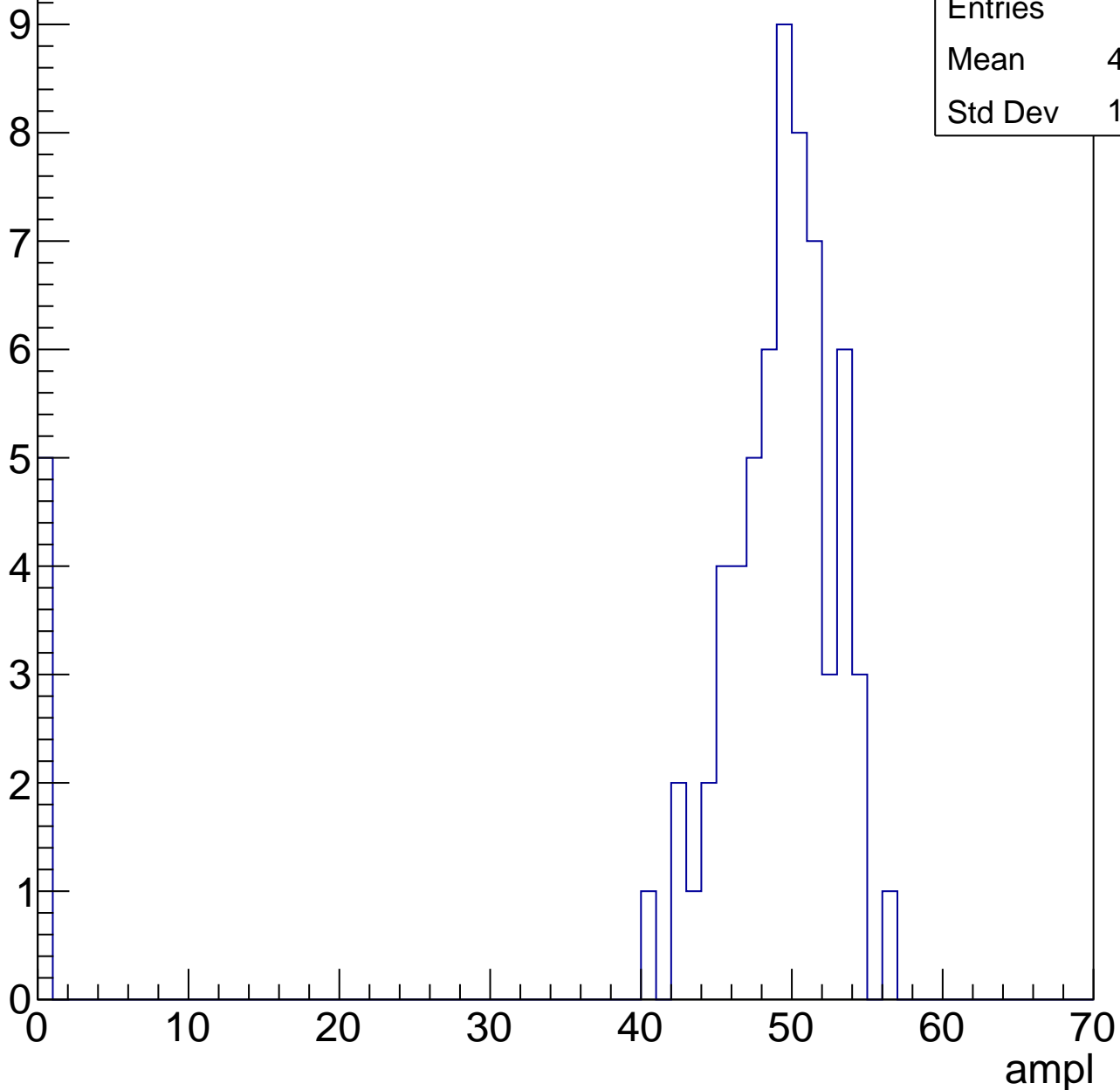


B1L103S, U18-ch79, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	45.25
Std Dev	13.24



B1L103S, U18-ch79, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	55.9
Std Dev	3.39

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

0

2

4

6

8

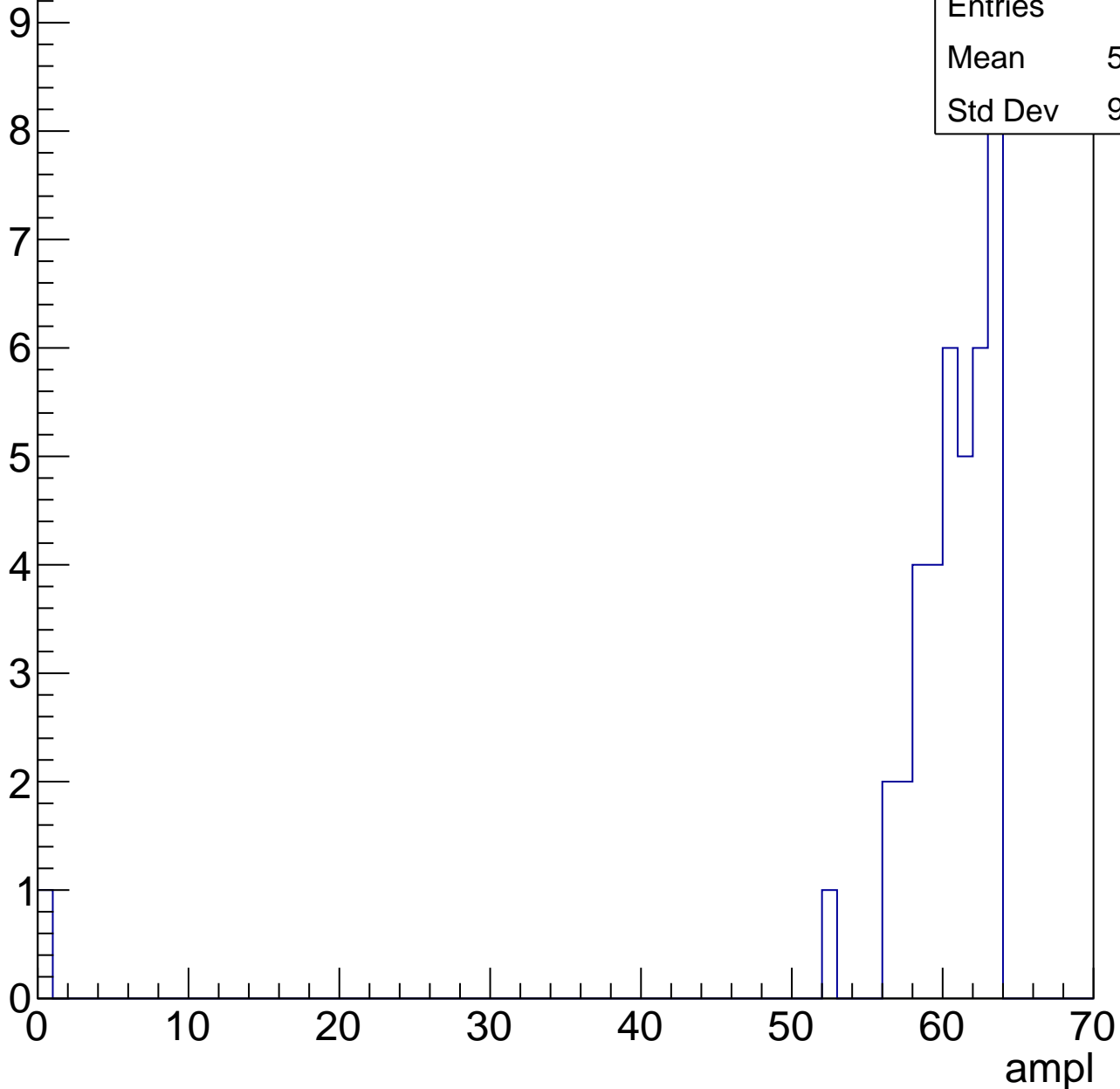
10

B1L103S, U18-ch79, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	58.75
Std Dev	9.723



B1L103S, U18-ch79, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U18-ch79, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

B1L103S, U18-ch80, adc0

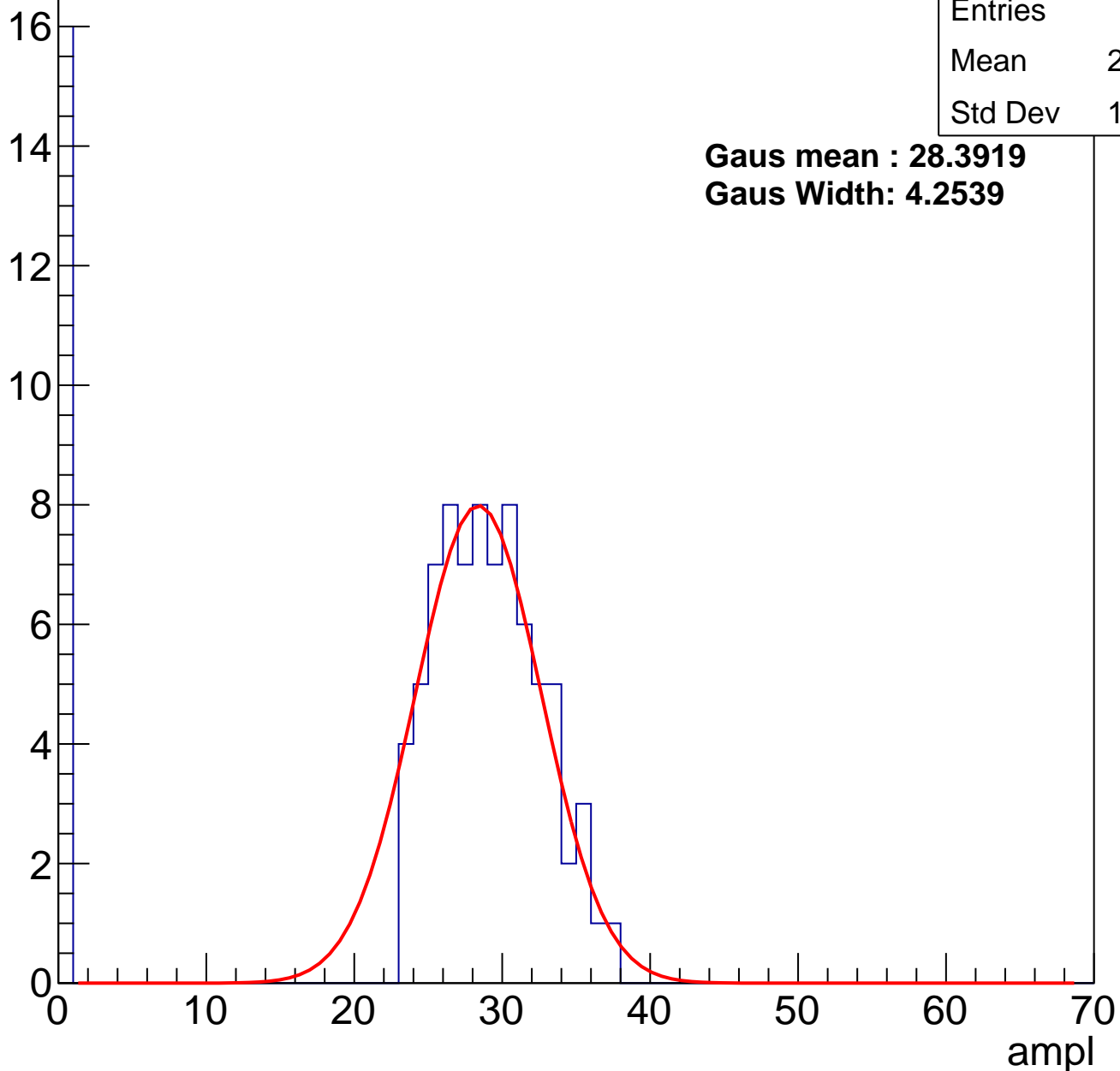
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	23.74
Std Dev	11.27

Gaus mean : 28.3919

Gaus Width: 4.2539

Entry



B1L103S, U18-ch80, adc1

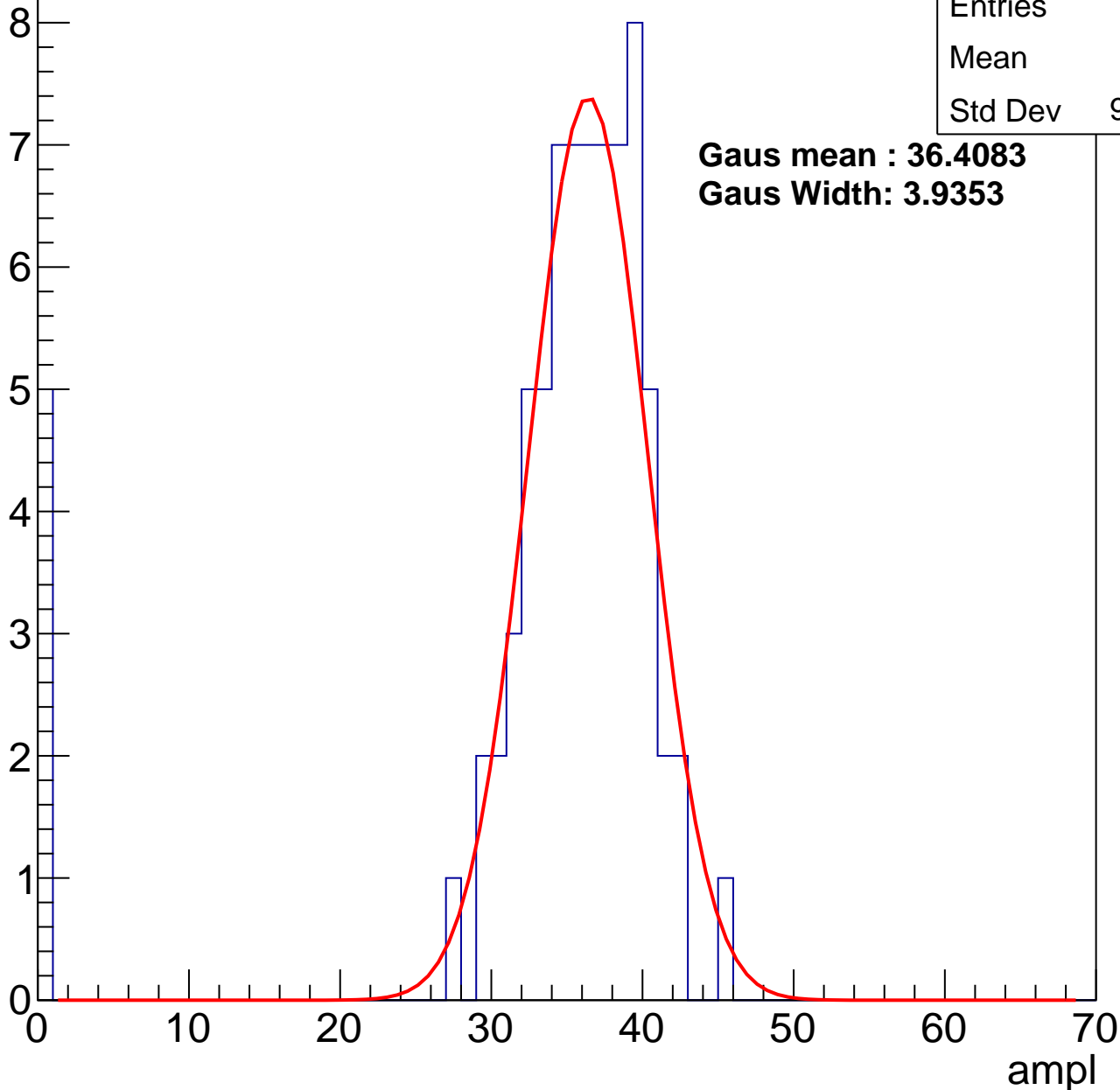
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	33.5
Std Dev	9.515

Gaus mean : 36.4083

Gaus Width: 3.9353



B1L103S, U18-ch80, adc2

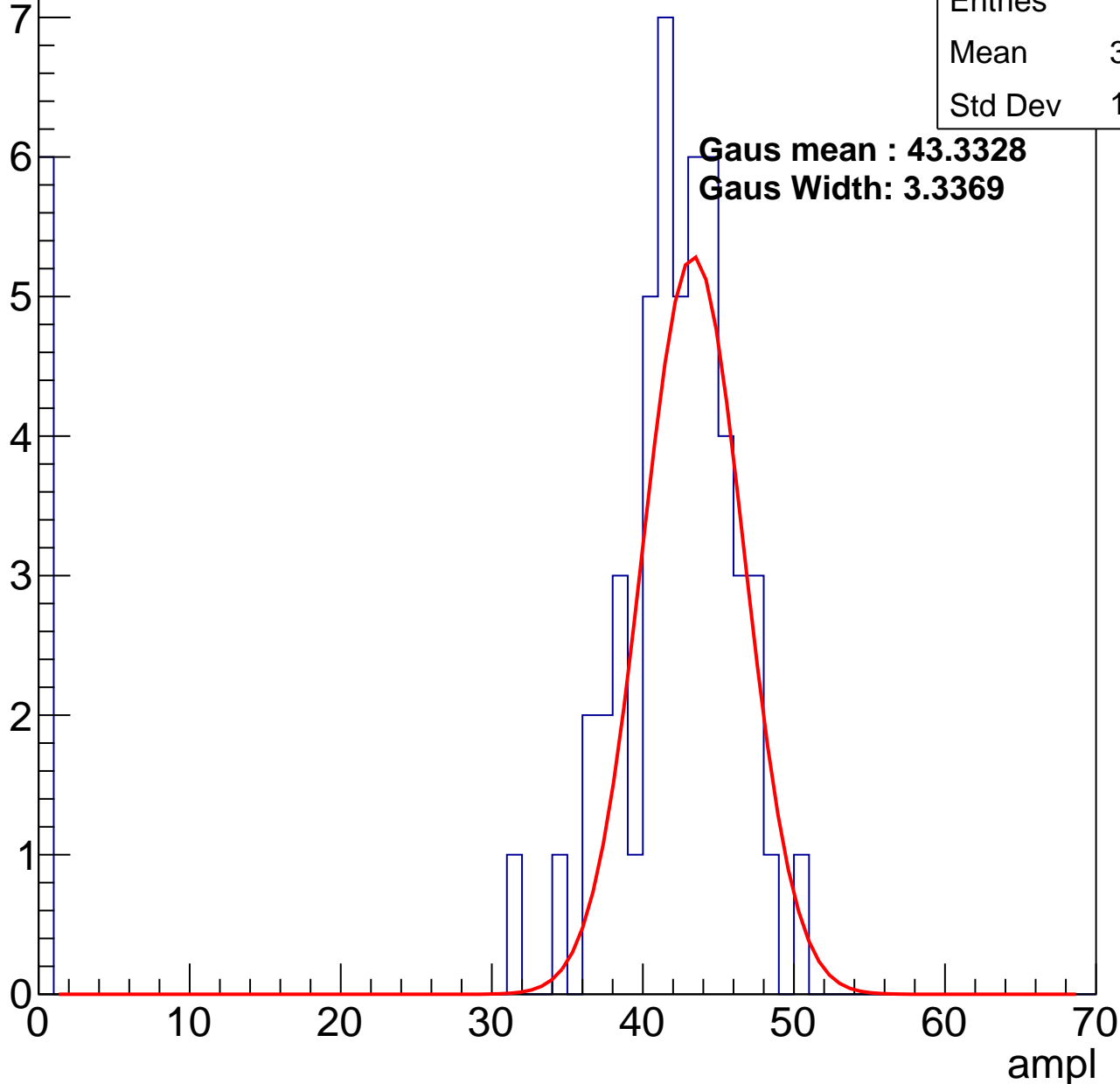
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	37.54
Std Dev	13.33

Gaus mean : 43.3328

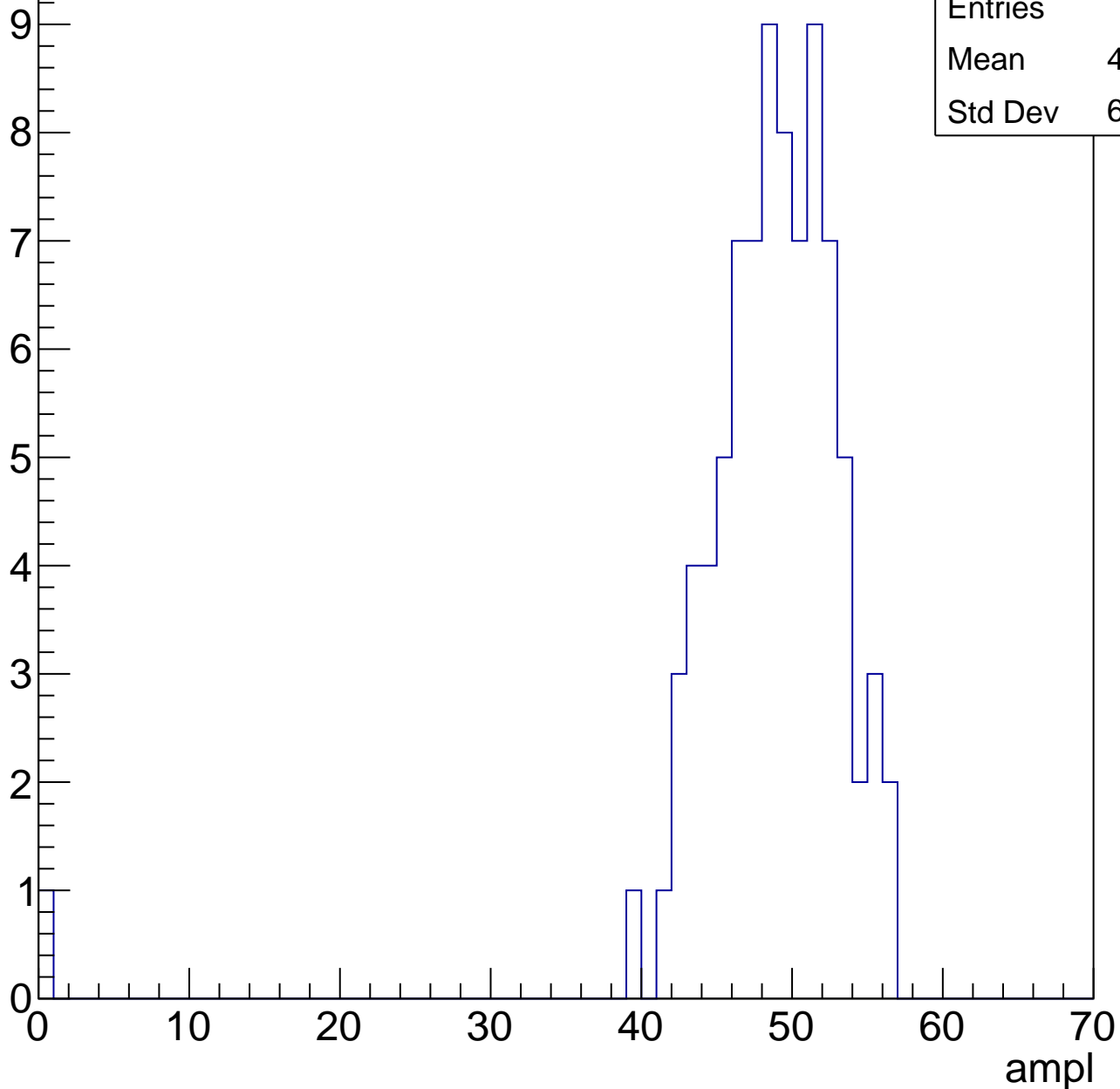
Gaus Width: 3.3369



B1L103S, U18-ch80, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

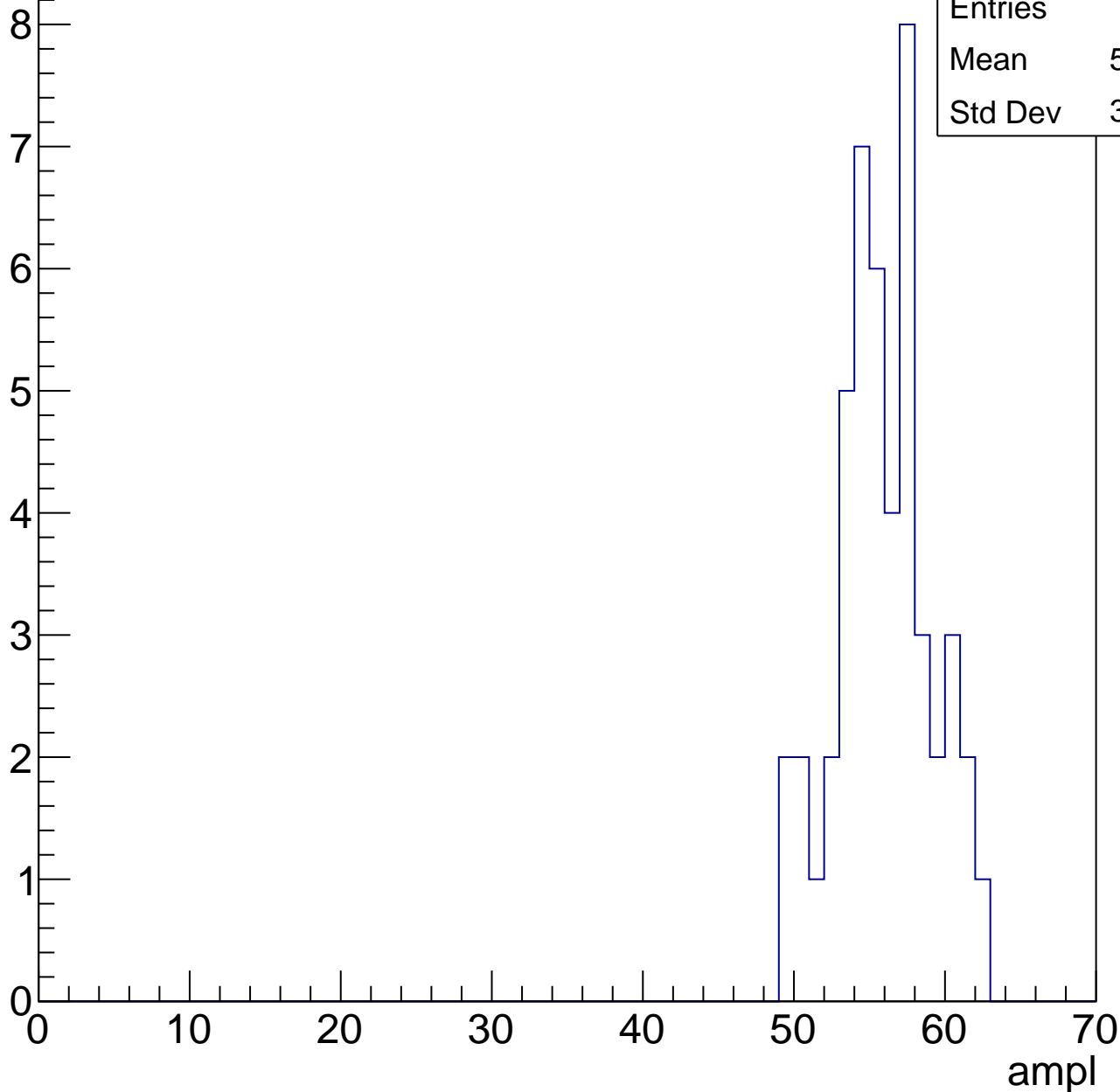


B1L103S, U18-ch80, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	55.46
Std Dev	3.109

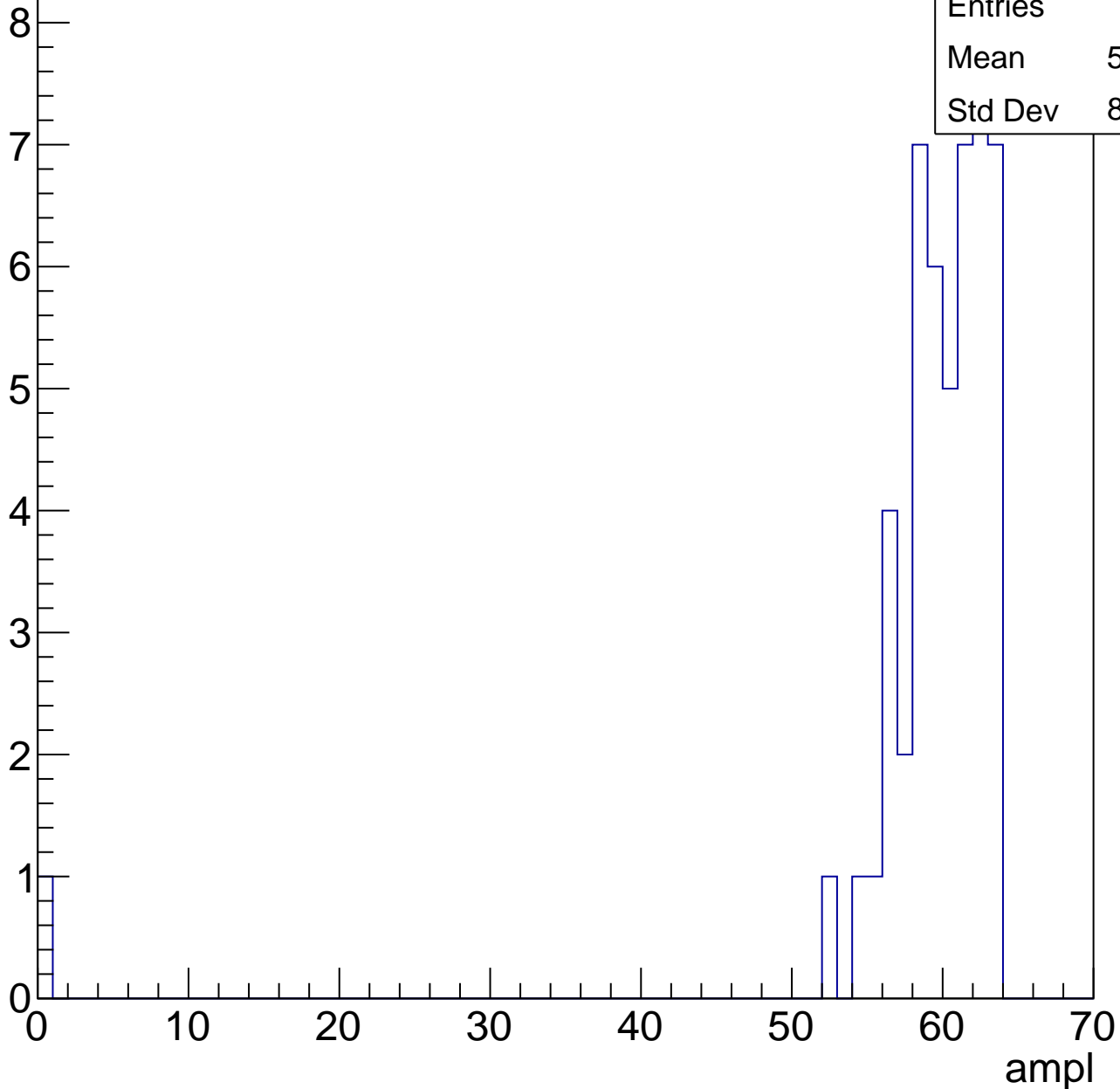


B1L103S, U18-ch80, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	58.46
Std Dev	8.746



B1L103S, U18-ch80, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	8
Mean	60.88
Std Dev	1.536

B1L103S, U18-ch80, adc7

calib_packv5_041523_1651.root, FC#0, port C2

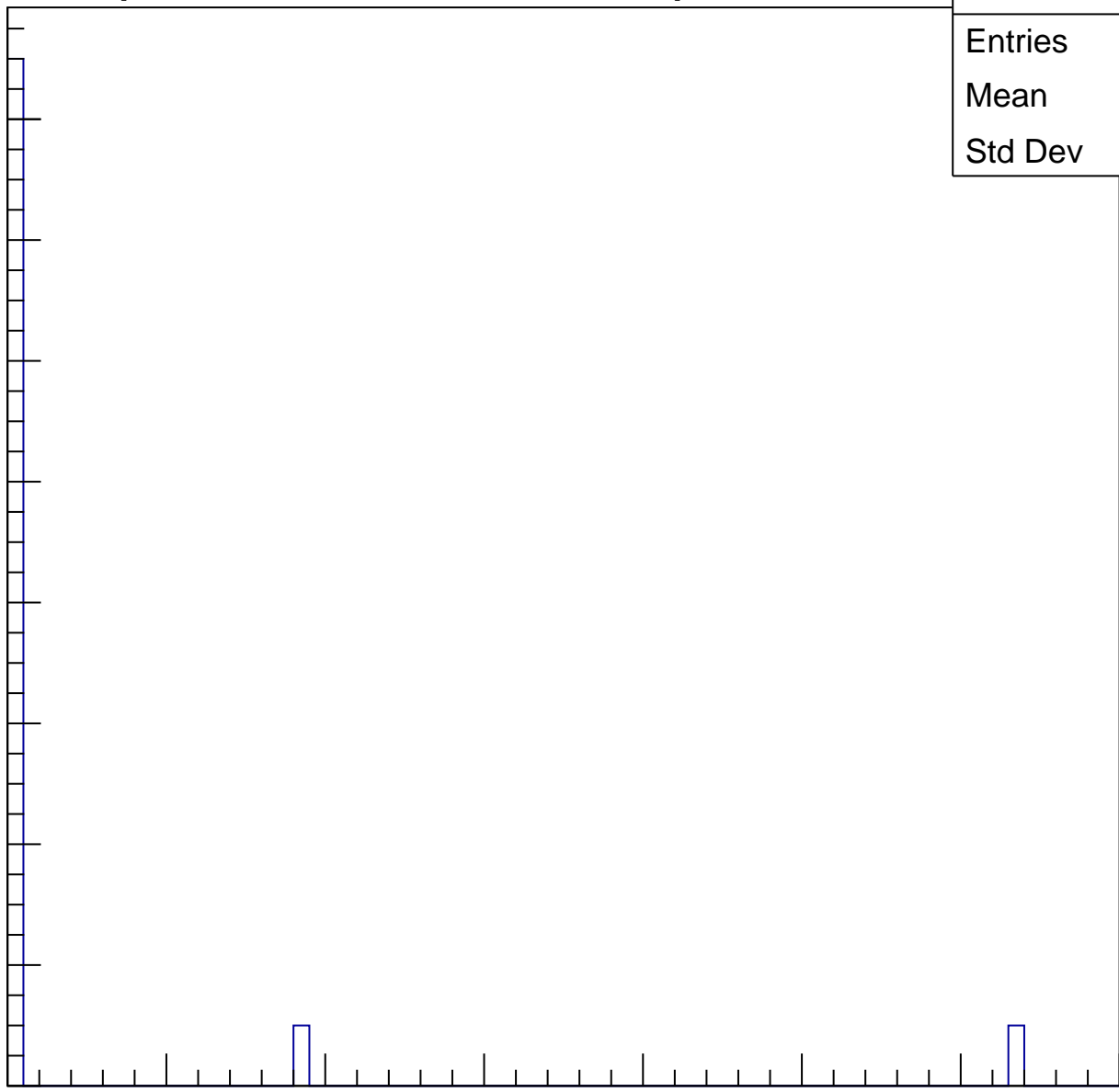
Entries	19
Mean	4.263
Std Dev	14.41

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U18-ch81, adc0

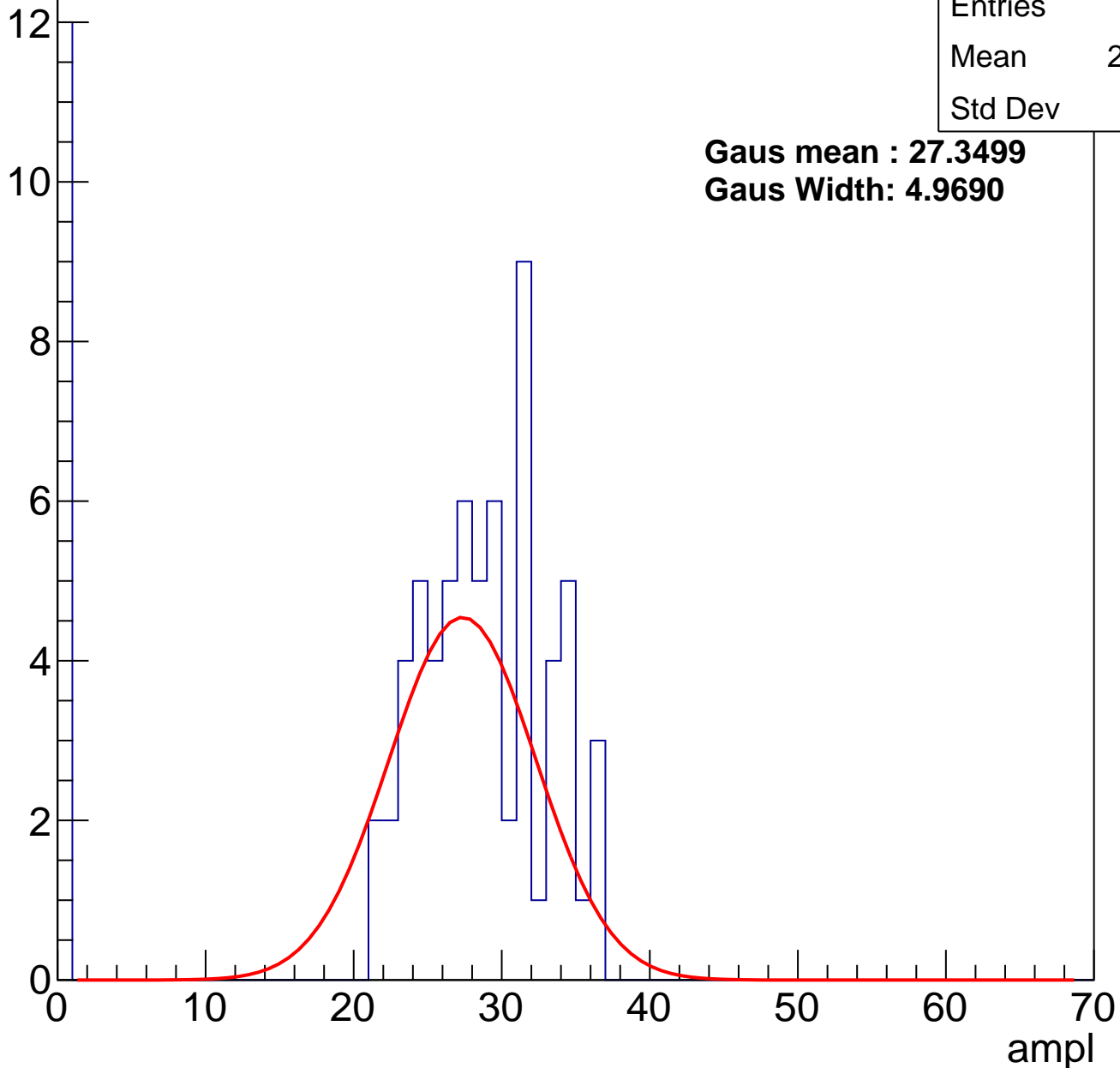
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	23.95
Std Dev	11

Gaus mean : 27.3499

Gaus Width: 4.9690

Entry



B1L103S, U18-ch81, adc1

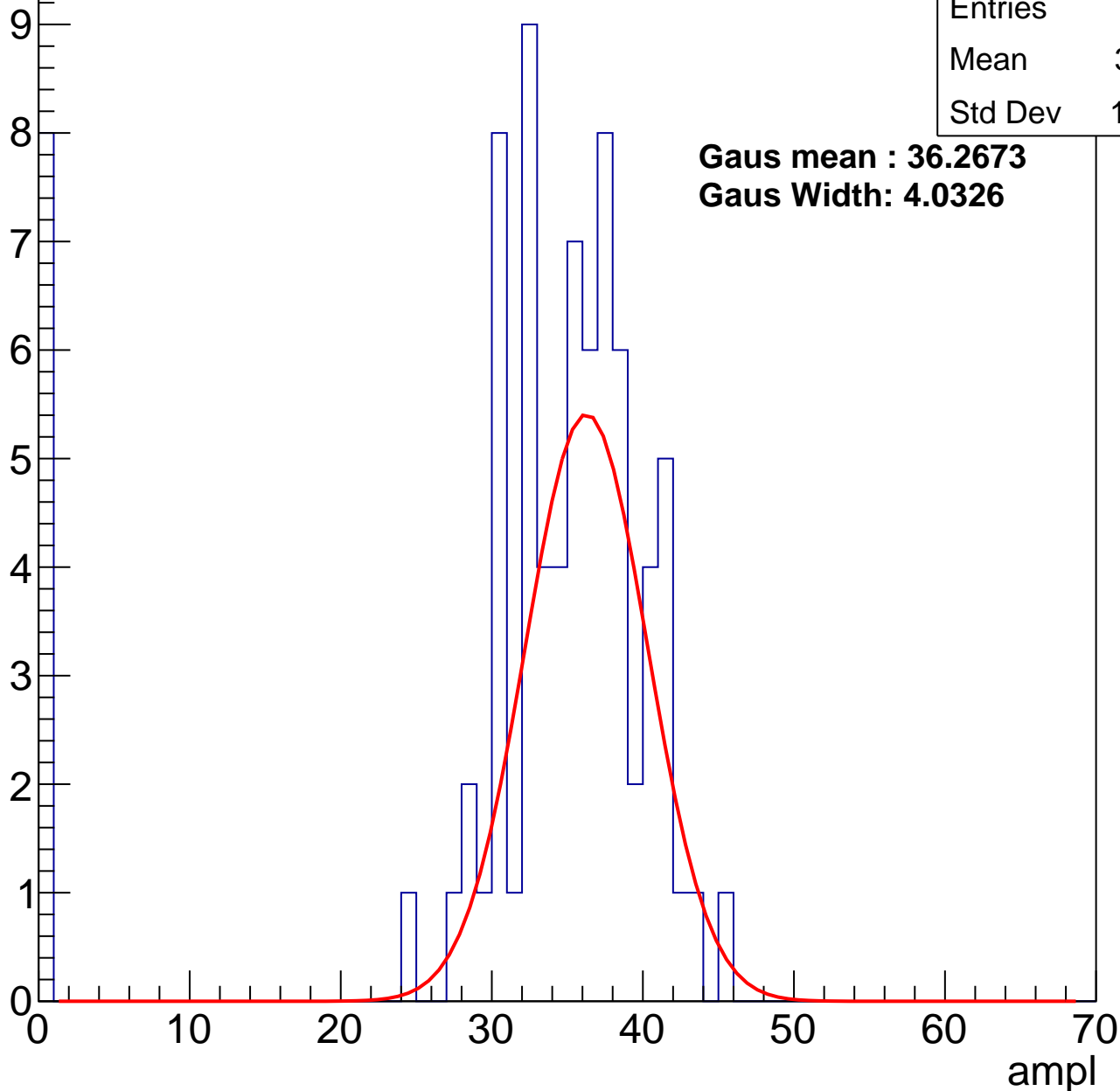
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	31.51
Std Dev	11.22

Gaus mean : 36.2673

Gaus Width: 4.0326



B1L103S, U18-ch81, adc2

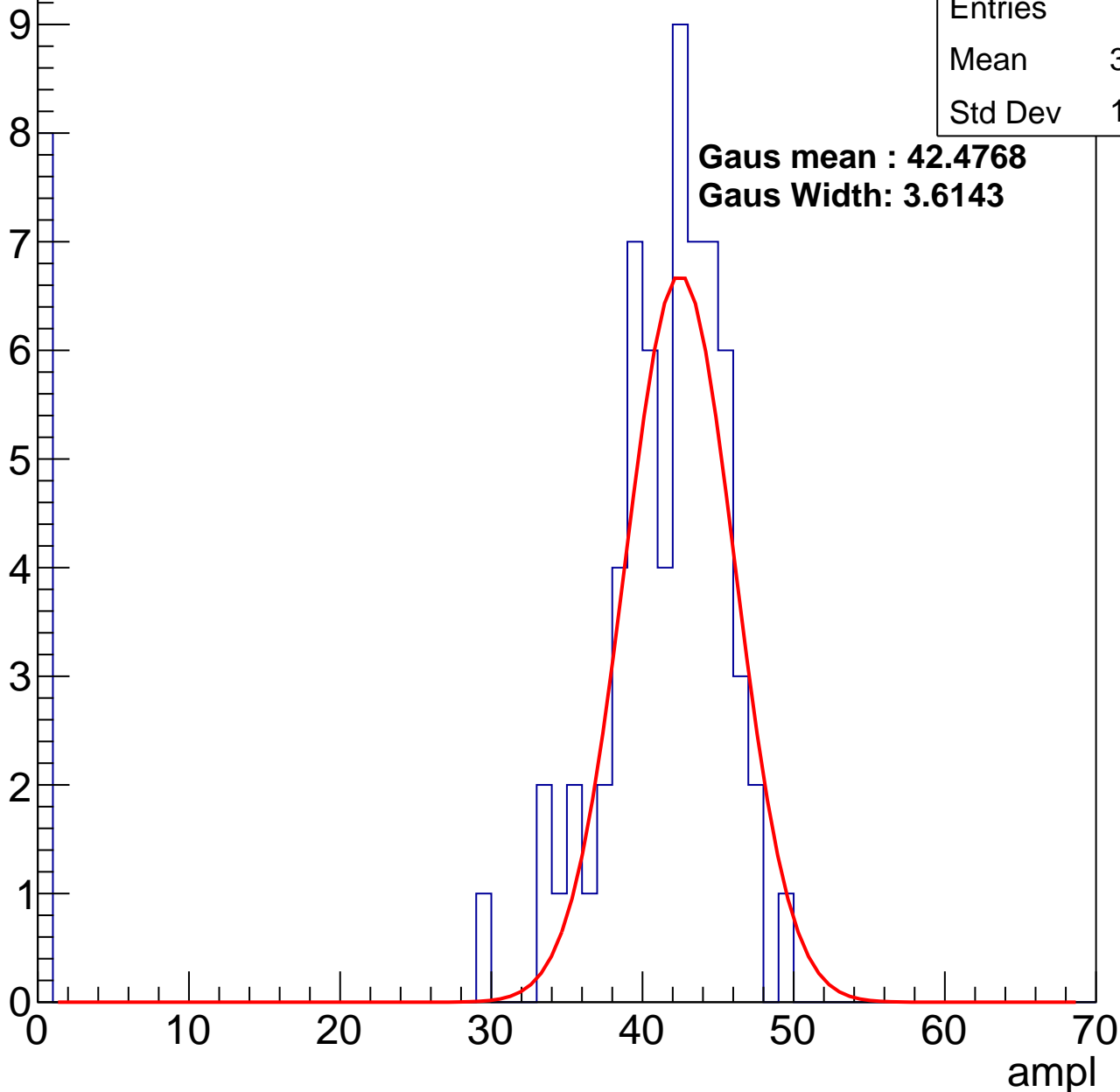
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	36.66
Std Dev	13.34

Gaus mean : 42.4768

Gaus Width: 3.6143

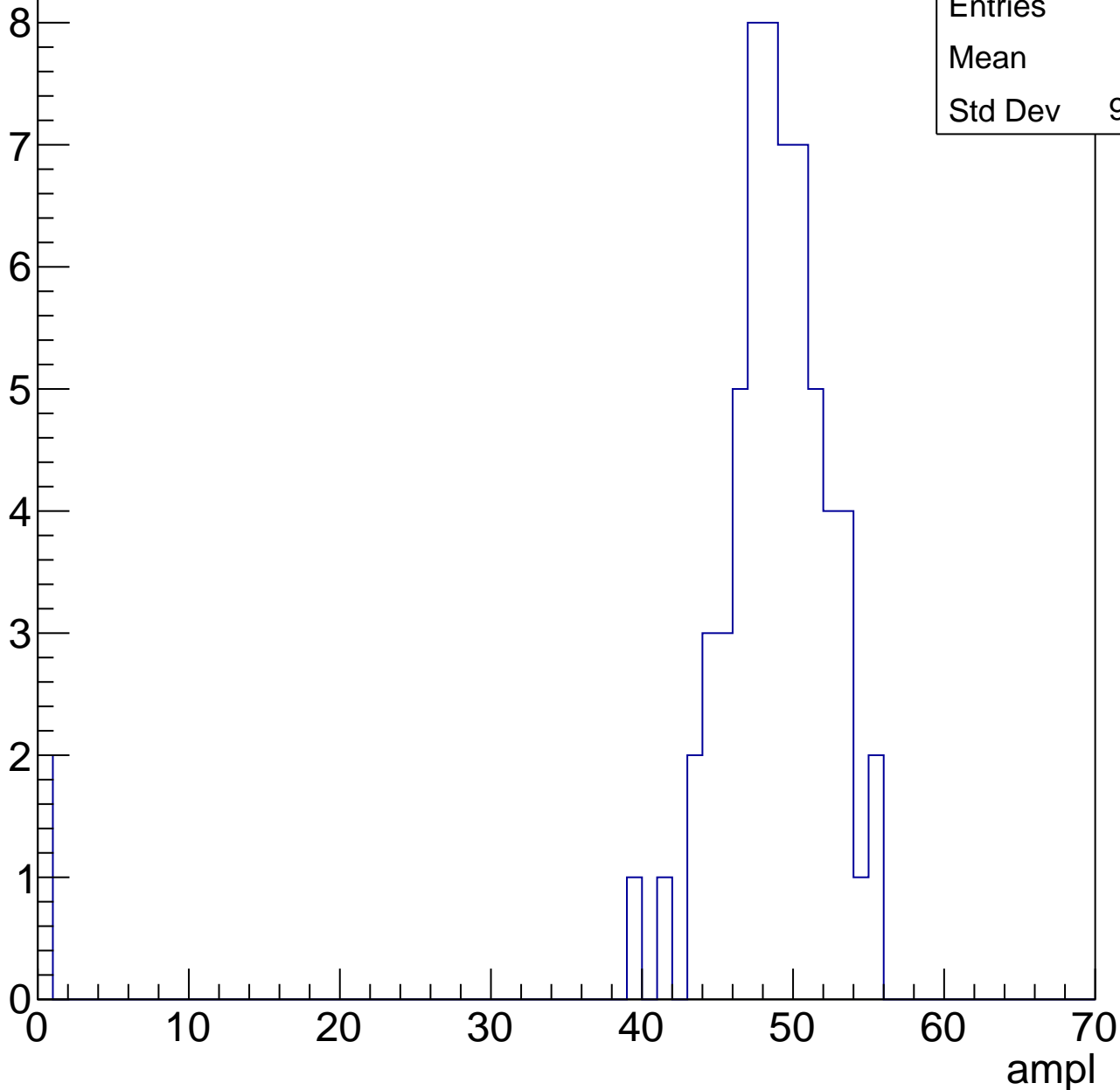


B1L103S, U18-ch81, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	46.9
Std Dev	9.079

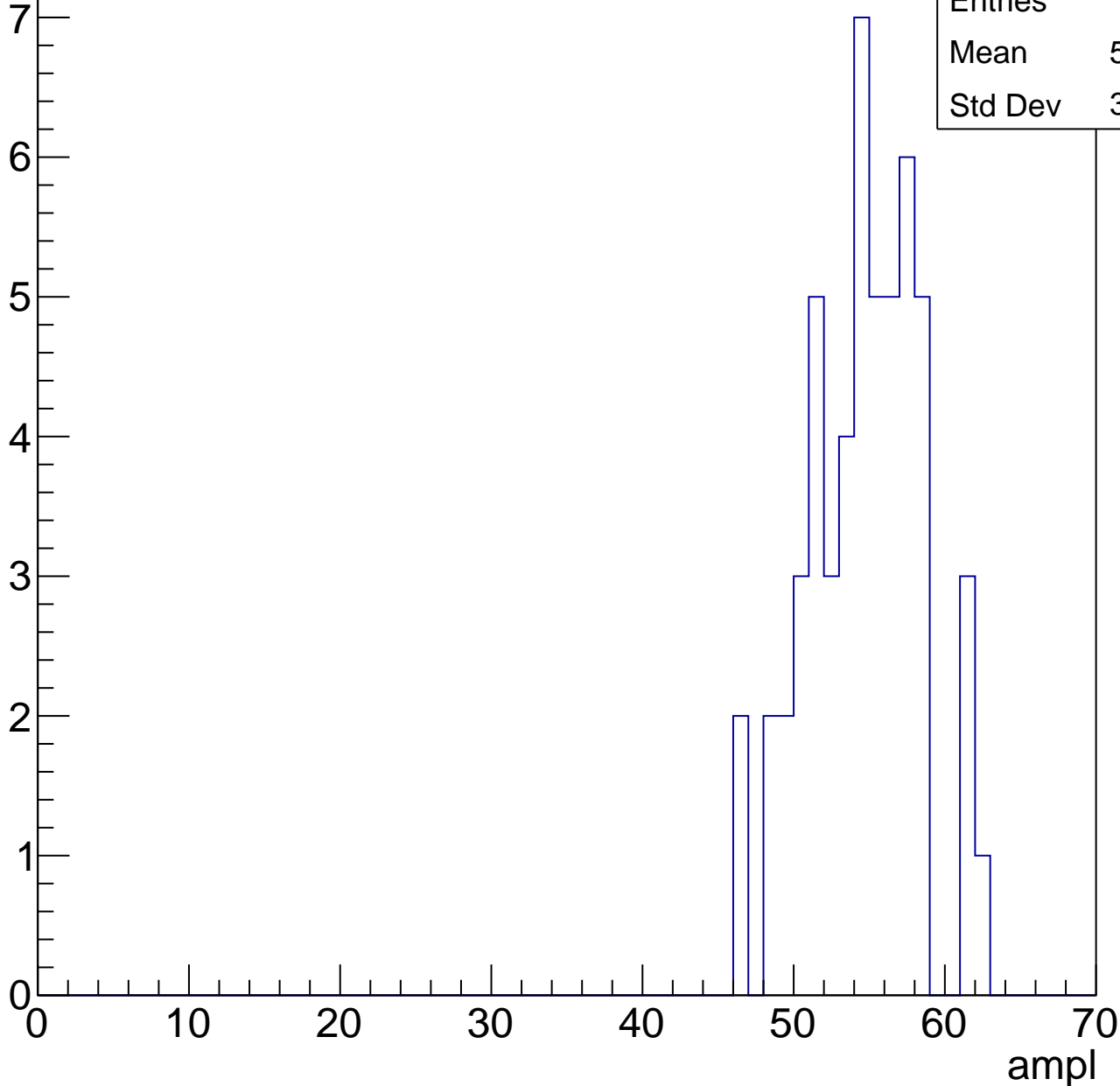


B1L103S, U18-ch81, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

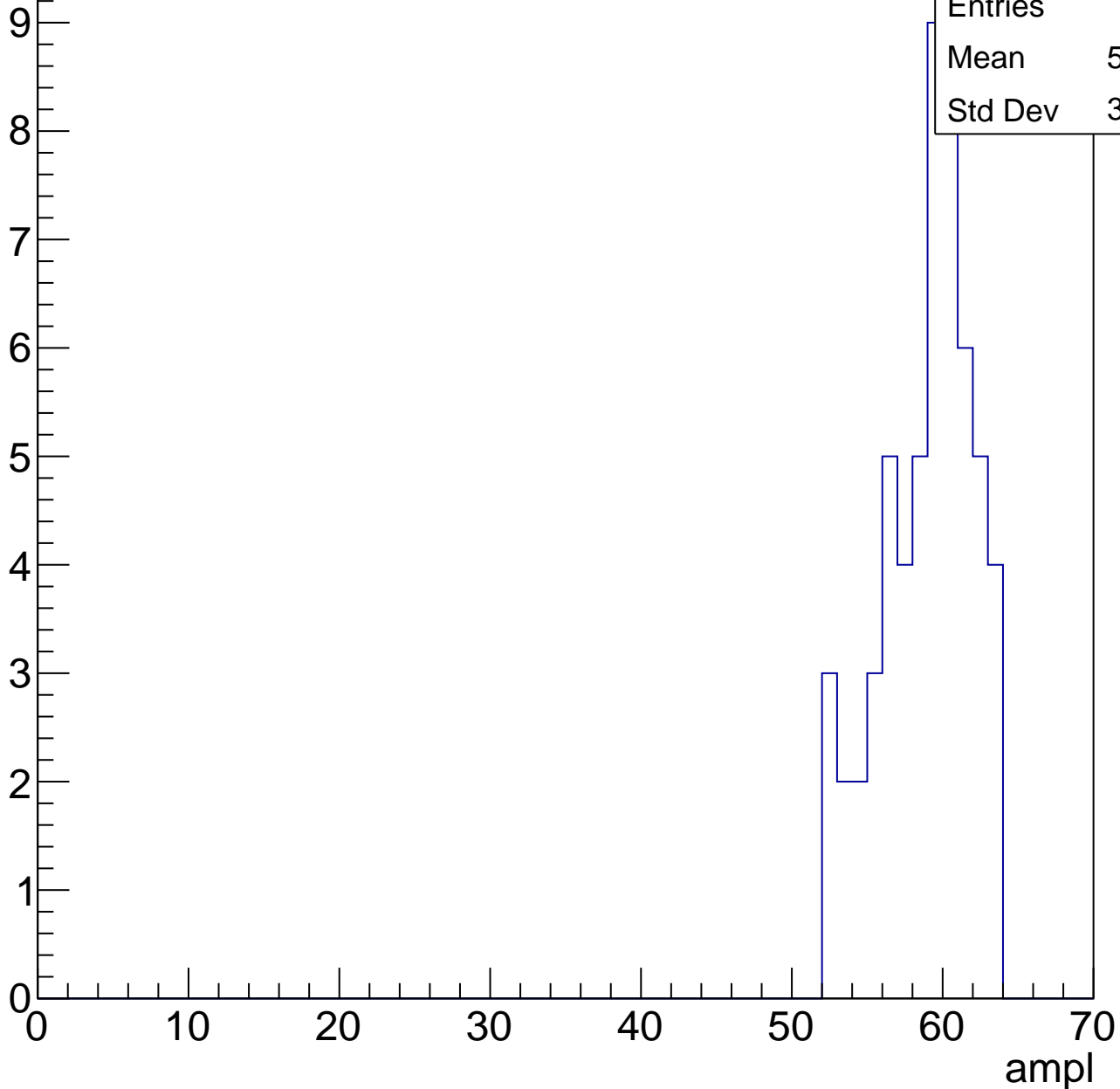
Entries	53
Mean	54.13
Std Dev	3.706



B1L103S, U18-ch81, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



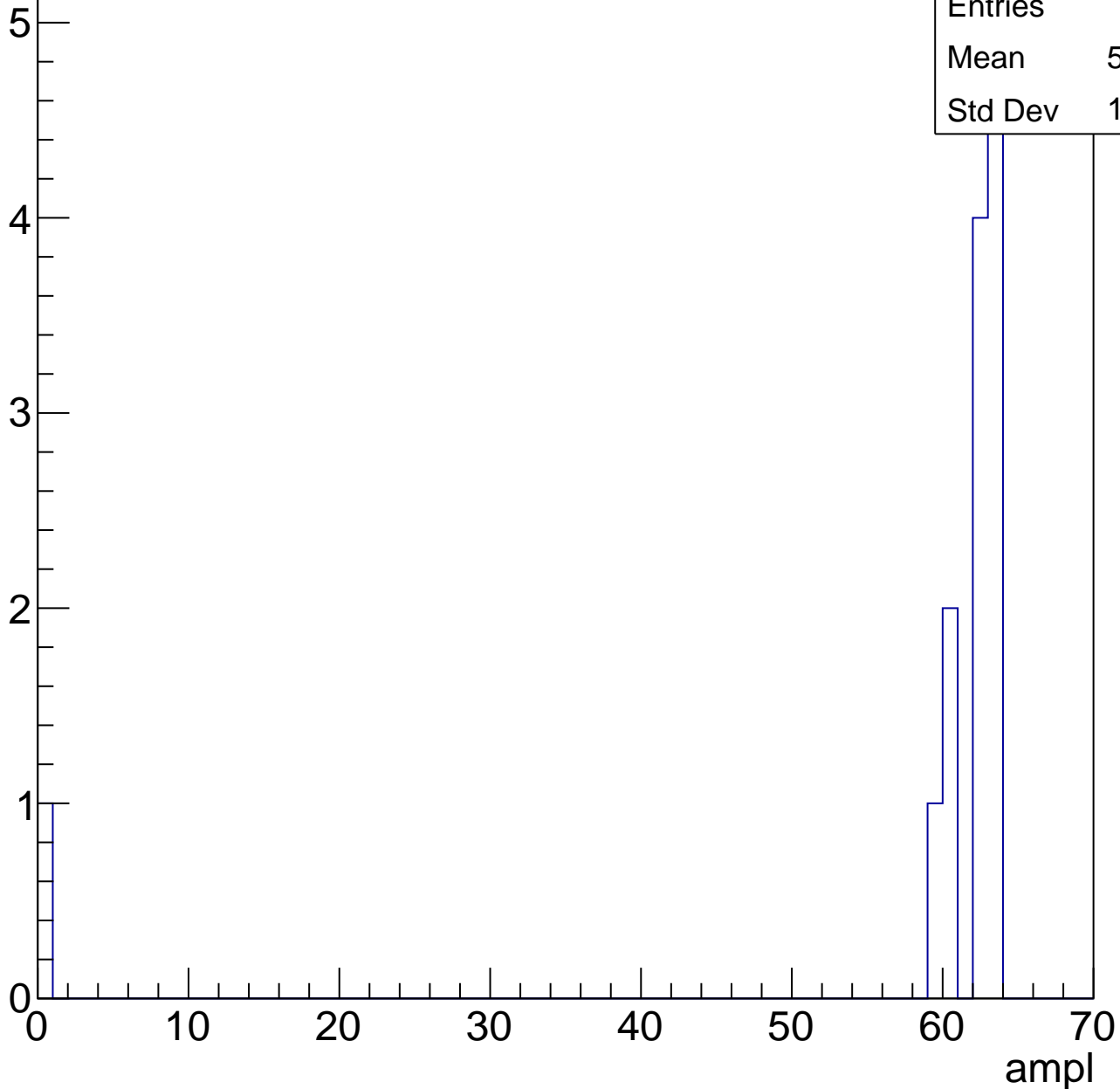
Entries	56
Mean	58.43
Std Dev	3.005

B1L103S, U18-ch81, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.08
Std Dev	16.53

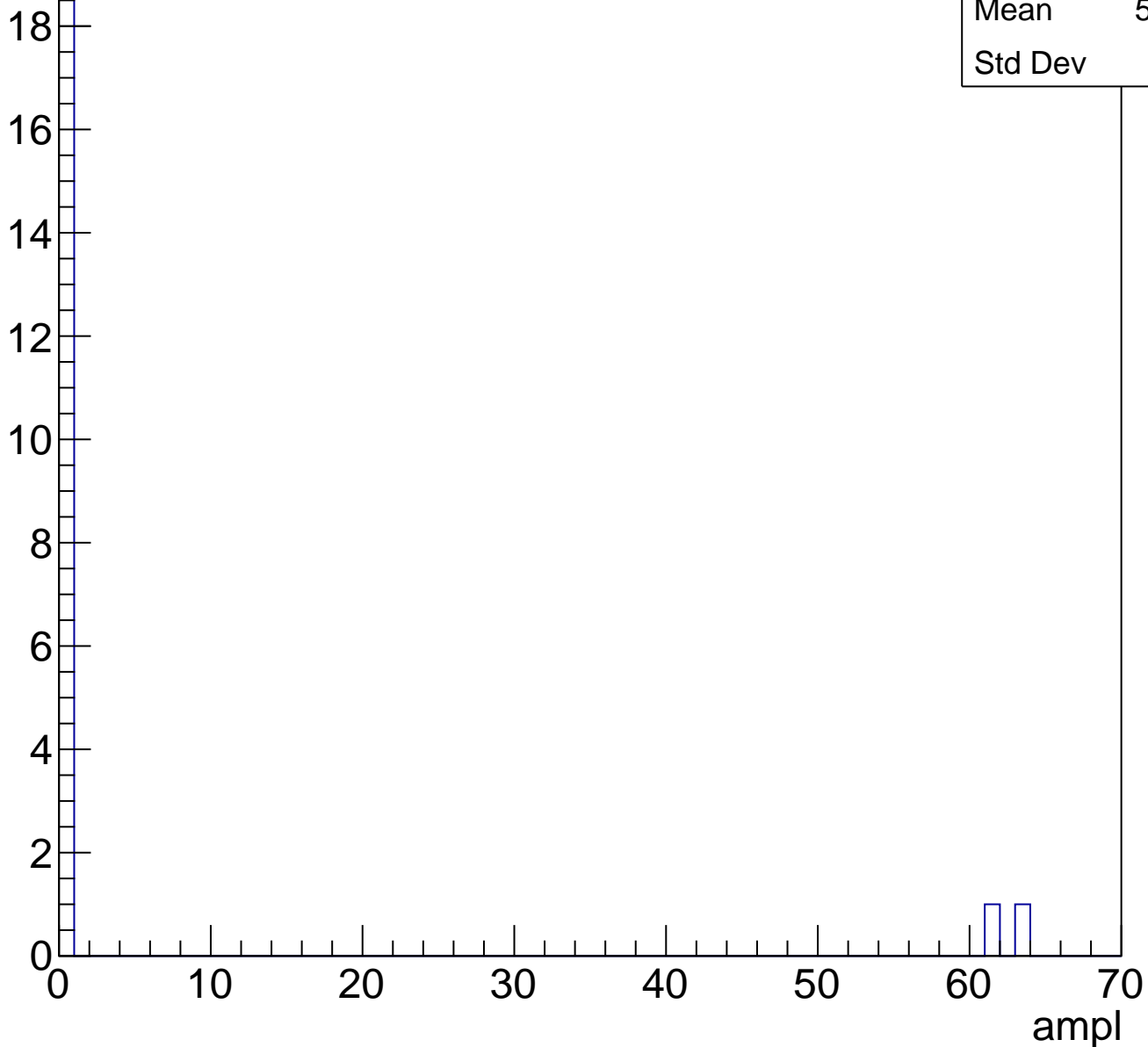


B1L103S, U18-ch81, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.905
Std Dev	18.2

Entry



B1L103S, U18-ch82, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	183
Mean	11.63
Std Dev	12.52

Gaus mean : 26.9225

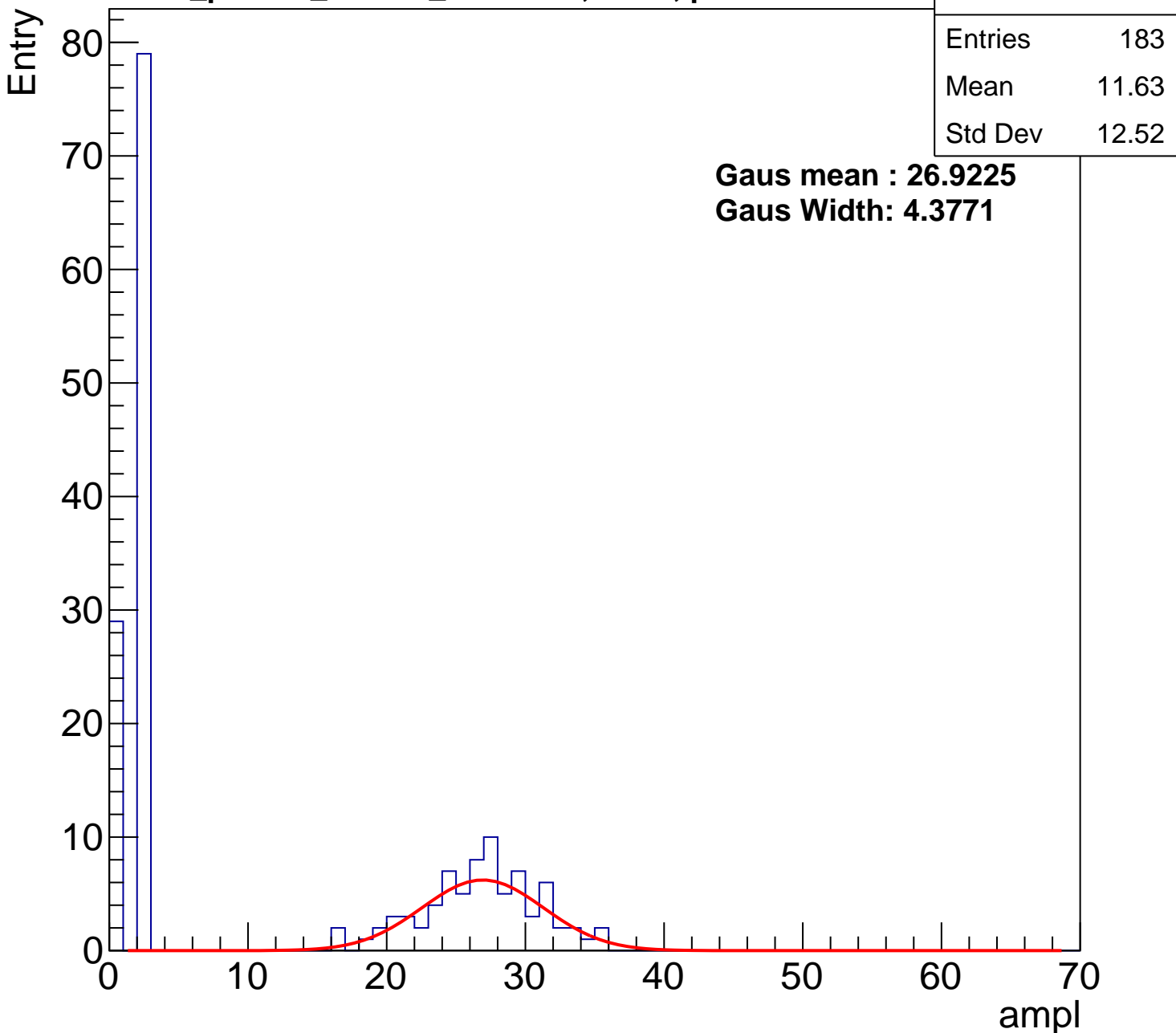
Gaus Width: 4.3771

Entry

80
70
60
50
40
30
20
10
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U18-ch82, adc1

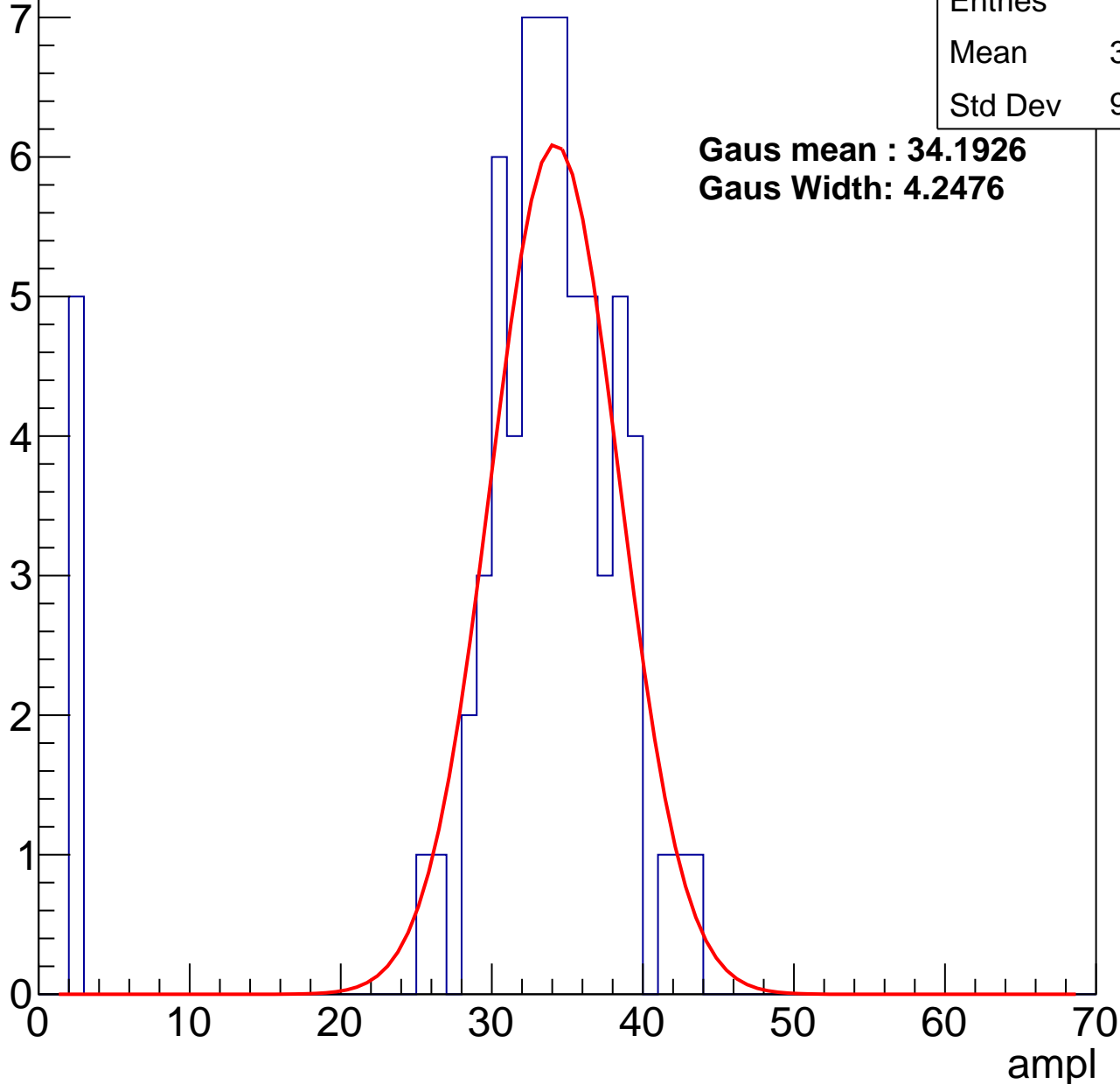
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	31.46
Std Dev	9.046

Gaus mean : 34.1926

Gaus Width: 4.2476



B1L103S, U18-ch82, adc2

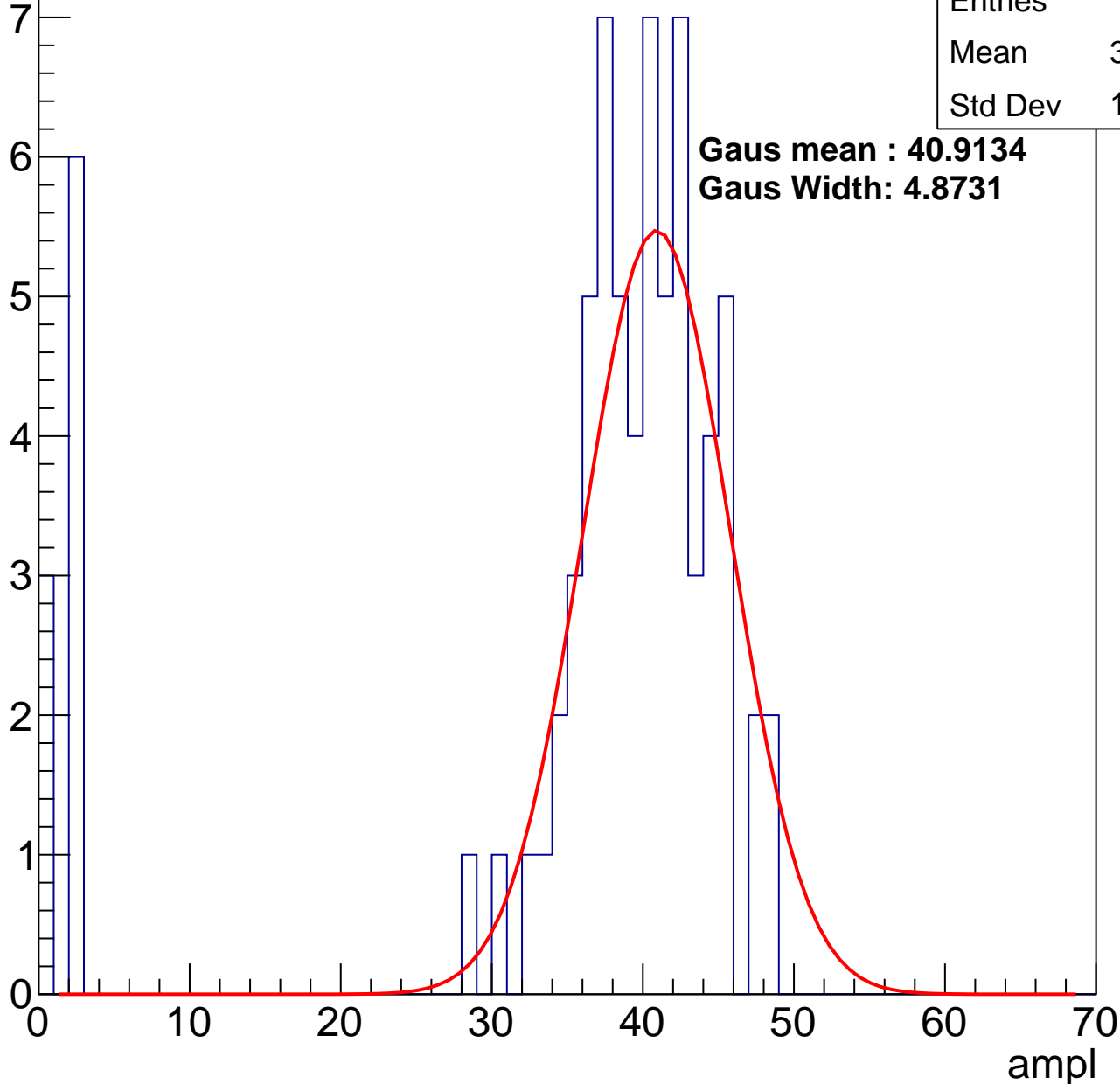
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	35.03
Std Dev	13.14

Gaus mean : 40.9134

Gaus Width: 4.8731



B1L103S, U18-ch82, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	78
Mean	36.45
Std Dev	18.6

Entry

12

10

8

6

4

2

0

0

10

20

30

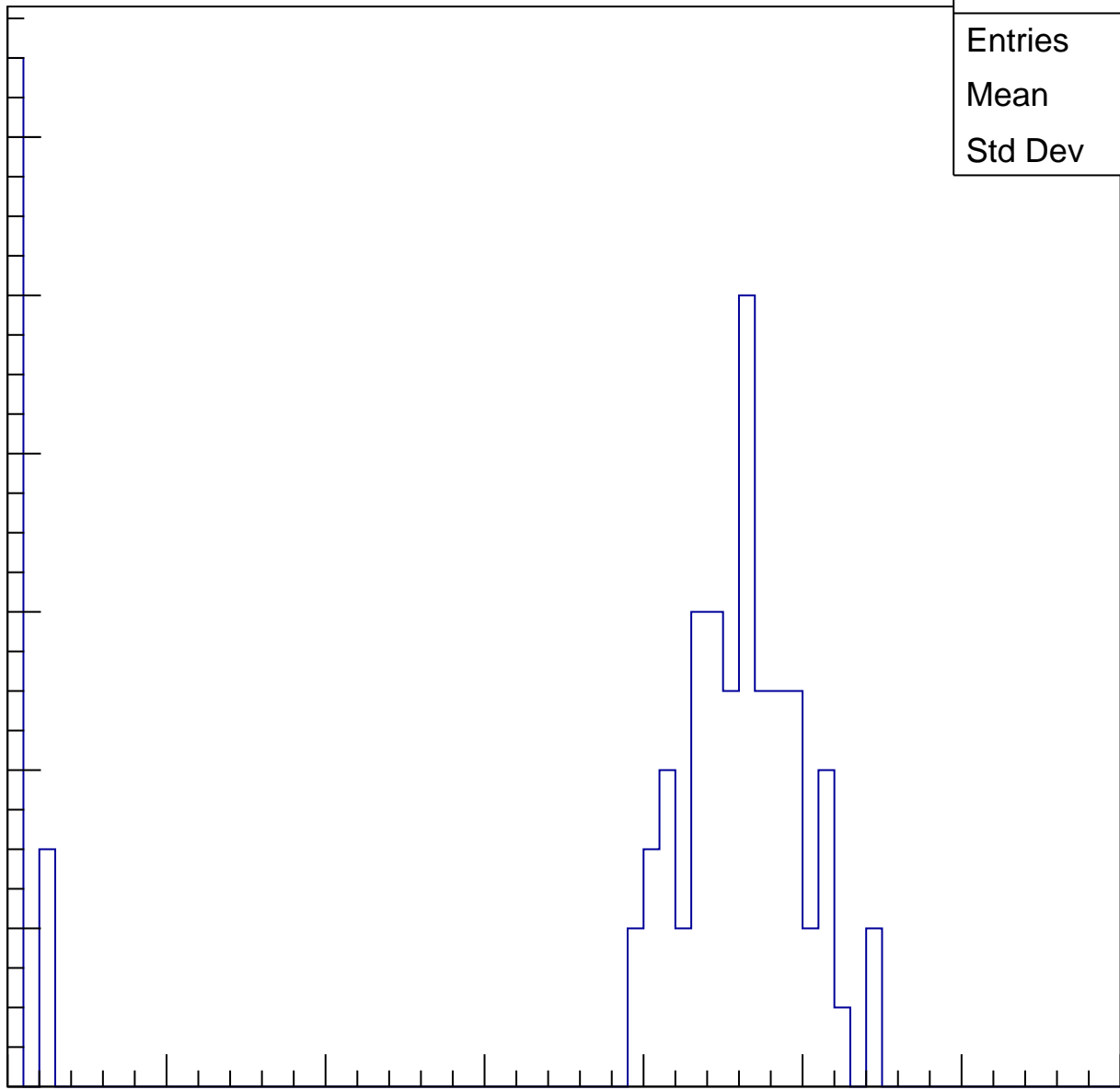
40

50

60

70

ampl

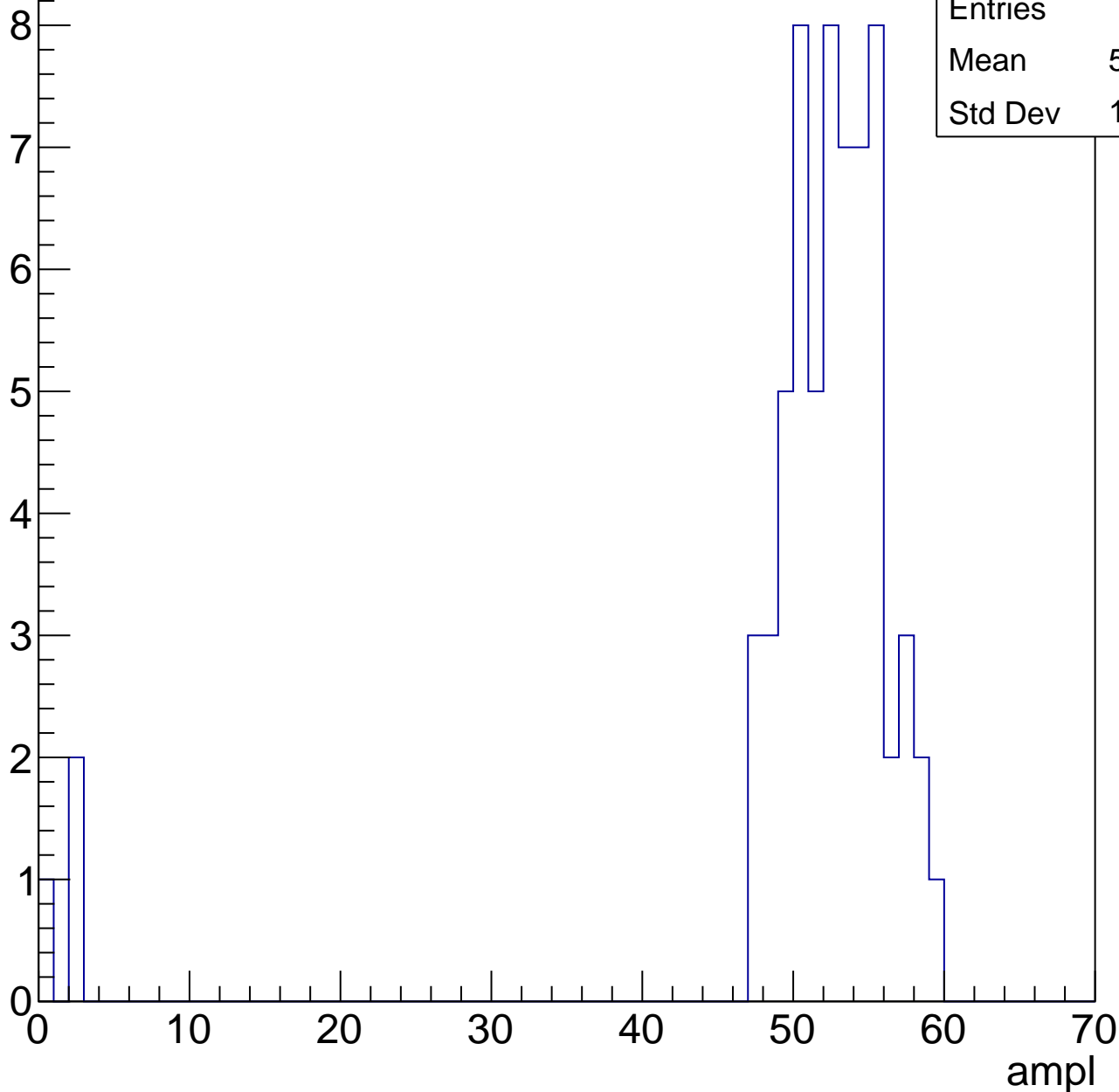


B1L103S, U18-ch82, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

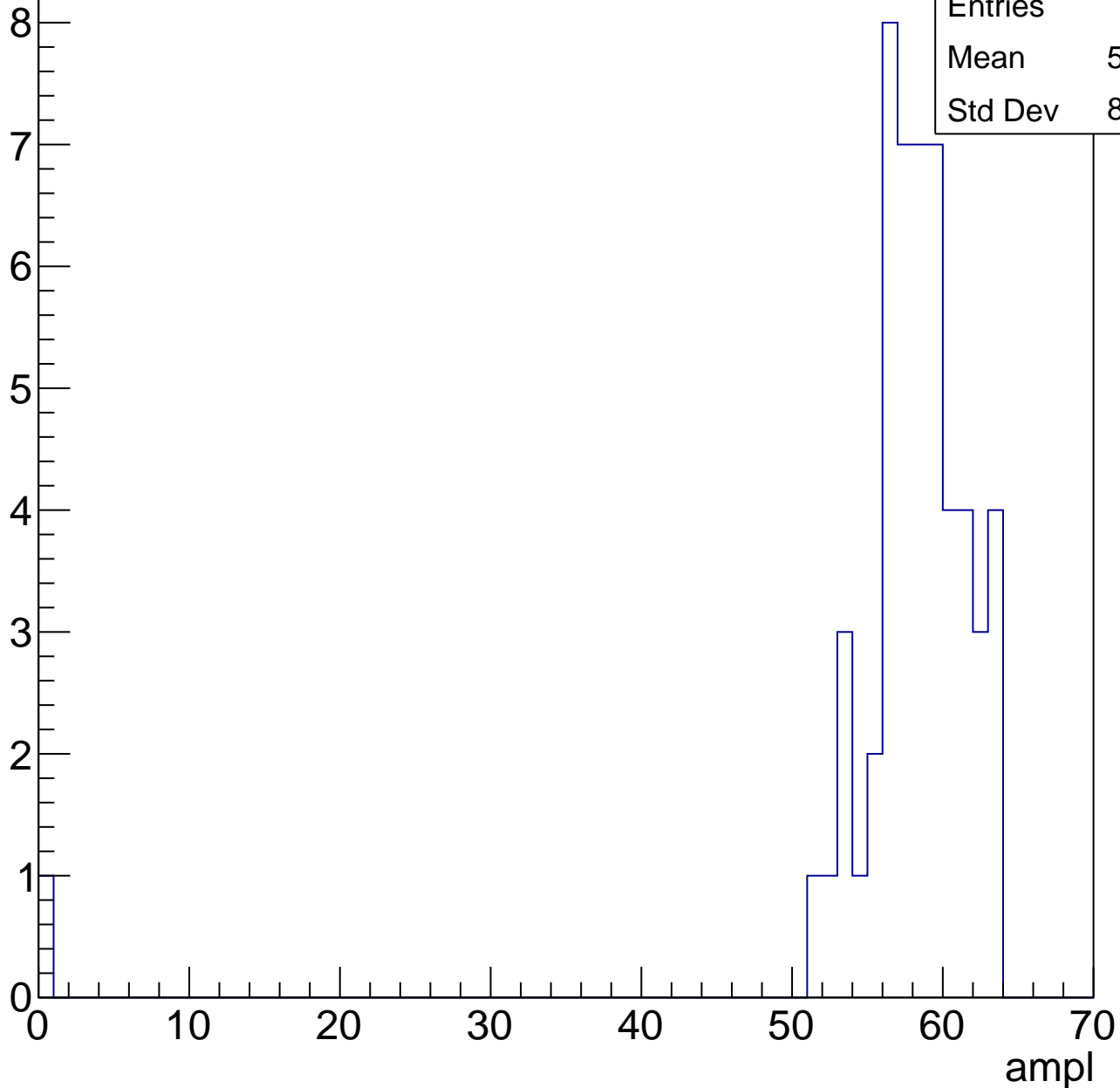
Entries	65
Mean	50.03
Std Dev	11.09



B1L103S, U18-ch82, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

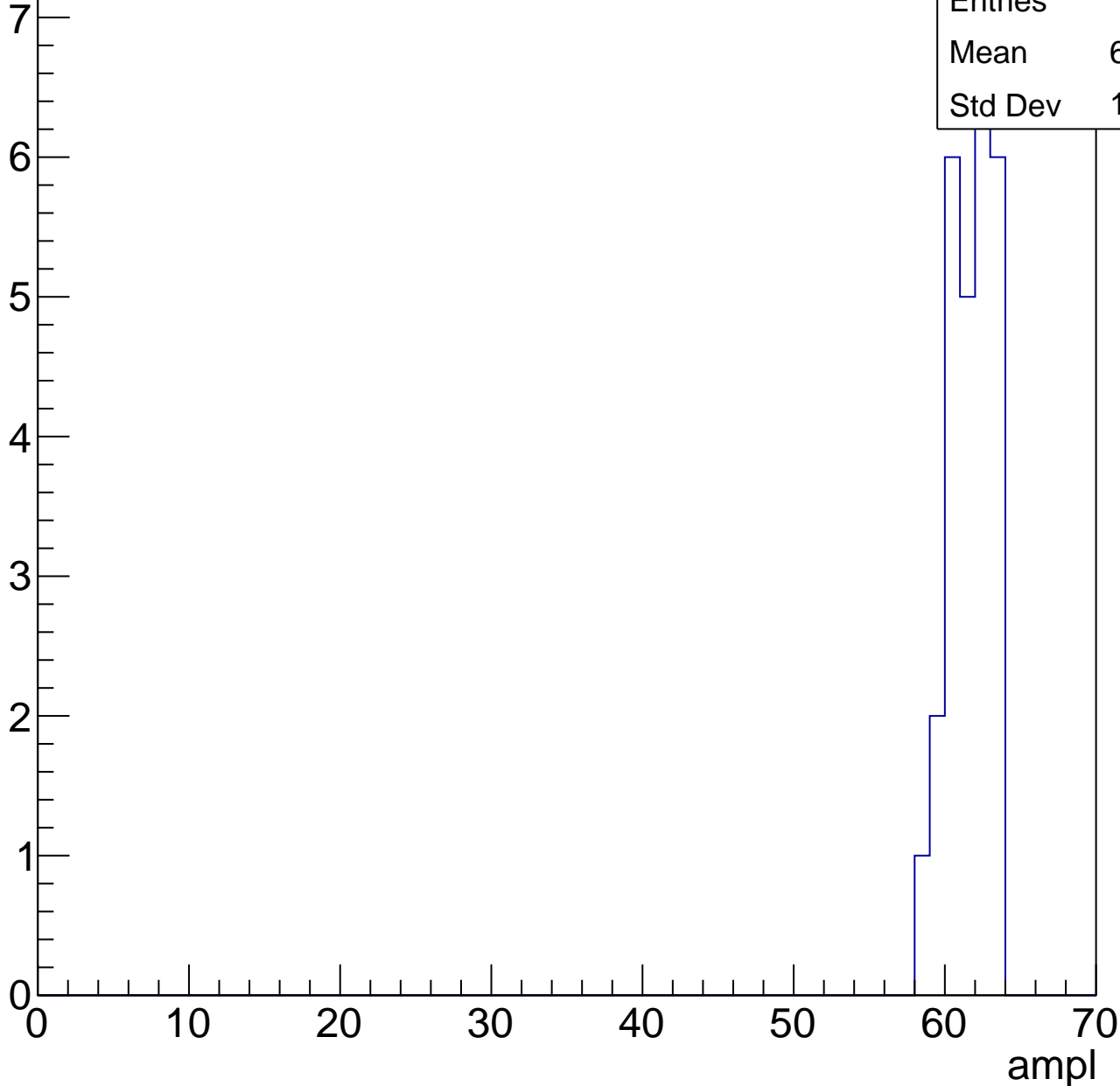


B1L103S, U18-ch82, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	61.22
Std Dev	1.397

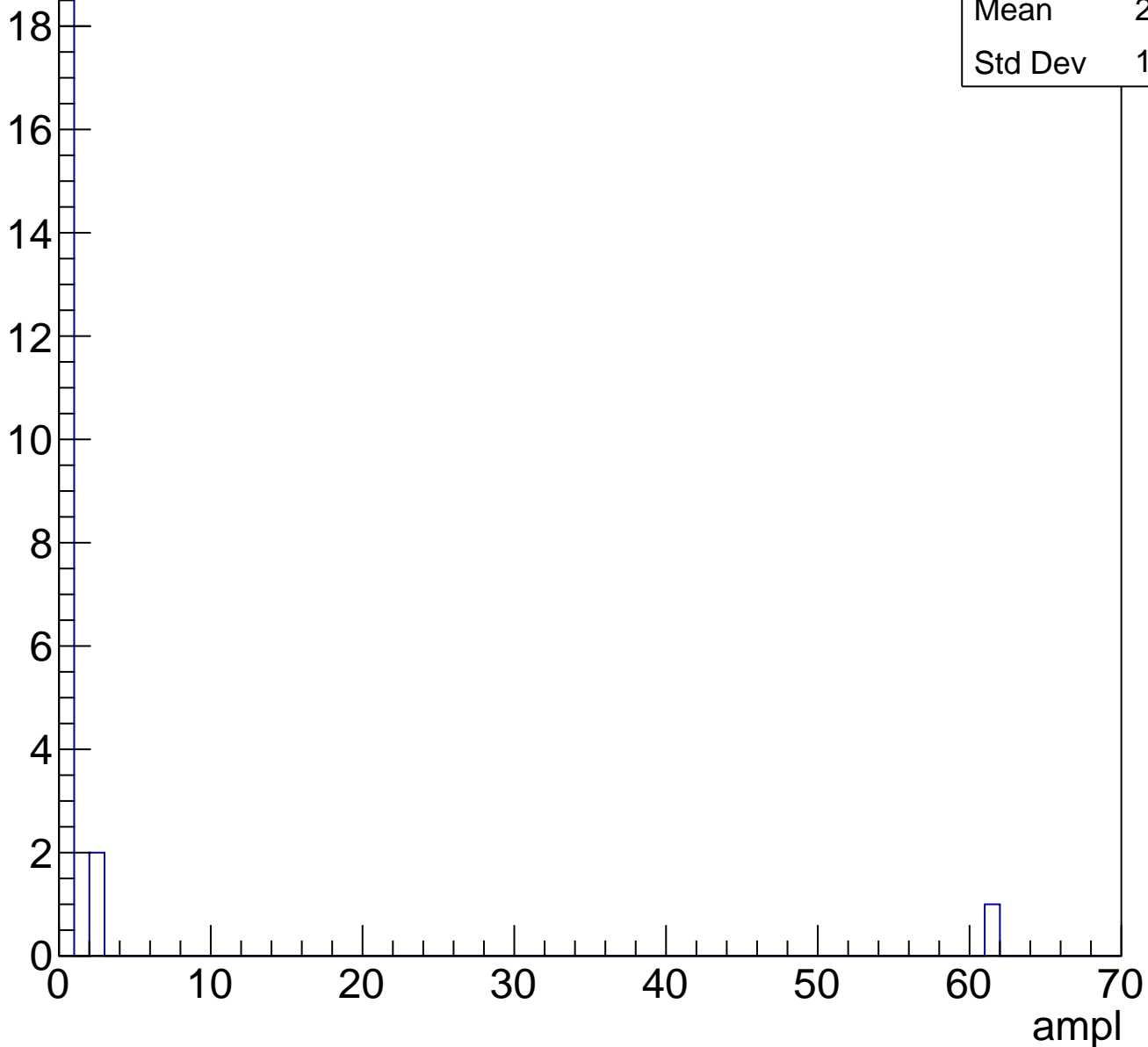


B1L103S, U18-ch82, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	2.955
Std Dev	12.68

Entry



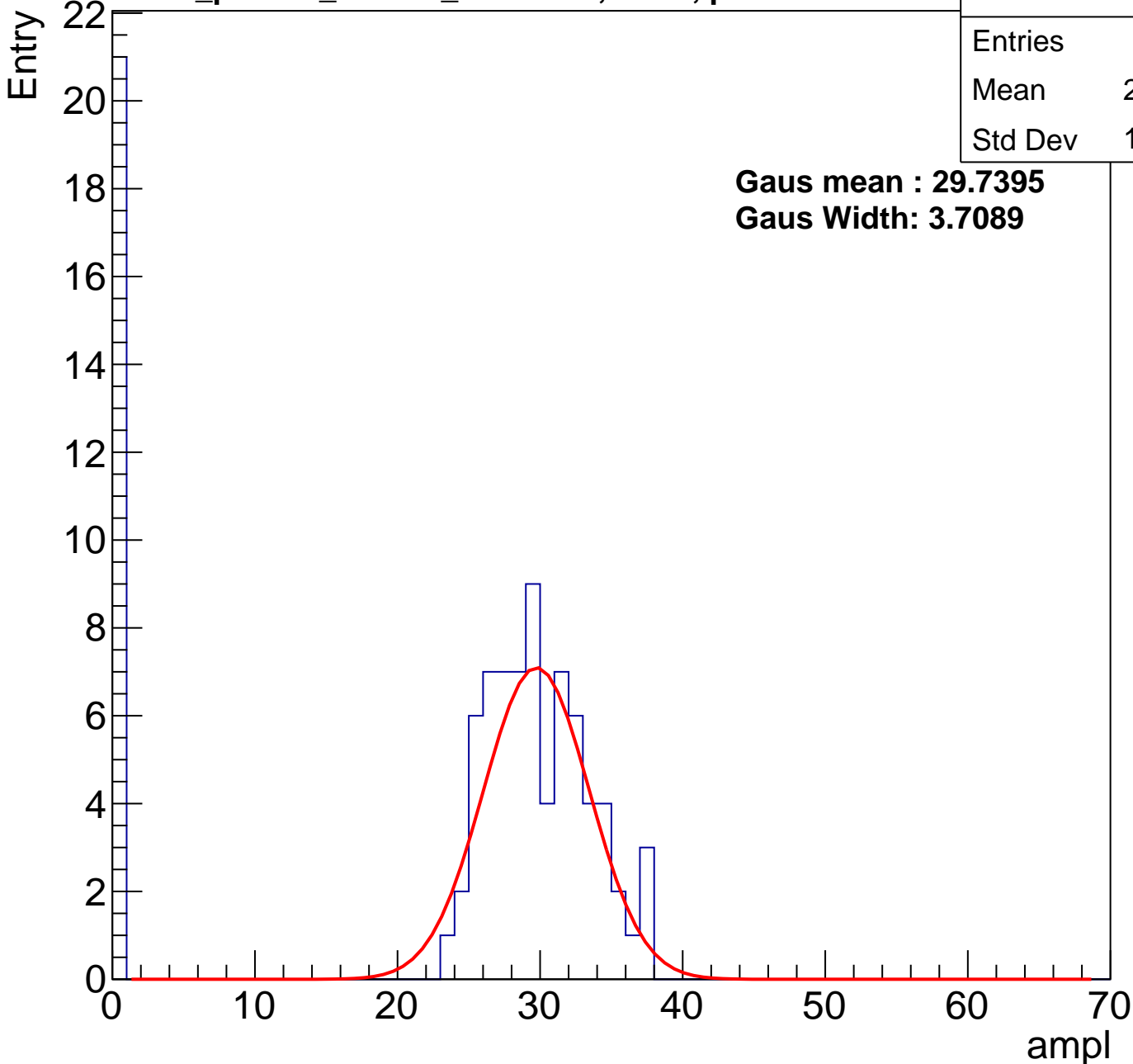
B1L103S, U18-ch83, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	22.67
Std Dev	12.78

Gaus mean : 29.7395

Gaus Width: 3.7089



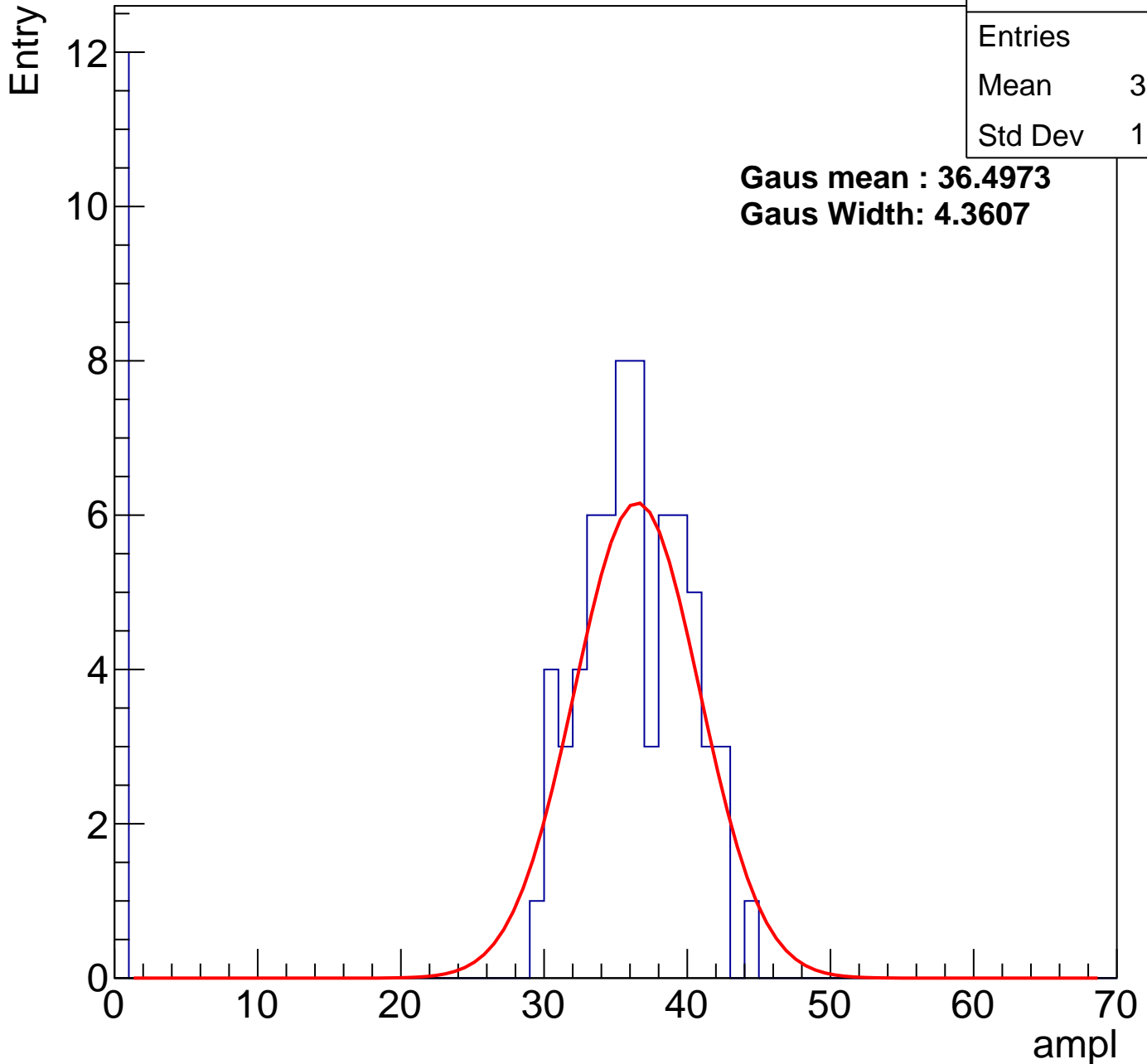
B1L103S, U18-ch83, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	30.46
Std Dev	13.29

Gaus mean : 36.4973

Gaus Width: 4.3607



B1L103S, U18-ch83, adc2

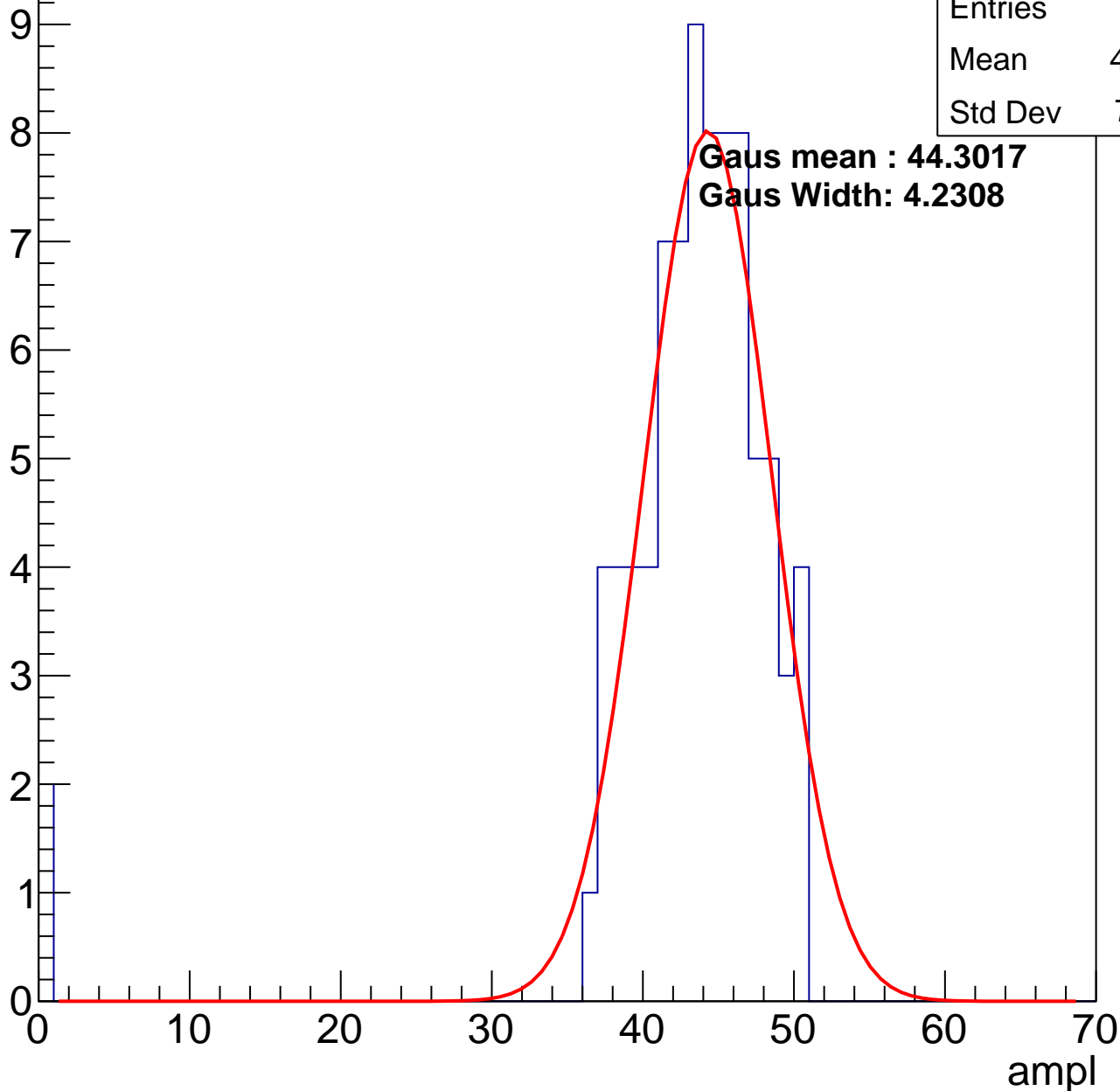
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	42.43
Std Dev	7.541

Gaus mean : 44.3017

Gaus Width: 4.2308

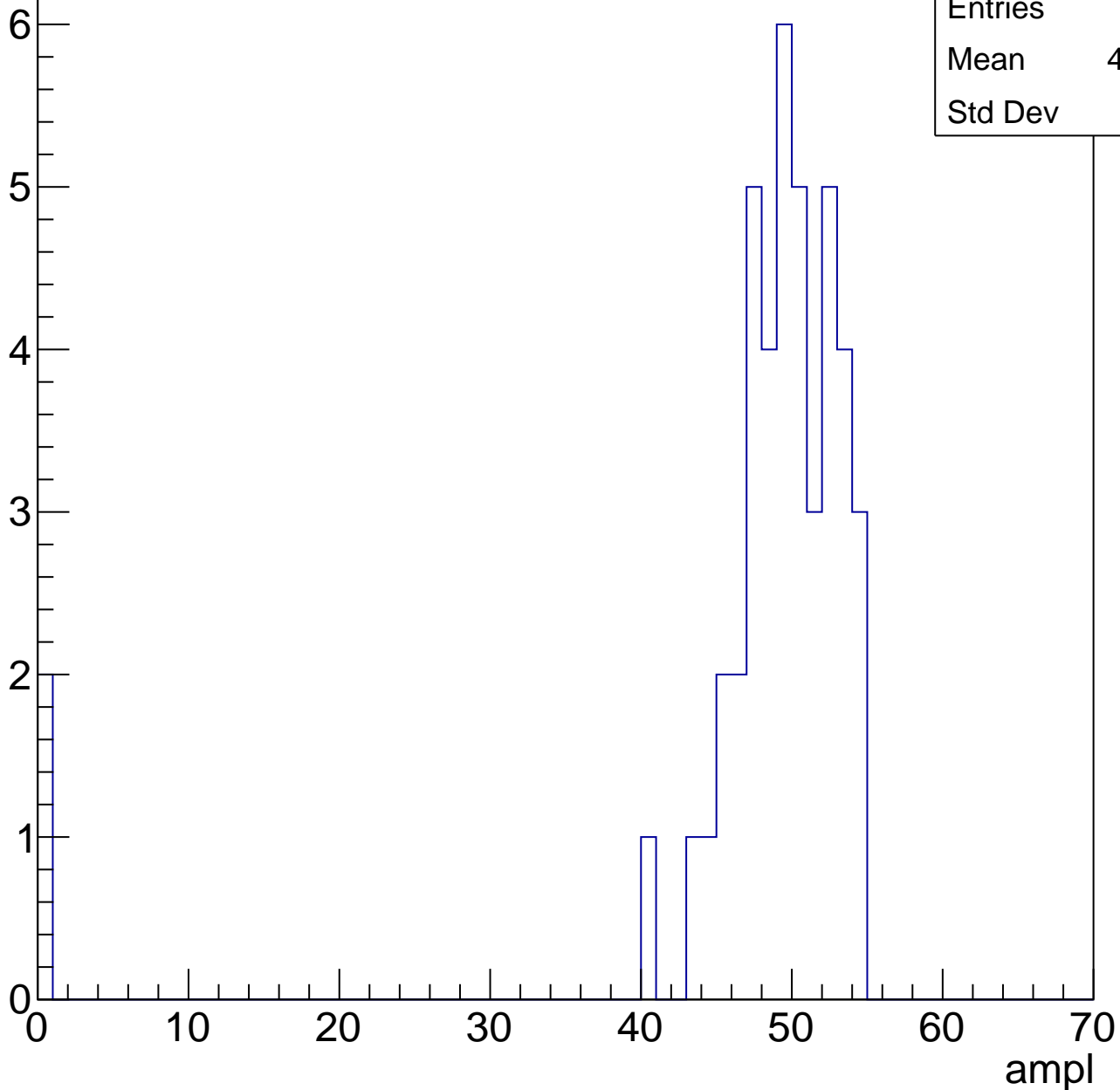


B1L103S, U18-ch83, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	46.98
Std Dev	10.7

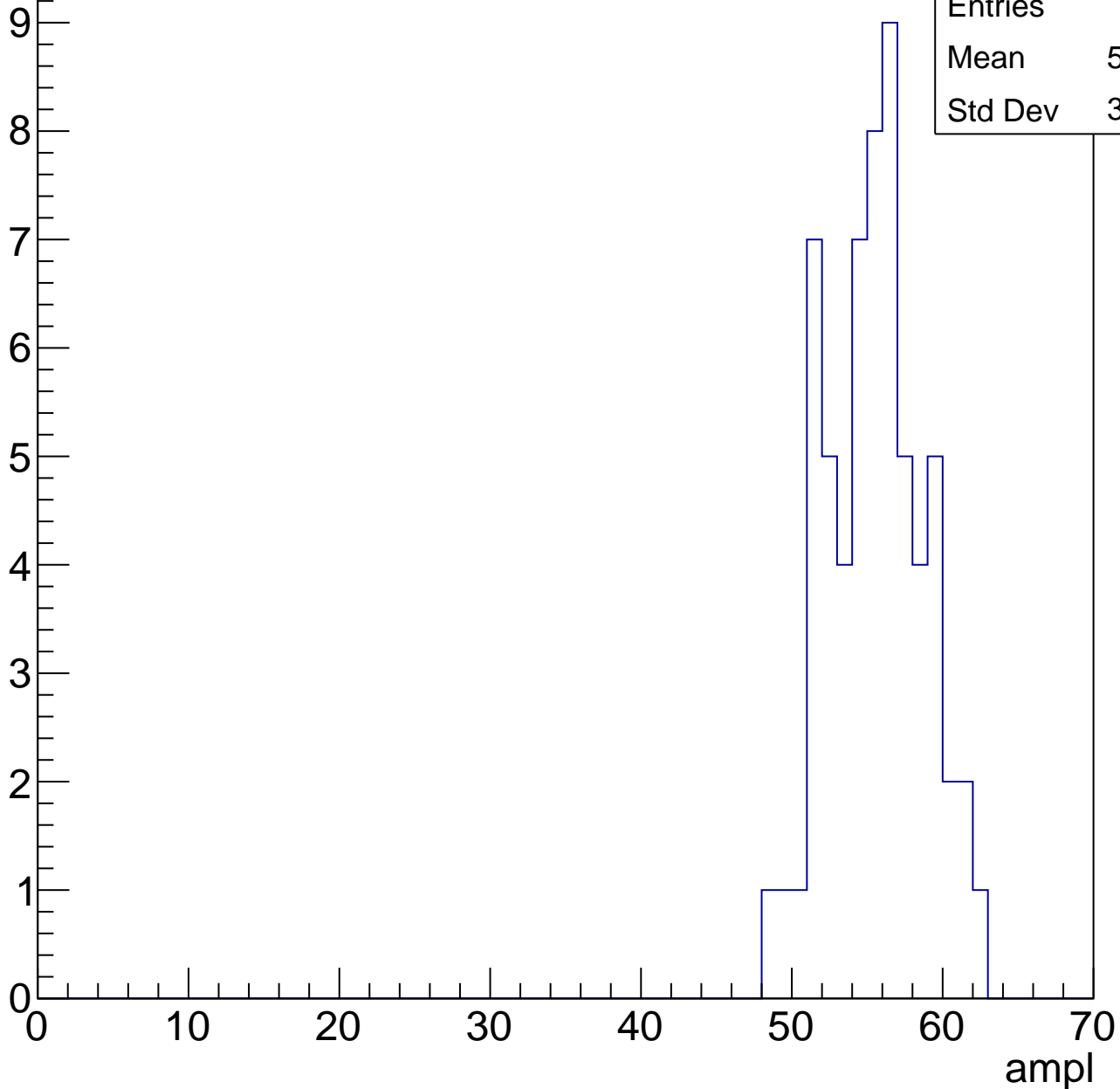


B1L103S, U18-ch83, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

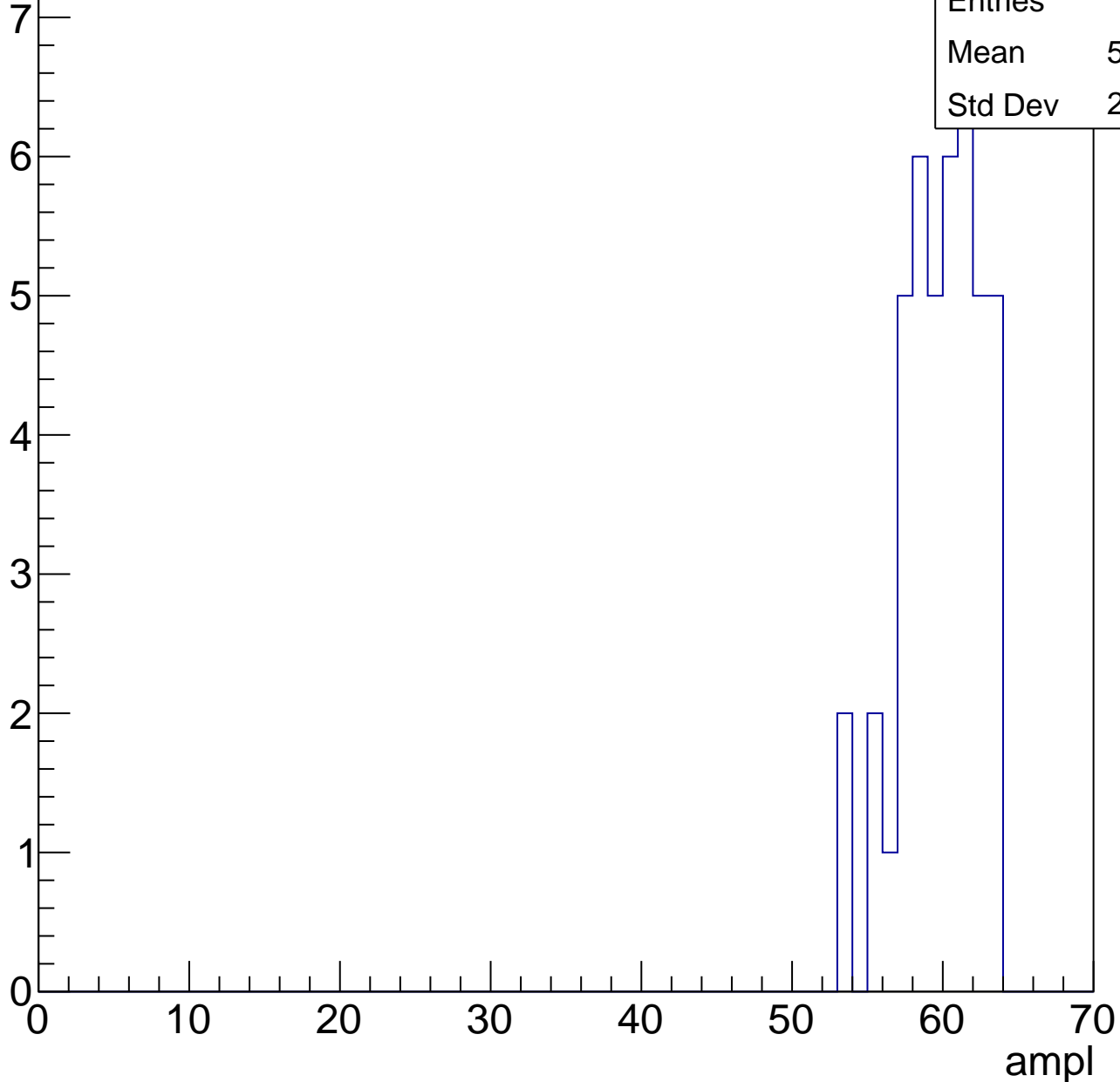
Entries	62
Mean	55.06
Std Dev	3.126



B1L103S, U18-ch83, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

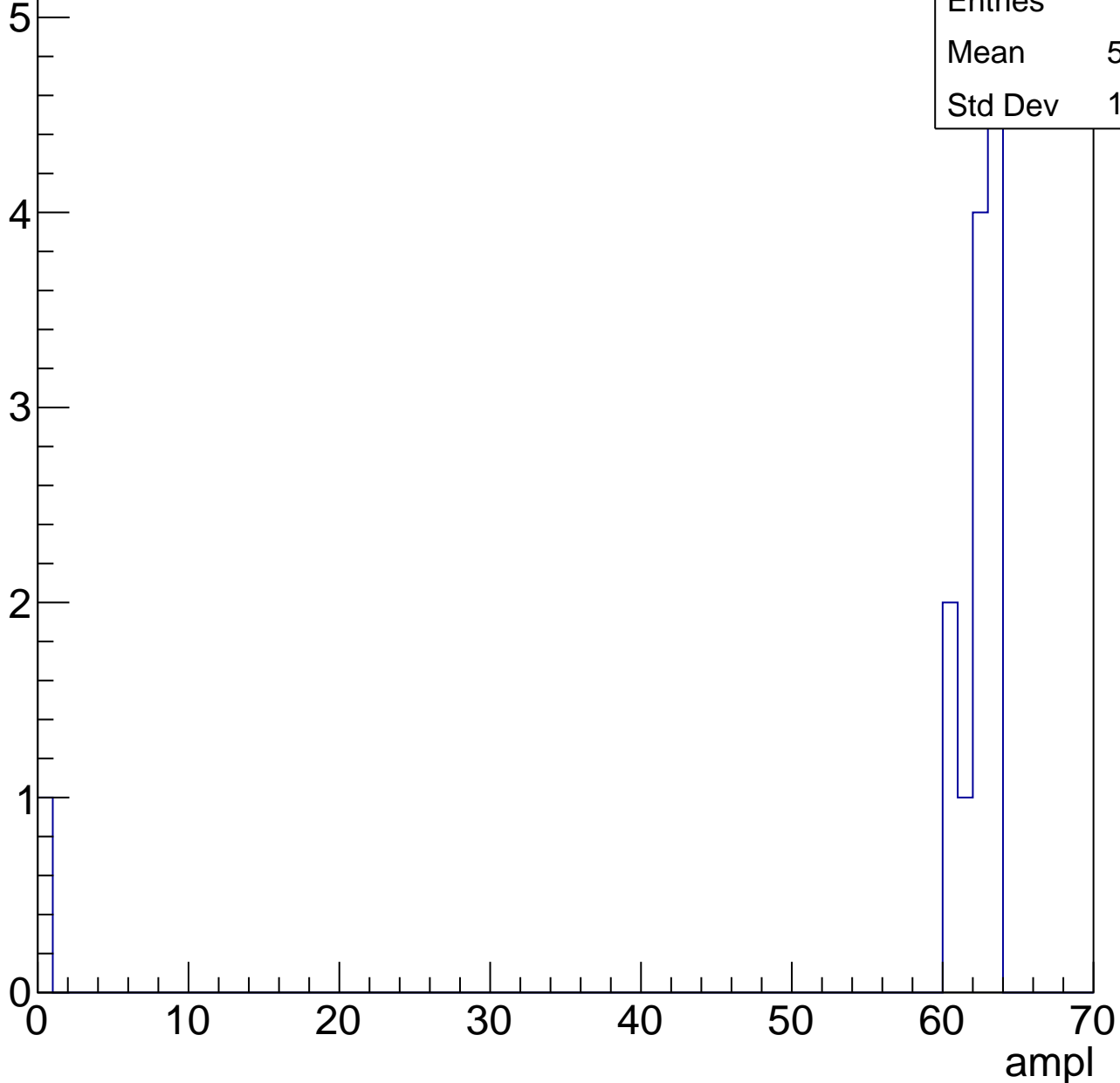


B1L103S, U18-ch83, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.23
Std Dev	16.55



B1L103S, U18-ch83, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch84, adc0

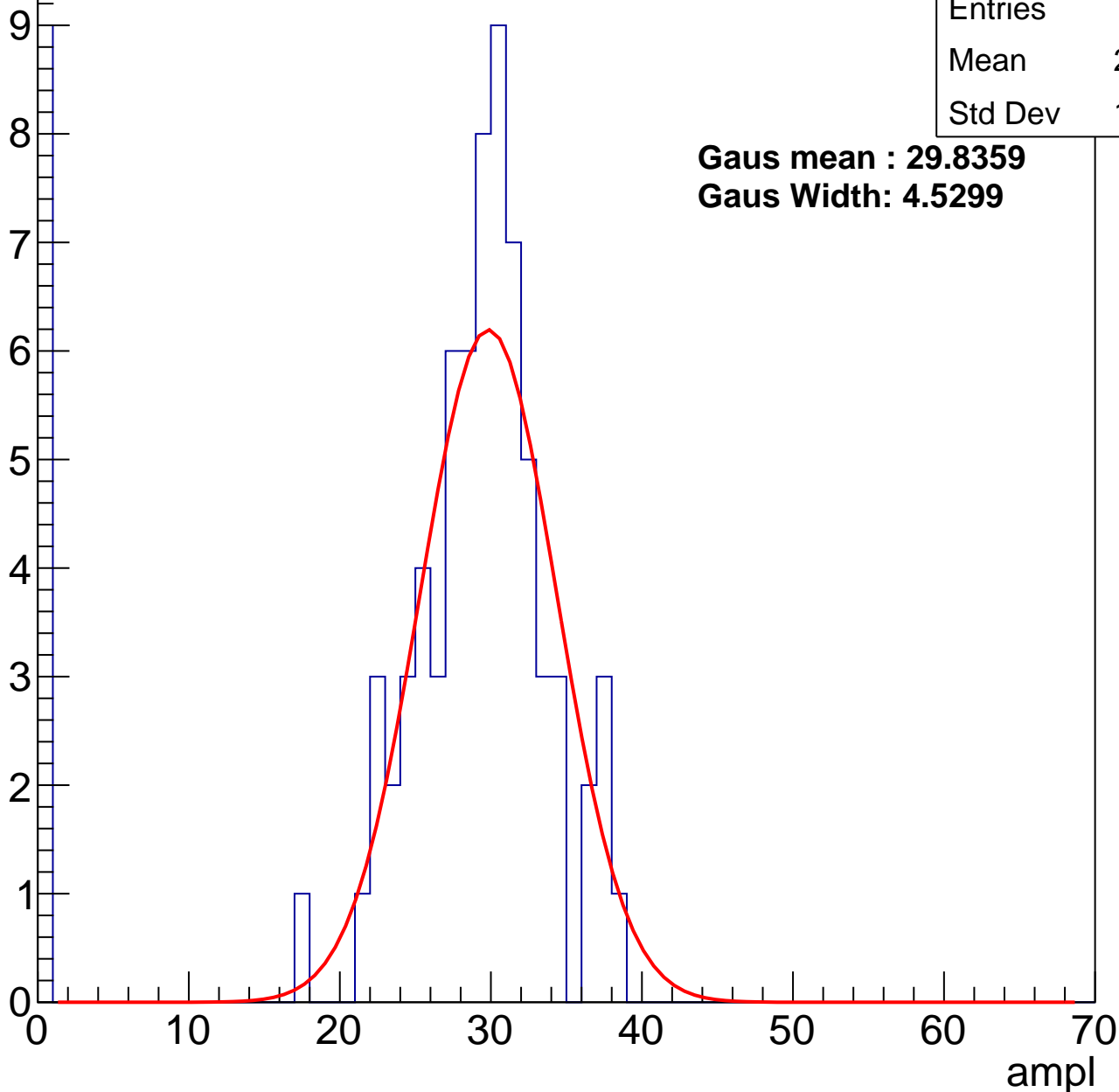
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	25.71
Std Dev	10.01

Gaus mean : 29.8359

Gaus Width: 4.5299



B1L103S, U18-ch84, adc1

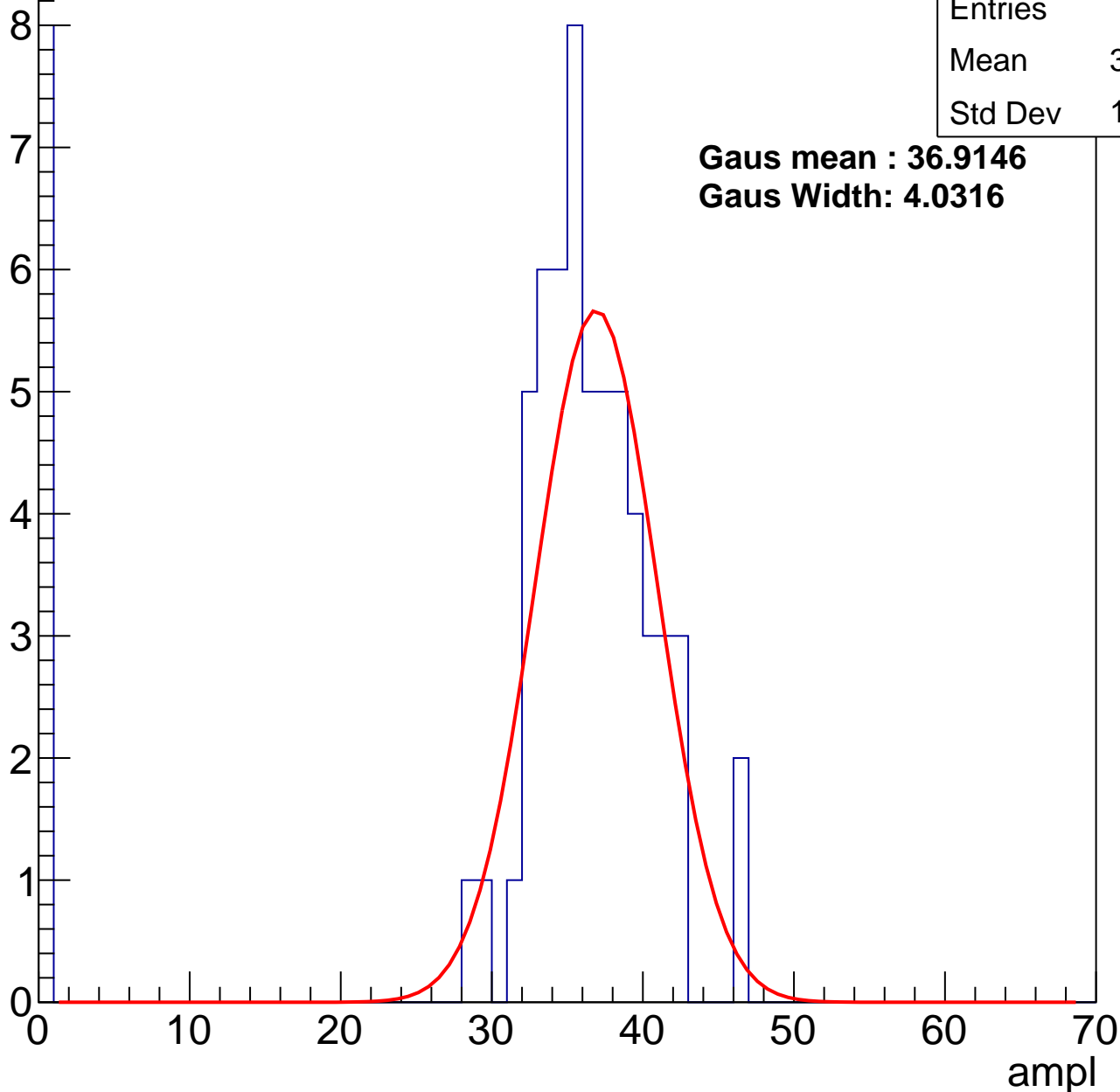
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	31.85
Std Dev	12.33

Gaus mean : 36.9146

Gaus Width: 4.0316



B1L103S, U18-ch84, adc2

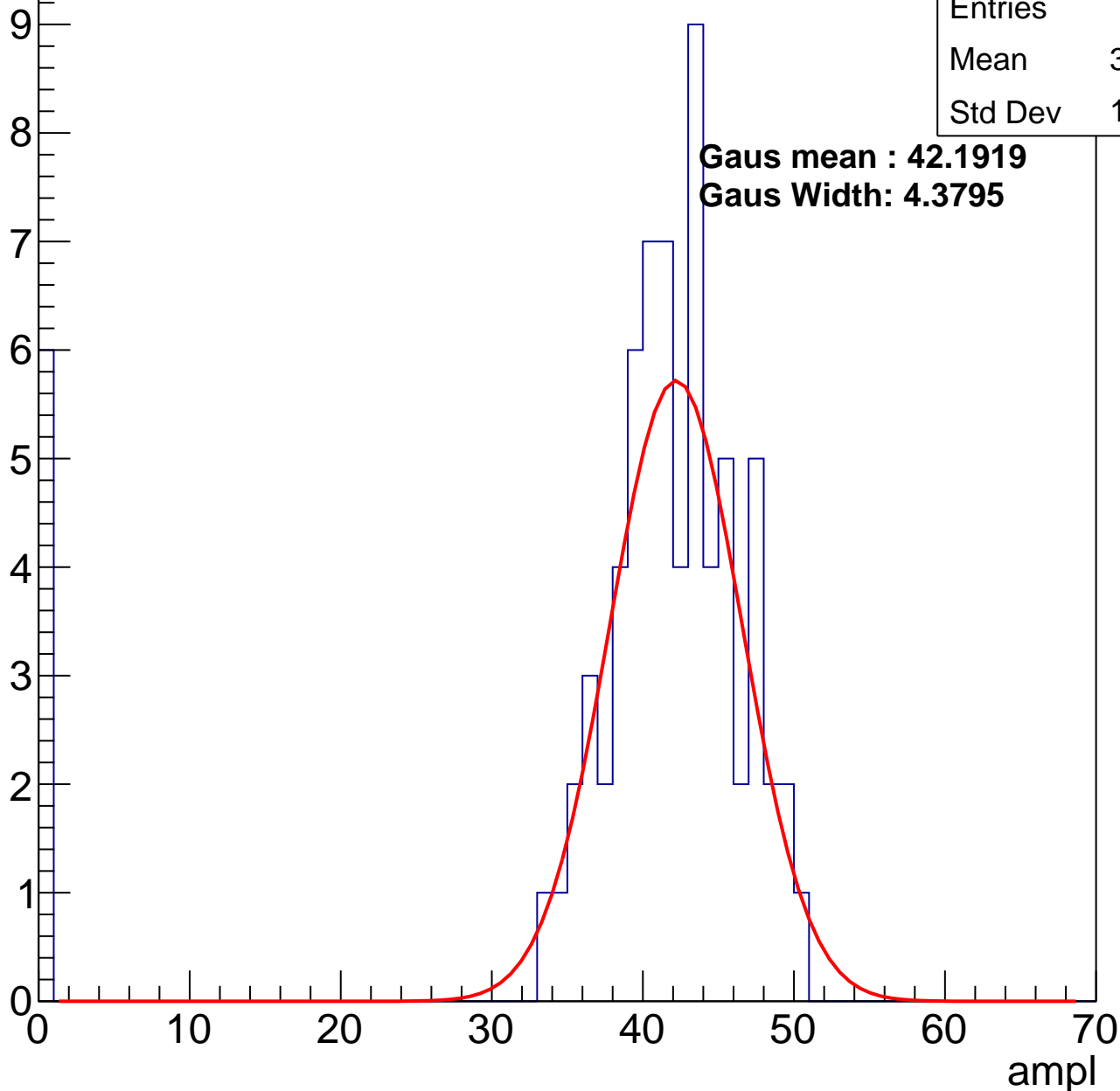
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	38.34
Std Dev	12.06

Gaus mean : 42.1919

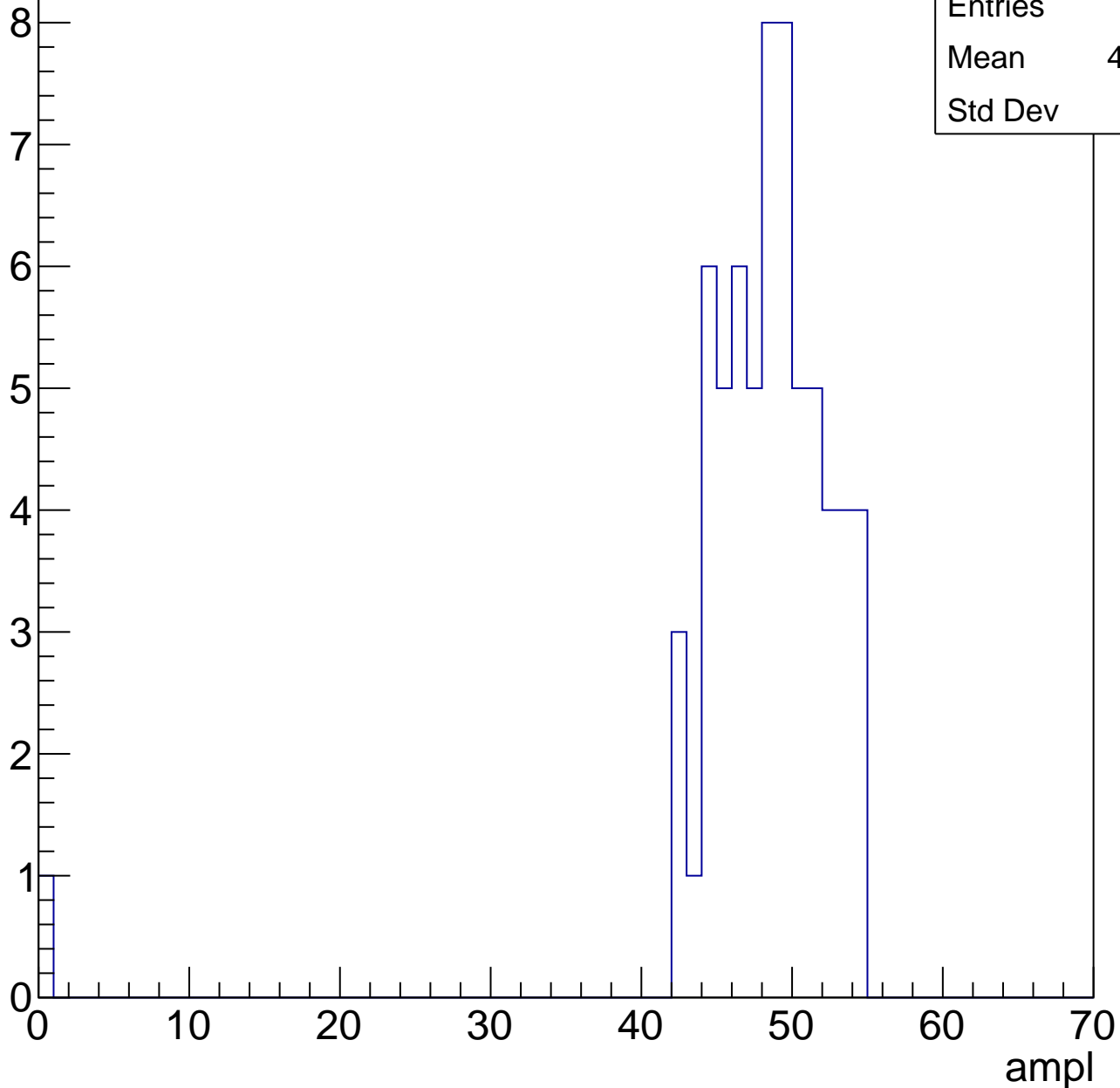
Gaus Width: 4.3795



B1L103S, U18-ch84, adc3

calib_packv5_041523_1651.root, FC#0, port C2

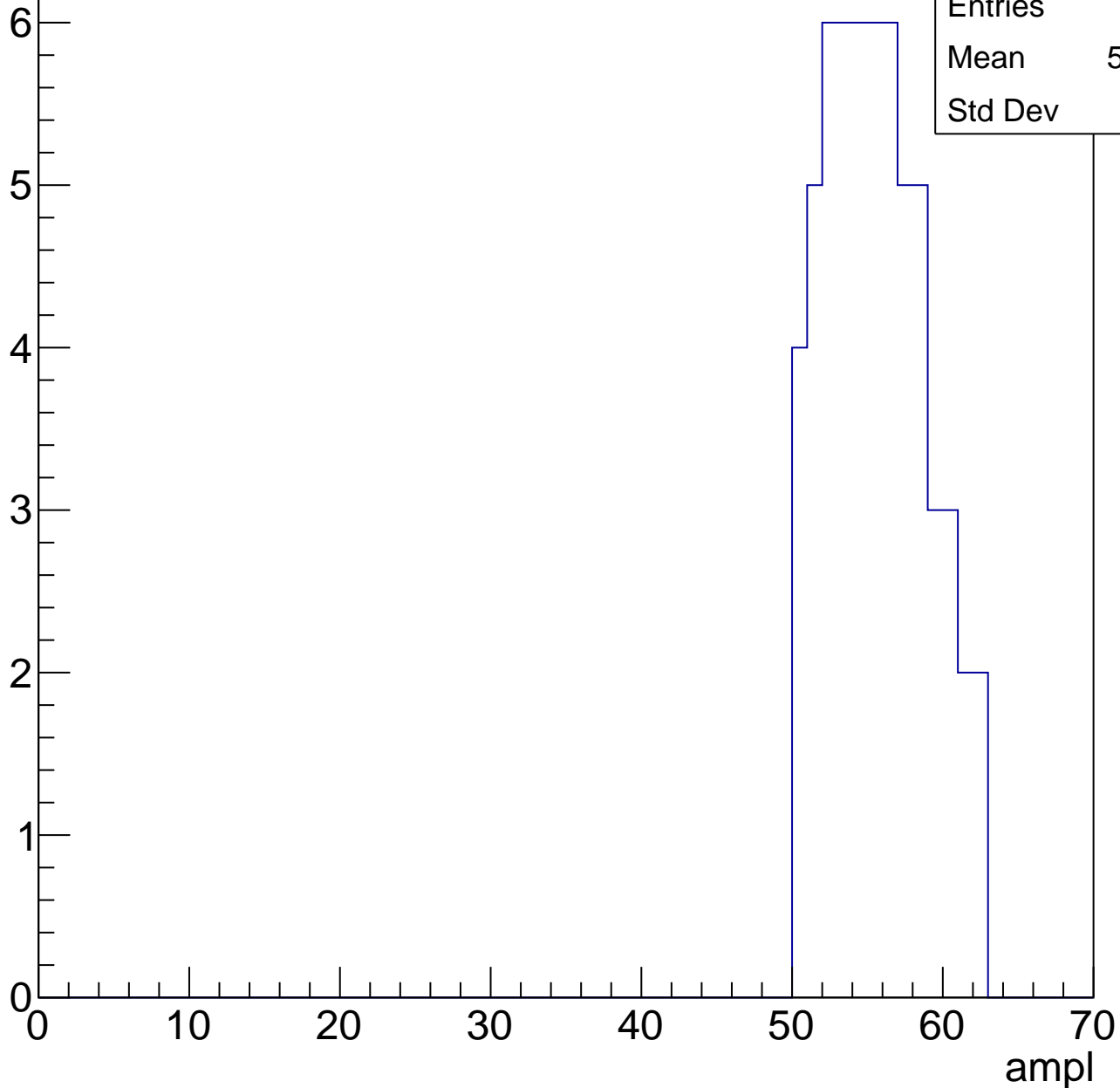
Entry



B1L103S, U18-ch84, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



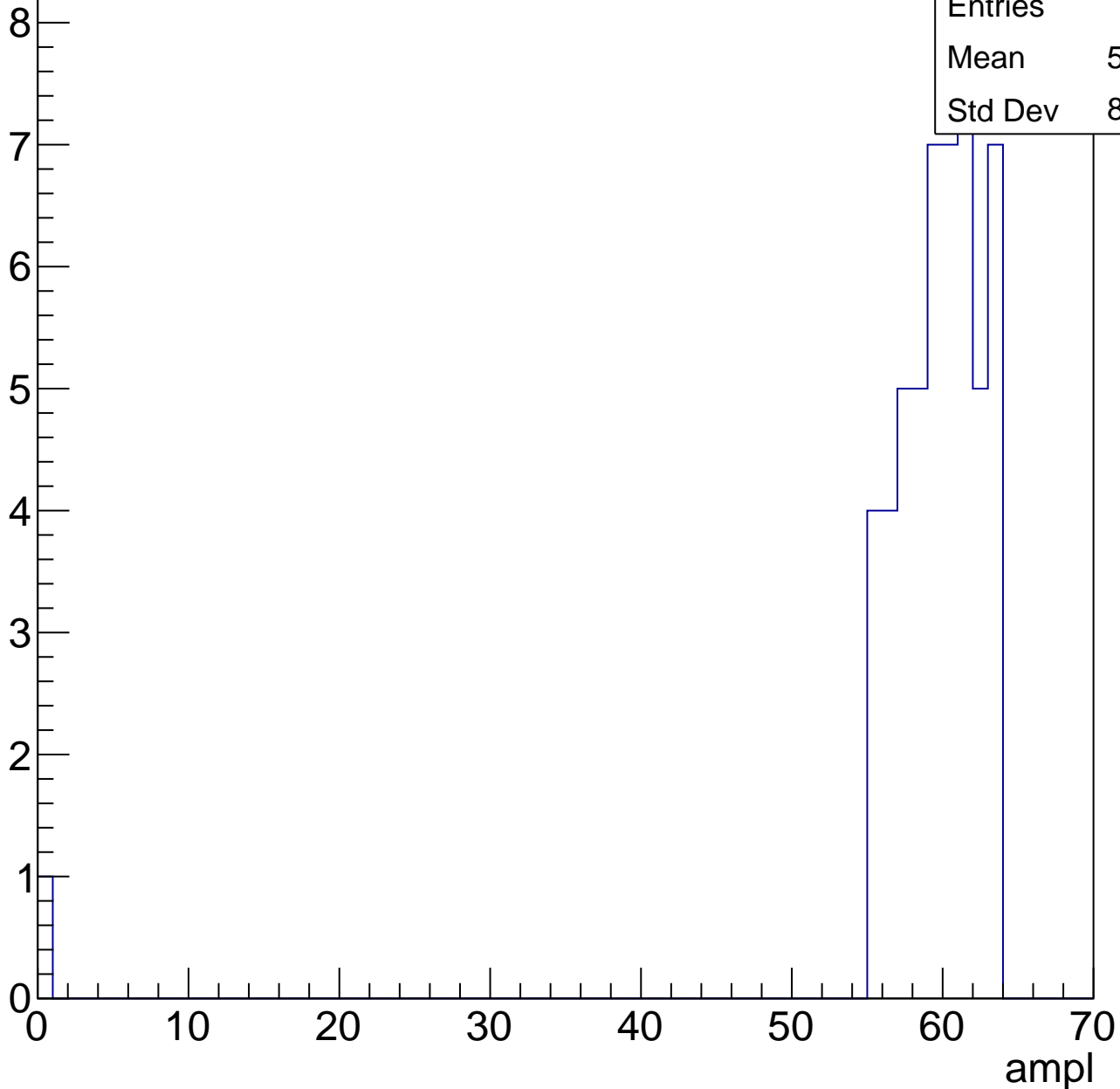
Entries	59
Mean	55.14
Std Dev	3.26

B1L103S, U18-ch84, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.32
Std Dev	8.442



B1L103S, U18-ch84, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch84, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



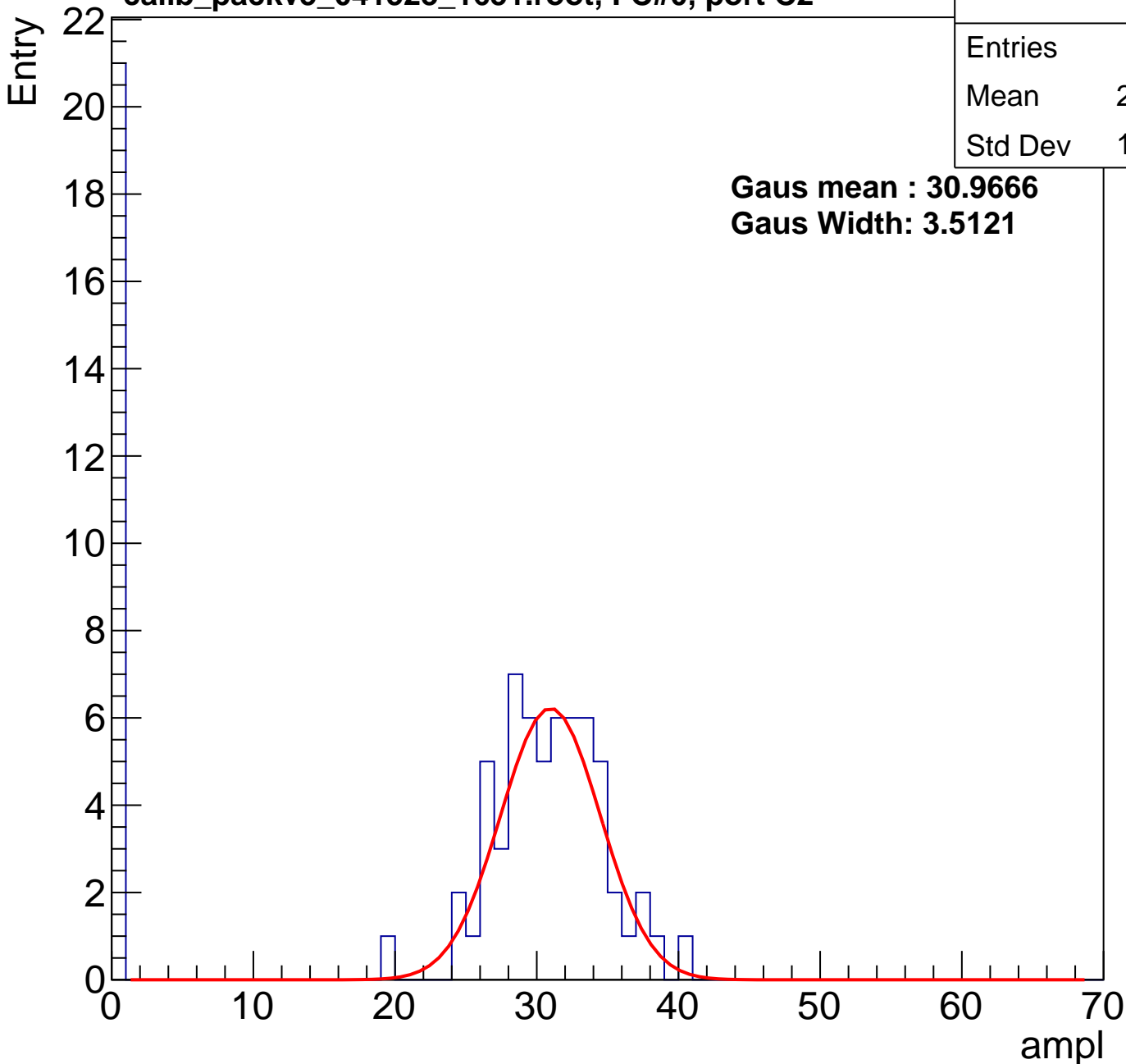
B1L103S, U18-ch85, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	22.56
Std Dev	13.74

Gaus mean : 30.9666

Gaus Width: 3.5121



B1L103S, U18-ch85, adc1

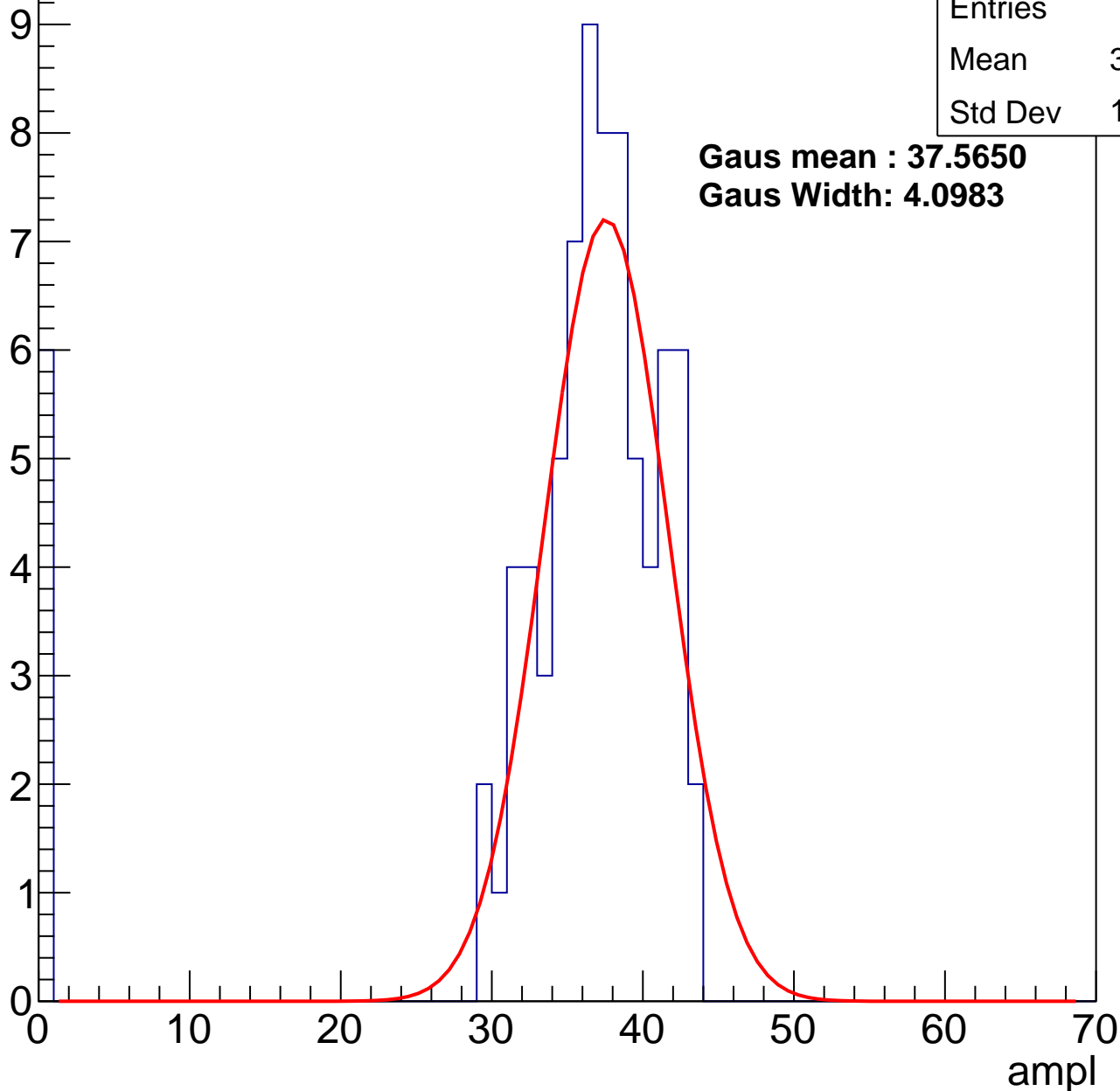
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	33.96
Std Dev	10.25

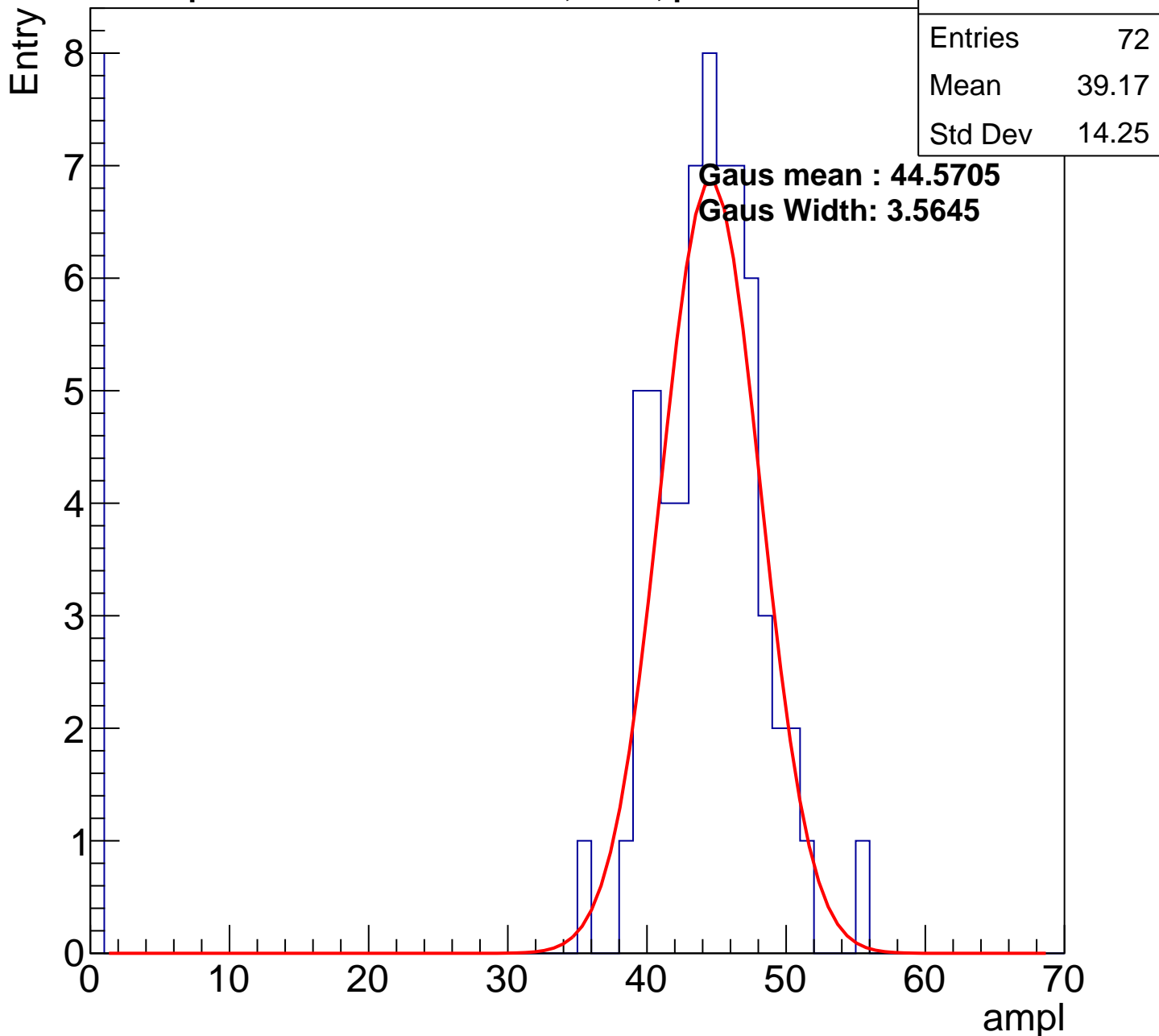
Gaus mean : 37.5650

Gaus Width: 4.0983



B1L103S, U18-ch85, adc2

calib_packv5_041523_1651.root, FC#0, port C2

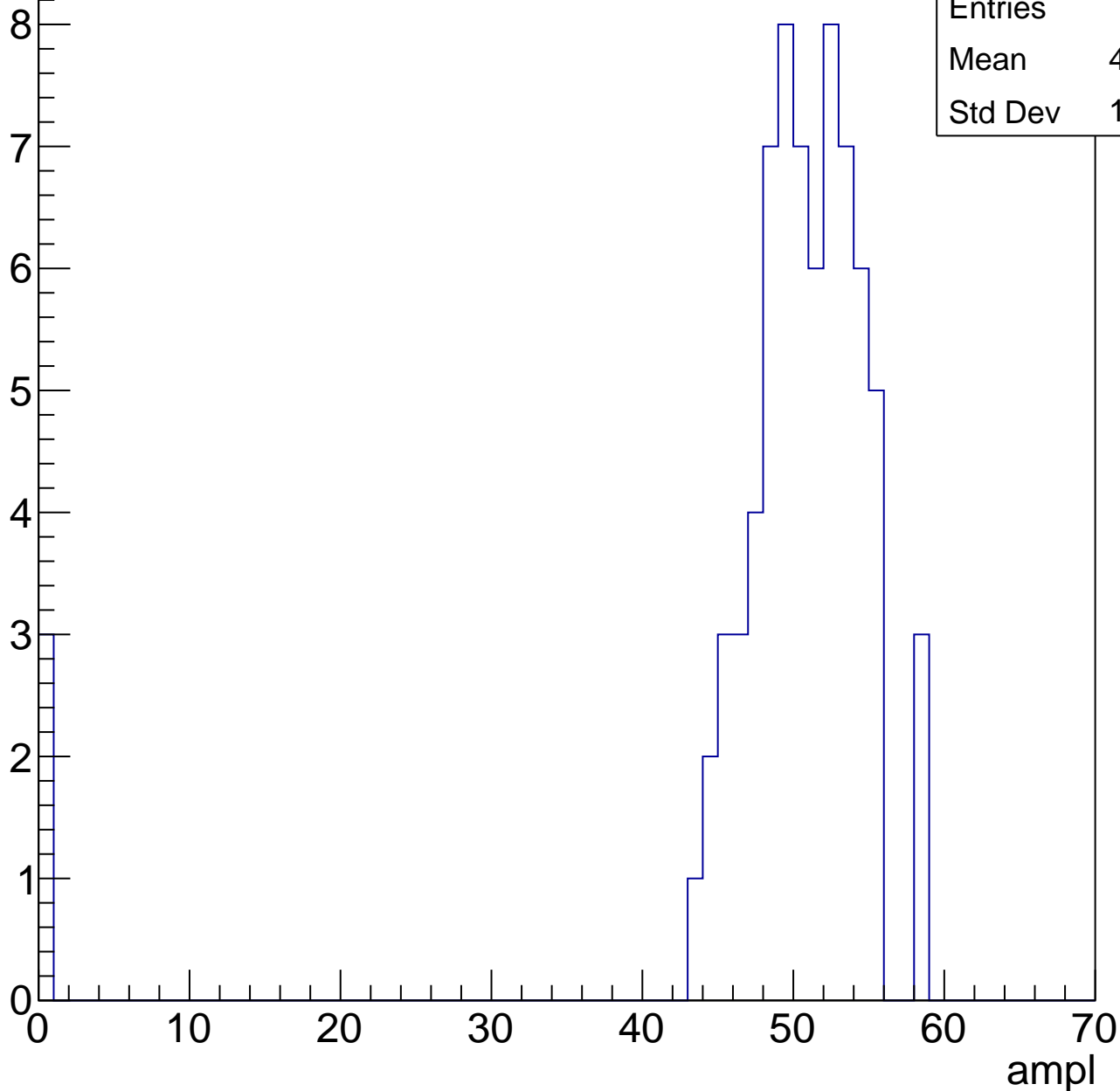


B1L103S, U18-ch85, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

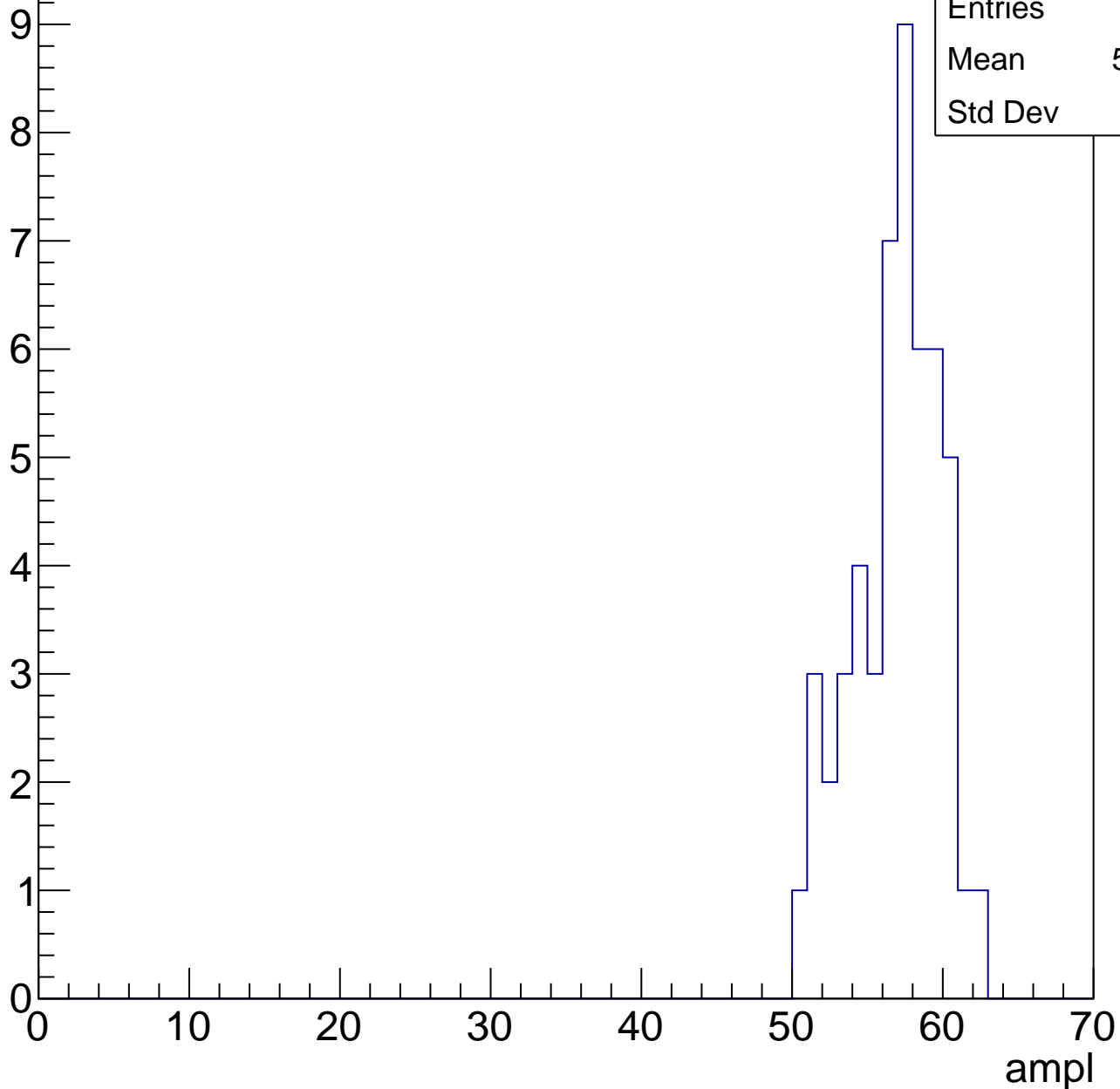
Entries	73
Mean	48.44
Std Dev	10.57



B1L103S, U18-ch85, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

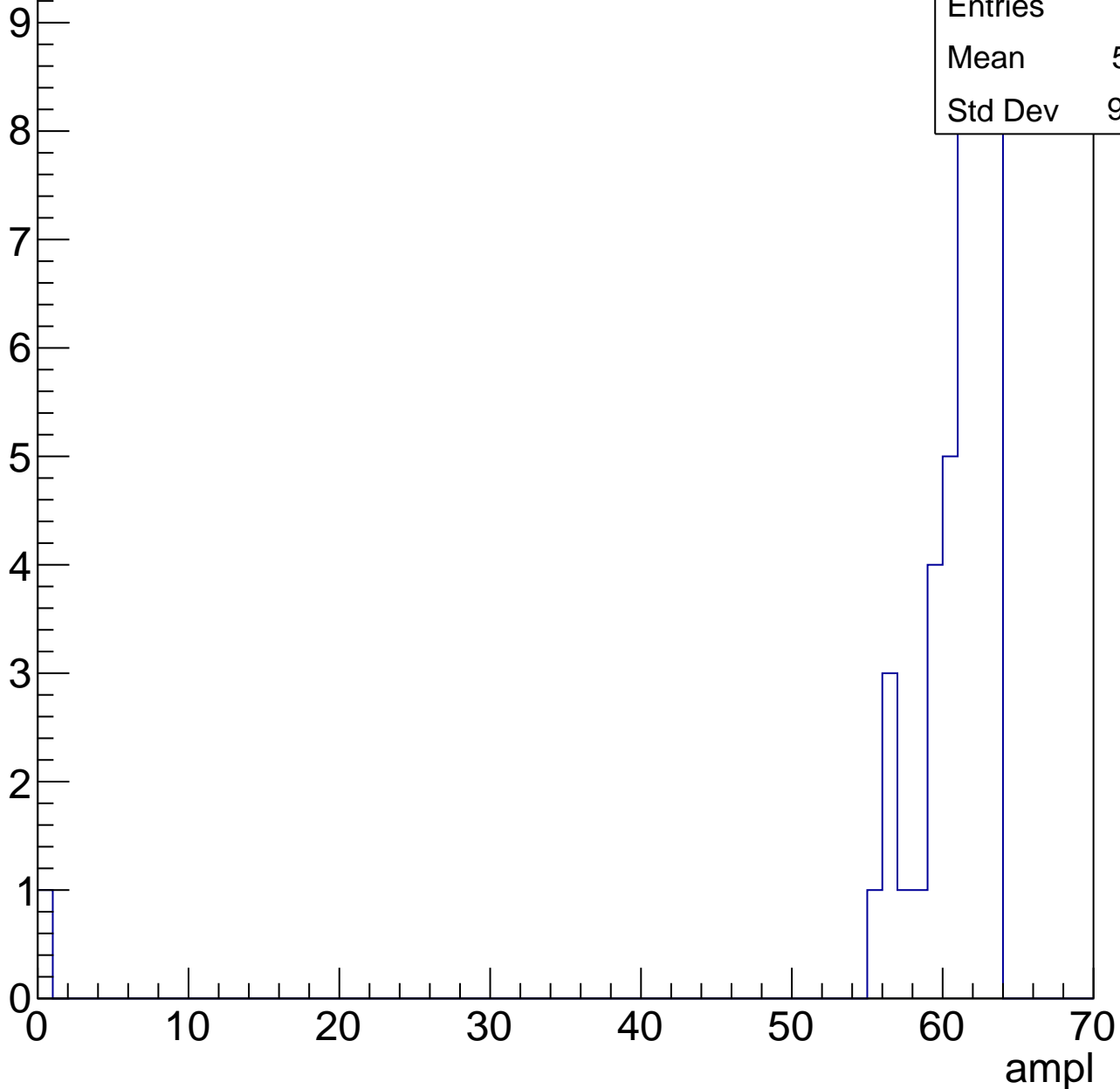


B1L103S, U18-ch85, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	59.21
Std Dev	9.498



B1L103S, U18-ch85, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch85, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

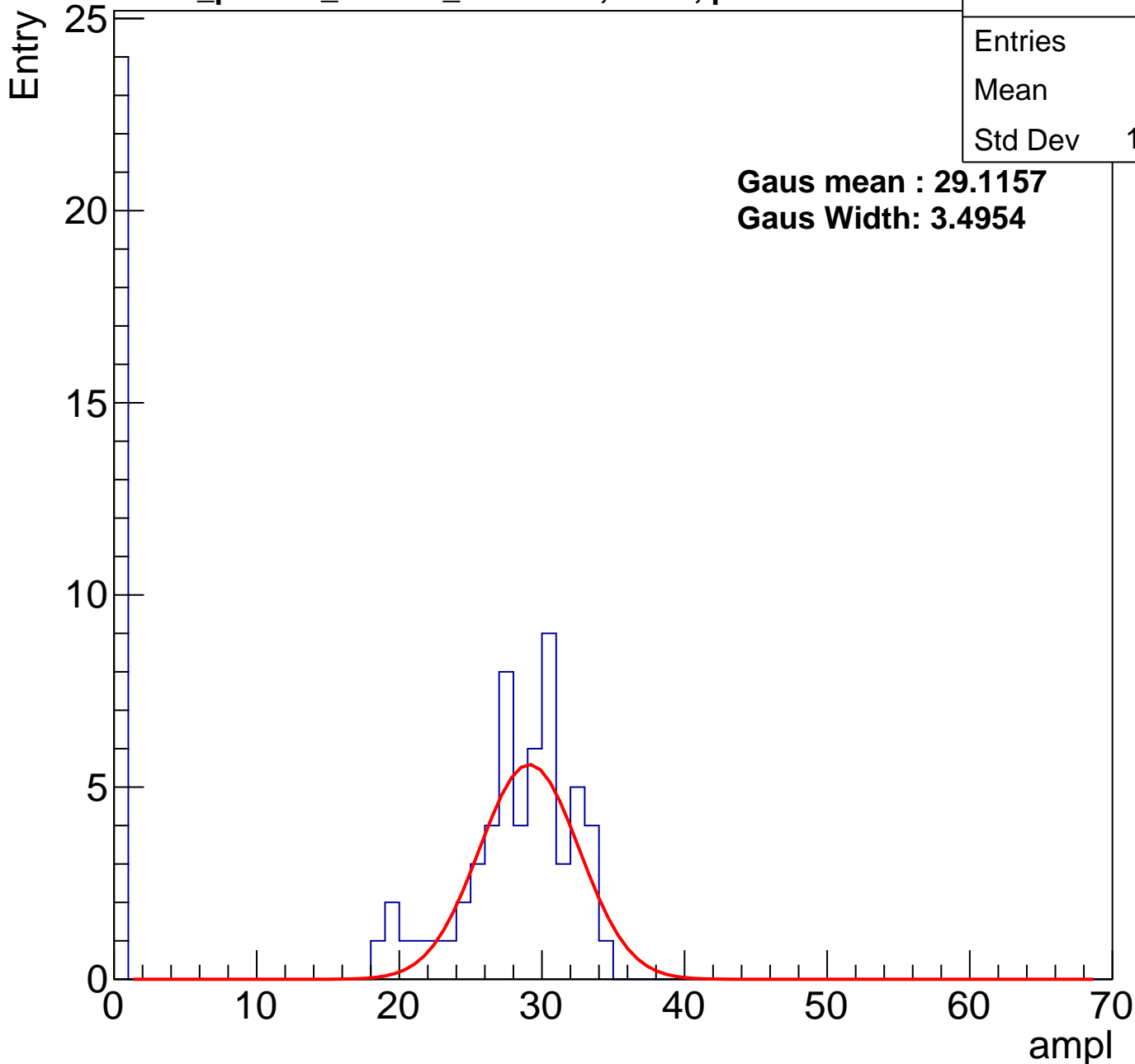
B1L103S, U18-ch86, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	80
Mean	19.5
Std Dev	13.15

Gaus mean : 29.1157

Gaus Width: 3.4954



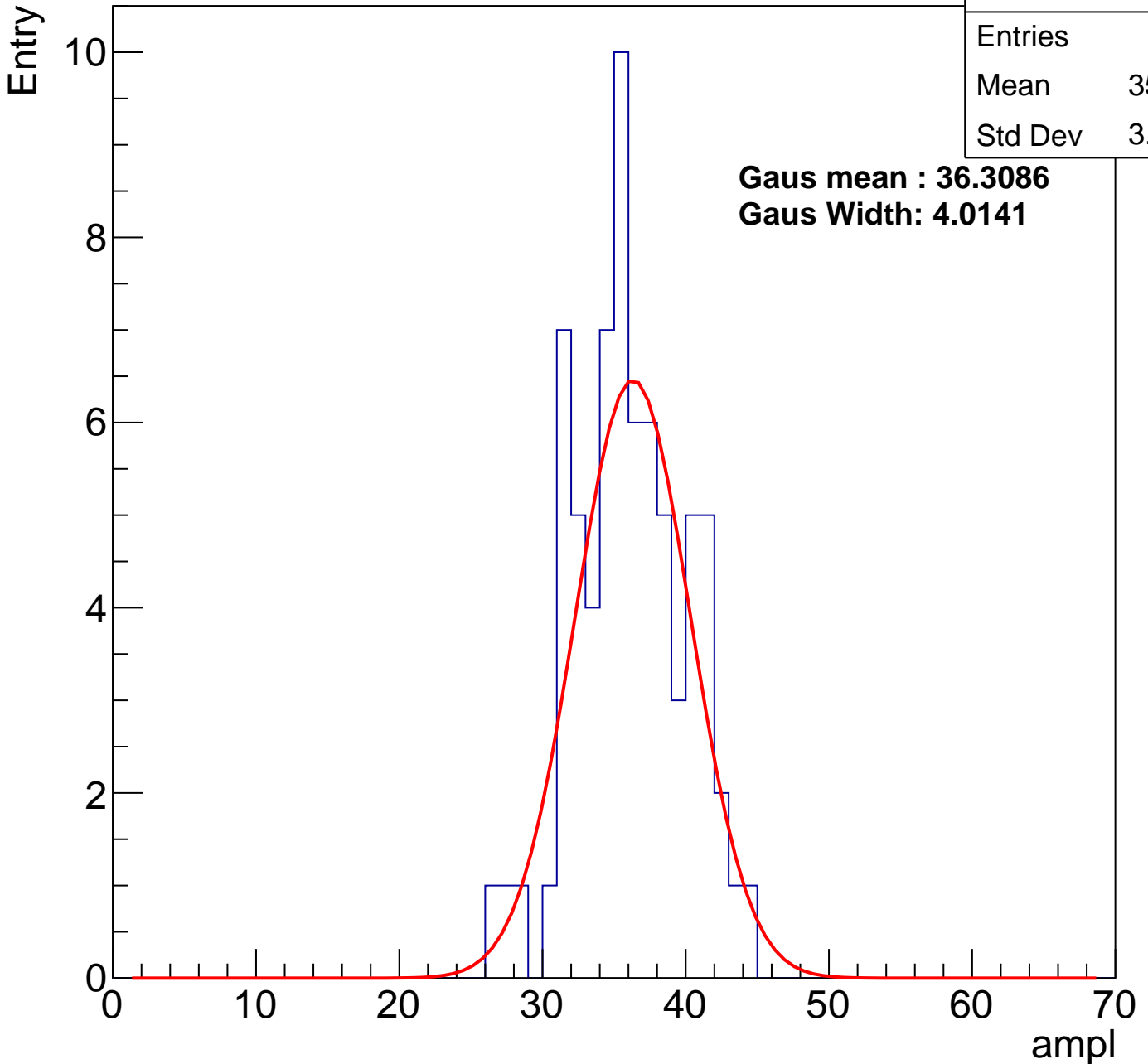
B1L103S, U18-ch86, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	35.62
Std Dev	3.832

Gaus mean : 36.3086

Gaus Width: 4.0141



B1L103S, U18-ch86, adc2

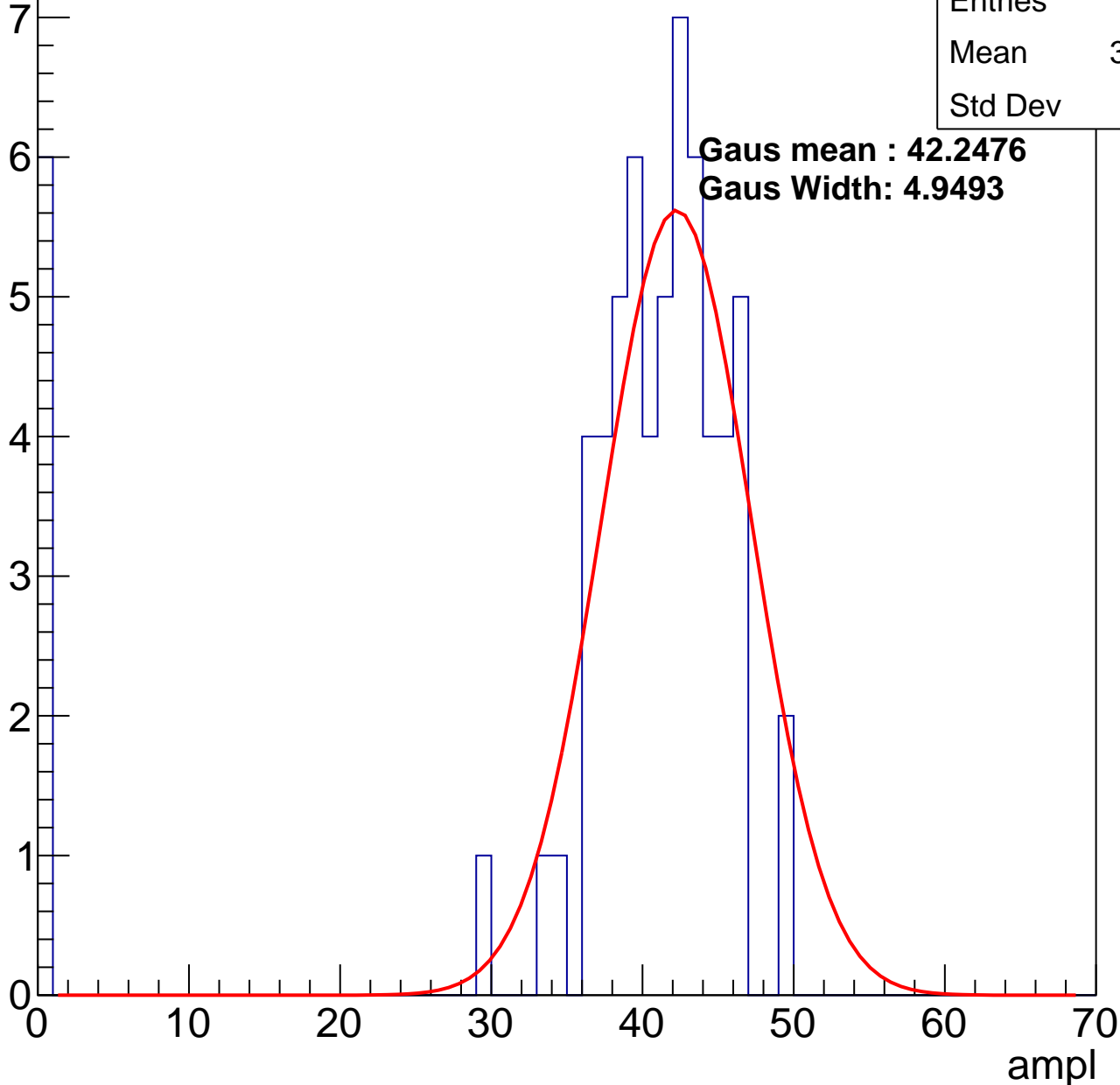
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	37.12
Std Dev	12.4

Gaus mean : 42.2476

Gaus Width: 4.9493



B1L103S, U18-ch86, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	40.74
Std Dev	17.51

Entry

10

8

6

4

2

0

0

10

20

30

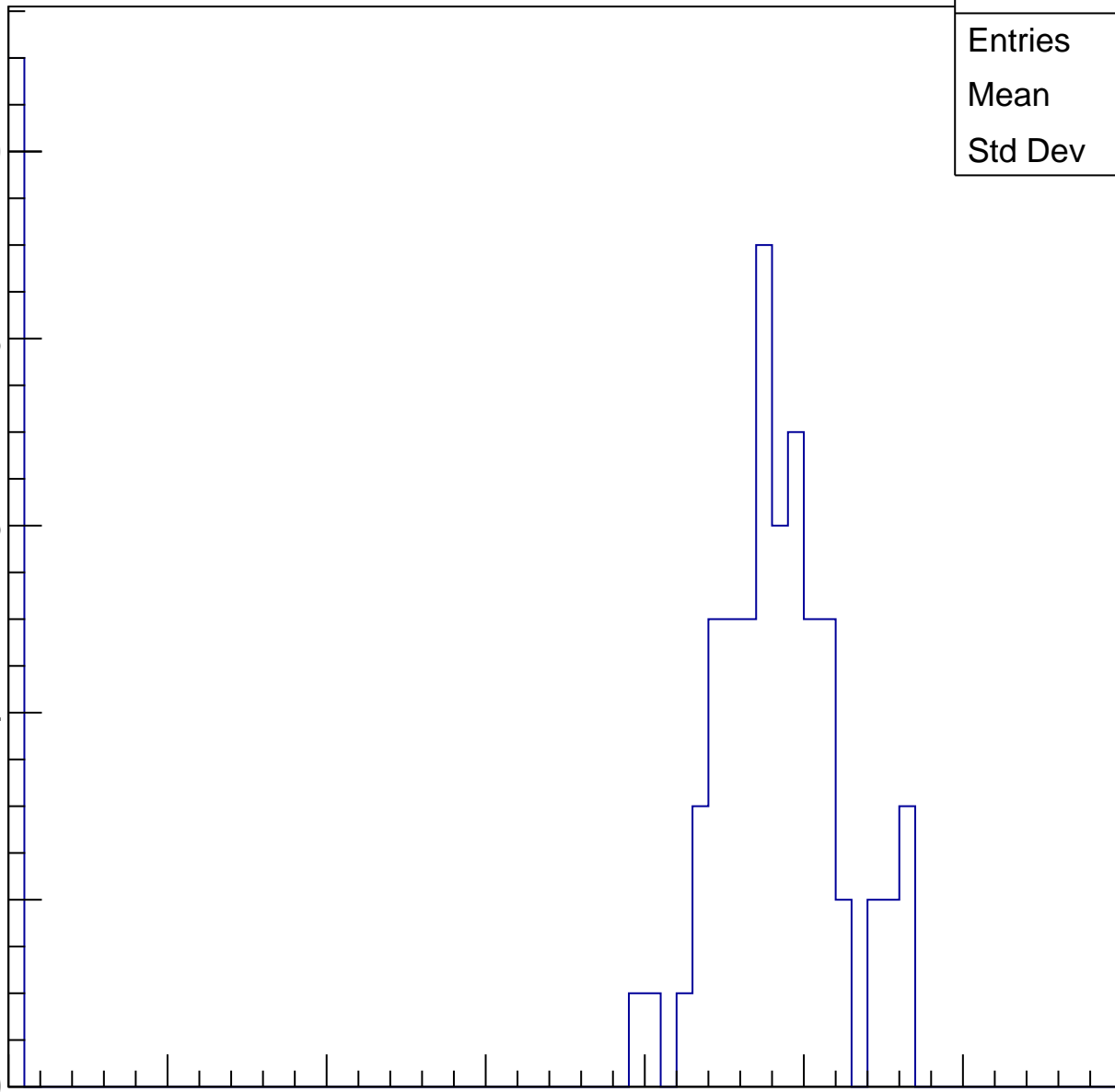
40

50

60

70

ampl



B1L103S, U18-ch86, adc4

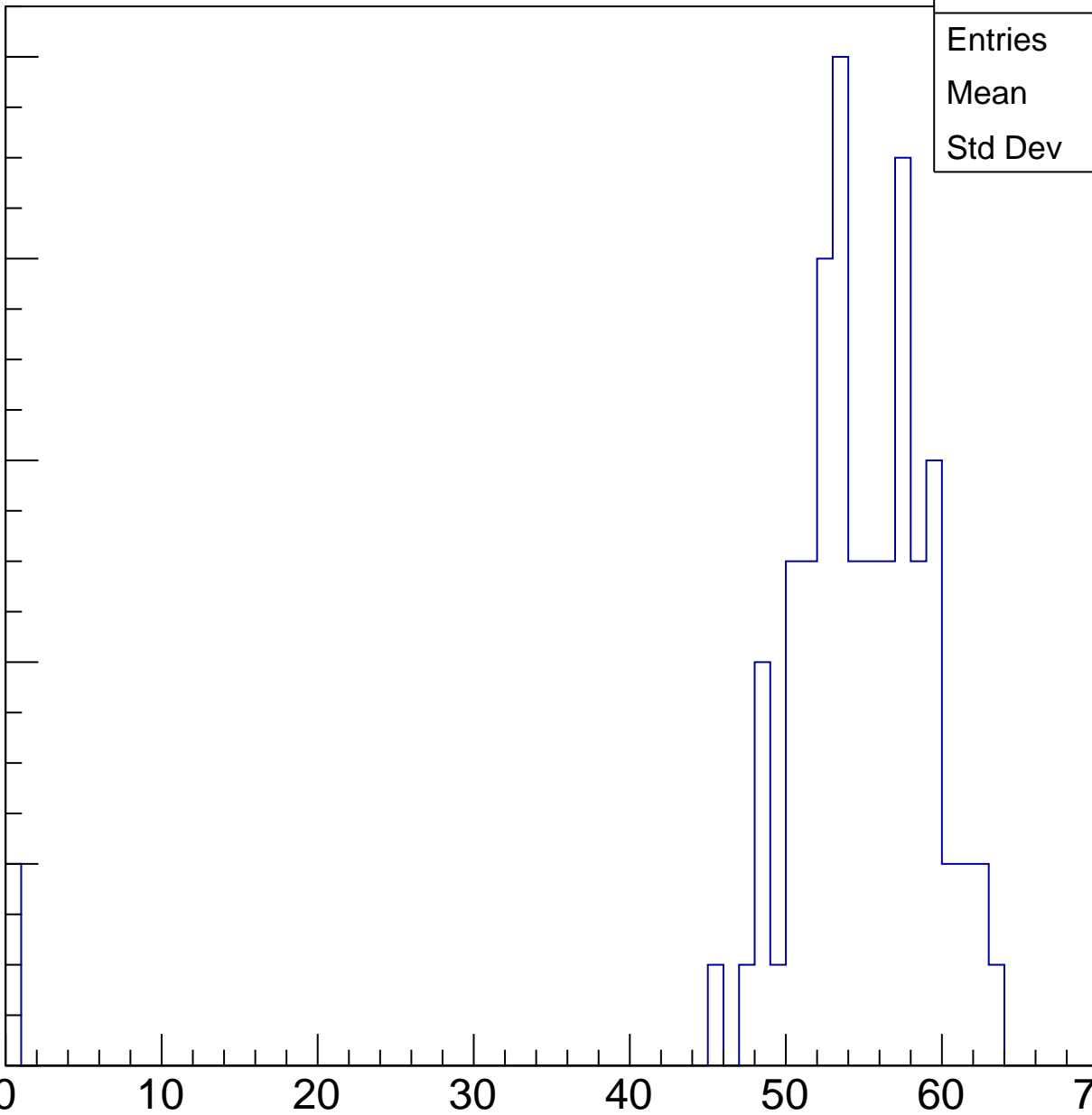
calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	53.1
Std Dev	9.379

Entry

10
8
6
4
2
0

ampl

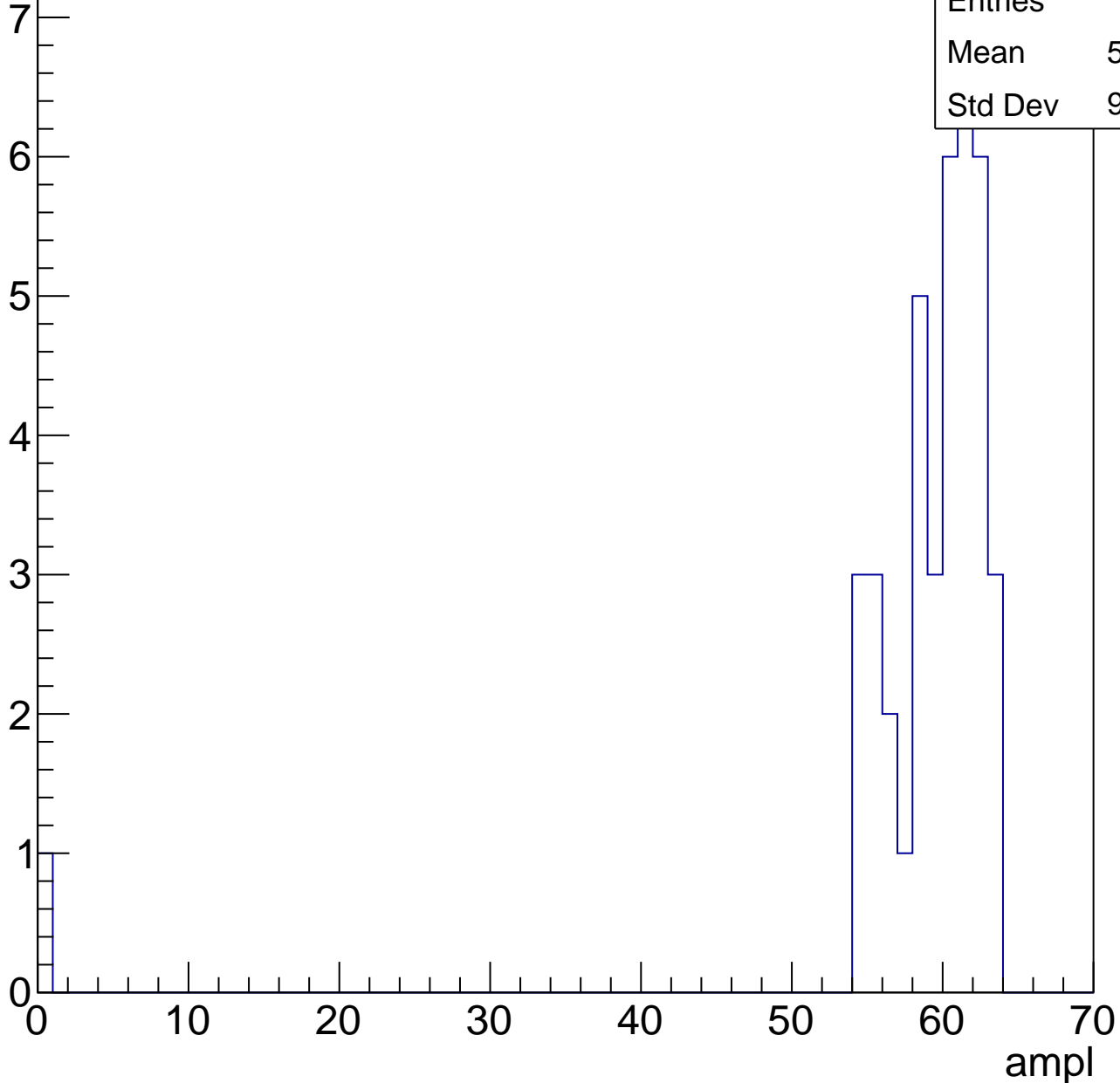


B1L103S, U18-ch86, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	57.77
Std Dev	9.627

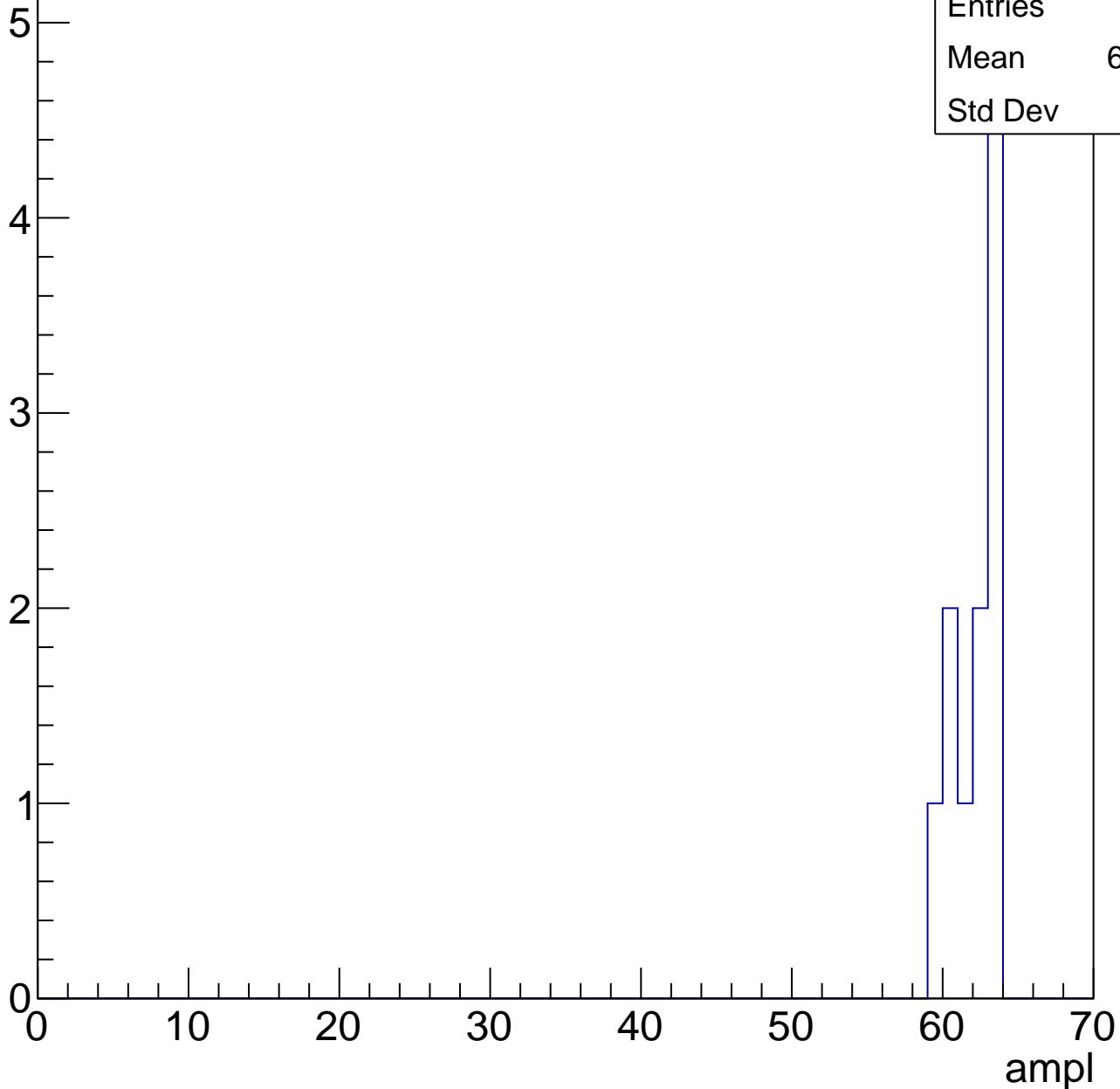


B1L103S, U18-ch86, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

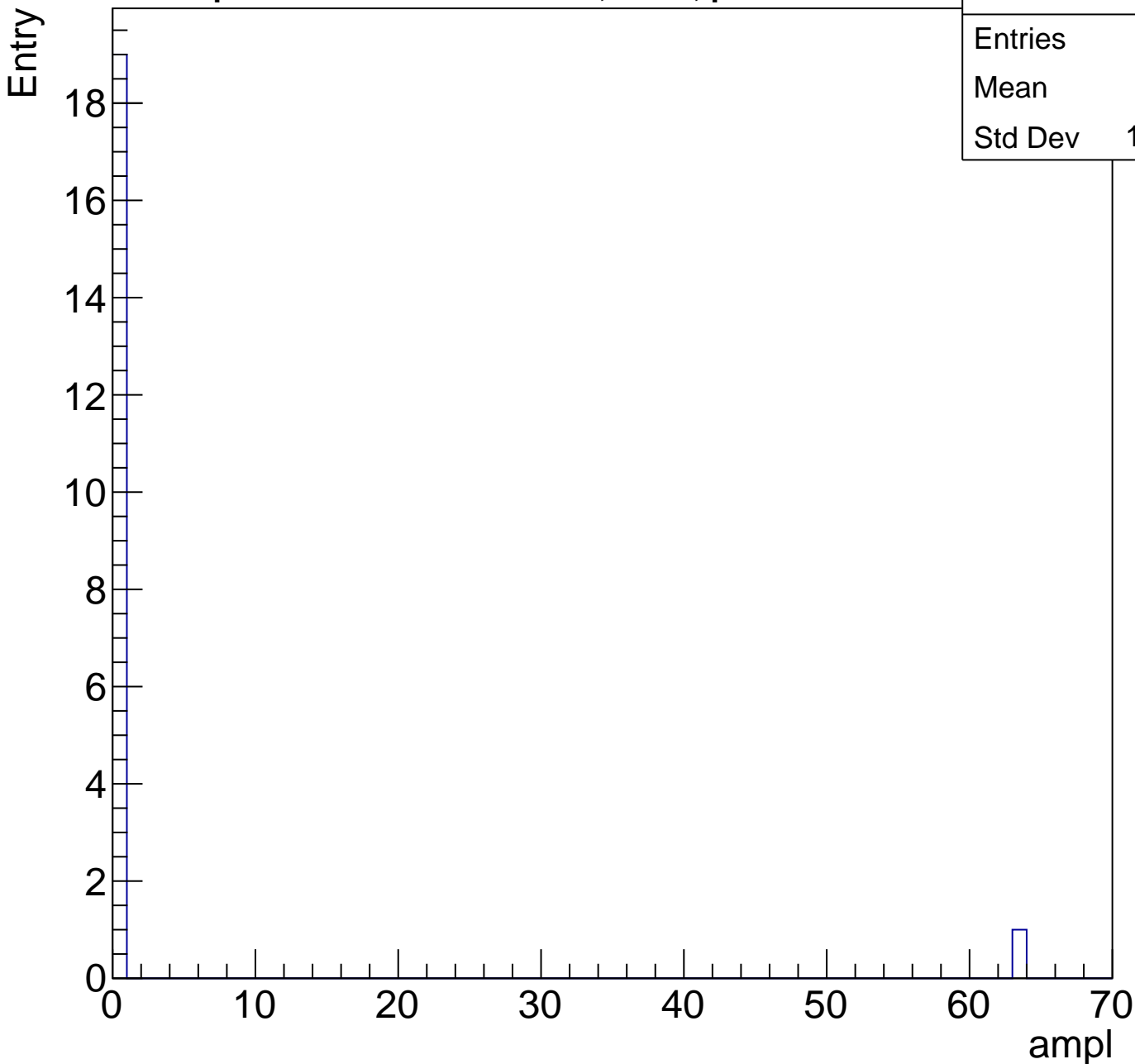
Entries	11
Mean	61.73
Std Dev	1.42



B1L103S, U18-ch86, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U18-ch87, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	81
Mean	22.9
Std Dev	11.32

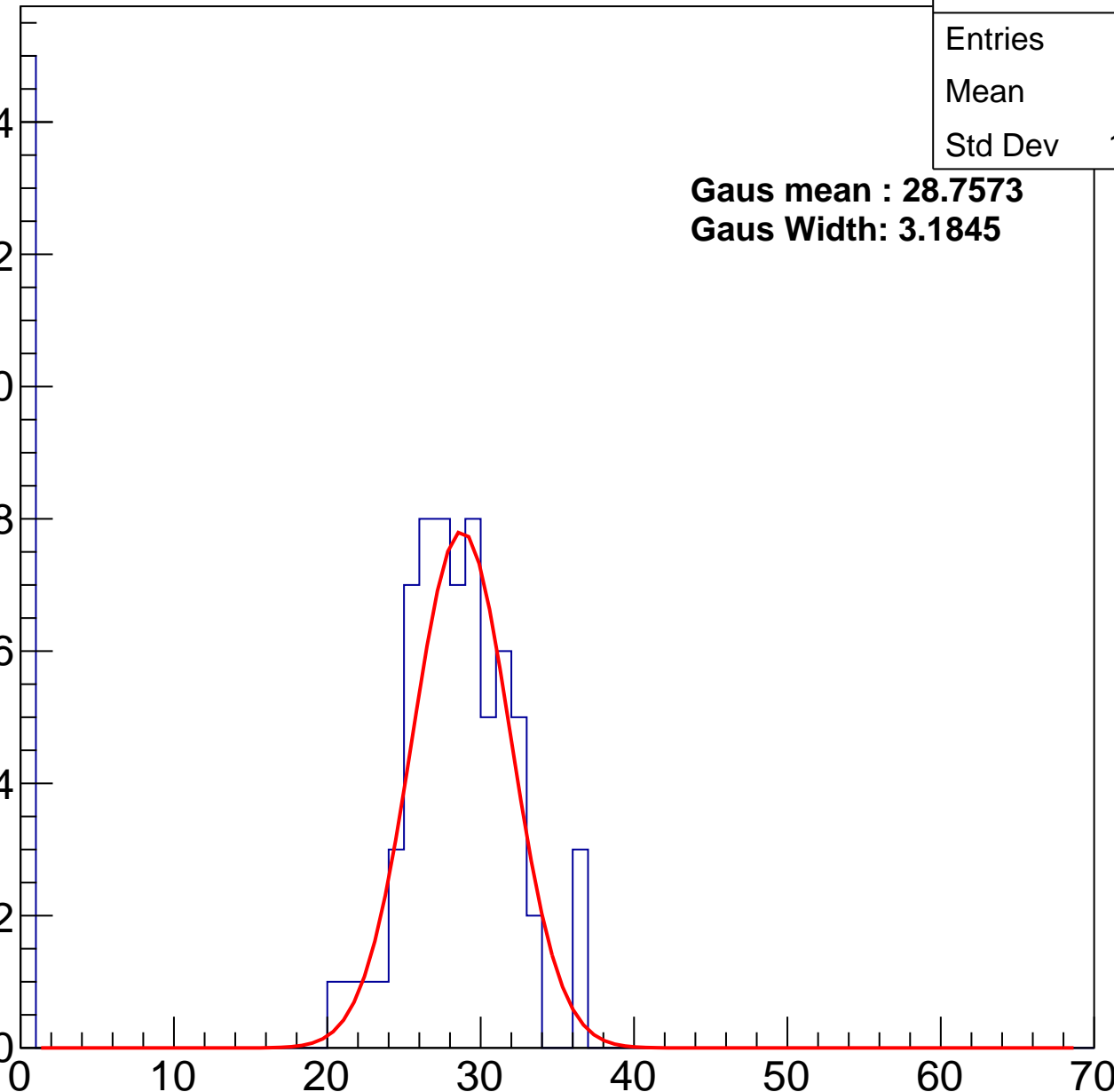
Gaus mean : 28.7573

Gaus Width: 3.1845

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U18-ch87, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	35.4
Std Dev	5.307

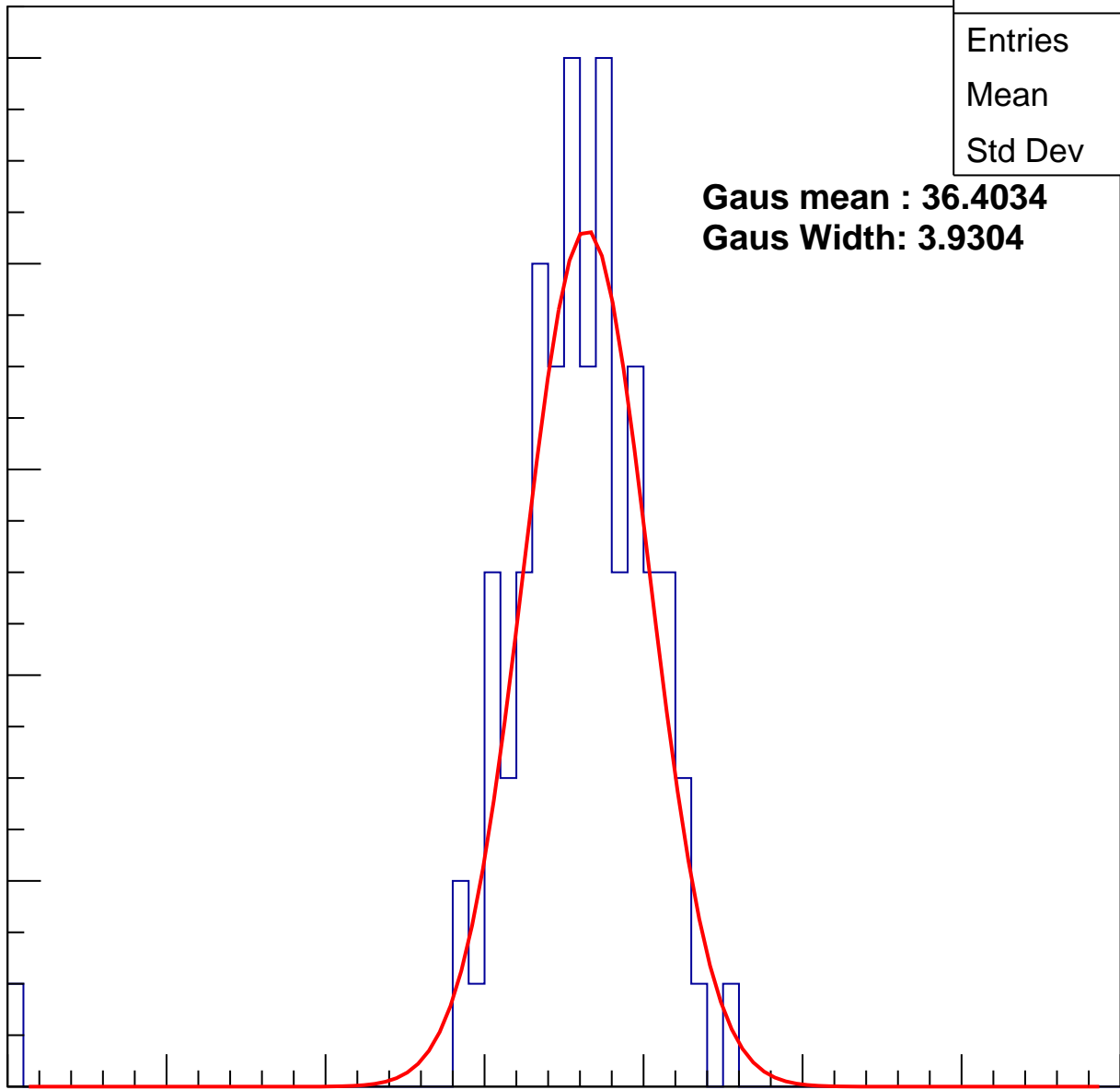
Gaus mean : 36.4034

Gaus Width: 3.9304

10
8
6
4
2
0

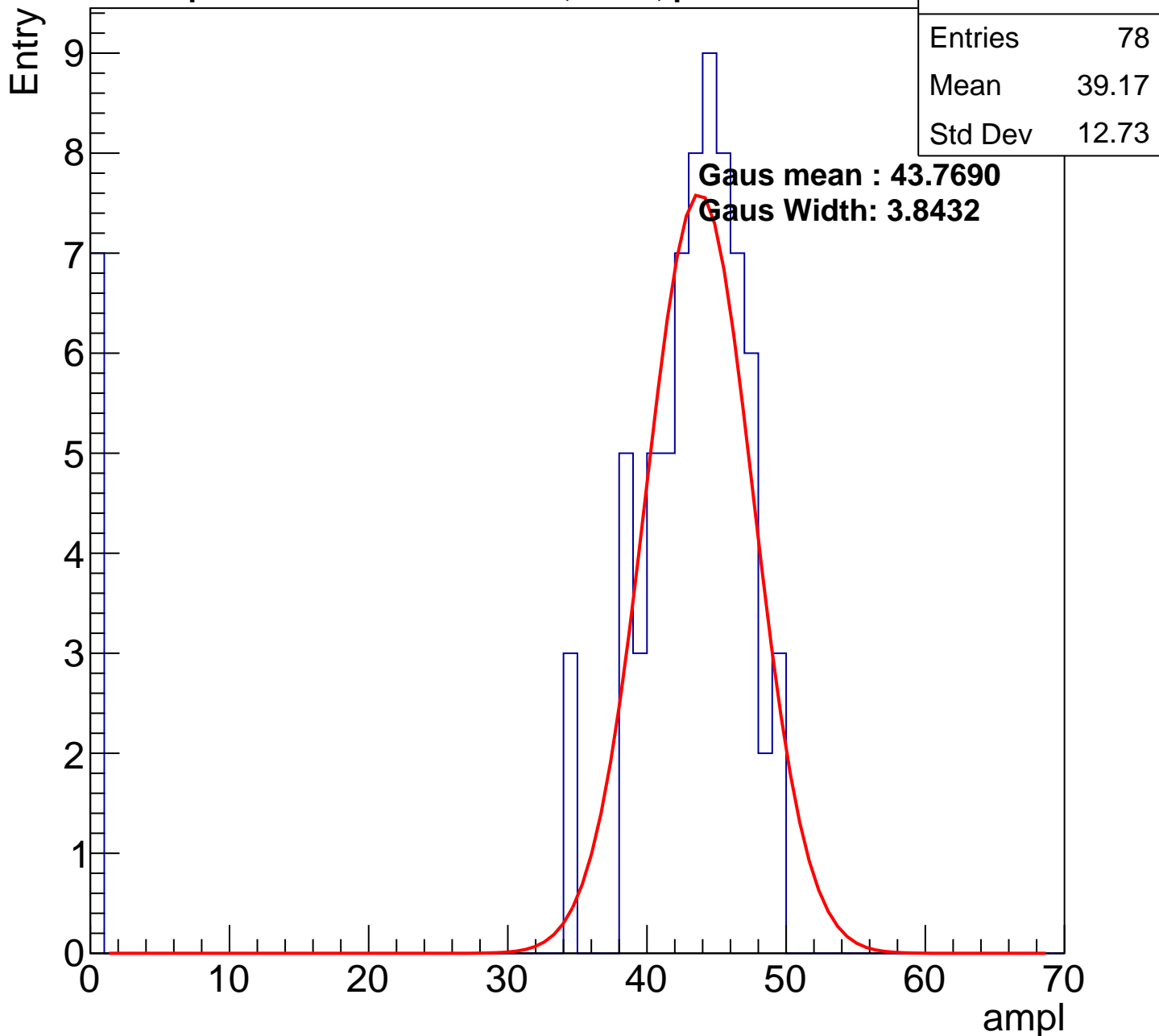
ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch87, adc2

calib_packv5_041523_1651.root, FC#0, port C2

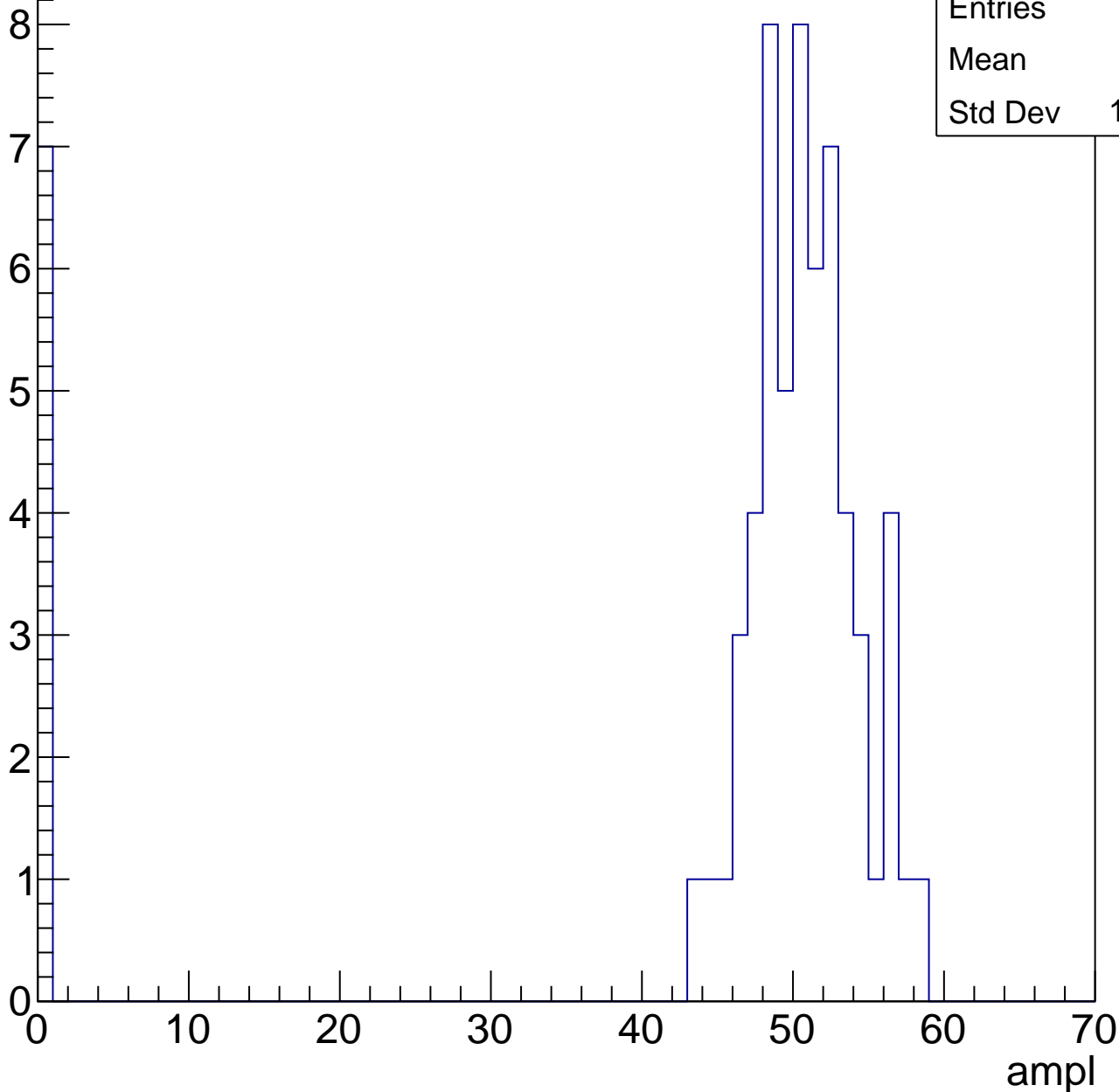


B1L103S, U18-ch87, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

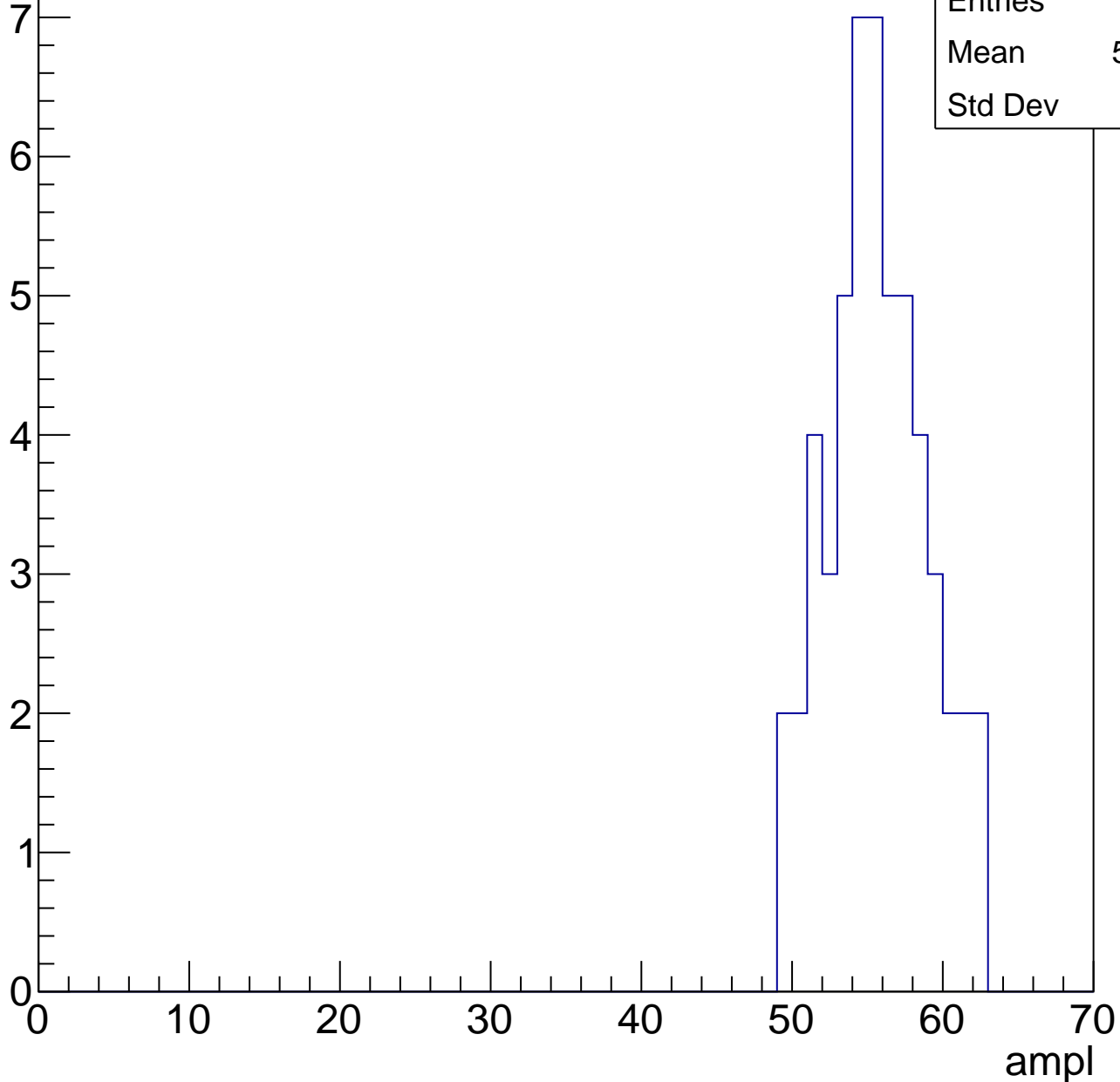
Entries	65
Mean	45
Std Dev	15.94



B1L103S, U18-ch87, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry



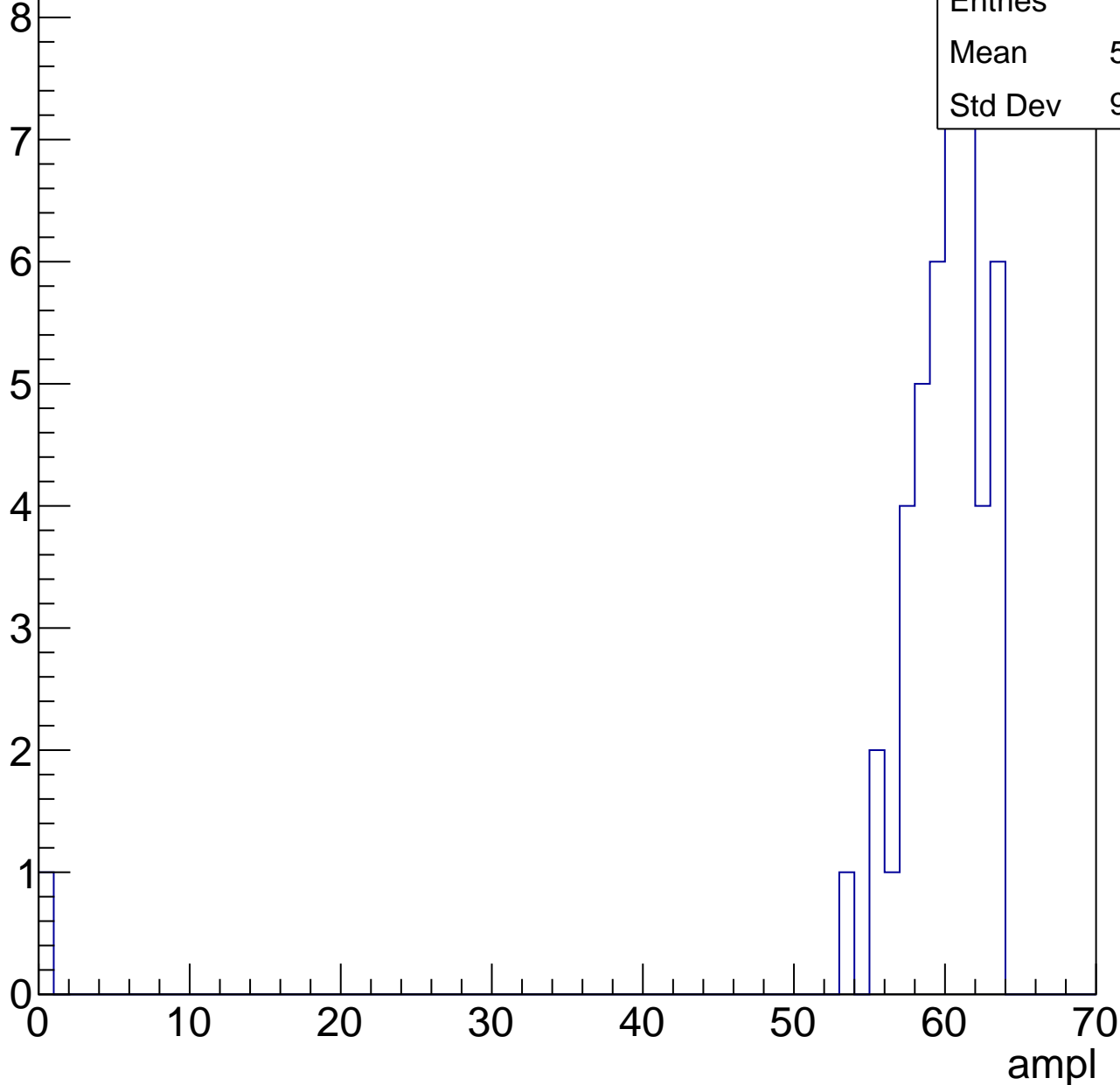
Entries	53
Mean	55.21
Std Dev	3.27

B1L103S, U18-ch87, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

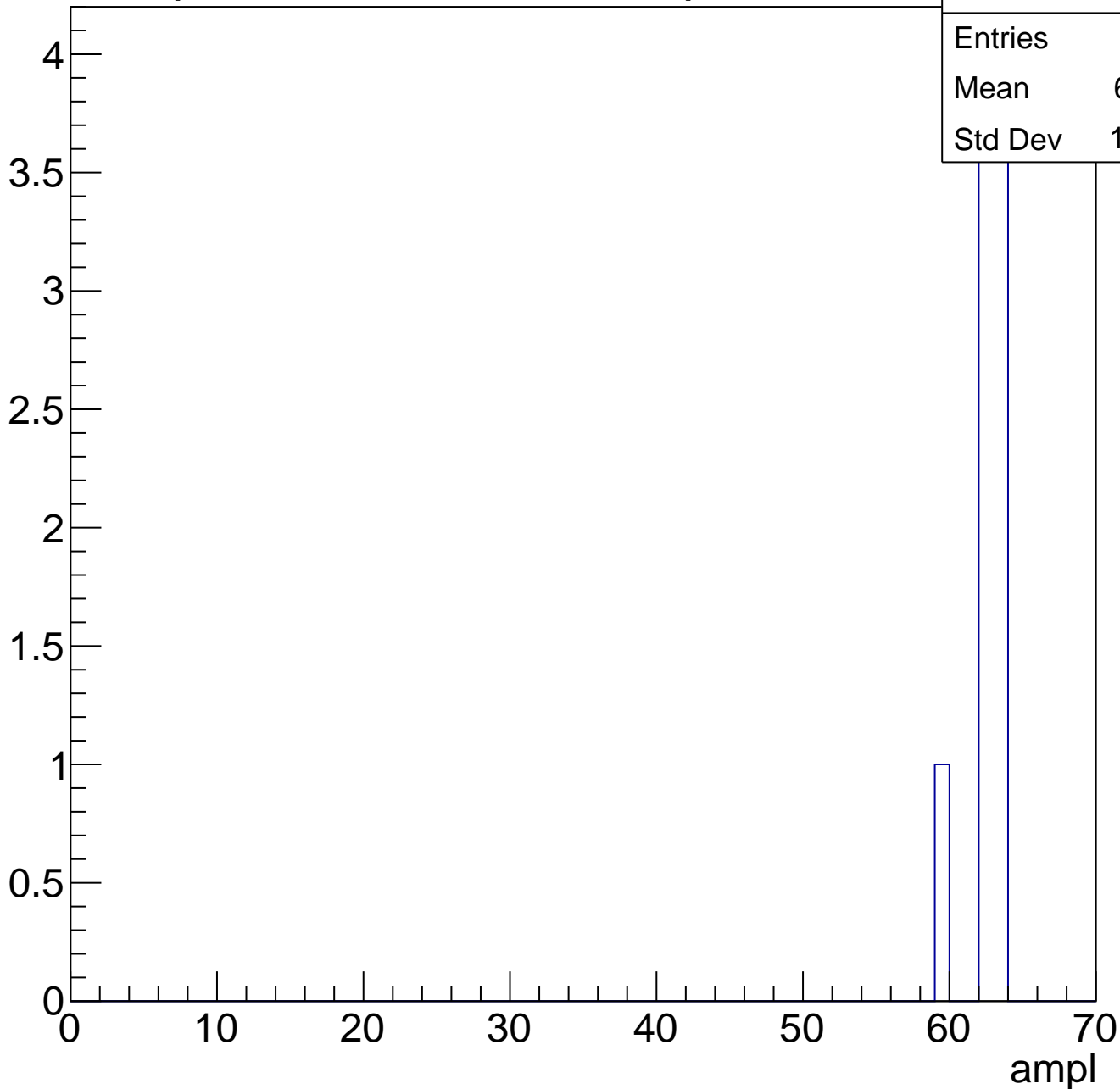
Entries	46
Mean	58.37
Std Dev	9.008



B1L103S, U18-ch87, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch87, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch88, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	83
Mean	21.12
Std Dev	12.31

Gaus mean : 27.7195

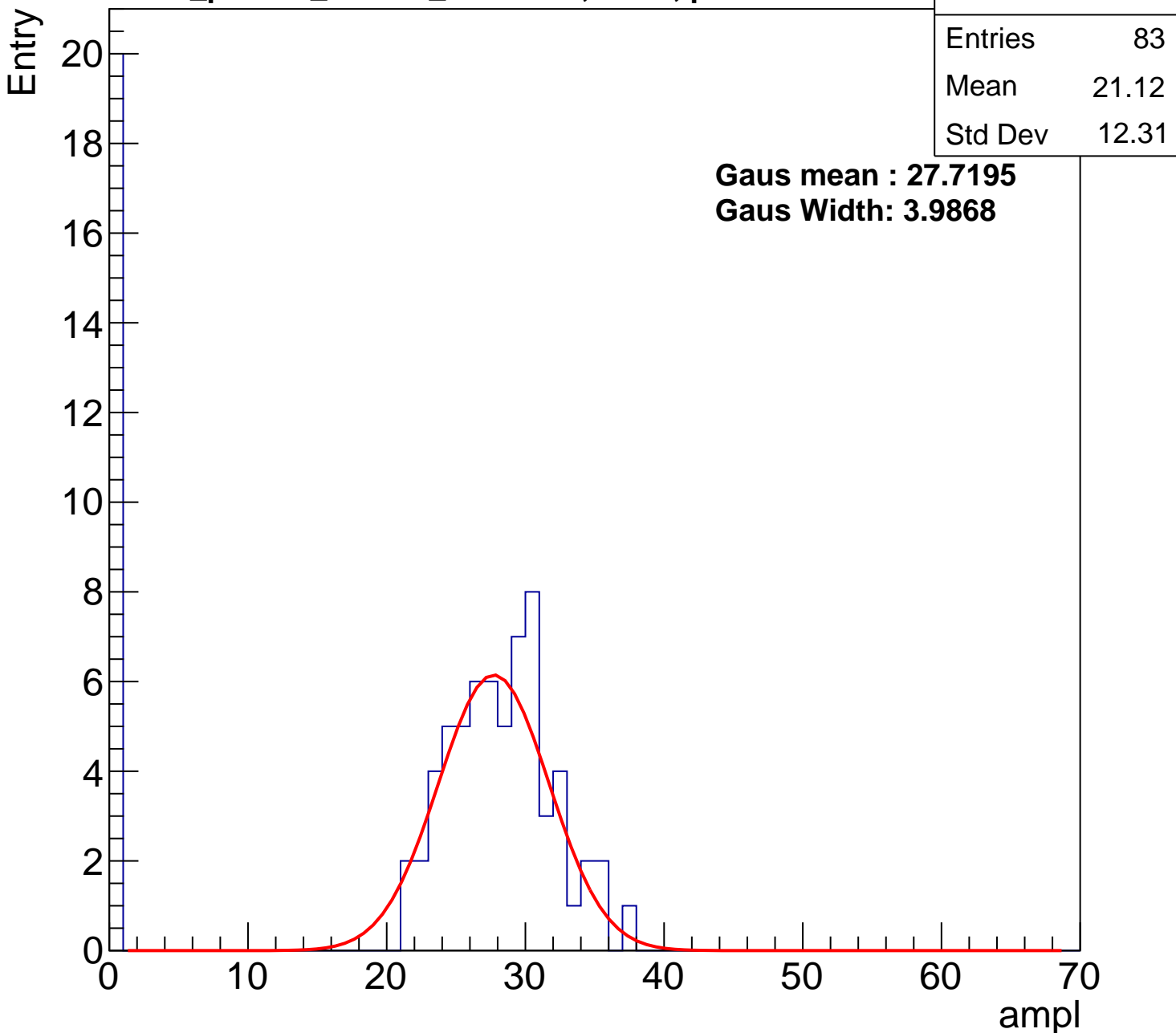
Gaus Width: 3.9868

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch88, adc1

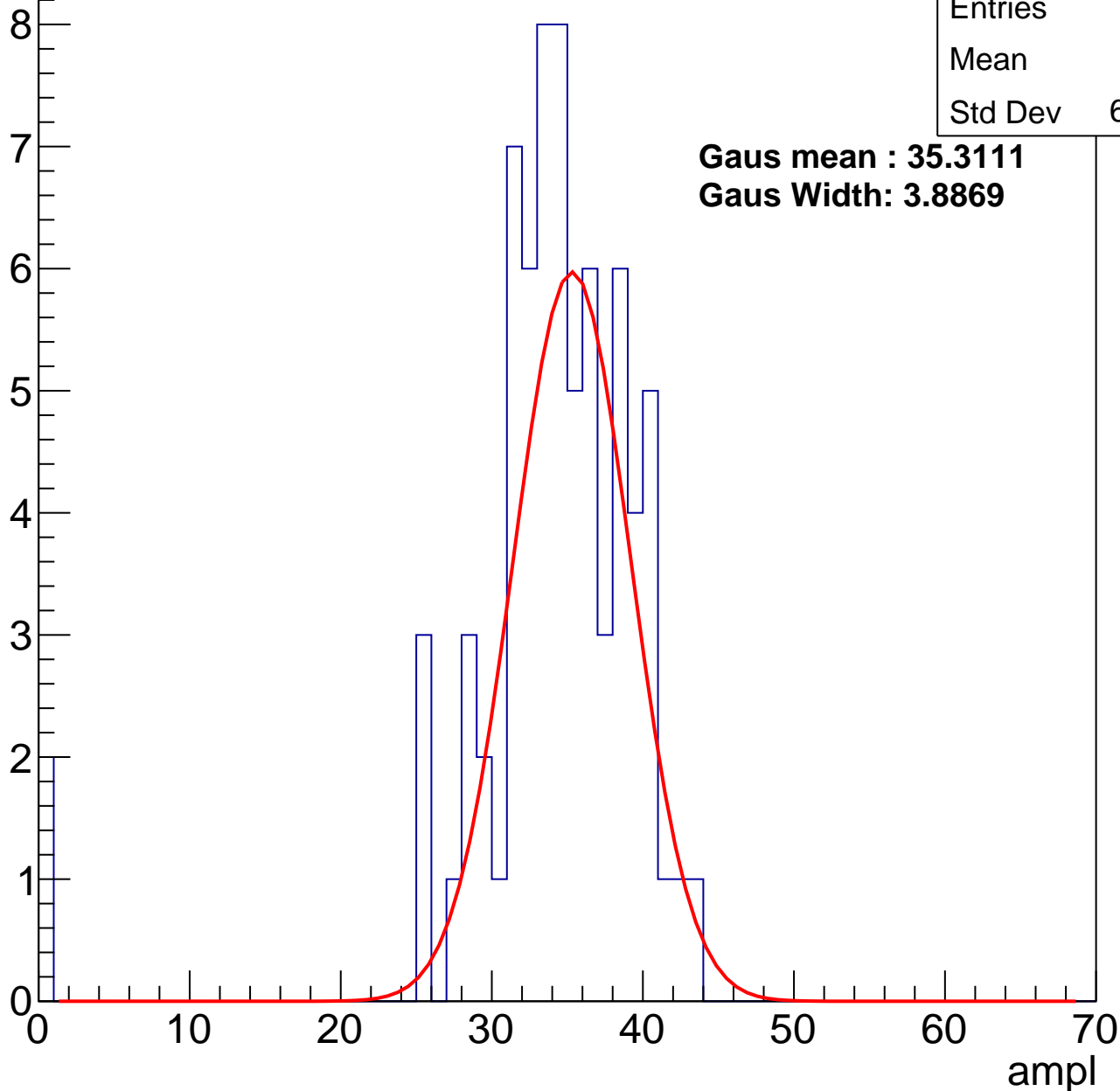
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	33.3
Std Dev	6.888

Gaus mean : 35.3111

Gaus Width: 3.8869



B1L103S, U18-ch88, adc2

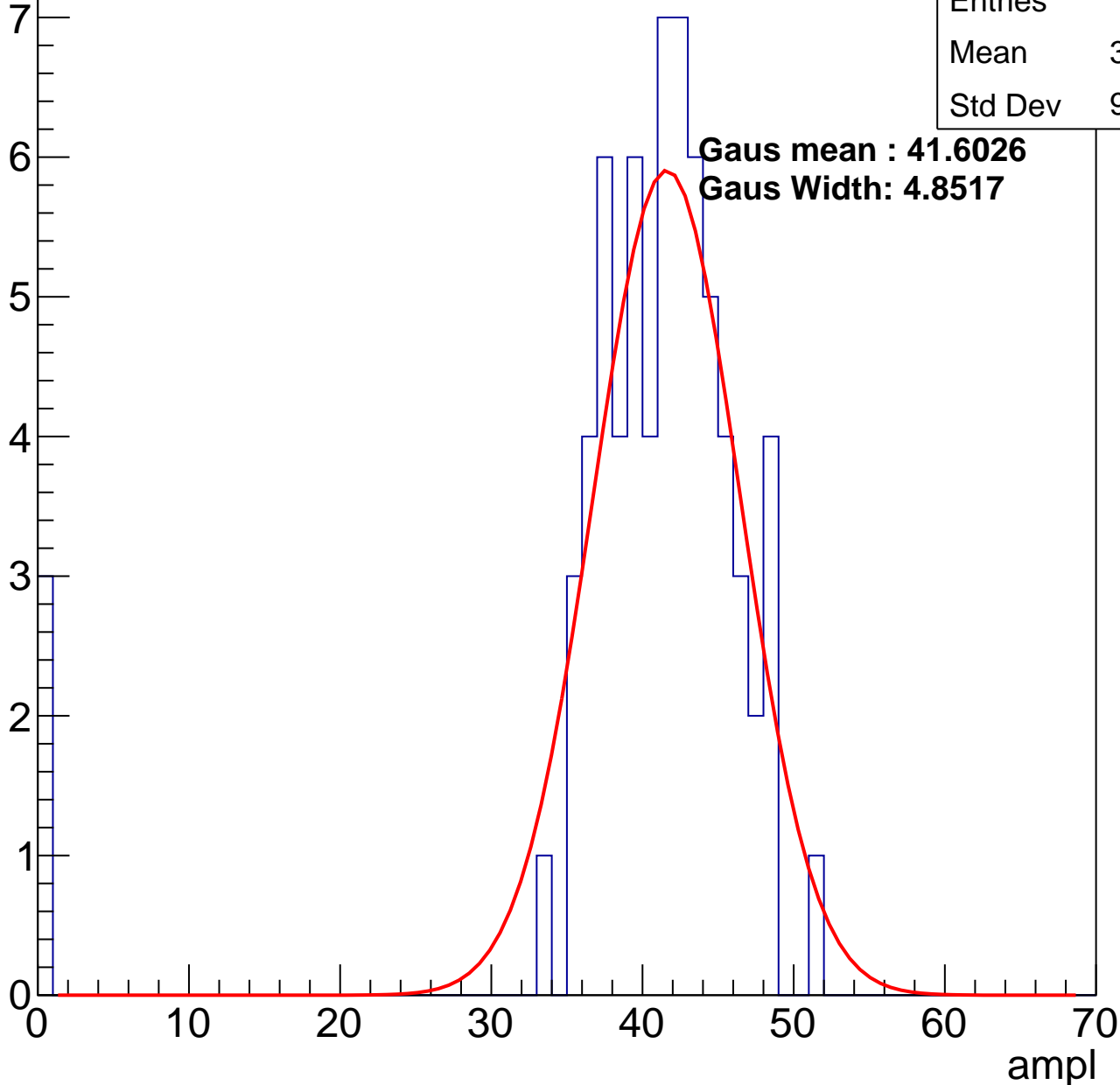
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	39.49
Std Dev	9.179

Gaus mean : 41.6026

Gaus Width: 4.8517



B1L103S, U18-ch88, adc3

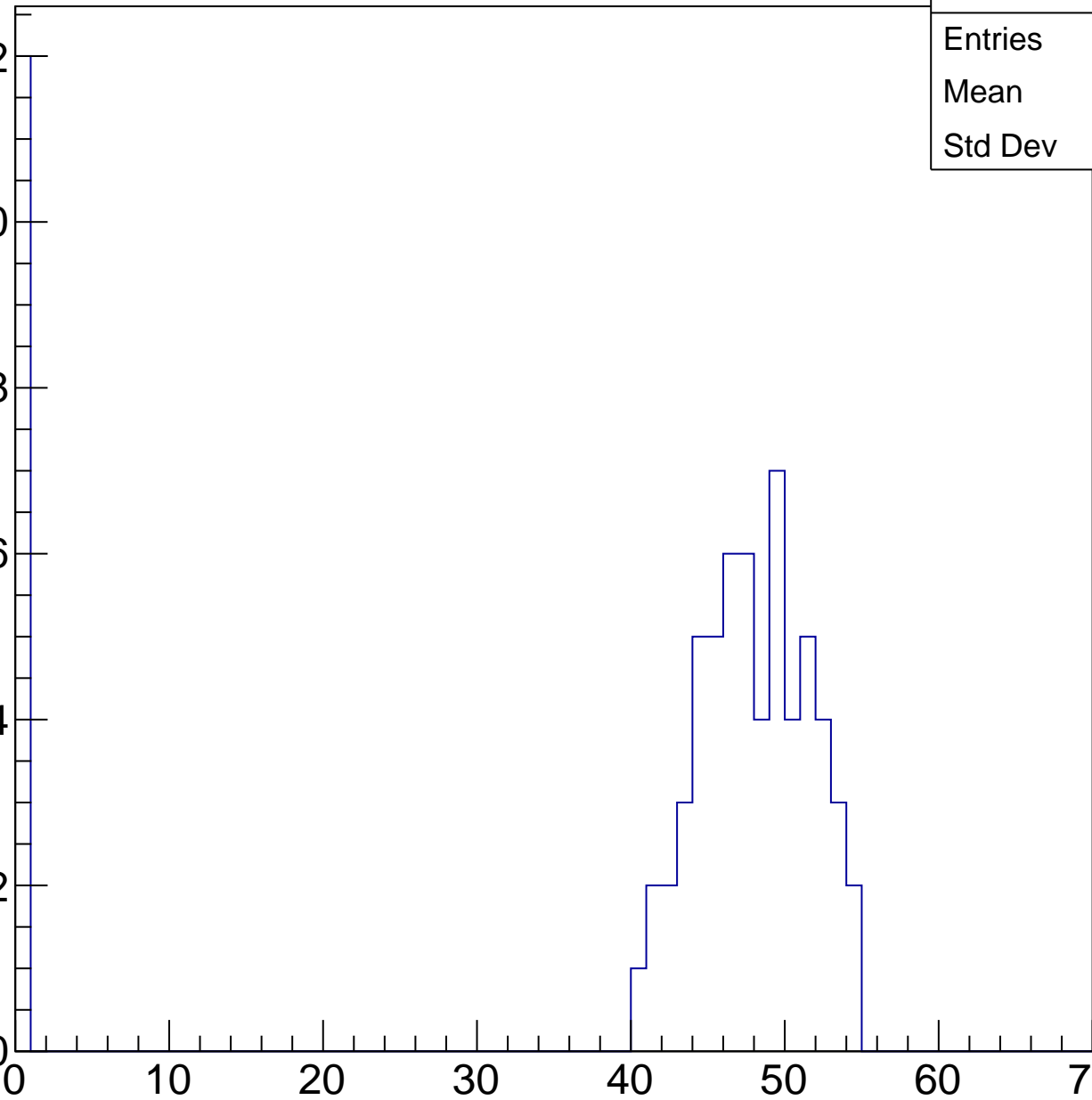
calib_packv5_041523_1651.root, FC#0, port C2

Entry

12
10
8
6
4
2
0

Entries	71
Mean	39.48
Std Dev	18.09

ampl

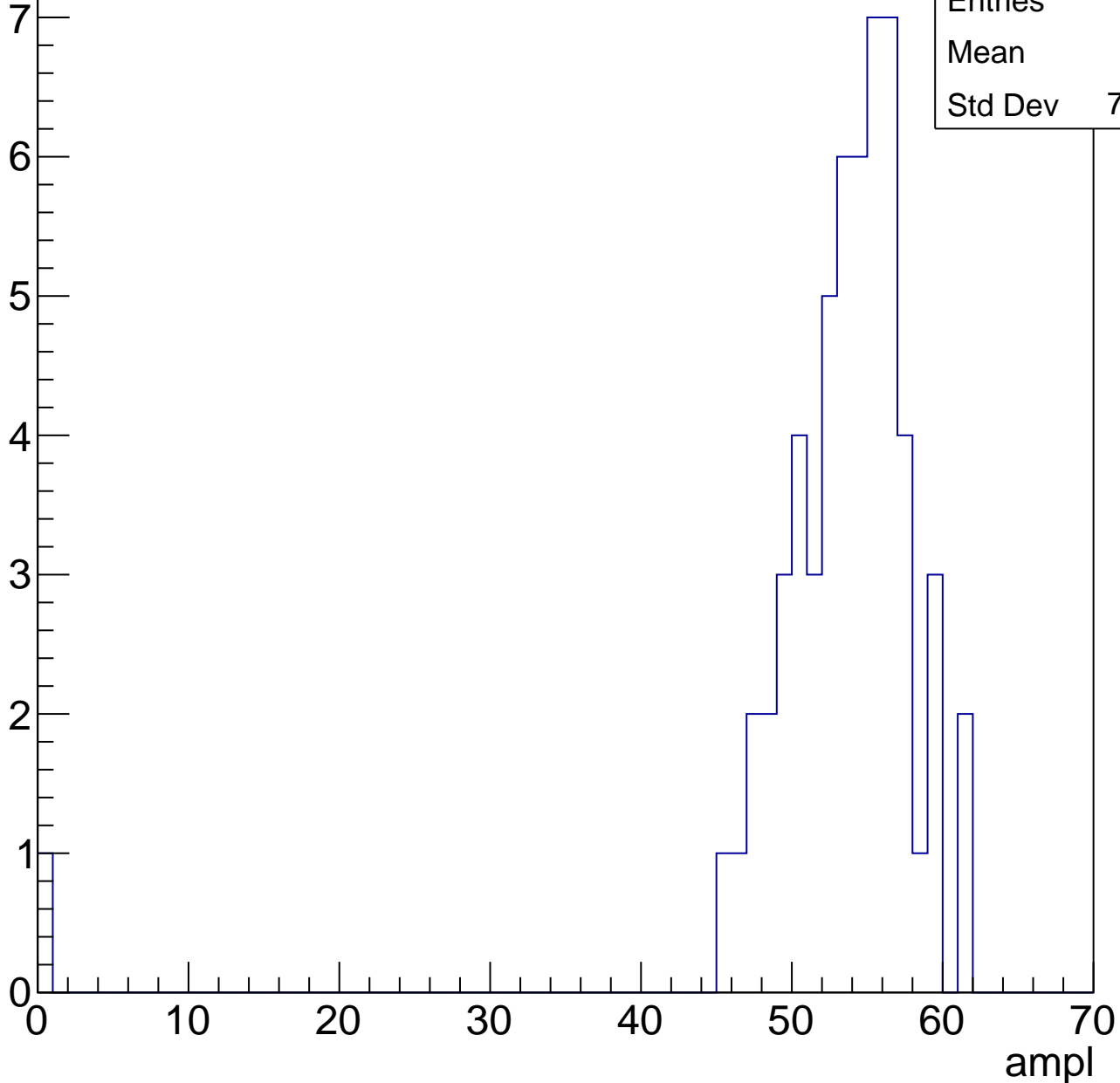


B1L103S, U18-ch88, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

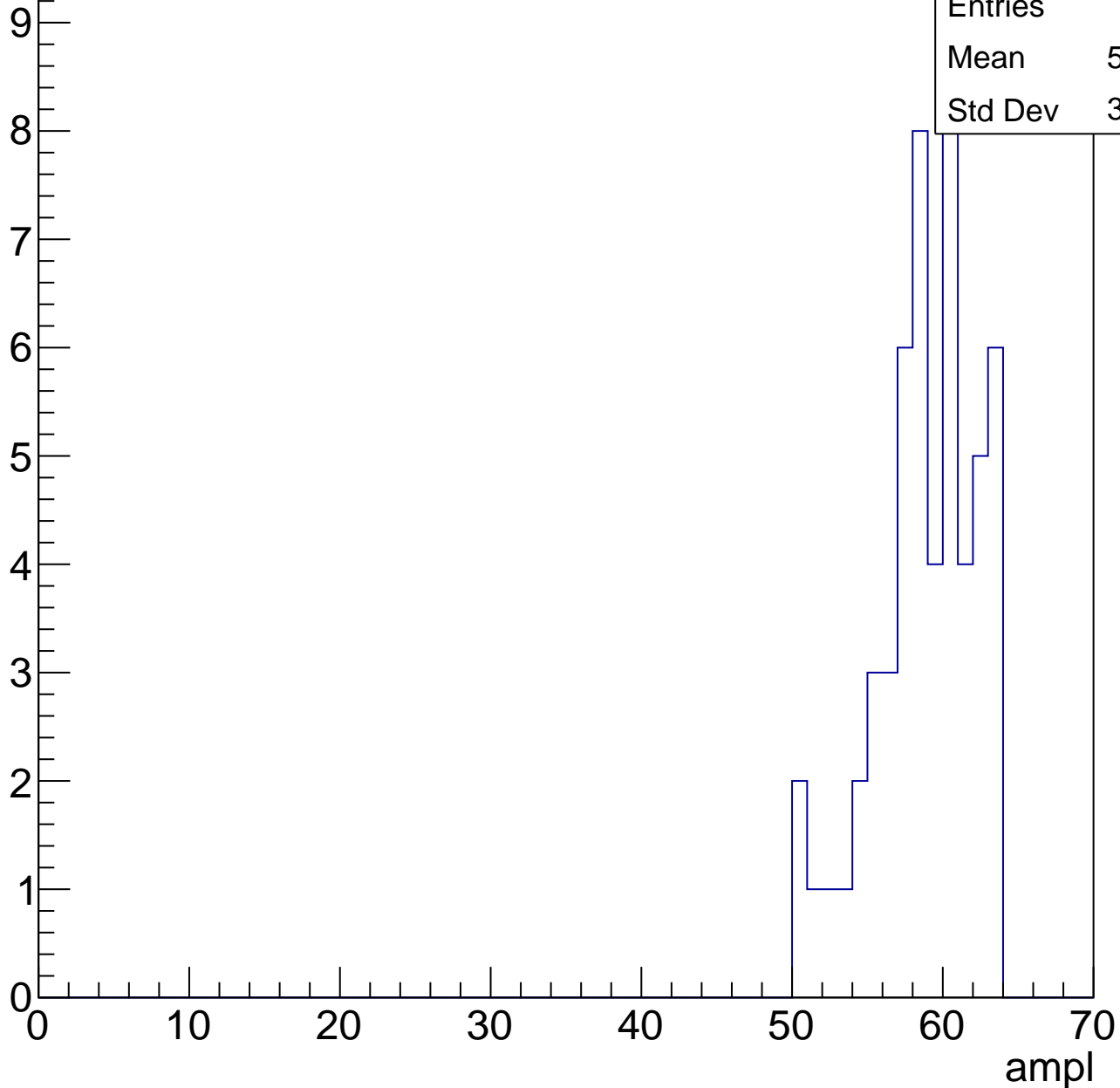
Entries	58
Mean	52.5
Std Dev	7.826



B1L103S, U18-ch88, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

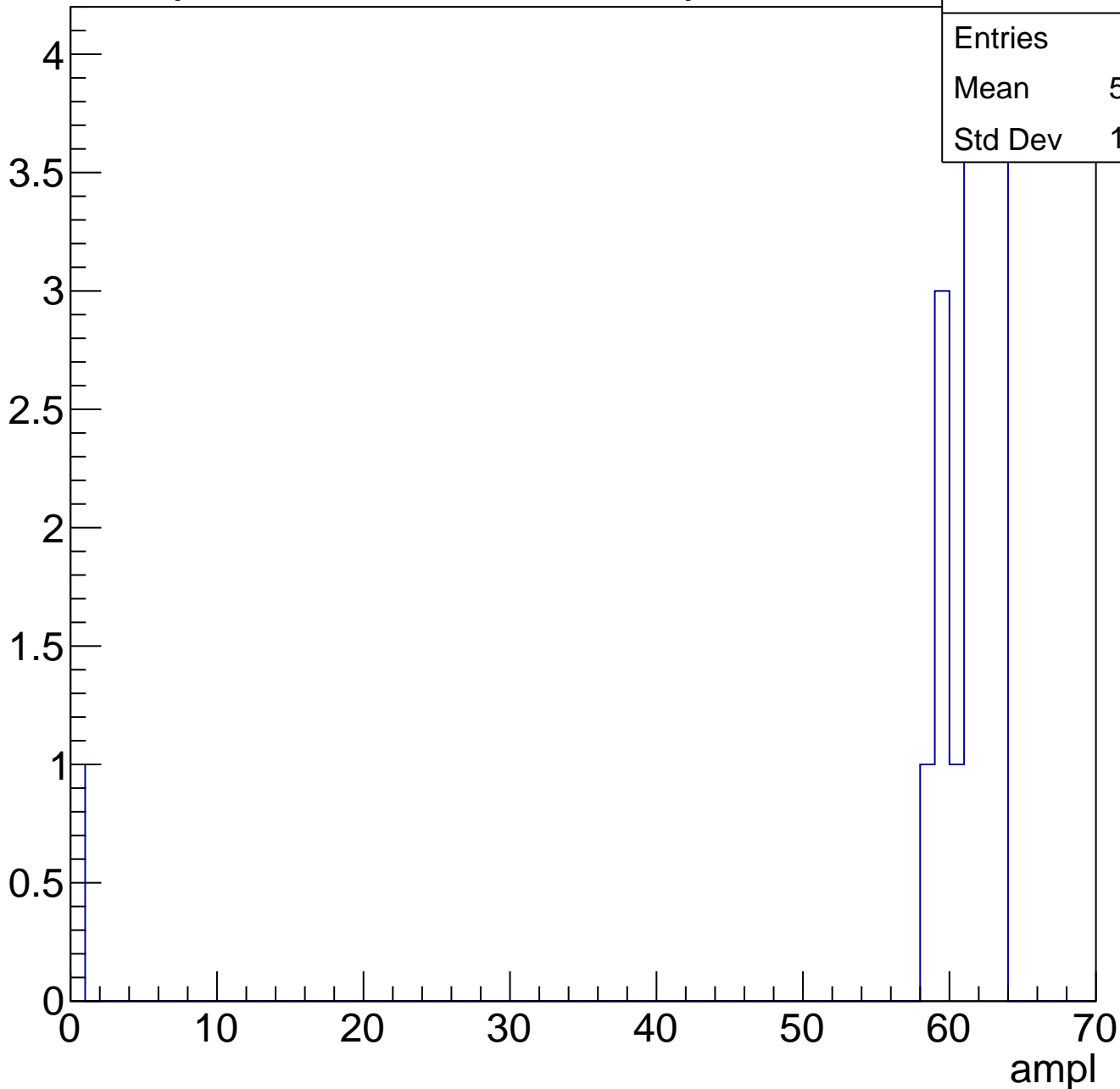


Entries	55
Mean	58.38
Std Dev	3.338

B1L103S, U18-ch88, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch88, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U18-ch89, adc0

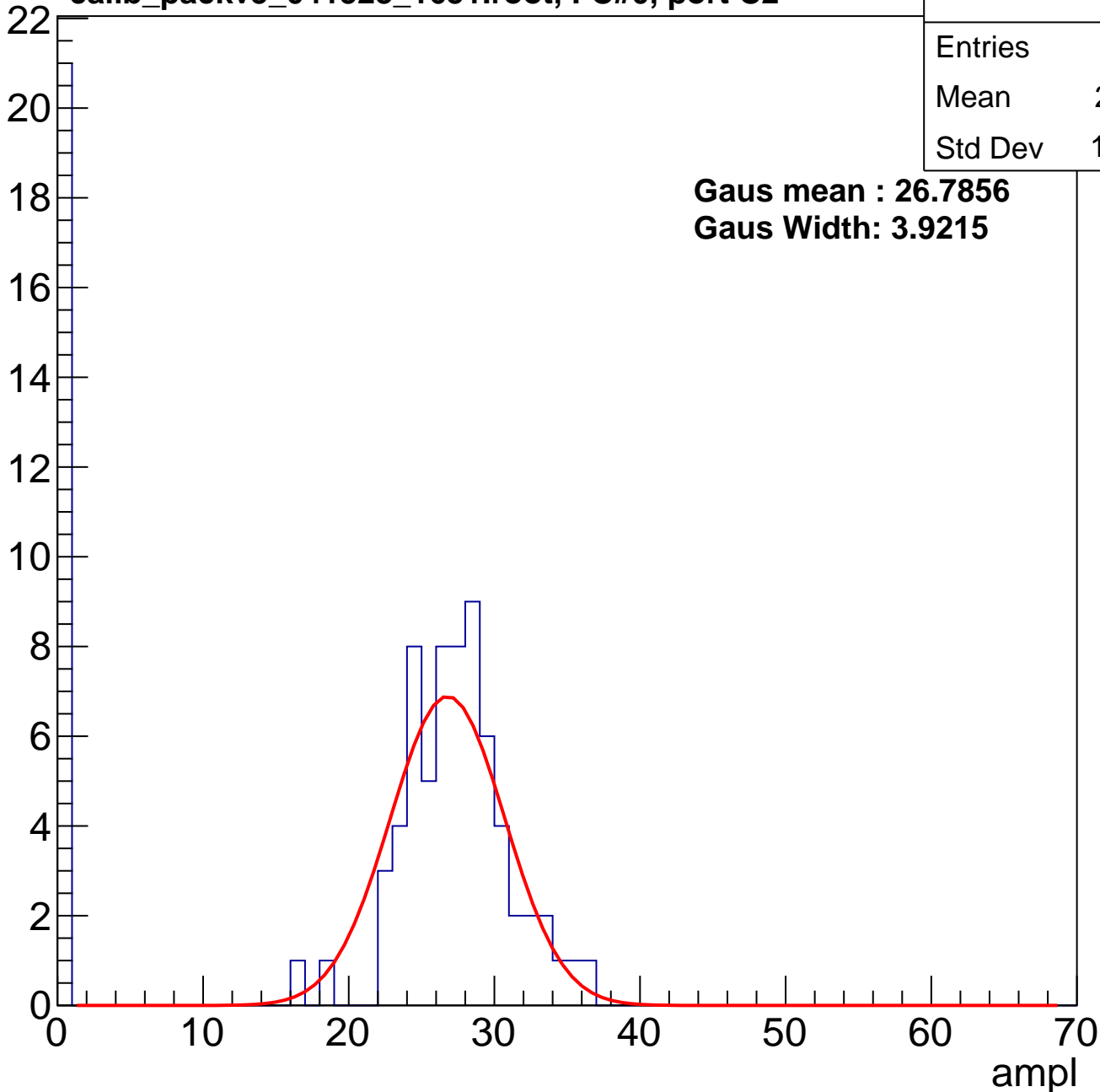
calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	20.41
Std Dev	11.94

Gaus mean : 26.7856

Gaus Width: 3.9215

Entry



B1L103S, U18-ch89, adc1

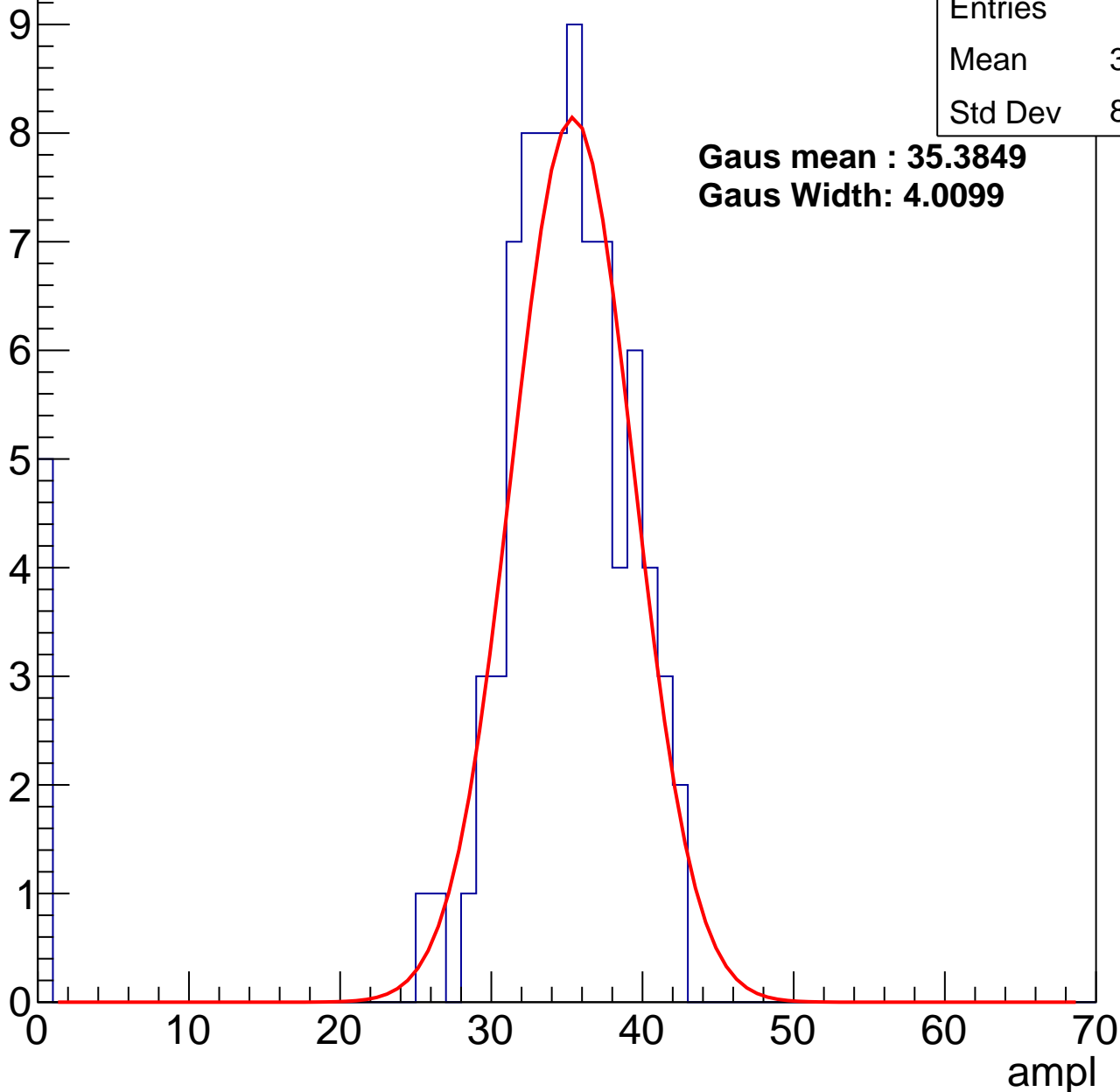
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	87
Mean	32.69
Std Dev	8.822

Gaus mean : 35.3849

Gaus Width: 4.0099



B1L103S, U18-ch89, adc2

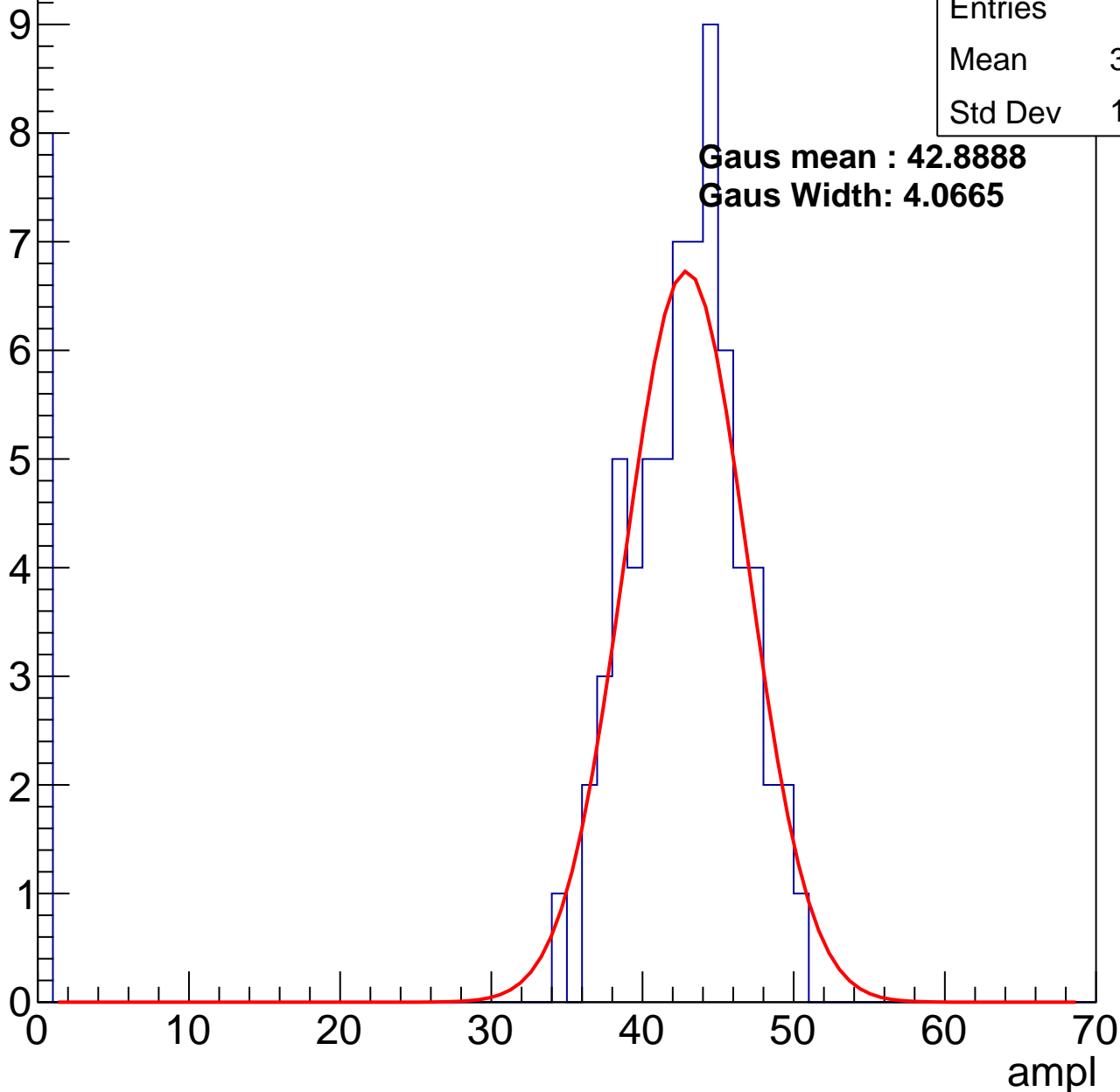
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	37.93
Std Dev	13.52

Gaus mean : 42.8888

Gaus Width: 4.0665

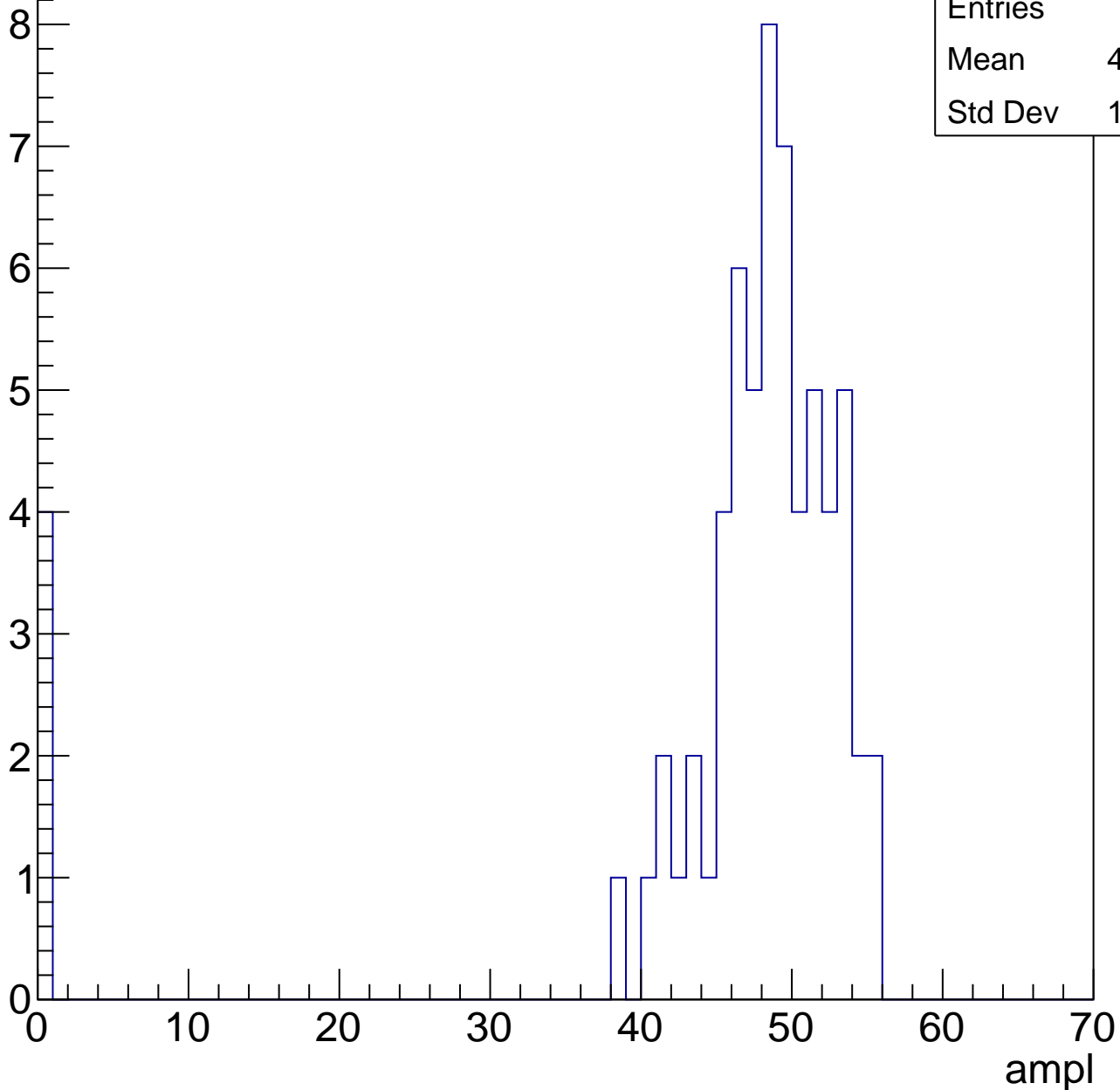


B1L103S, U18-ch89, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	45.25
Std Dev	12.24

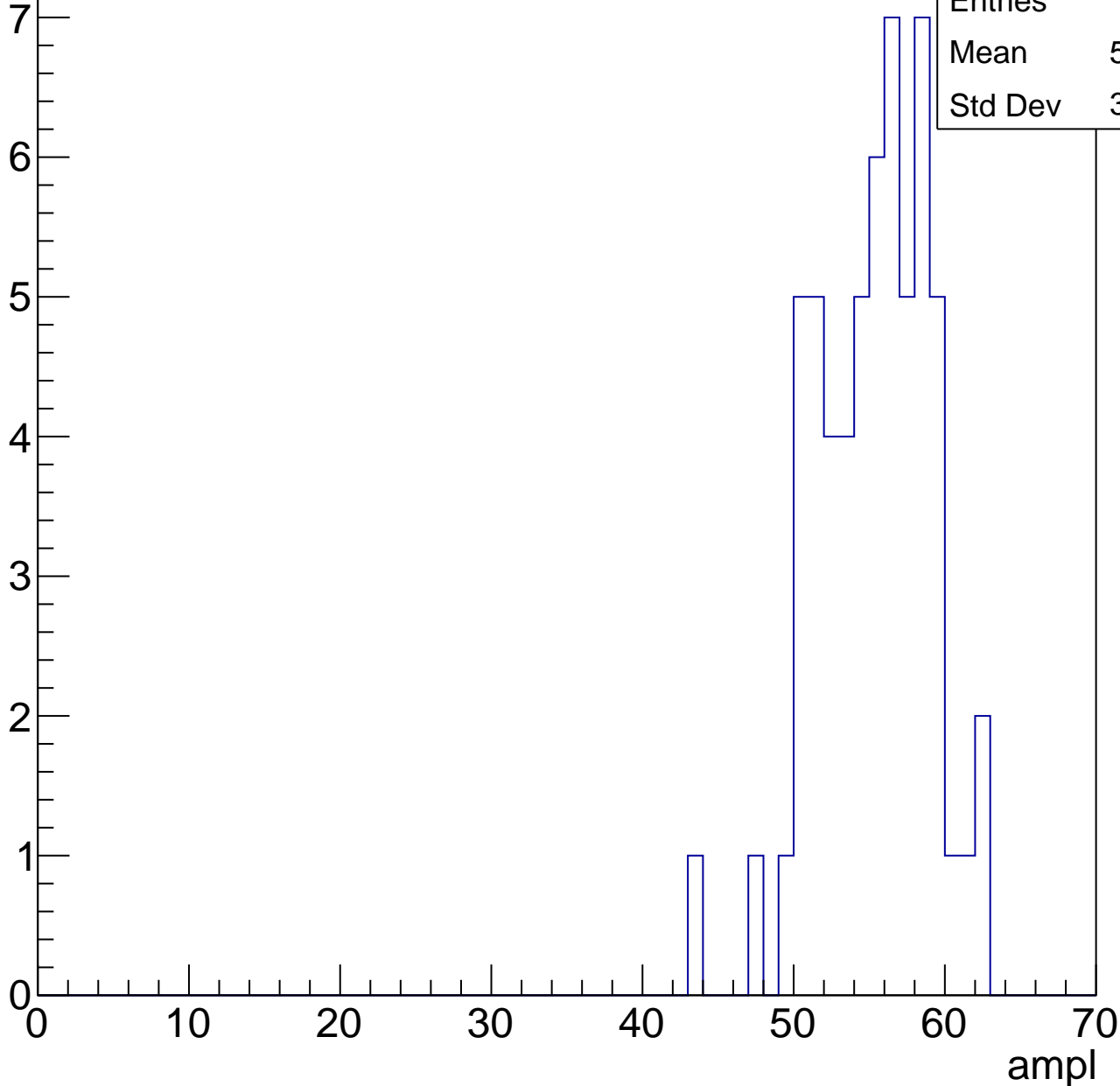


B1L103S, U18-ch89, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

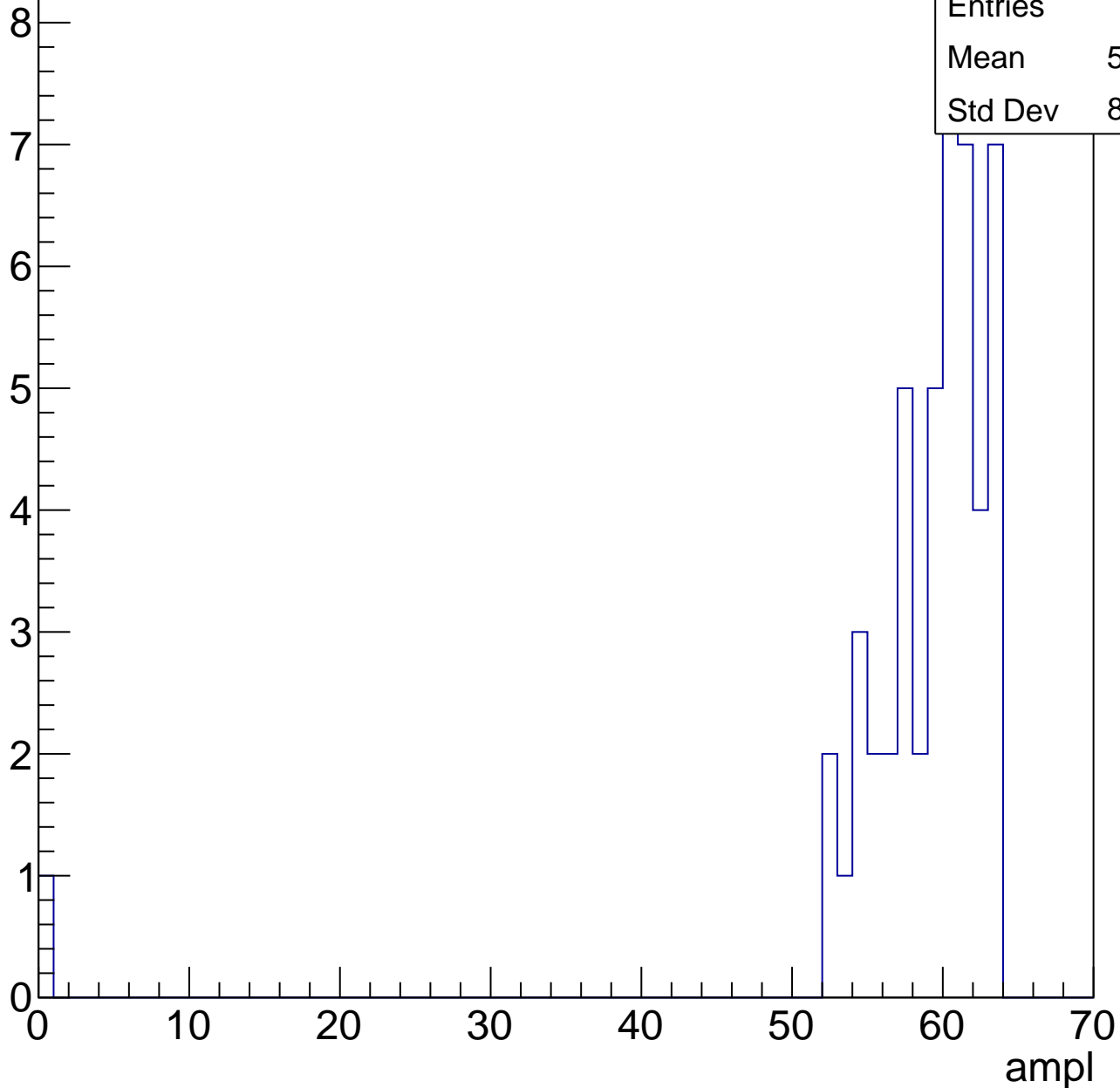
Entries	60
Mean	54.78
Std Dev	3.724



B1L103S, U18-ch89, adc5

calib_packv5_041523_1651.root, FC#0, port C2

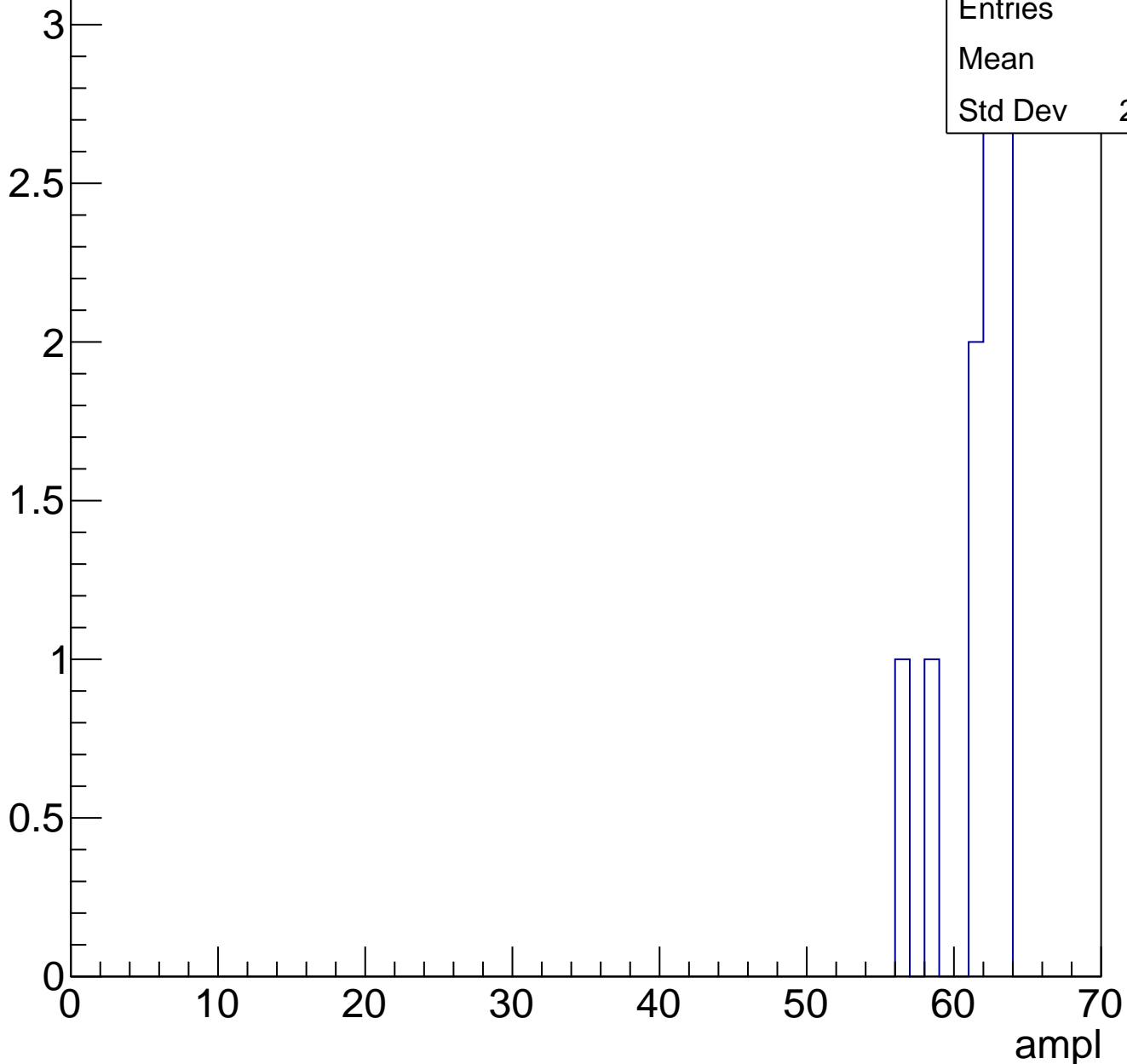
Entry



B1L103S, U18-ch89, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



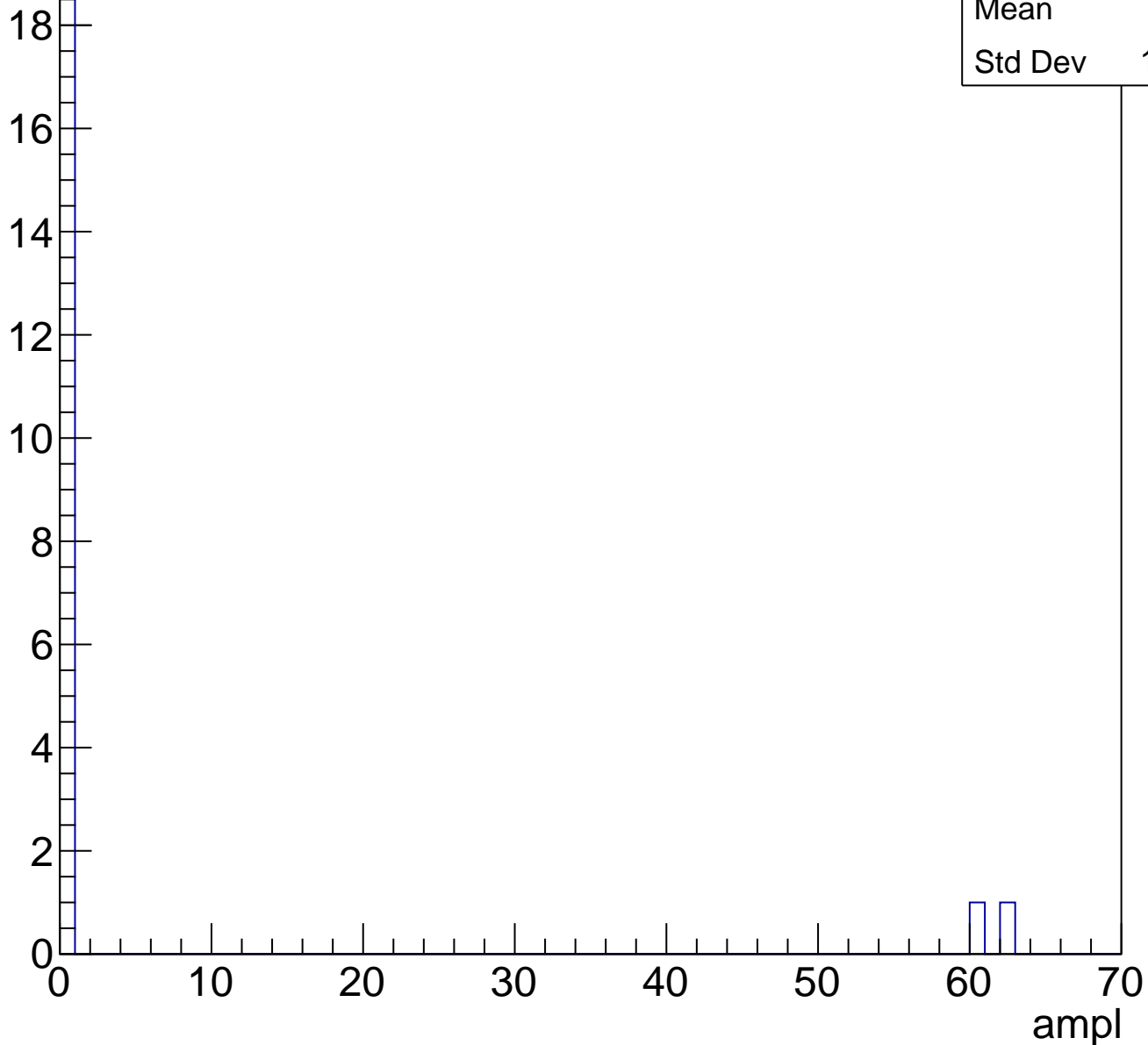
Entries	10
Mean	61.1
Std Dev	2.211

B1L103S, U18-ch89, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.91

Entry



B1L103S, U18-ch90, adc0

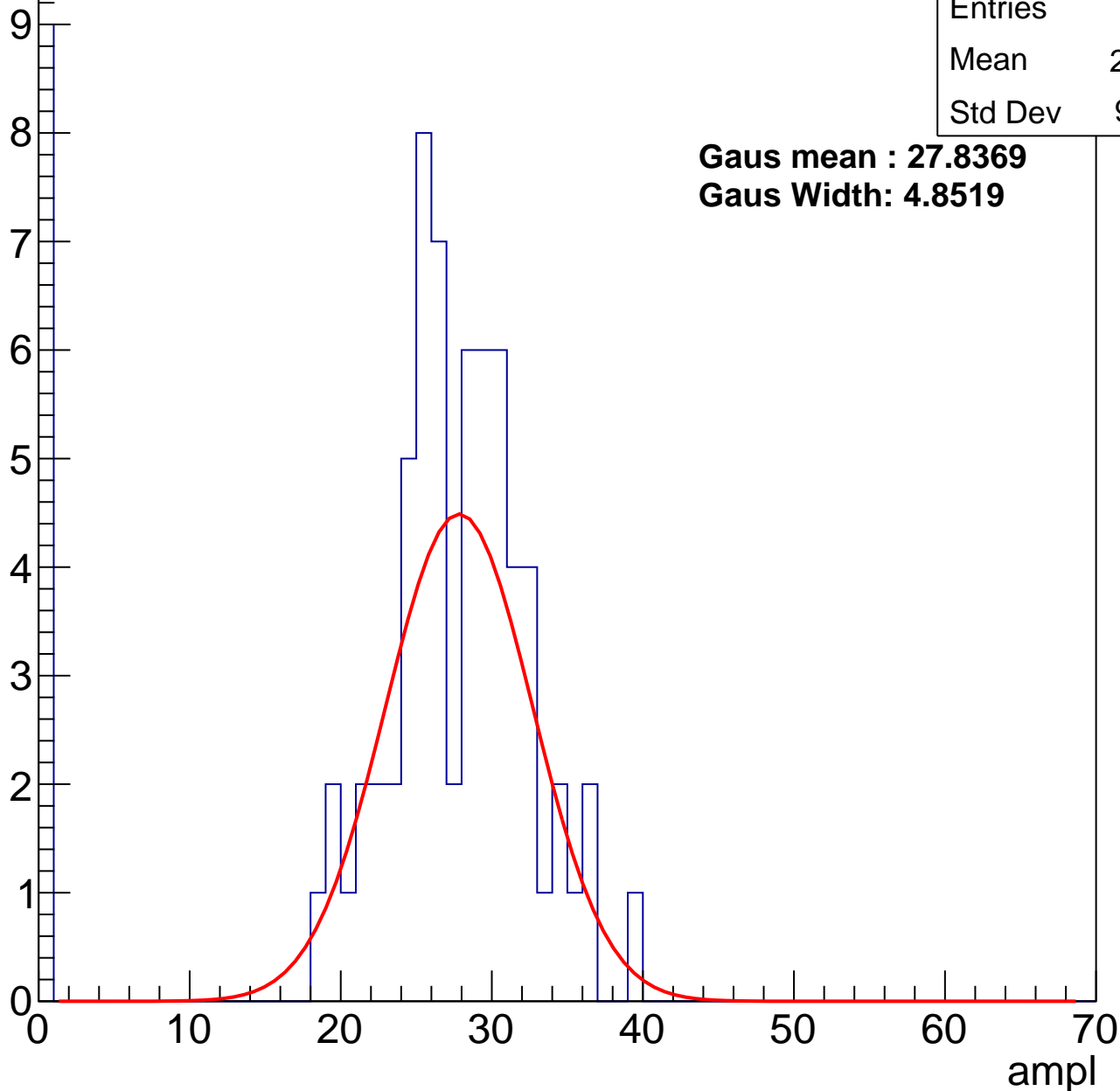
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	74
Mean	24.12
Std Dev	9.861

Gaus mean : 27.8369

Gaus Width: 4.8519



B1L103S, U18-ch90, adc1

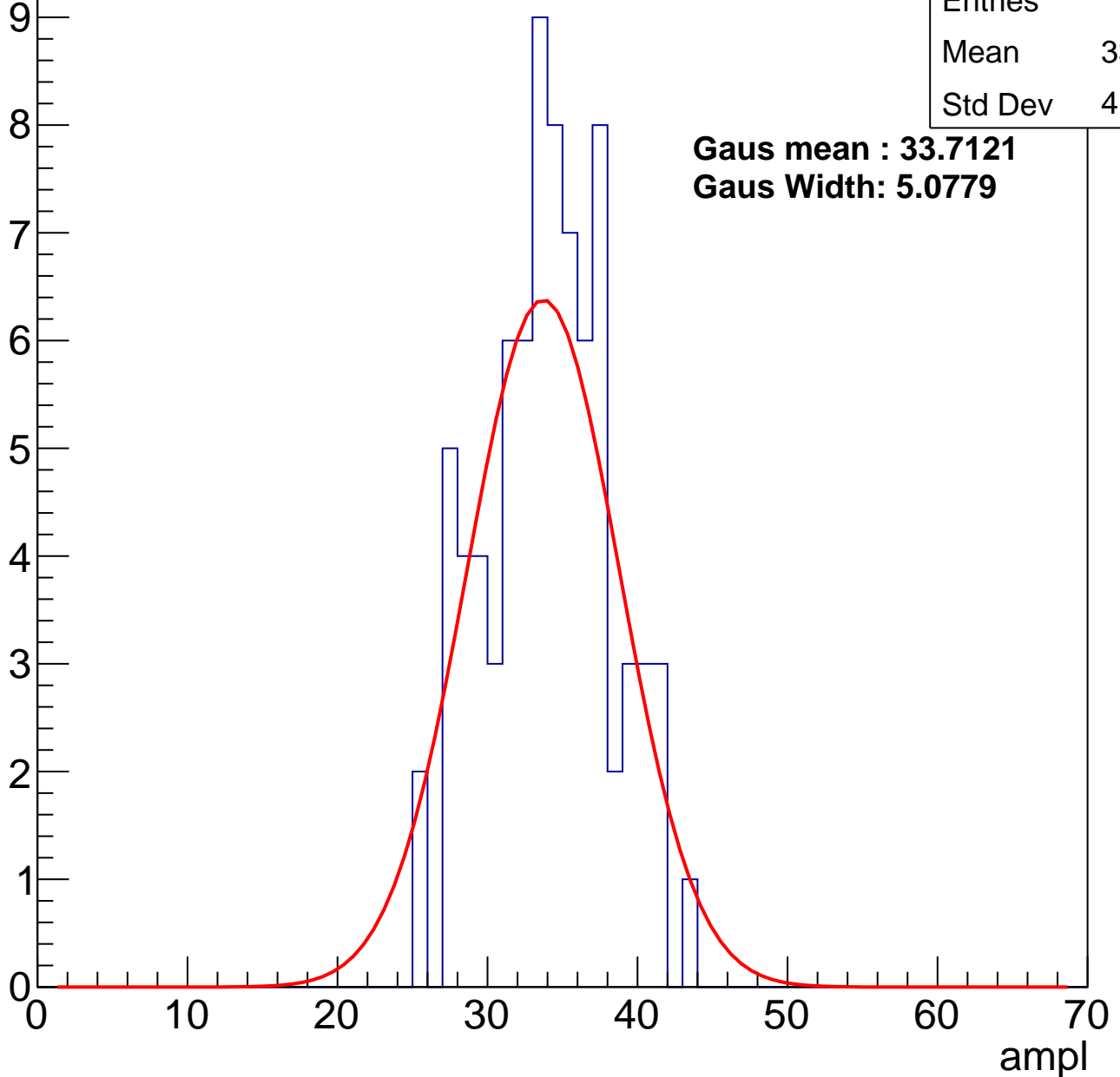
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	80
Mean	33.58
Std Dev	4.058

Gaus mean : 33.7121

Gaus Width: 5.0779



B1L103S, U18-ch90, adc2

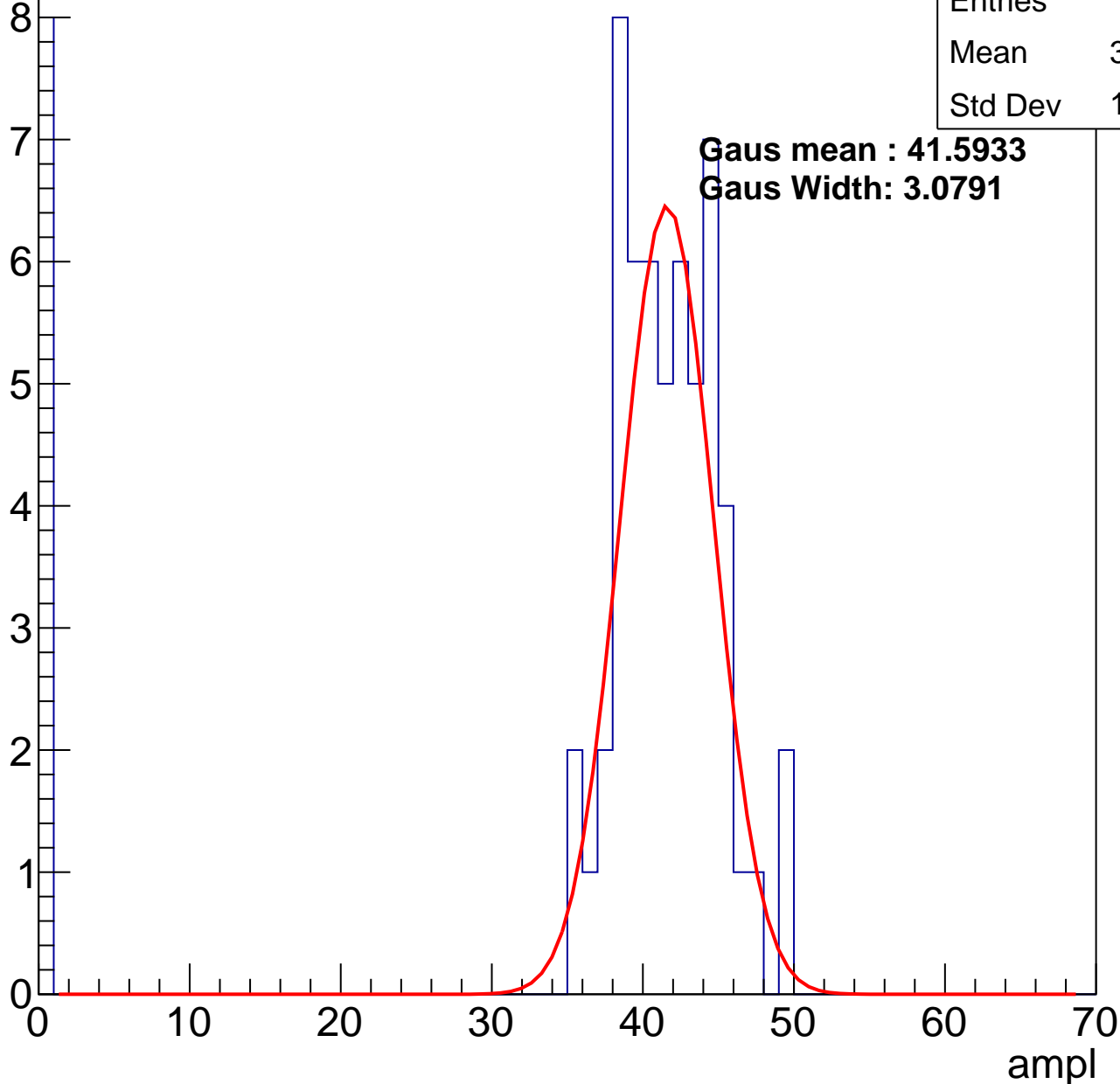
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.08
Std Dev	13.96

Gaus mean : 41.5933

Gaus Width: 3.0791

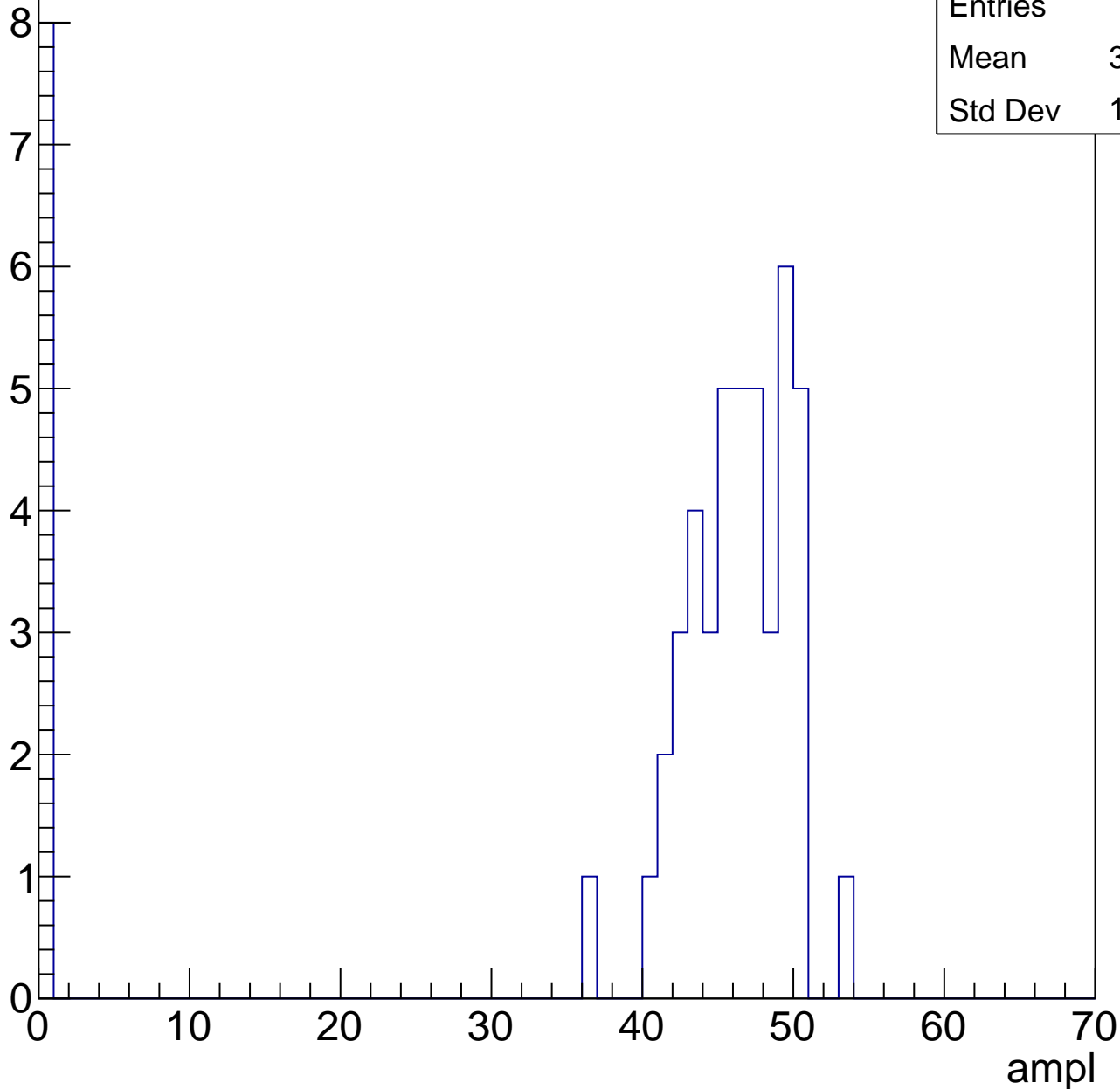


B1L103S, U18-ch90, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	52
Mean	38.83
Std Dev	16.84



B1L103S, U18-ch90, adc4

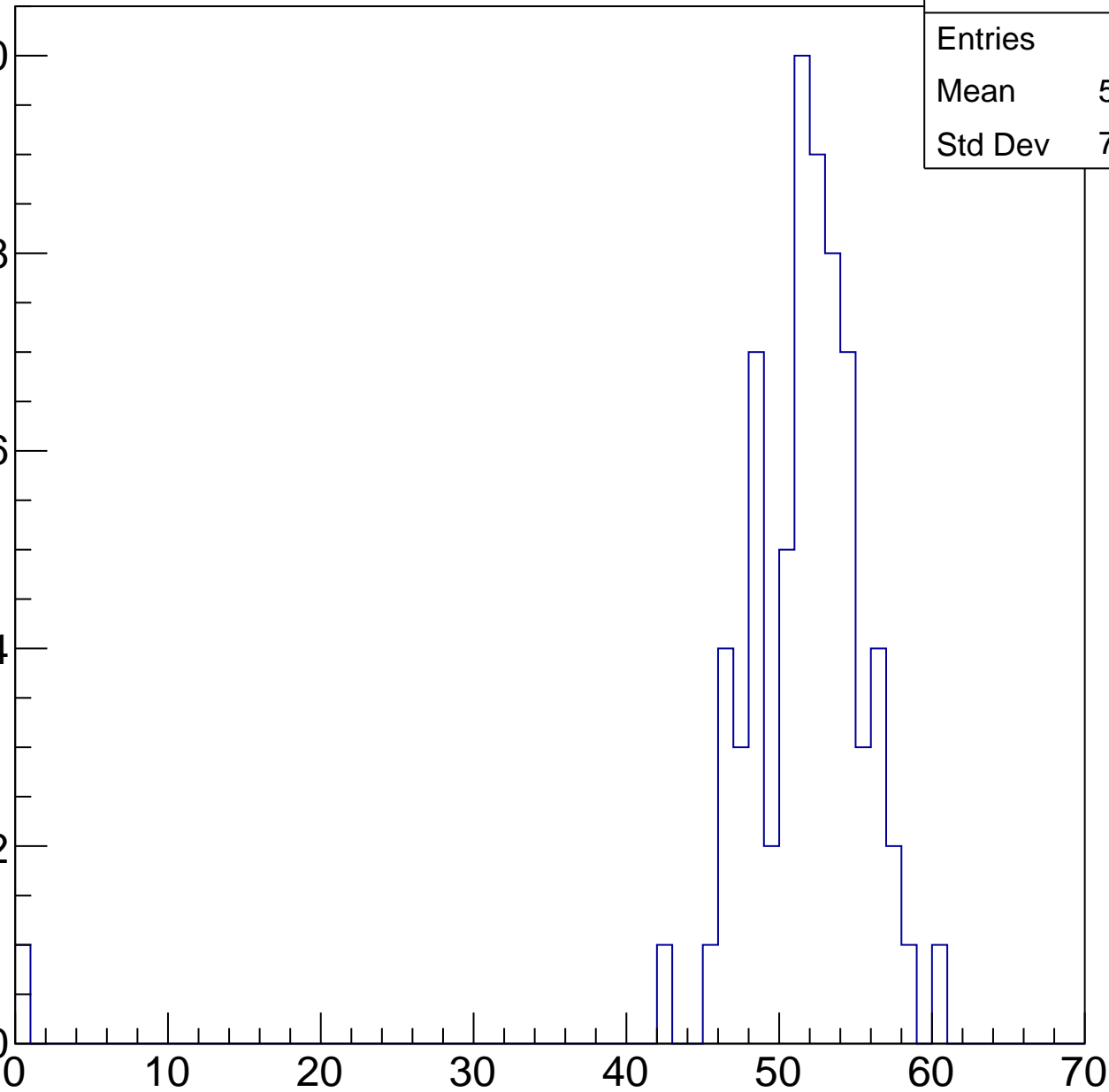
calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	50.68
Std Dev	7.008

Entry

10
8
6
4
2
0

ampl

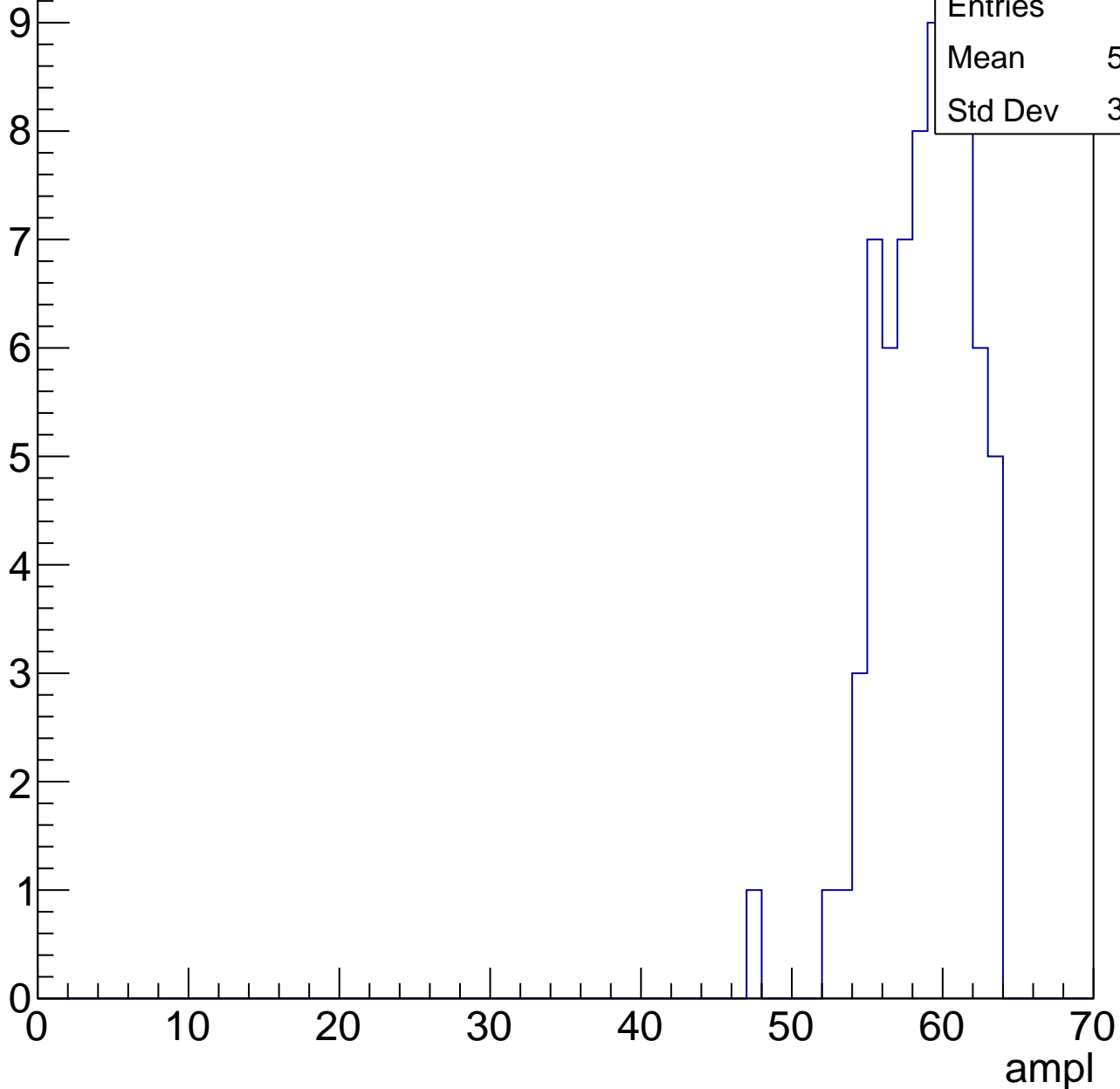


B1L103S, U18-ch90, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

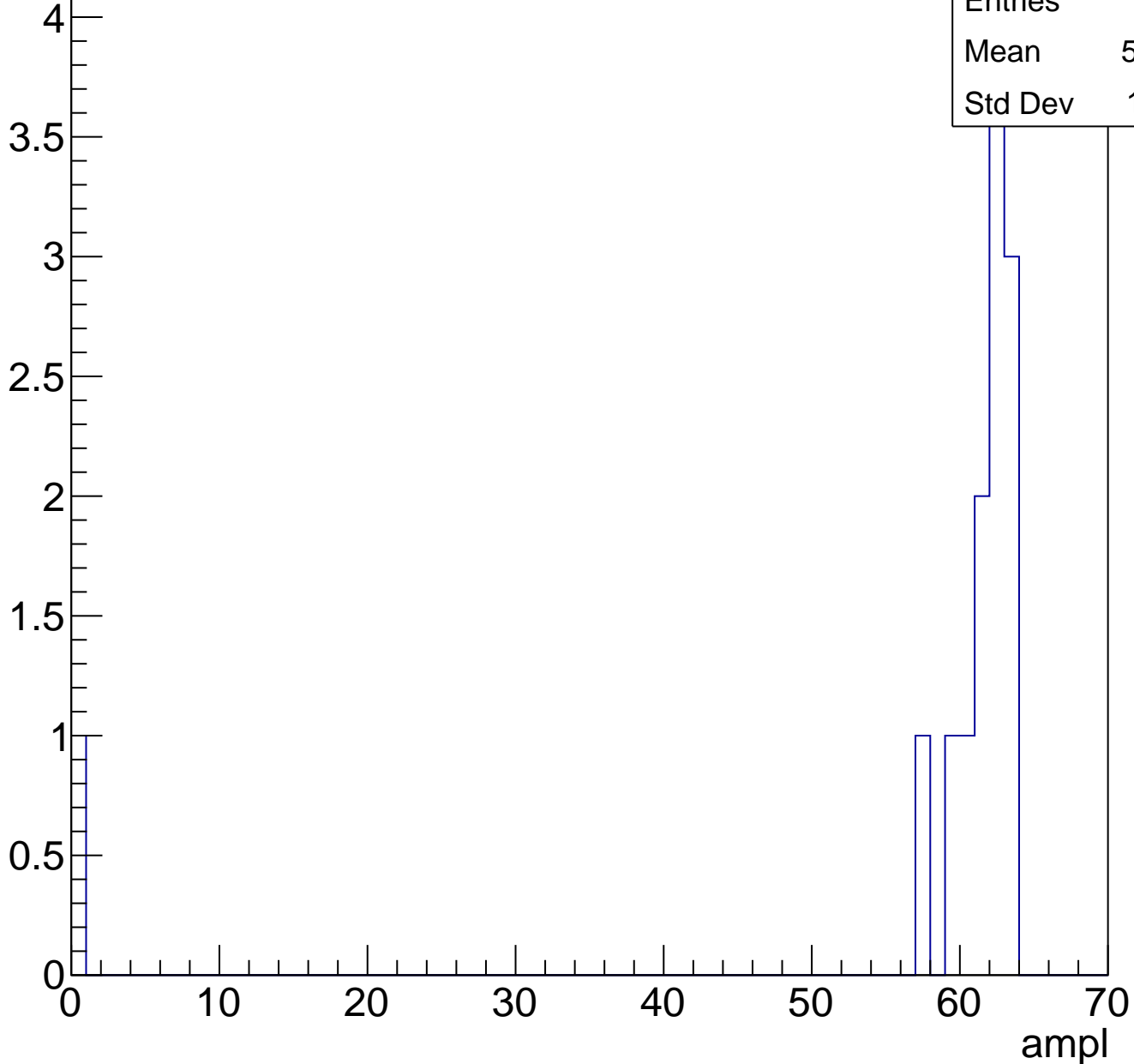
Entries	70
Mean	58.34
Std Dev	3.047



B1L103S, U18-ch90, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



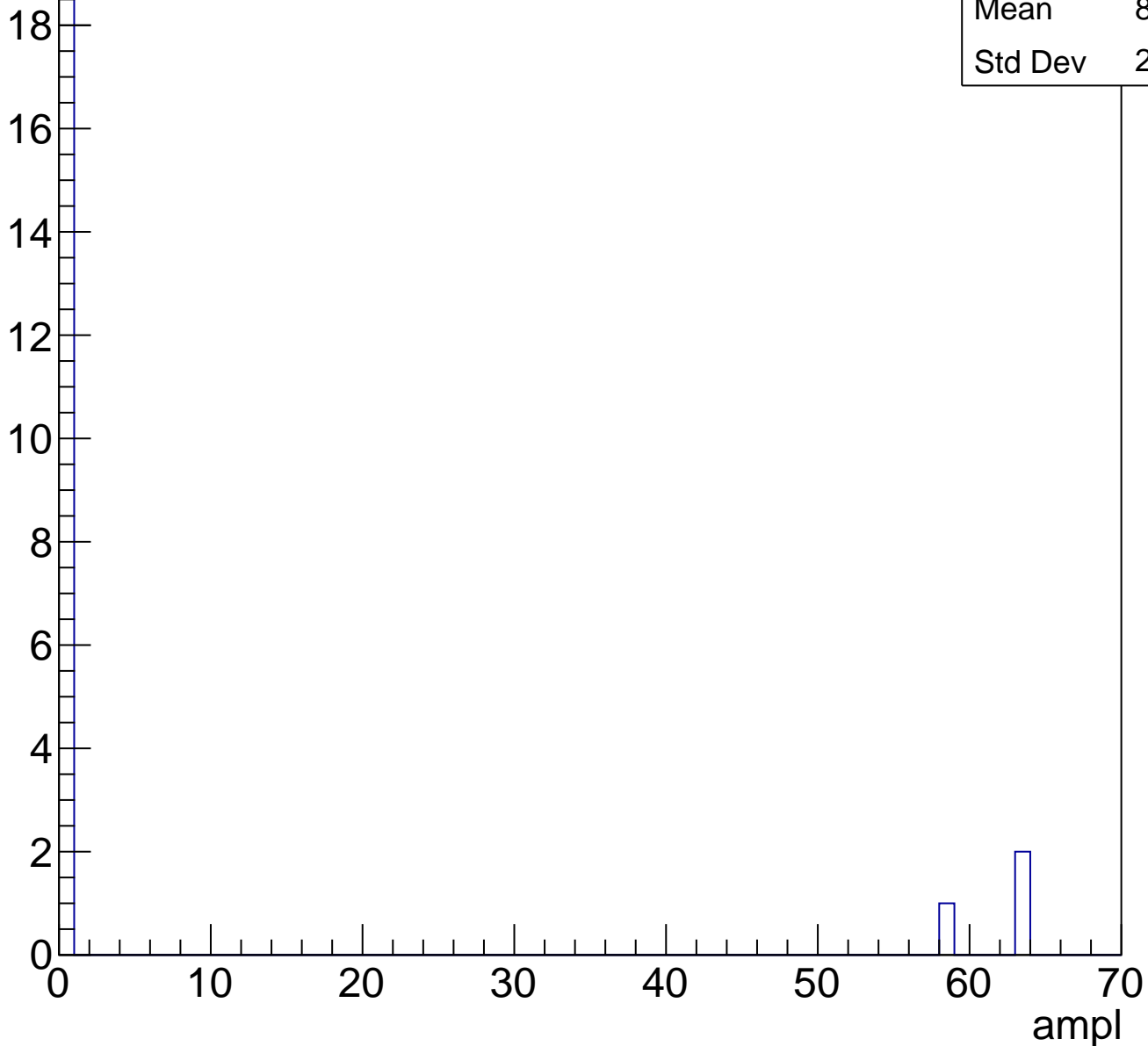
Entries	13
Mean	56.54
Std Dev	16.41

B1L103S, U18-ch90, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.364
Std Dev	21.07

Entry



B1L103S, U18-ch91, adc0

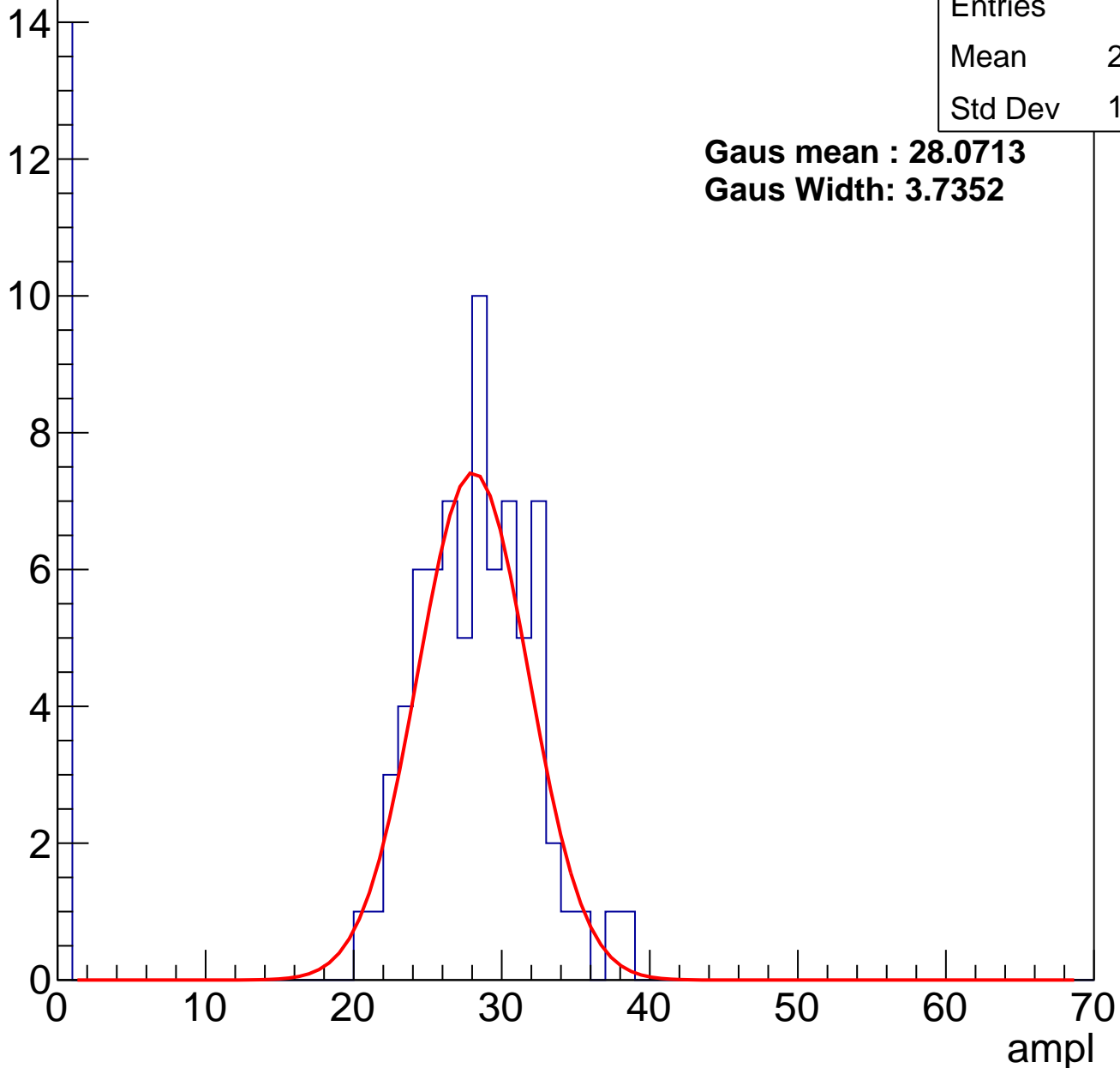
calib_packv5_041523_1651.root, FC#0, port C2

Entries	88
Mean	23.44
Std Dev	10.74

Gaus mean : 28.0713

Gaus Width: 3.7352

Entry



B1L103S, U18-ch91, adc1

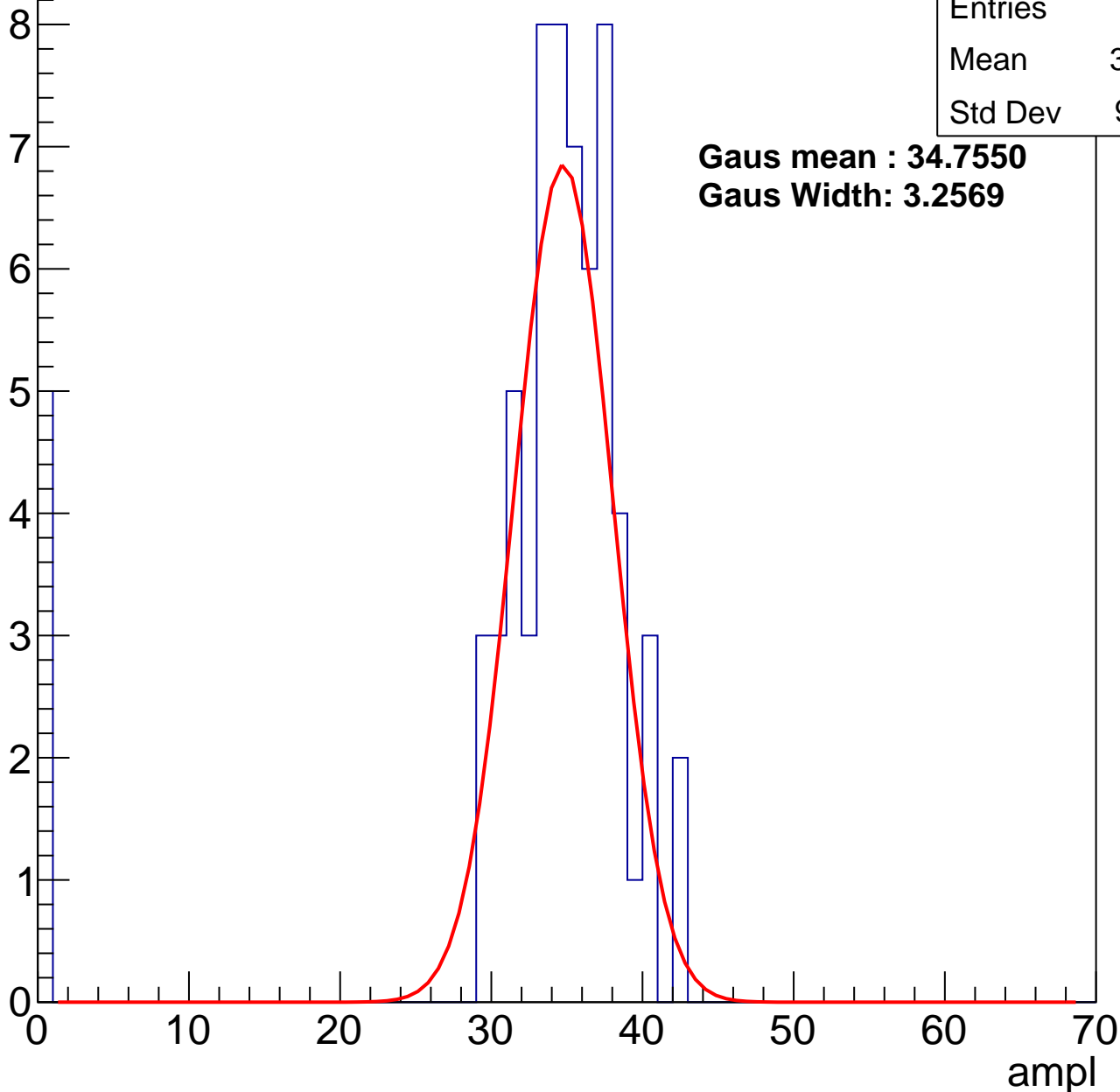
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	32.06
Std Dev	9.651

Gaus mean : 34.7550

Gaus Width: 3.2569



B1L103S, U18-ch91, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	37.45
Std Dev	12.51

Gaus mean : 41.0097

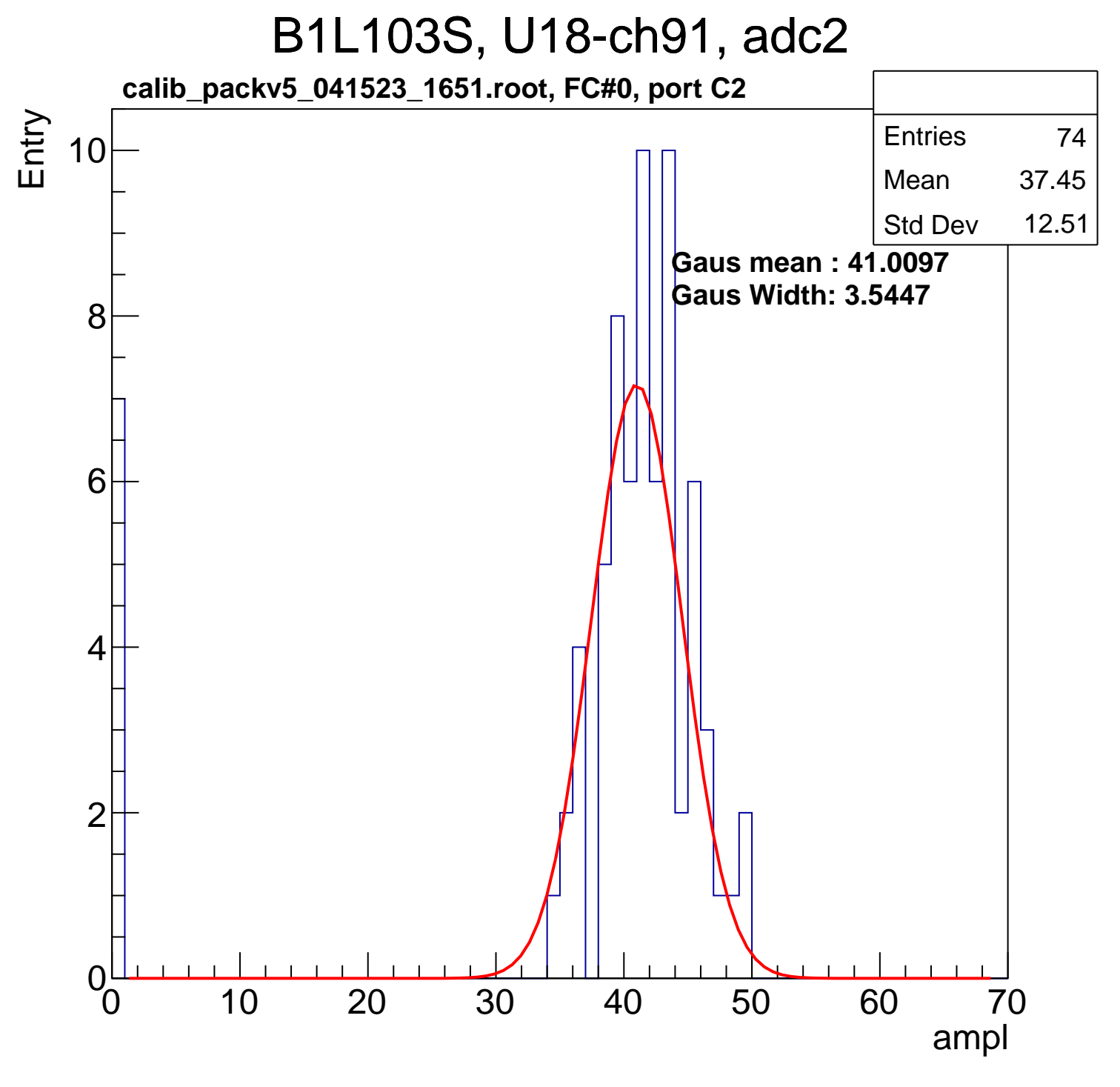
Gaus Width: 3.5447

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

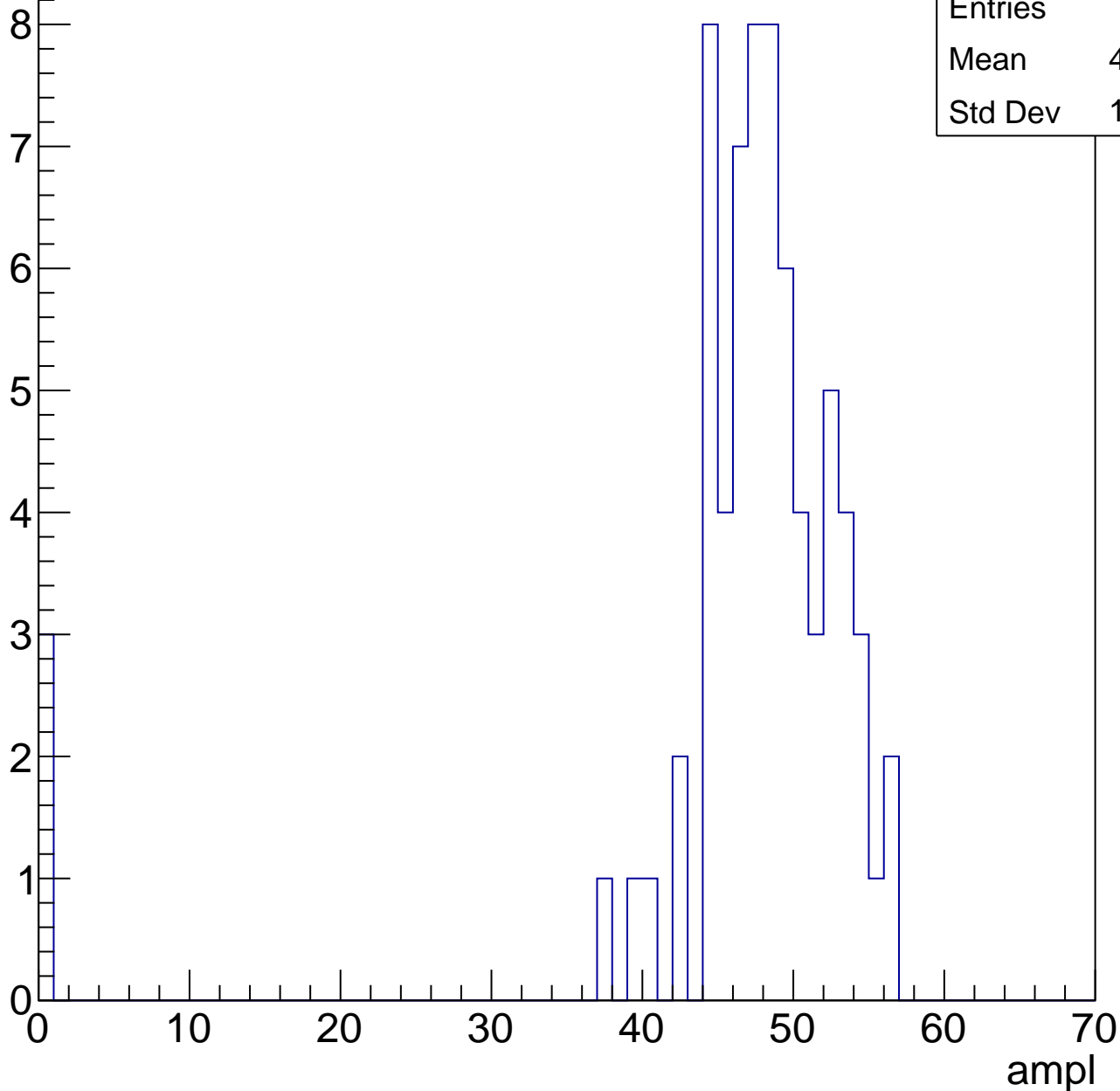


B1L103S, U18-ch91, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	45.94
Std Dev	10.39

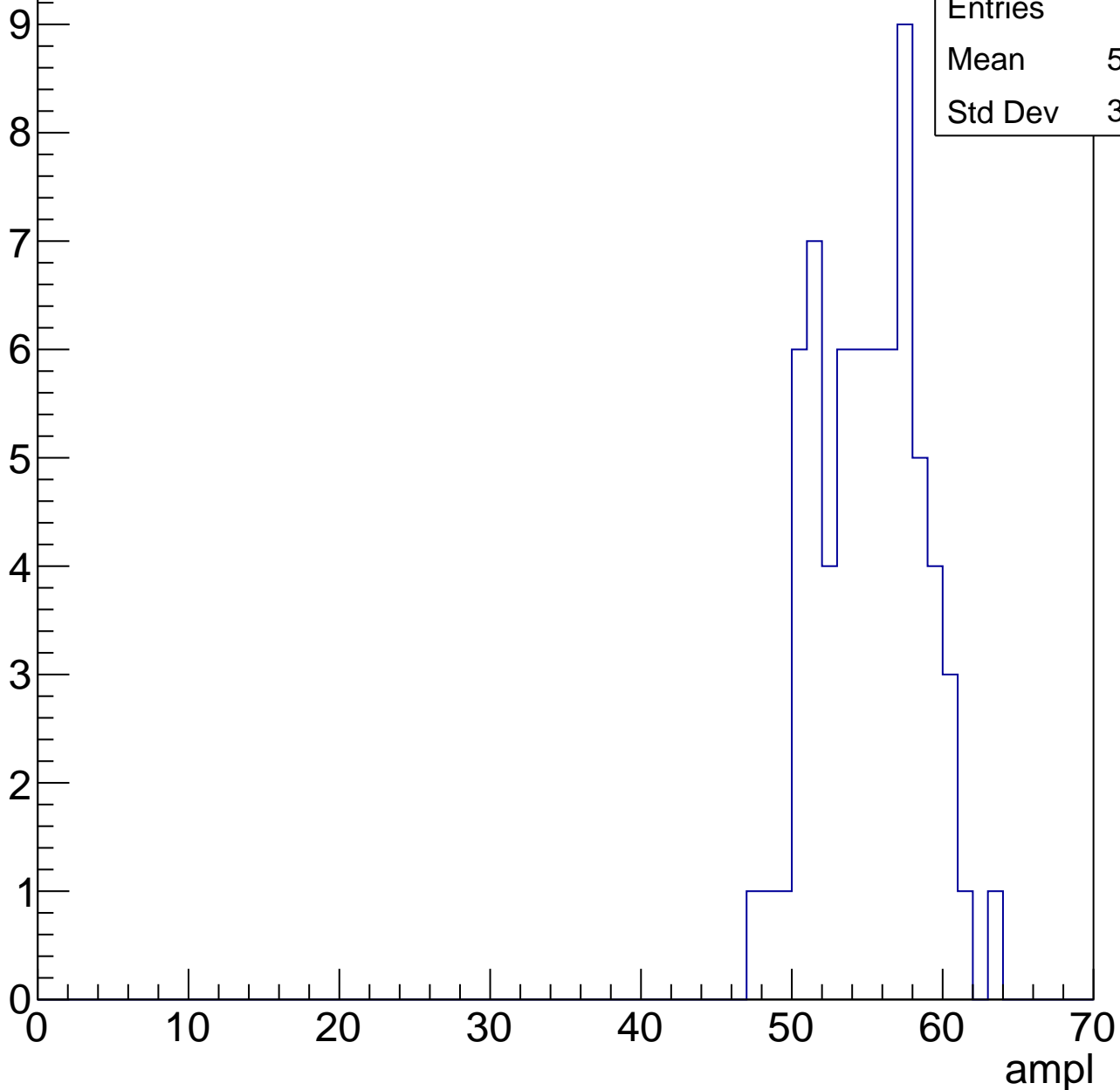


B1L103S, U18-ch91, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	54.63
Std Dev	3.442

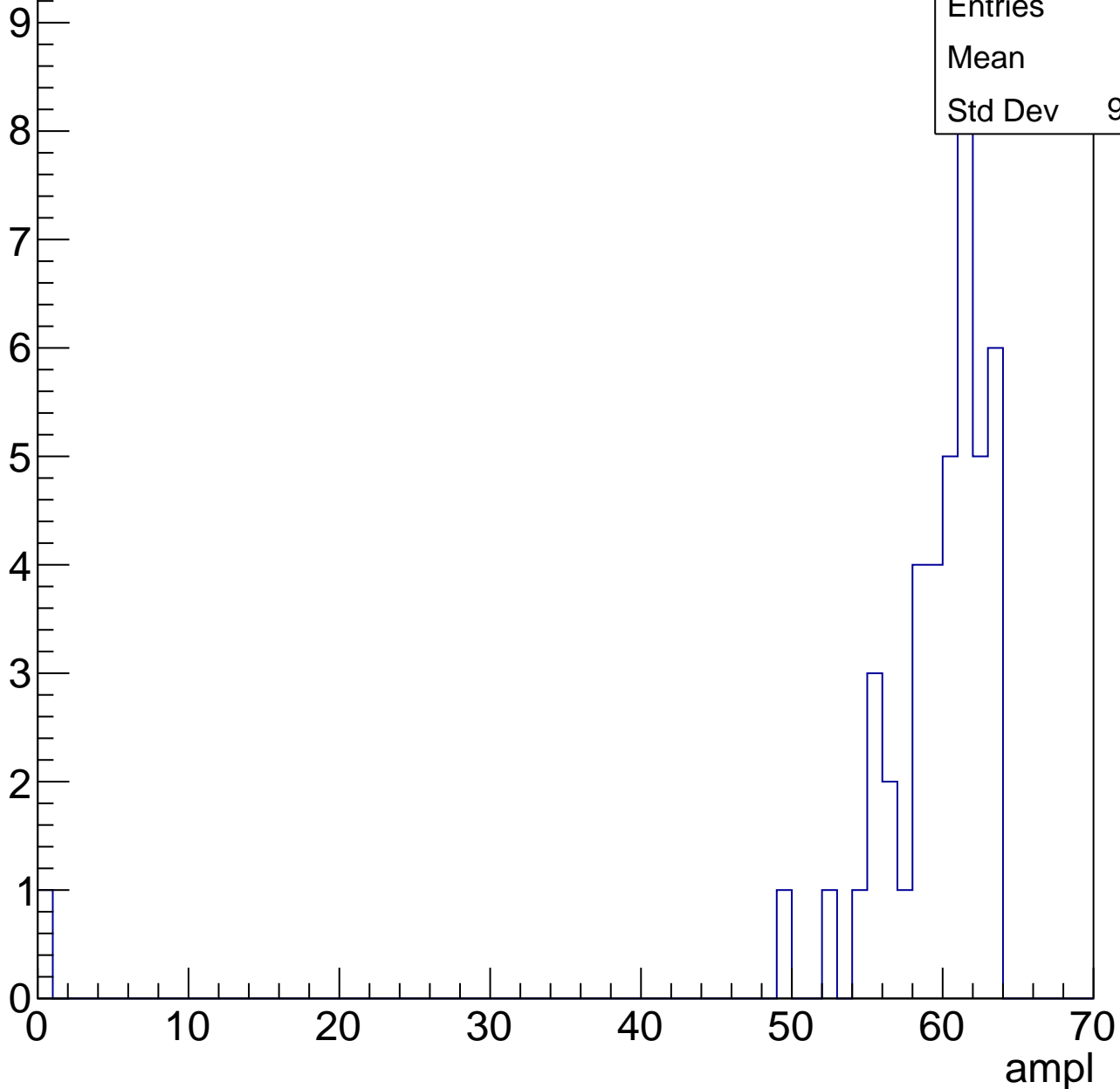


B1L103S, U18-ch91, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	43
Mean	58
Std Dev	9.484

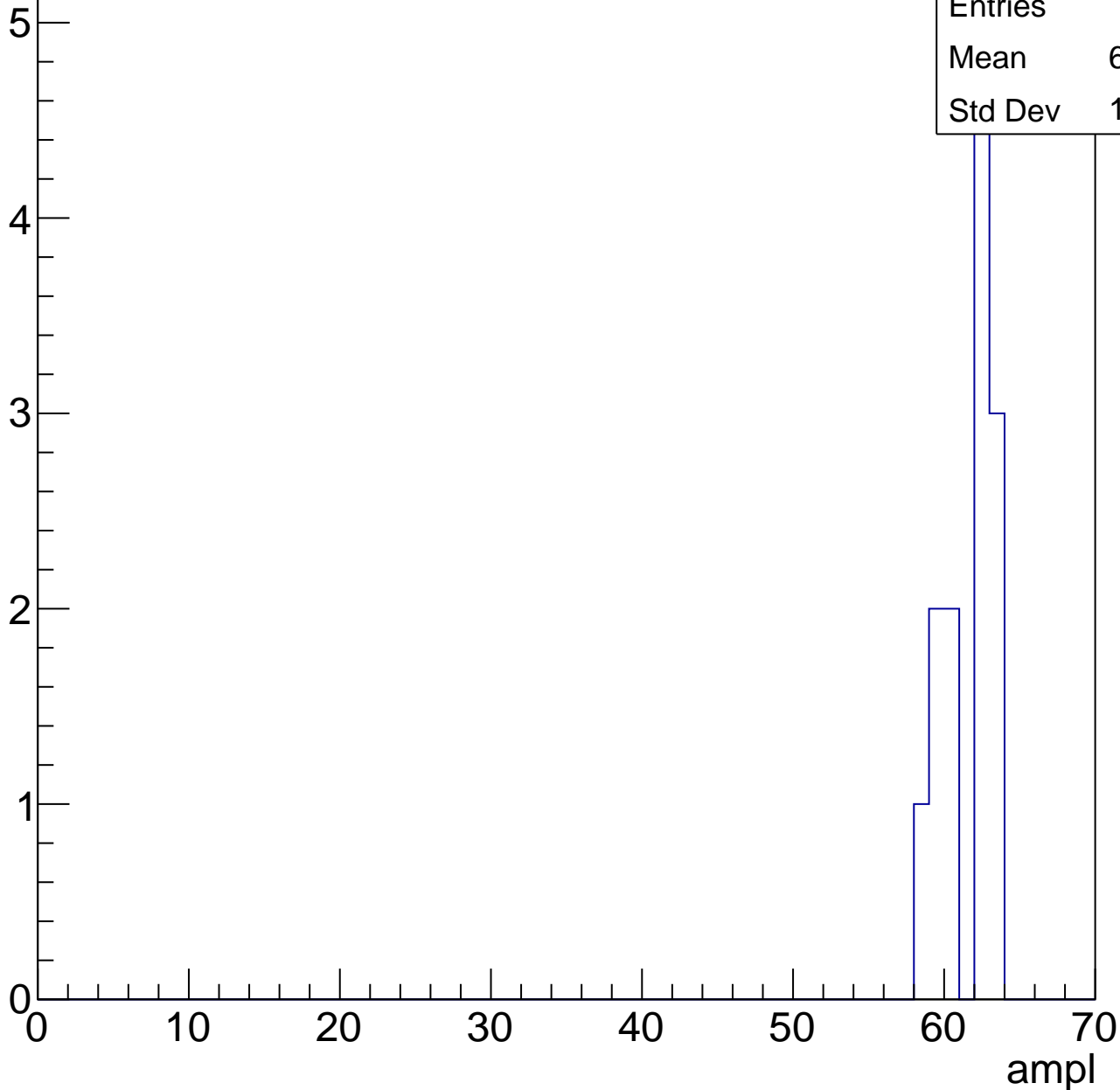


B1L103S, U18-ch91, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.15
Std Dev	1.657



B1L103S, U18-ch91, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

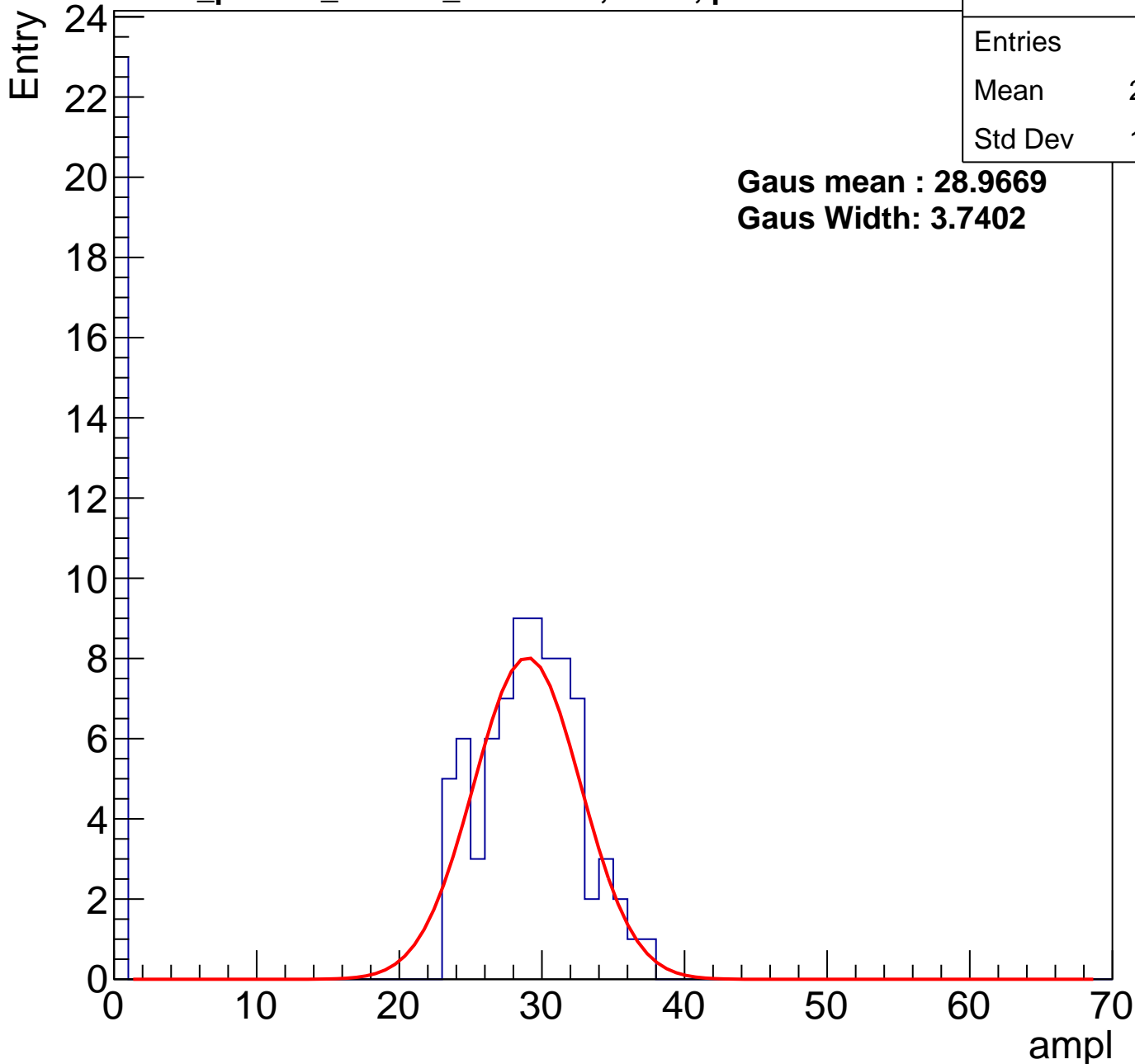
B1L103S, U18-ch92, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	100
Mean	22.15
Std Dev	12.46

Gaus mean : 28.9669

Gaus Width: 3.7402



B1L103S, U18-ch92, adc1

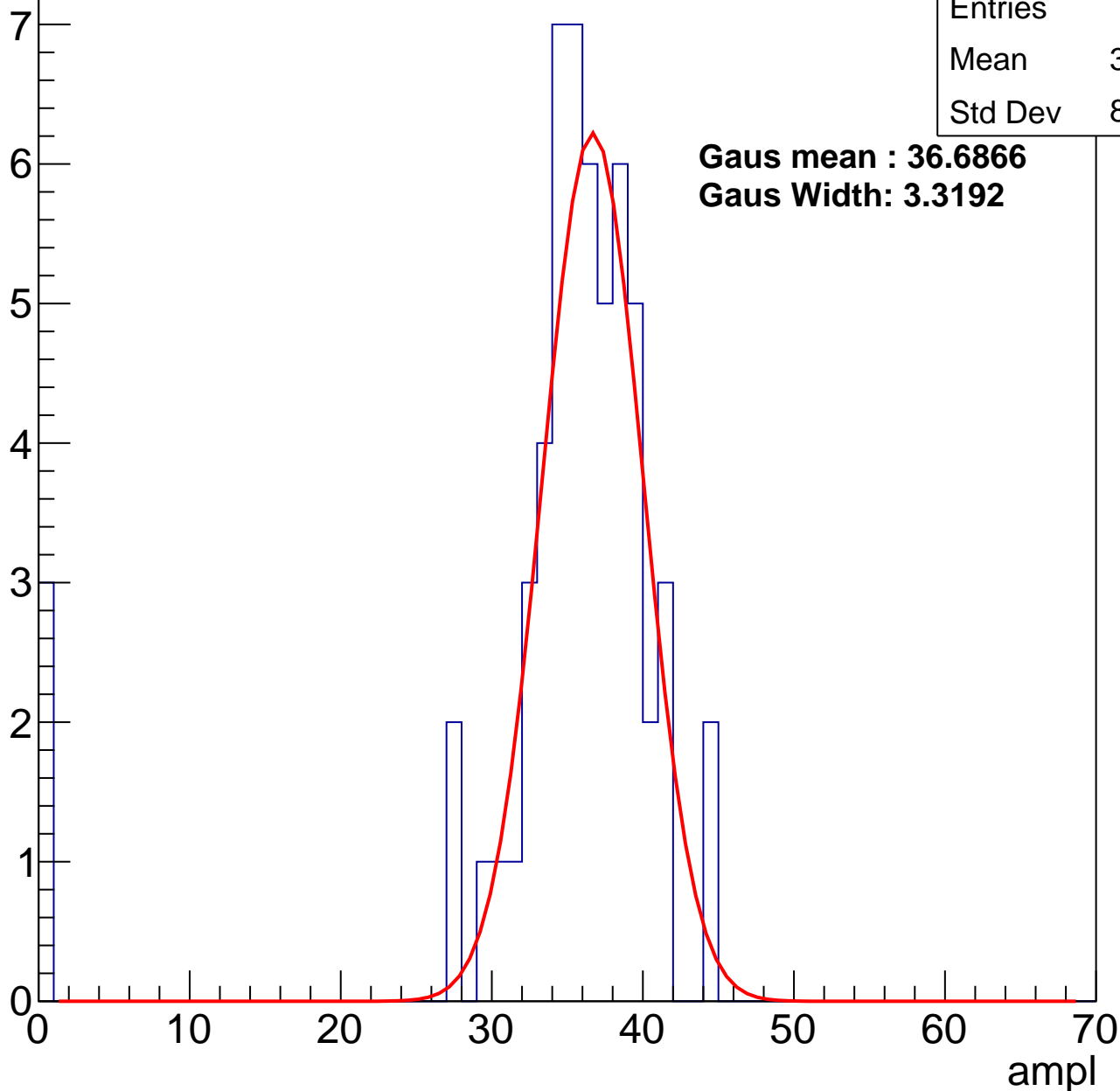
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	33.97
Std Dev	8.658

Gaus mean : 36.6866

Gaus Width: 3.3192



B1L103S, U18-ch92, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	34.74
Std Dev	15.38

Gaus mean : 41.5697

Gaus Width: 3.7150

Entry

10

8

6

4

2

0

0

10

20

30

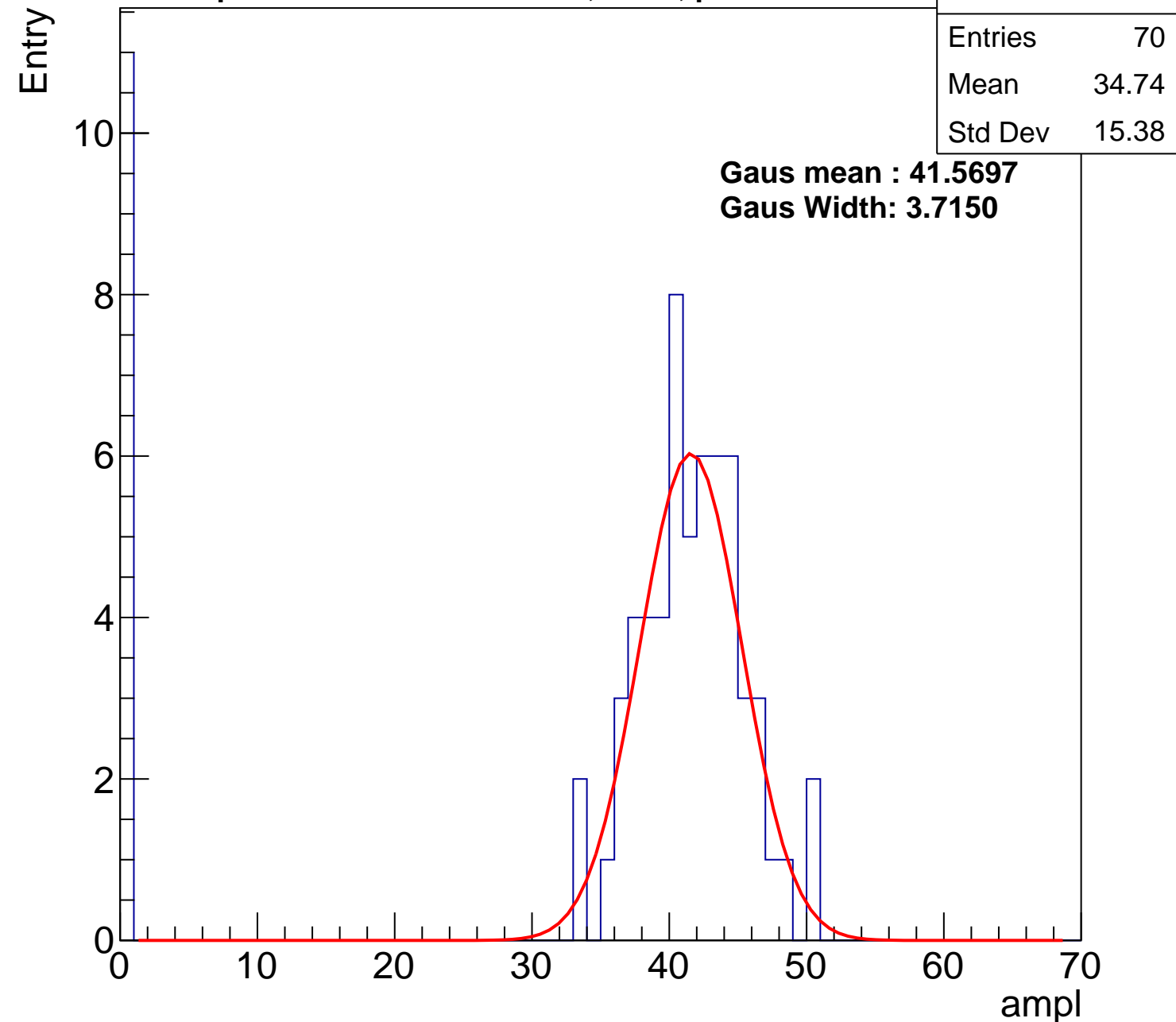
40

50

60

70

ampl

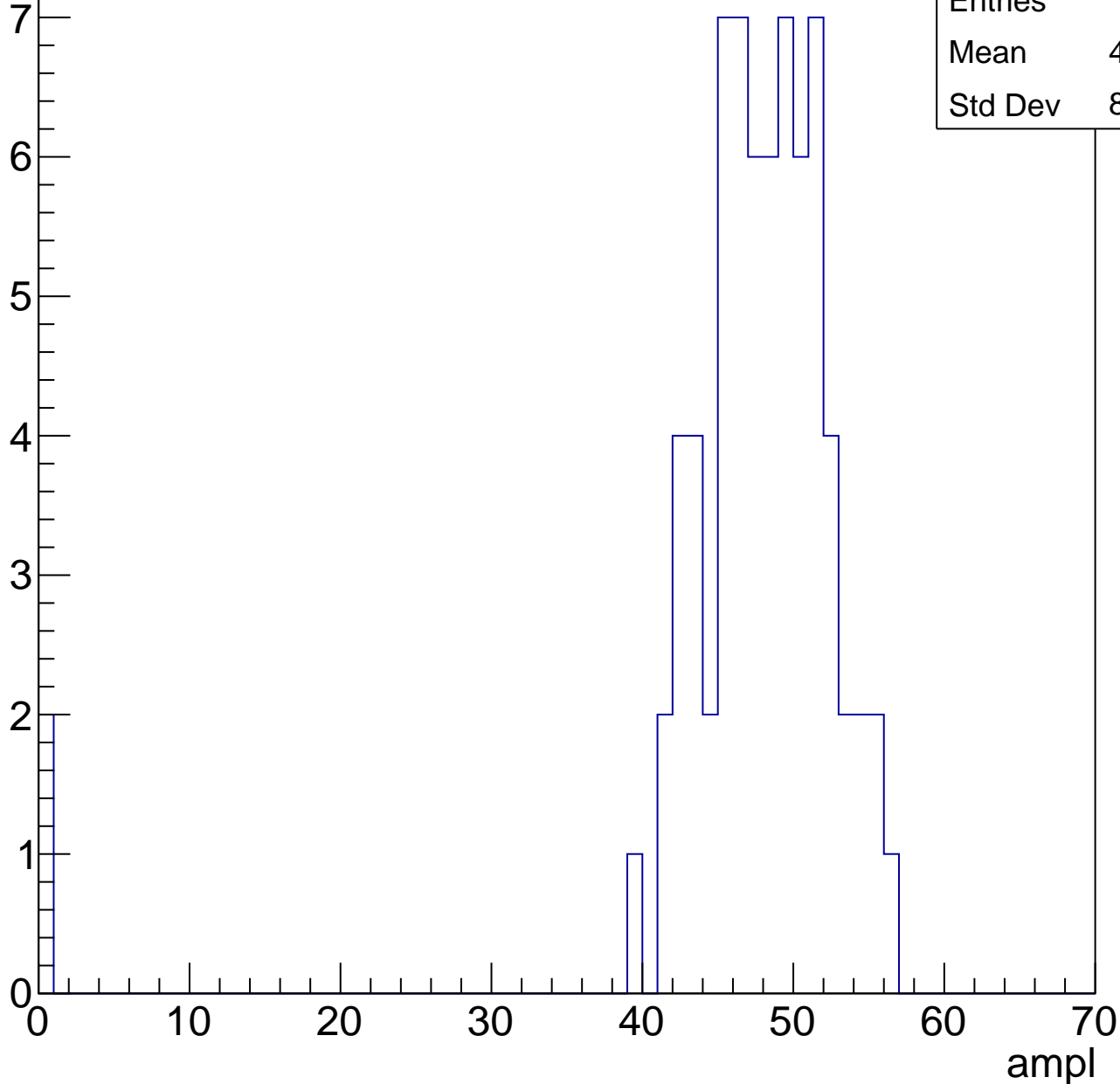


B1L103S, U18-ch92, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	46.44
Std Dev	8.682

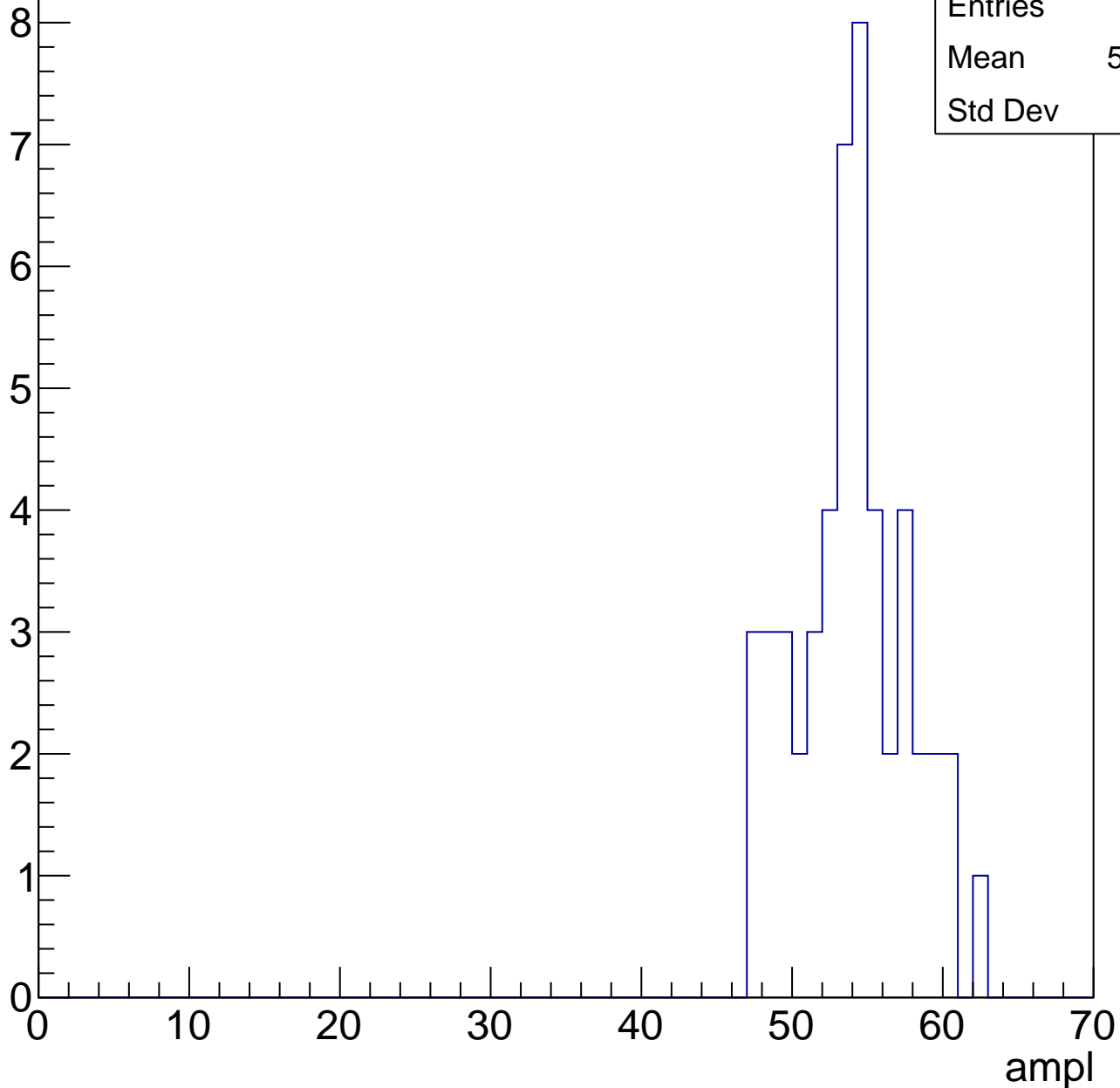


B1L103S, U18-ch92, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

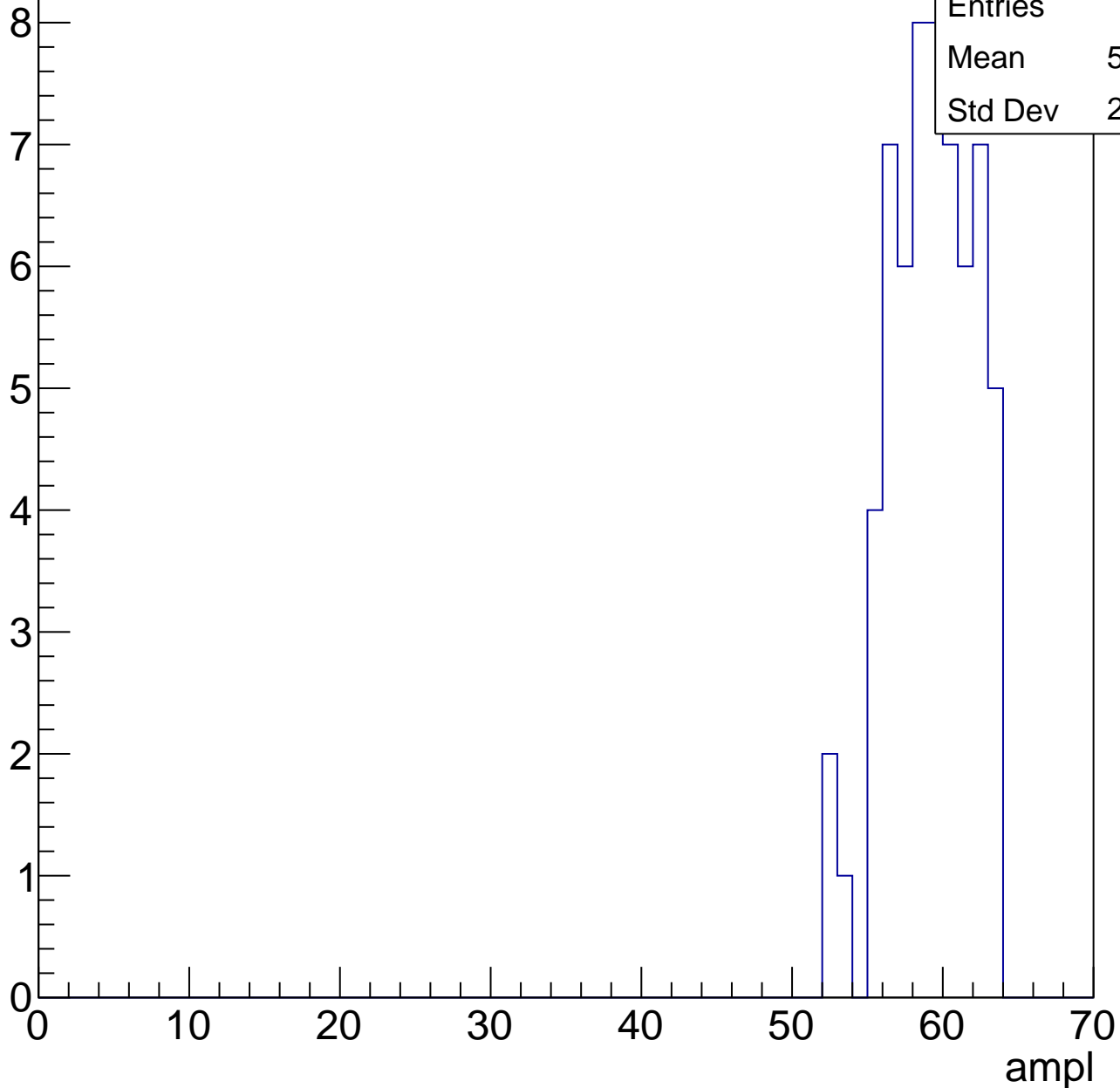
Entries	50
Mean	53.44
Std Dev	3.64



B1L103S, U18-ch92, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

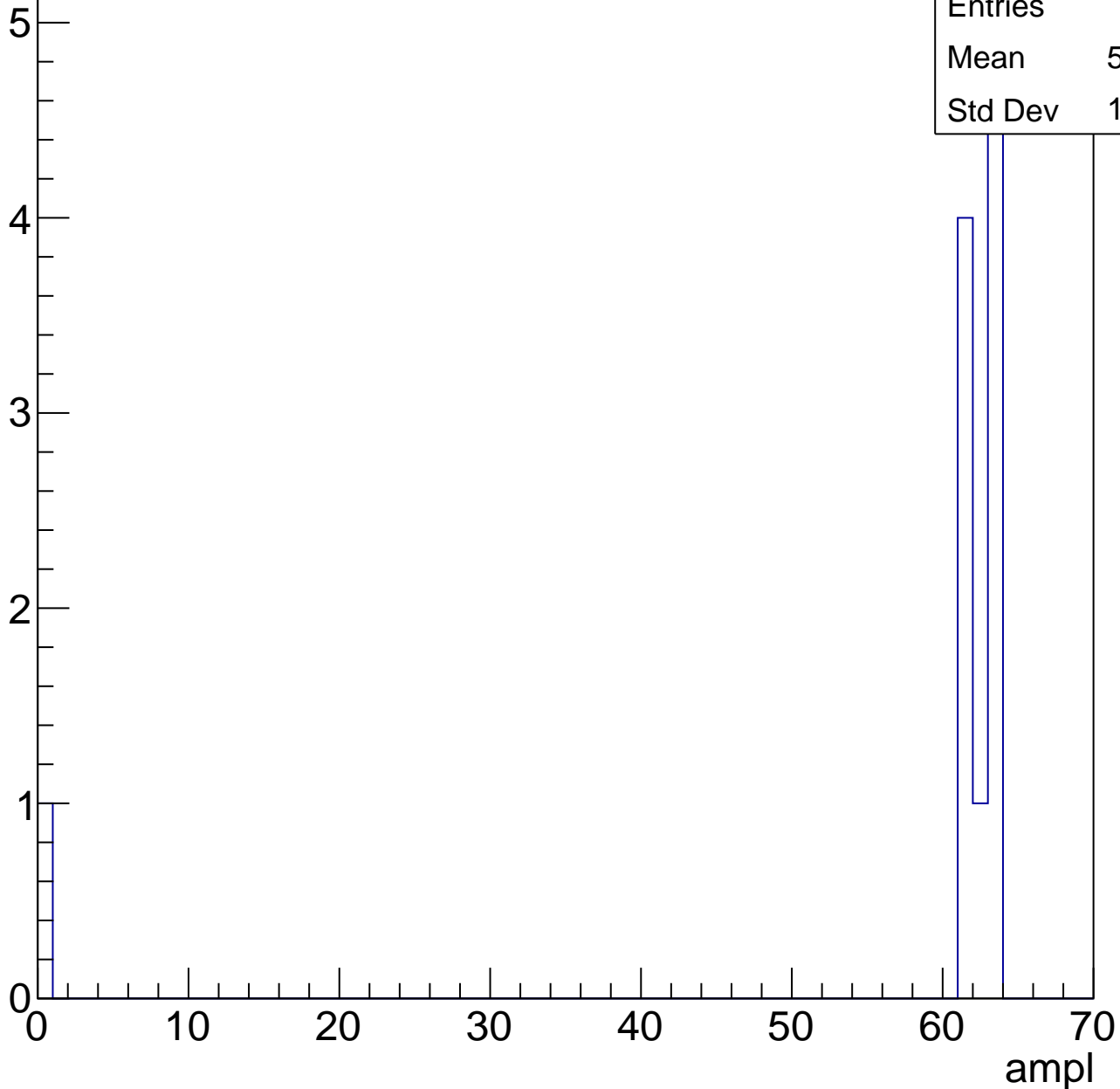


B1L103S, U18-ch92, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

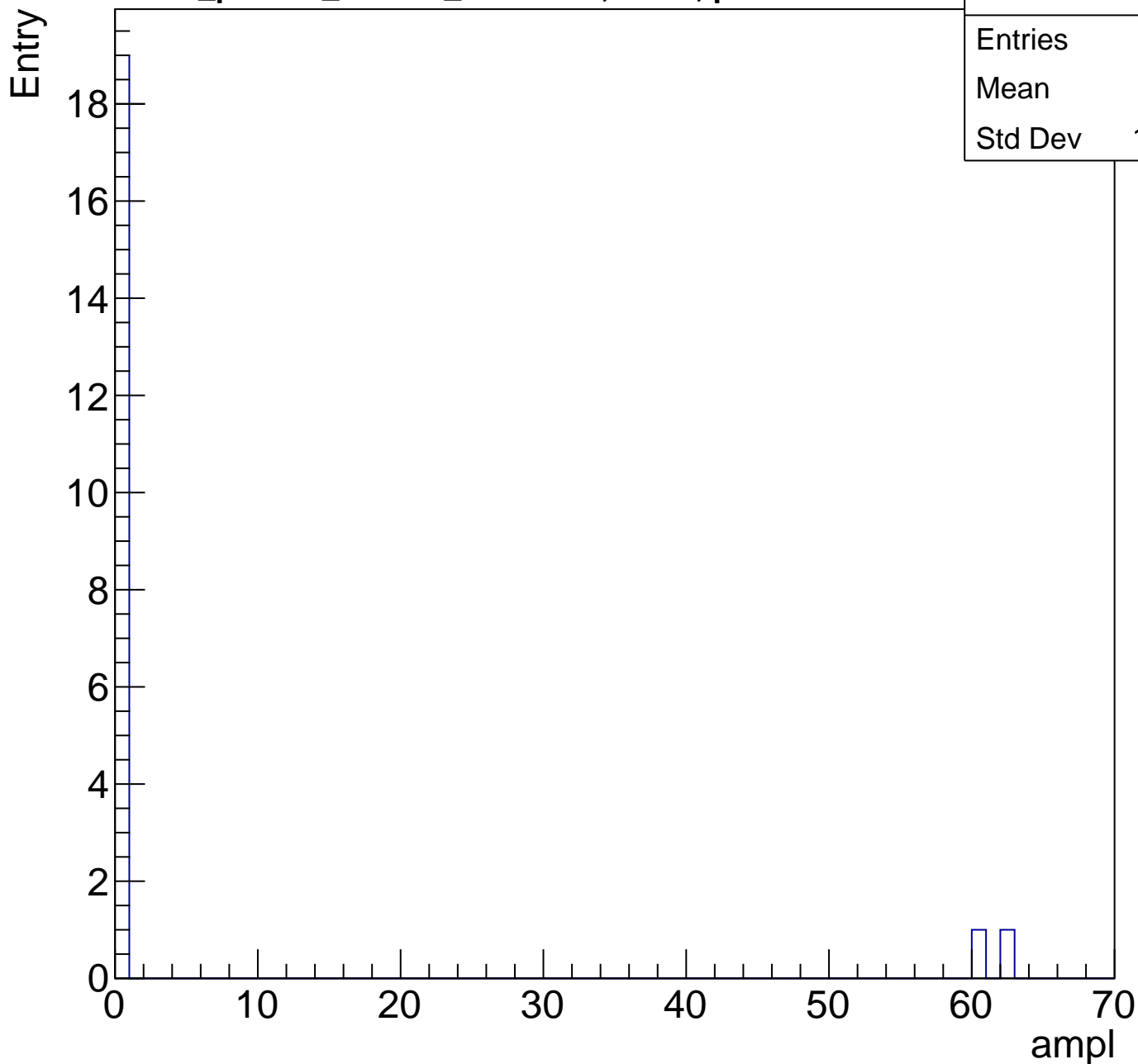
Entries	11
Mean	56.45
Std Dev	17.88



B1L103S, U18-ch92, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.91



B1L103S, U18-ch93, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	22.93
Std Dev	13.24

Gaus mean : 30.1938

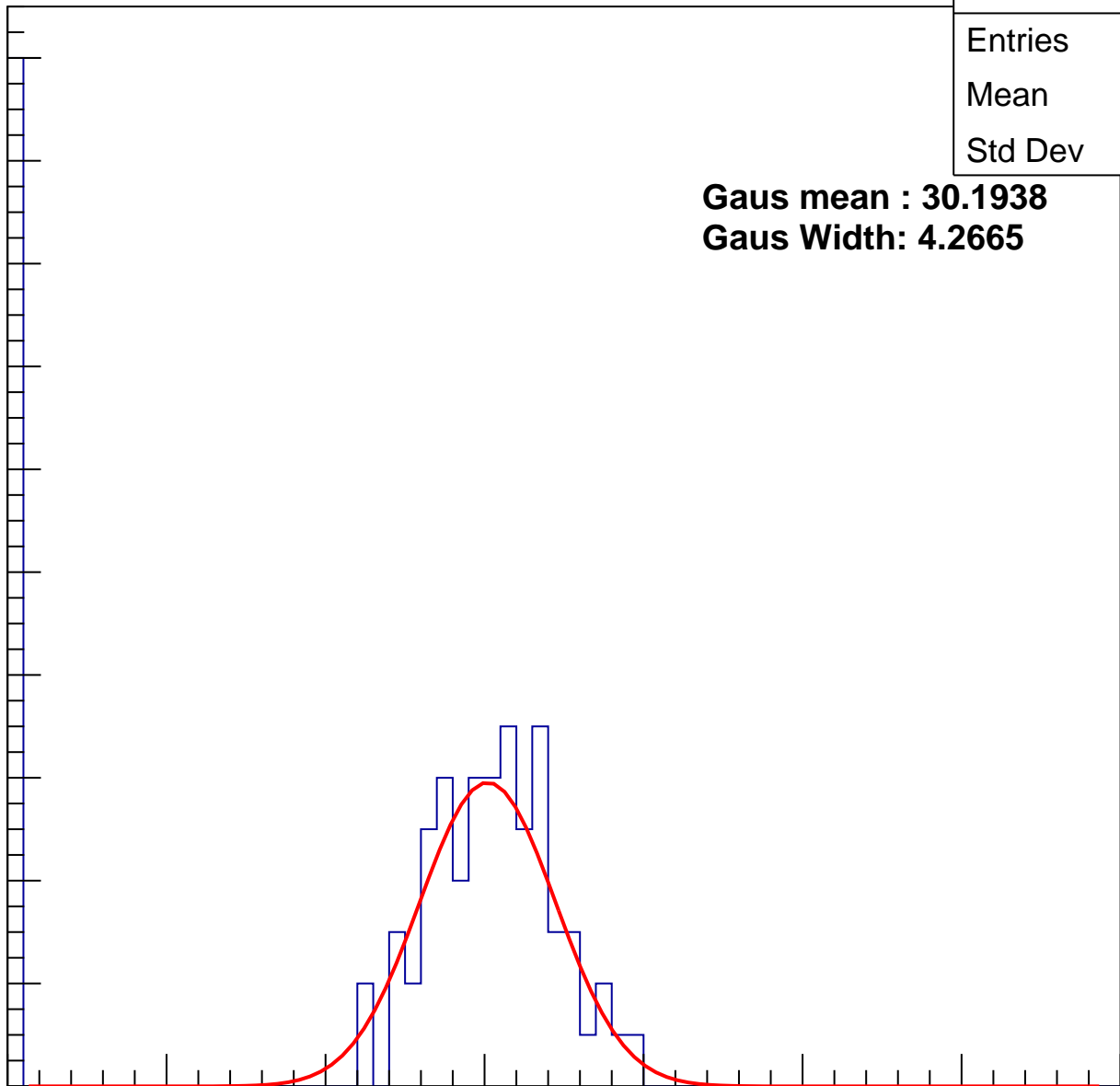
Gaus Width: 4.2665

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch93, adc1

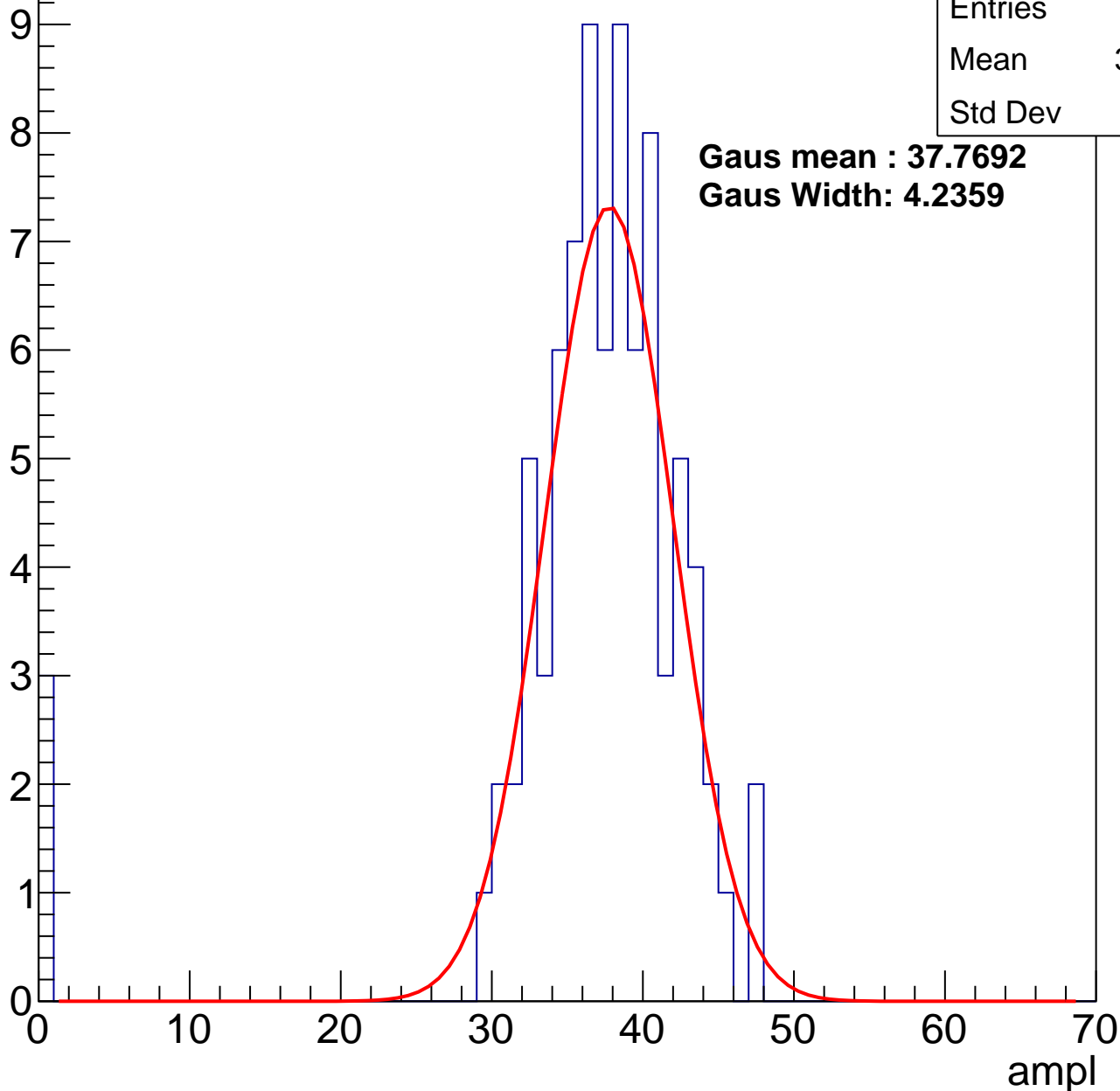
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	84
Mean	36.11
Std Dev	7.96

Gaus mean : 37.7692

Gaus Width: 4.2359



B1L103S, U18-ch93, adc2

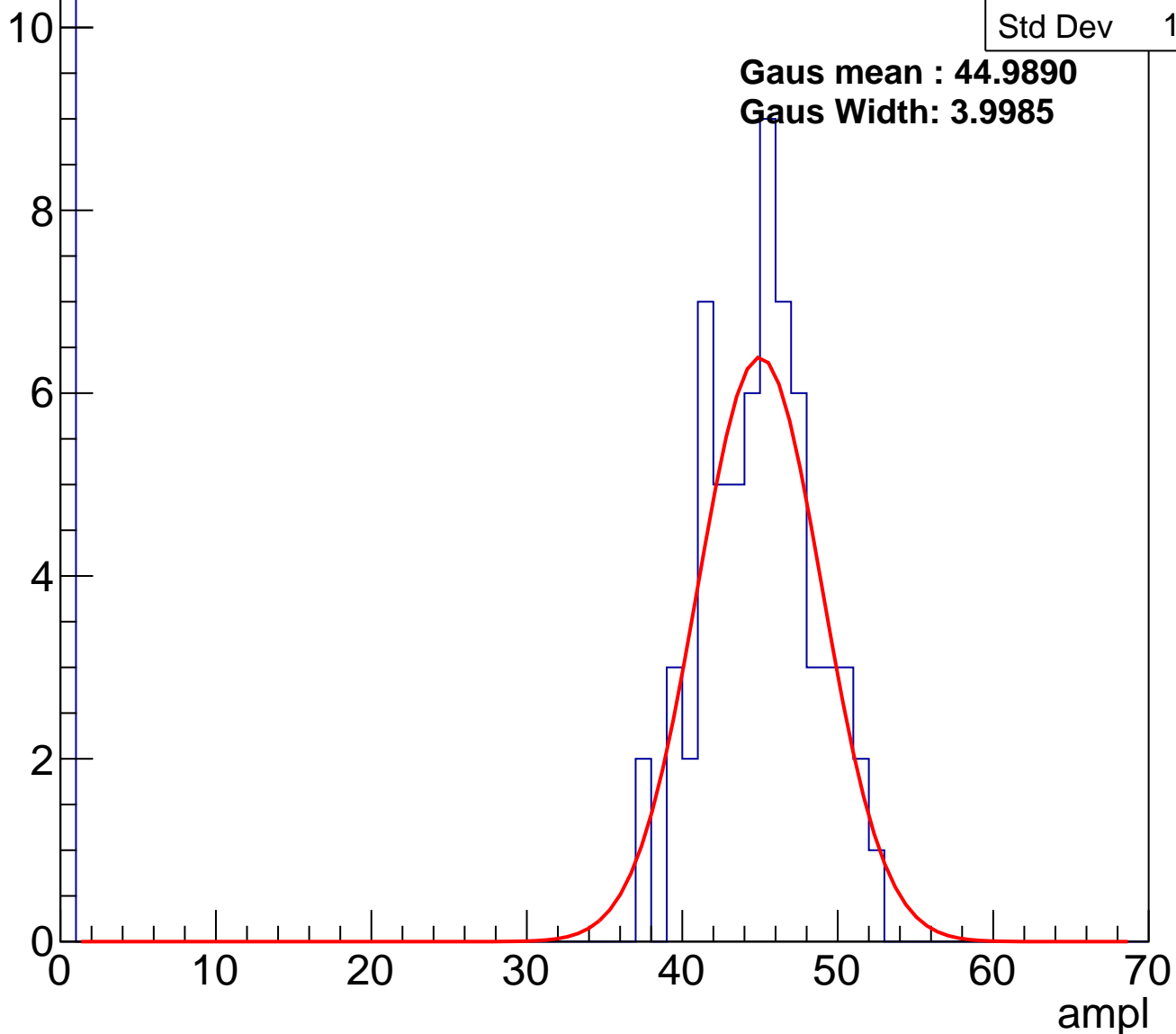
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	38.01
Std Dev	16.08

Gaus mean : 44.9890

Gaus Width: 3.9985

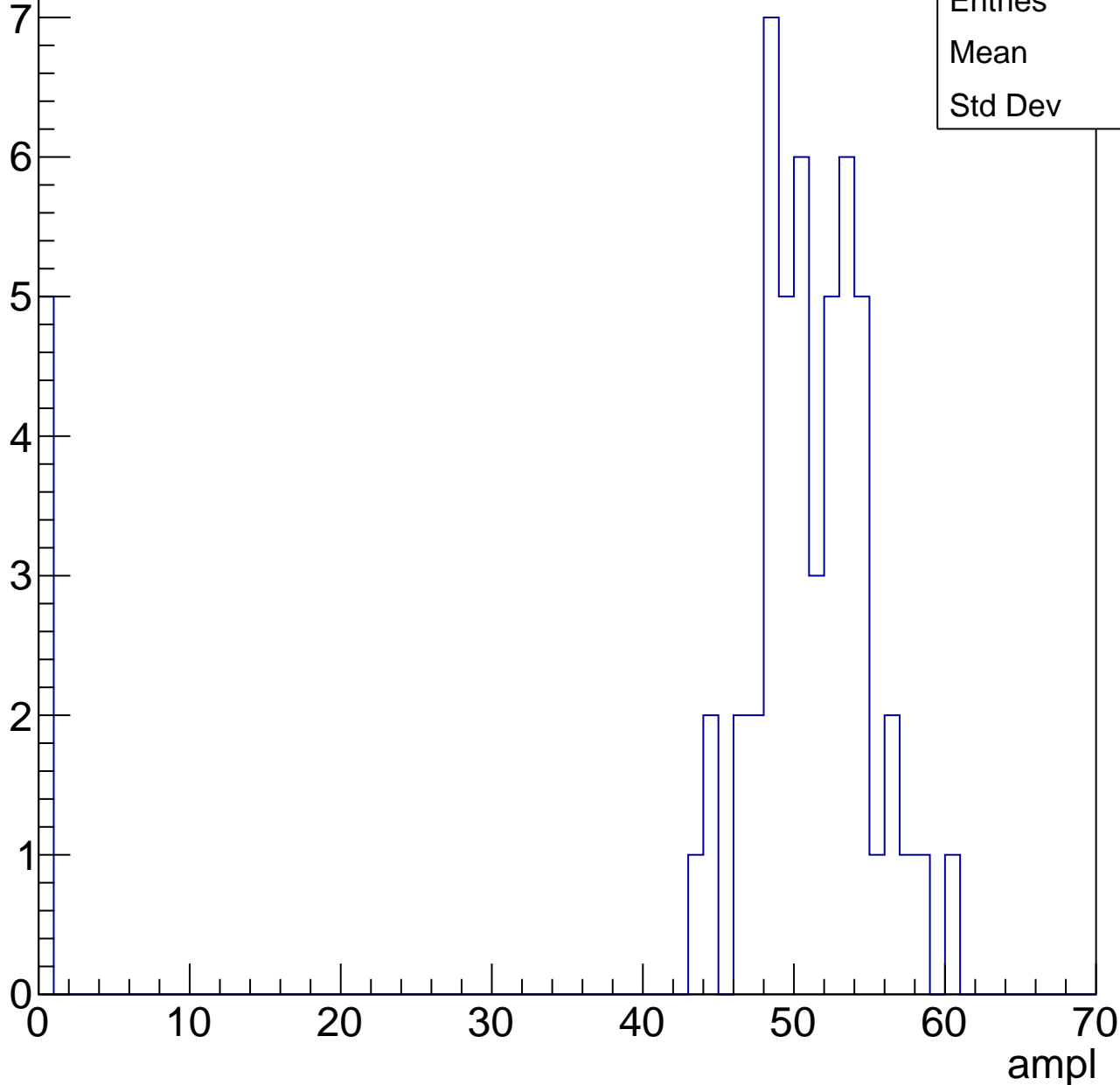


B1L103S, U18-ch93, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	46.2
Std Dev	15

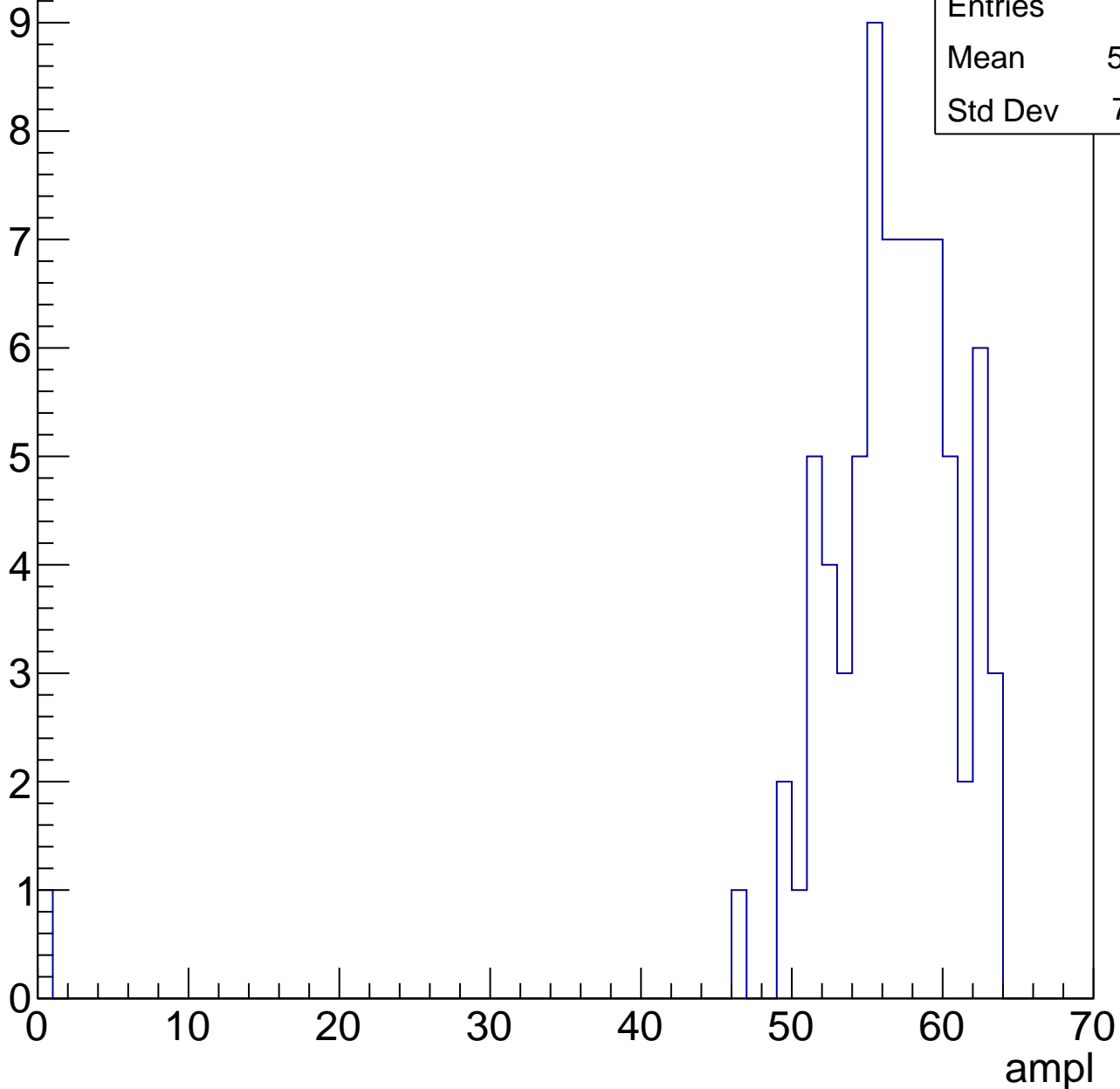


B1L103S, U18-ch93, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	55.65
Std Dev	7.481

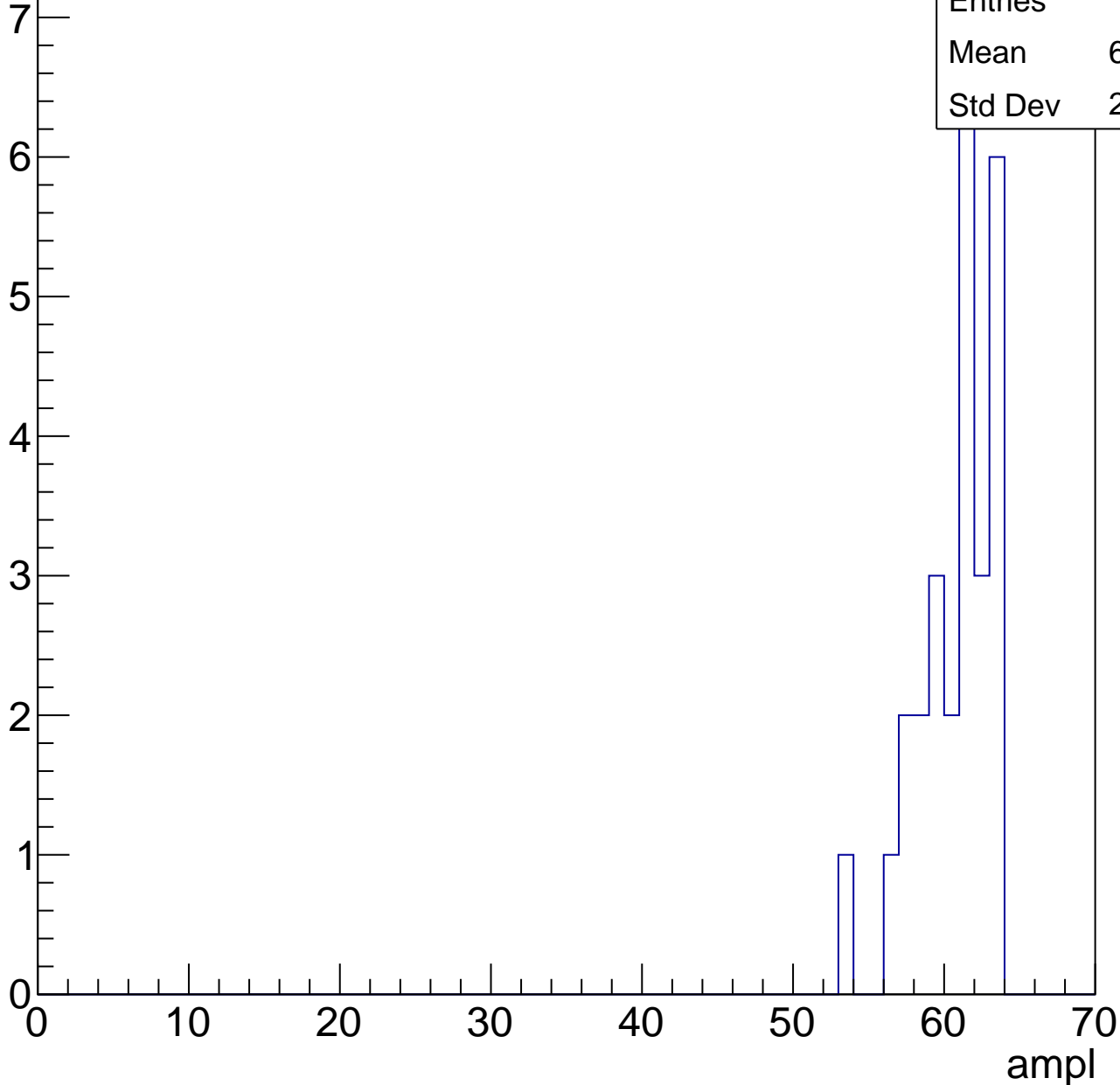


B1L103S, U18-ch93, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	27
Mean	60.26
Std Dev	2.473



B1L103S, U18-ch93, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

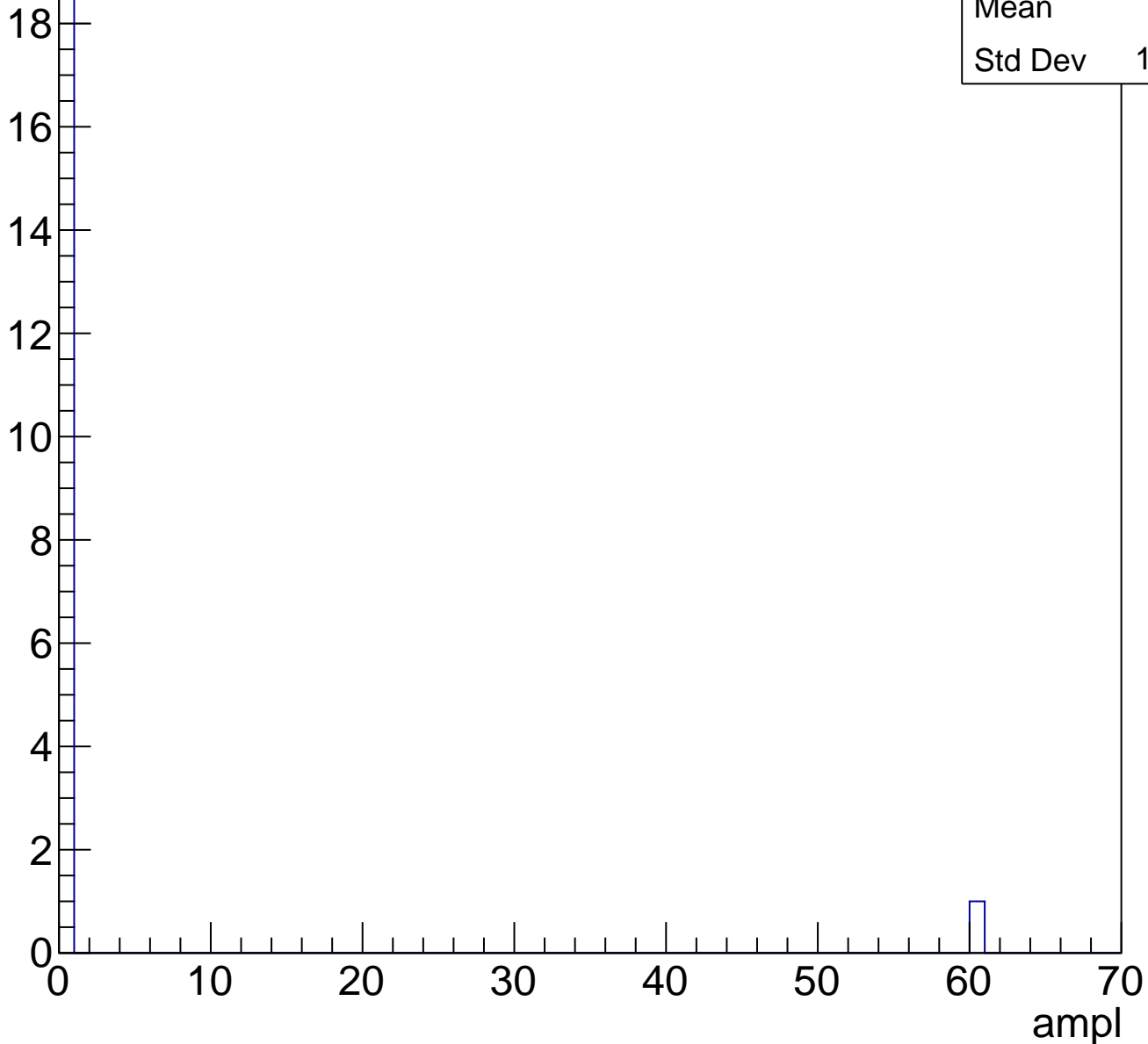


B1L103S, U18-ch93, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



B1L103S, U18-ch94, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	20.88
Std Dev	12.62

Gaus mean : 28.5458

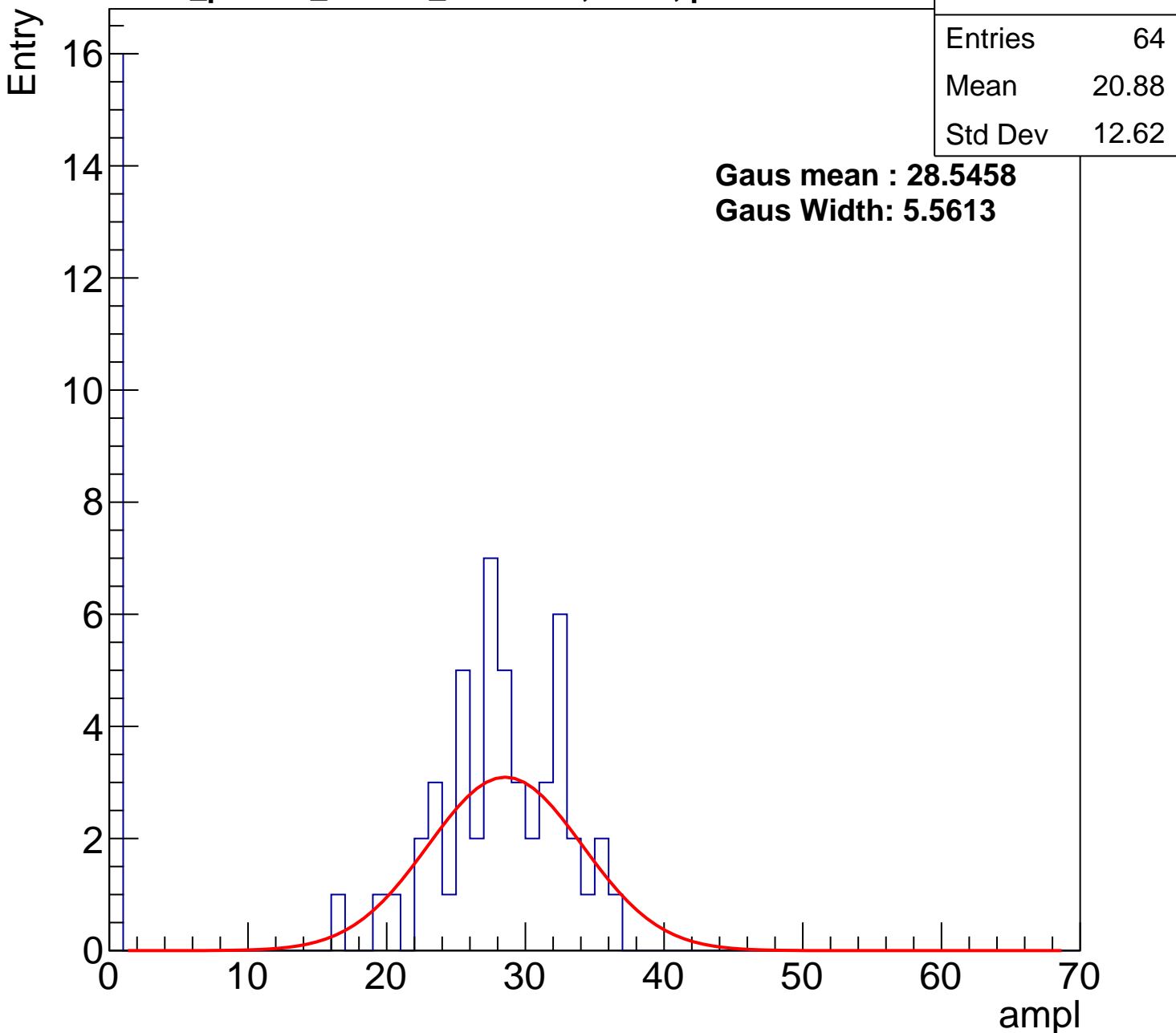
Gaus Width: 5.5613

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch94, adc1

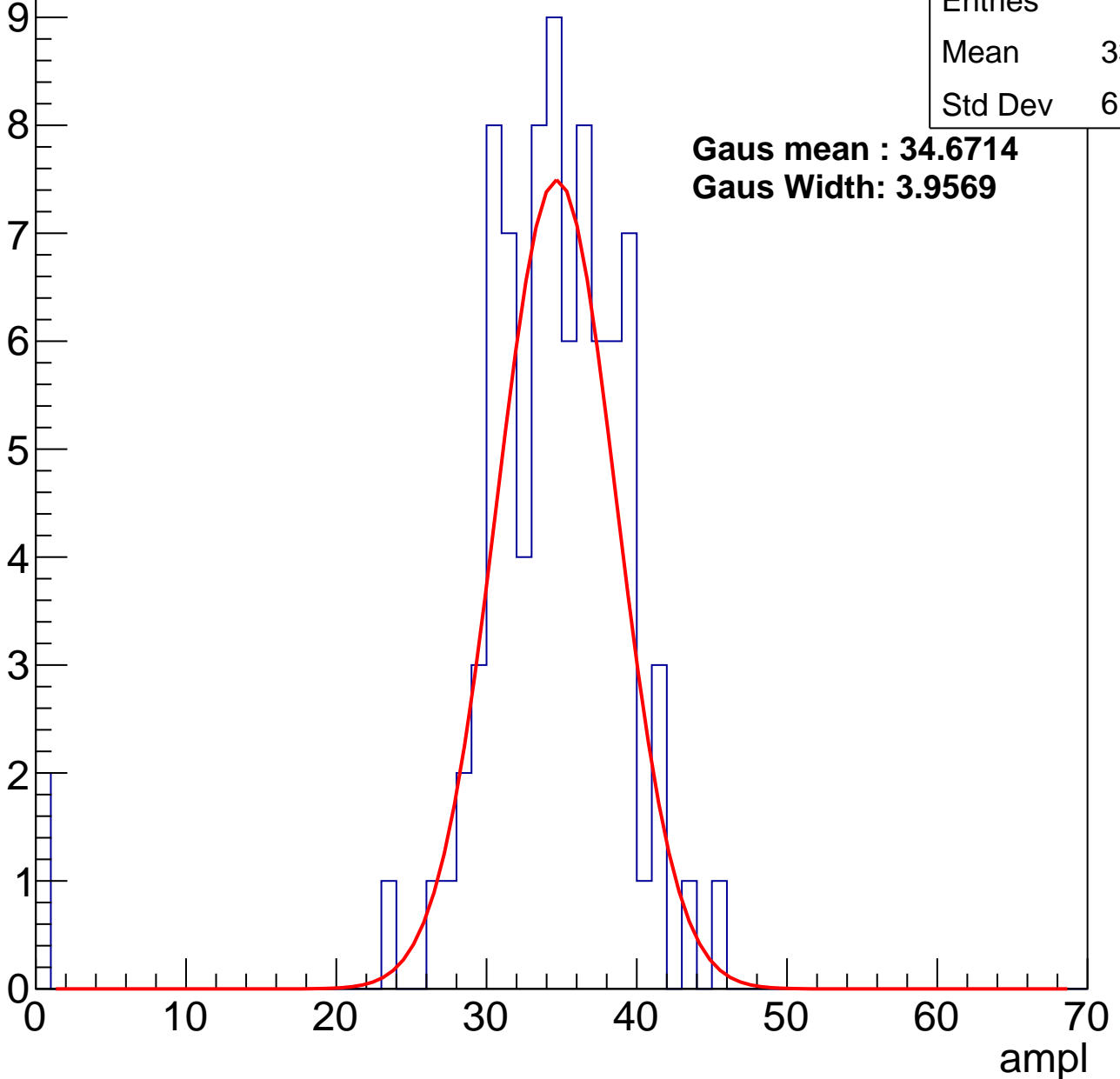
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	33.48
Std Dev	6.538

Gaus mean : 34.6714

Gaus Width: 3.9569



B1L103S, U18-ch94, adc2

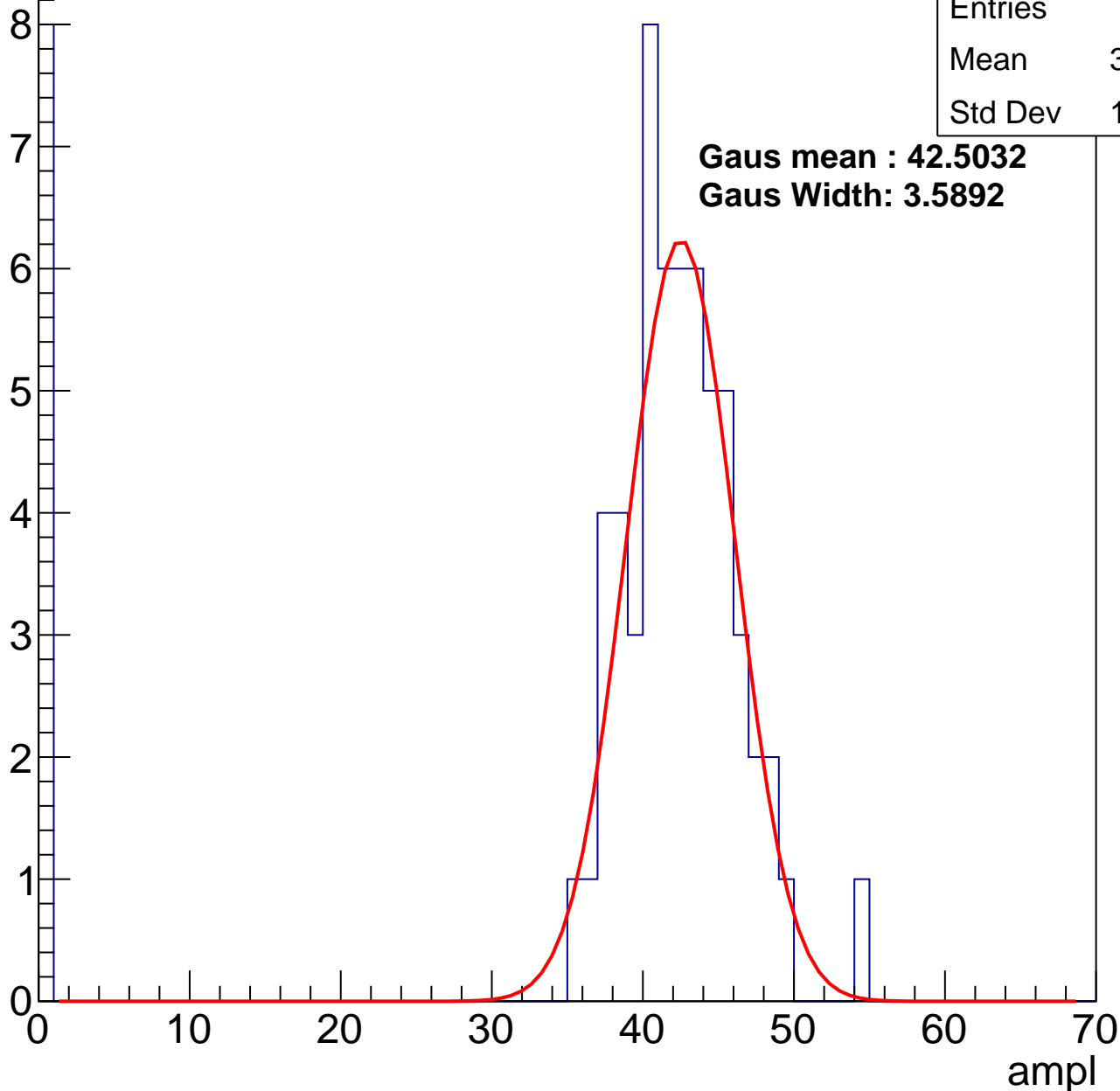
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.97
Std Dev	14.14

Gaus mean : 42.5032

Gaus Width: 3.5892

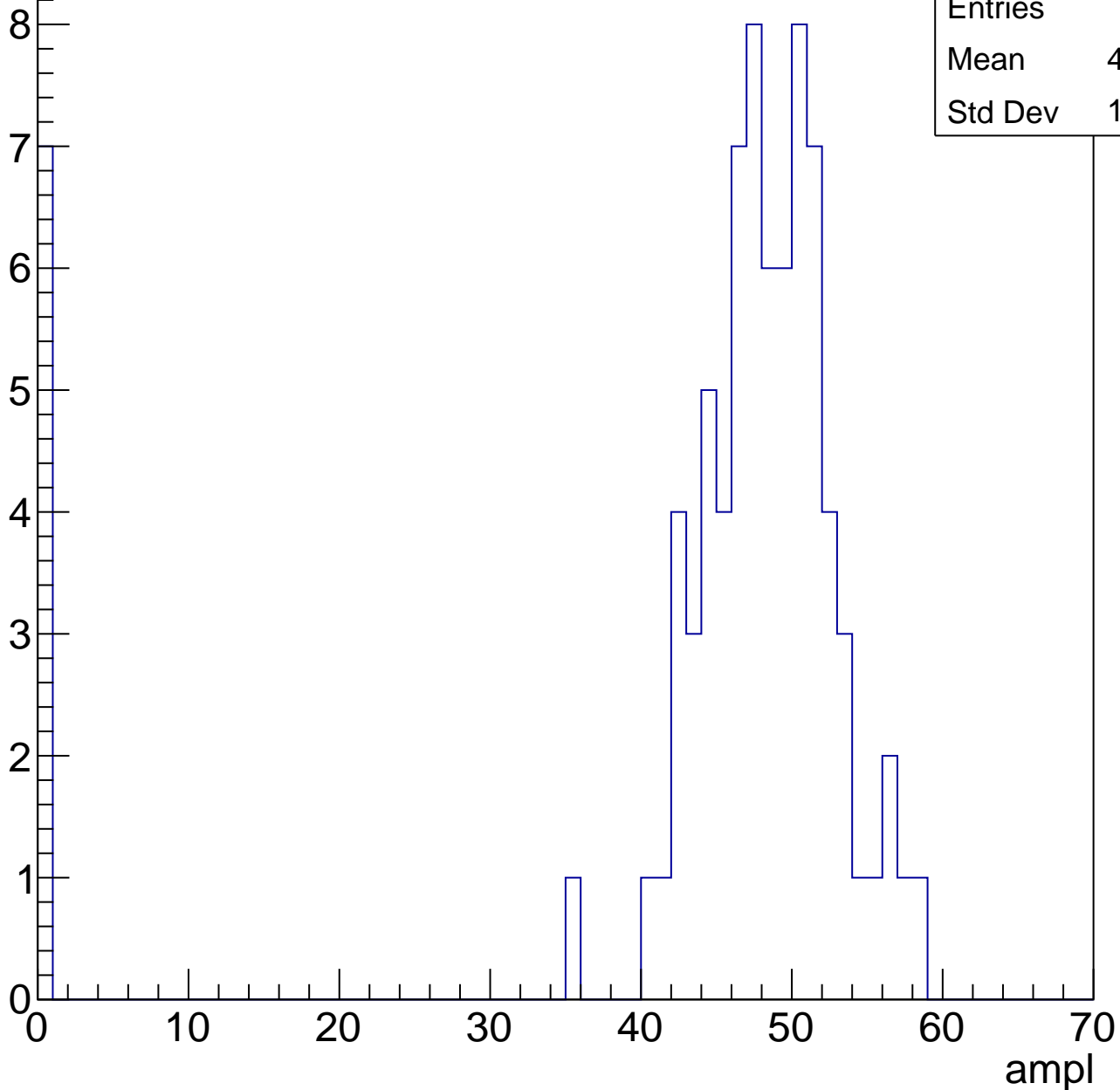


B1L103S, U18-ch94, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	43.86
Std Dev	14.07

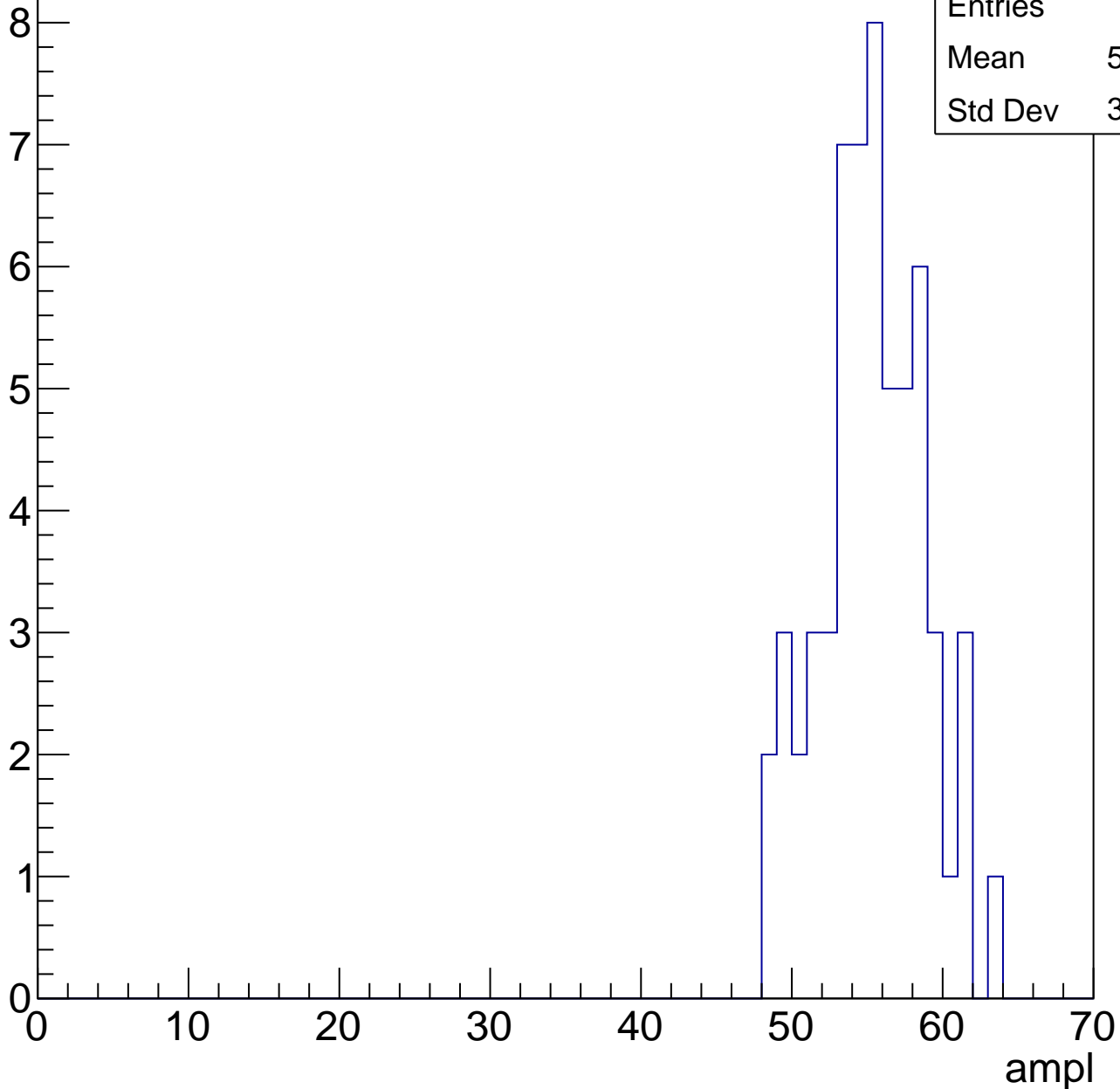


B1L103S, U18-ch94, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.86
Std Dev	3.417

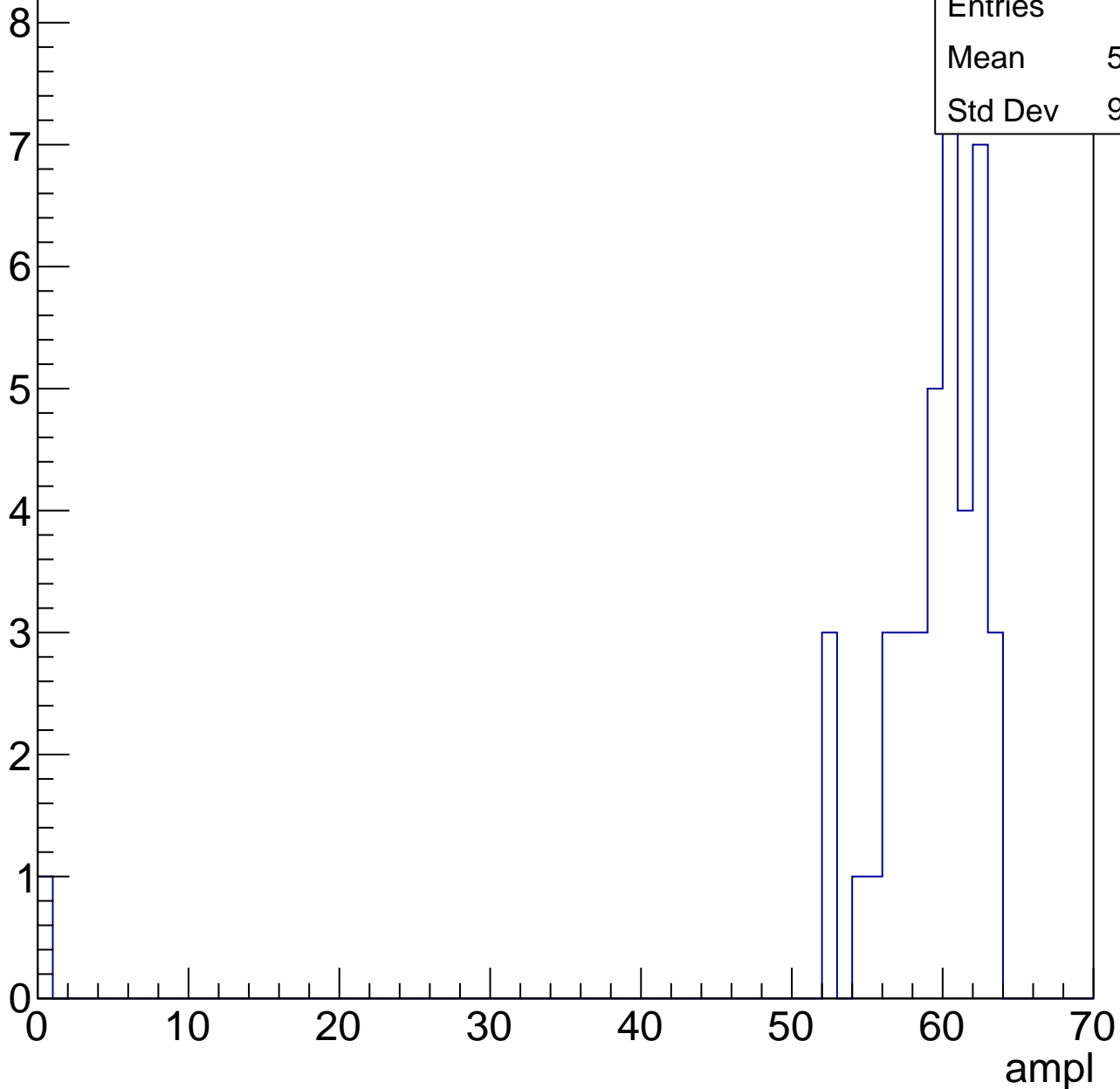


B1L103S, U18-ch94, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	57.62
Std Dev	9.469

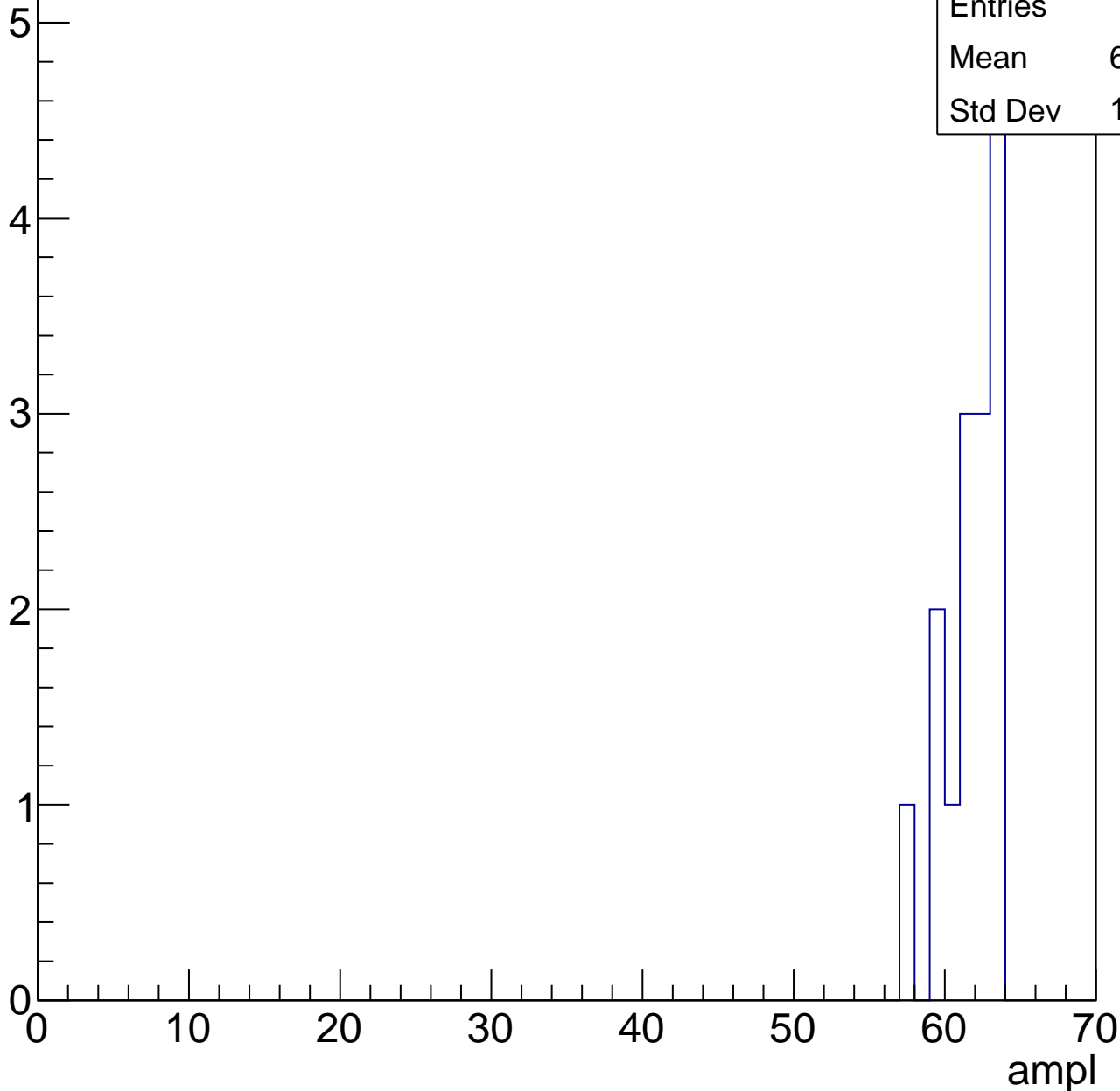


B1L103S, U18-ch94, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	61.27
Std Dev	1.769



B1L103S, U18-ch94, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U18-ch95, adc0

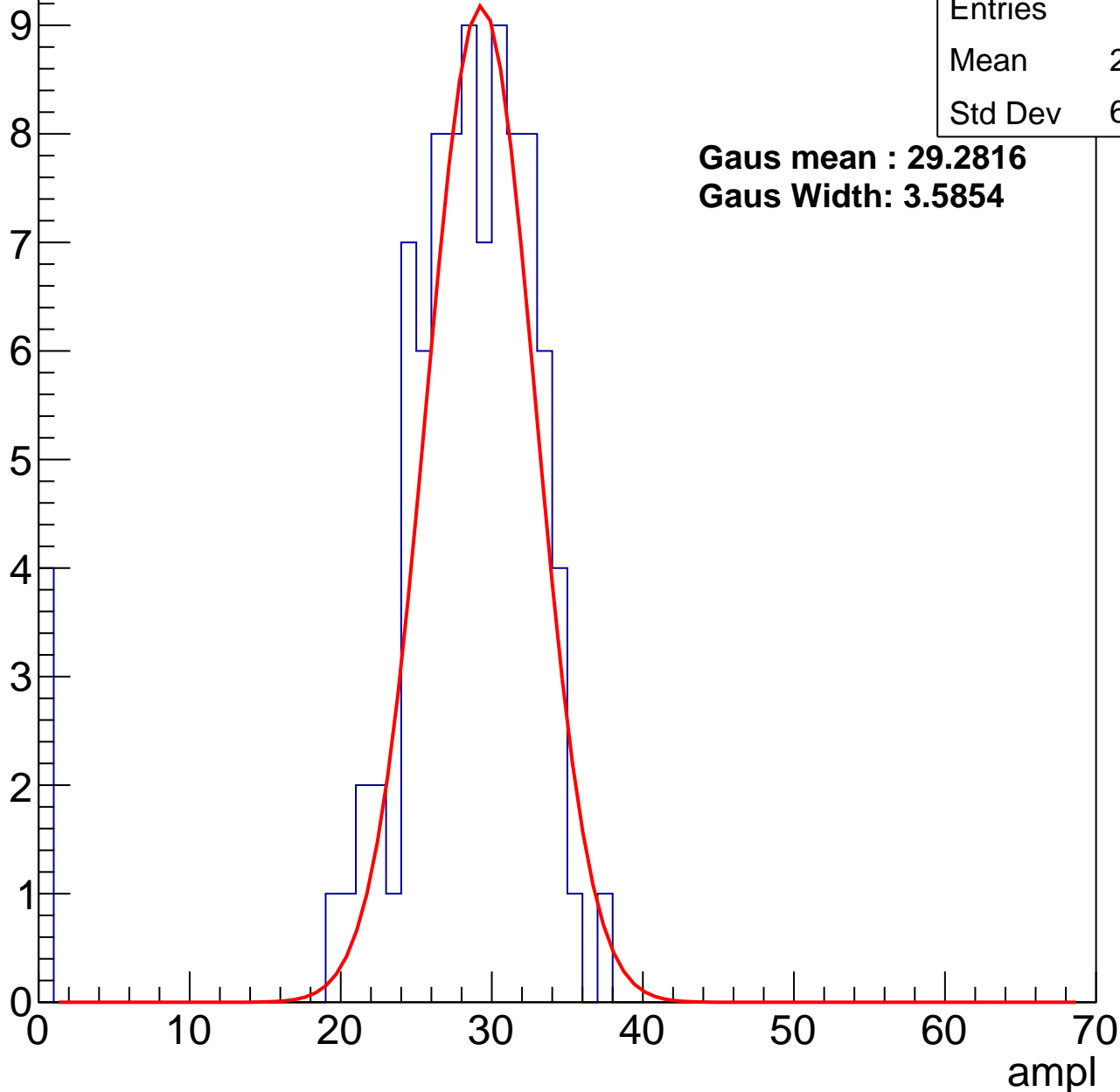
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	93
Mean	27.15
Std Dev	6.785

Gaus mean : 29.2816

Gaus Width: 3.5854



B1L103S, U18-ch95, adc1

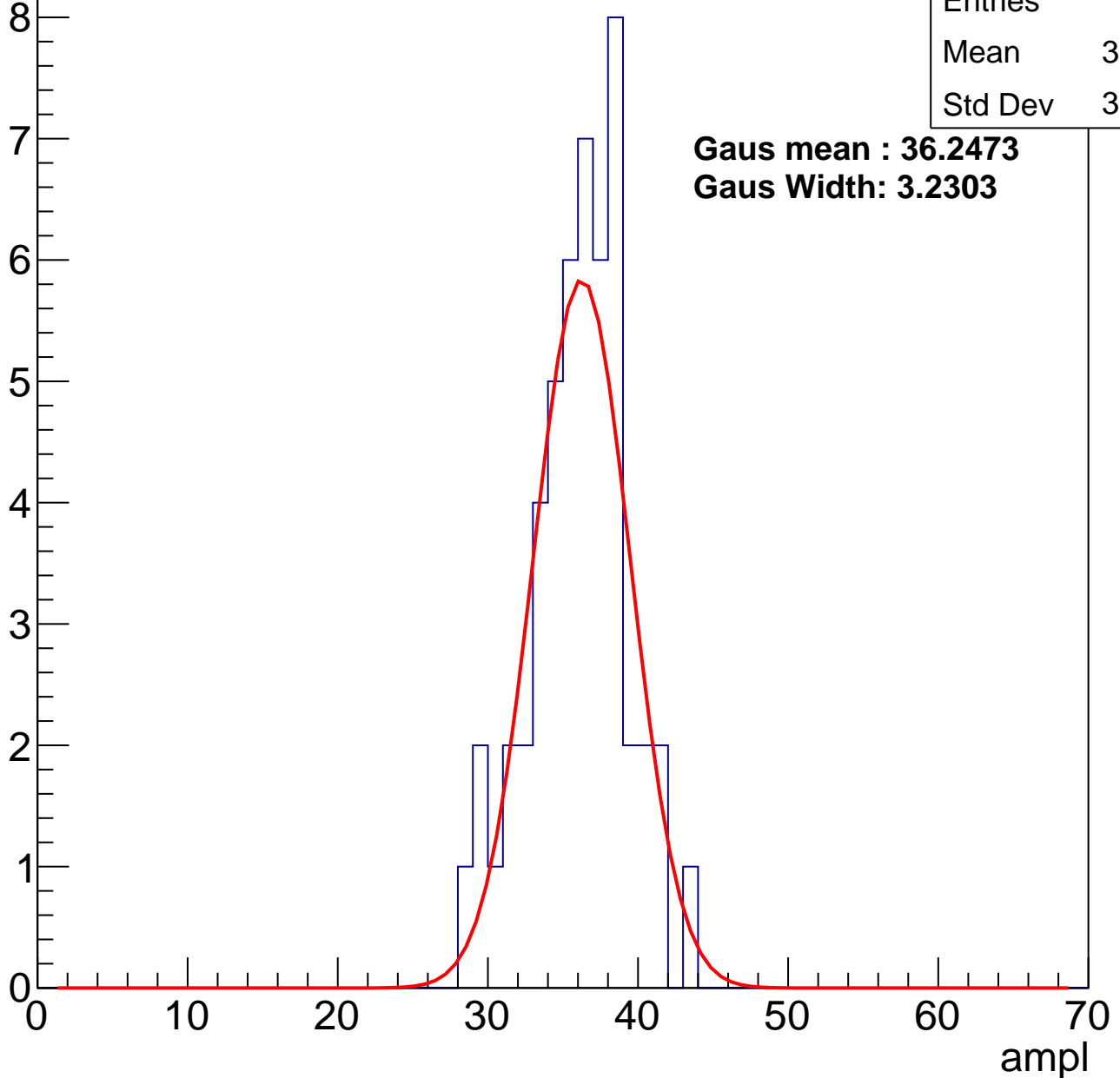
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	35.59
Std Dev	3.194

Gaus mean : 36.2473

Gaus Width: 3.2303



B1L103S, U18-ch95, adc2

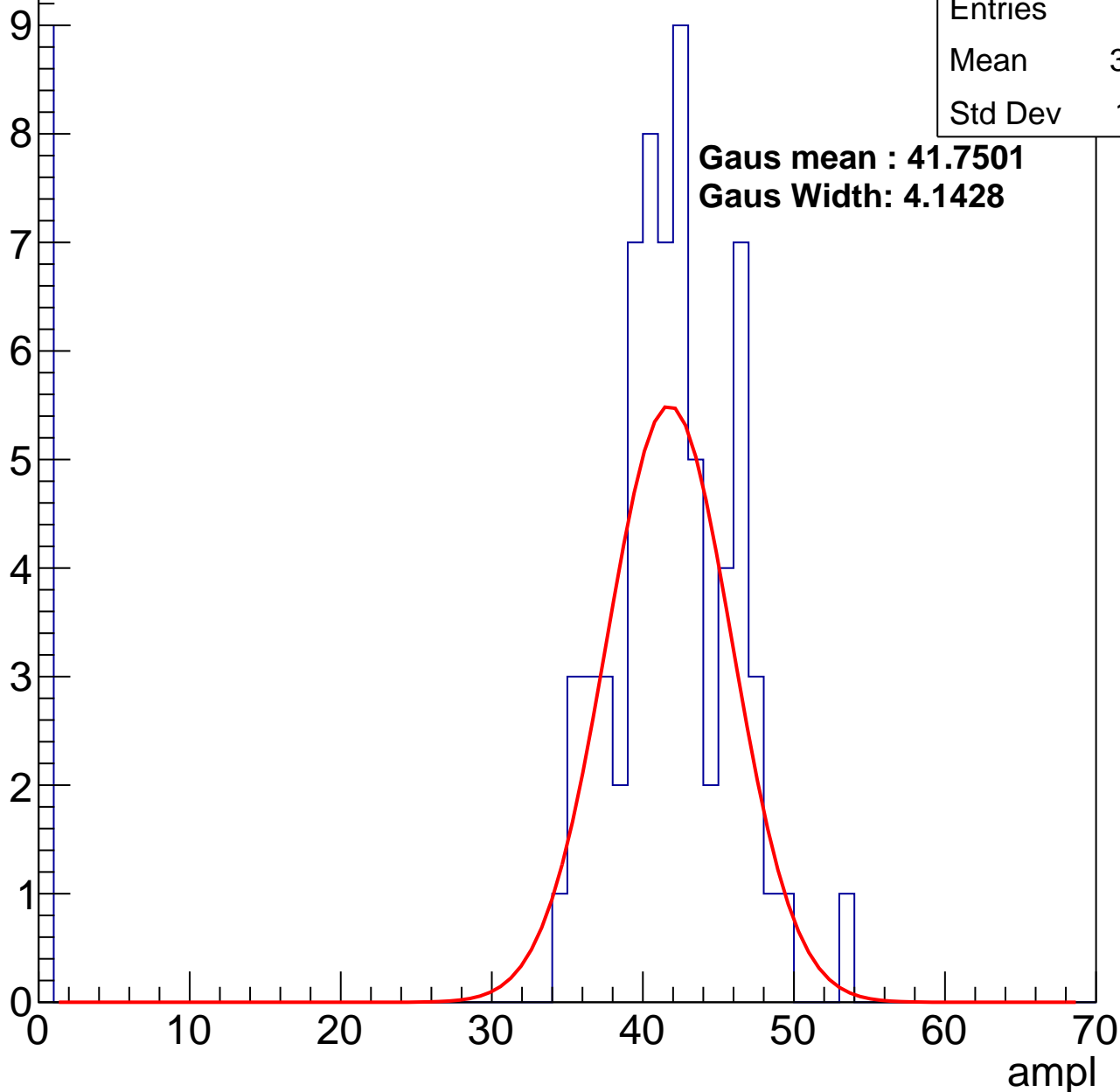
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	36.68
Std Dev	13.91

Gaus mean : 41.7501

Gaus Width: 4.1428

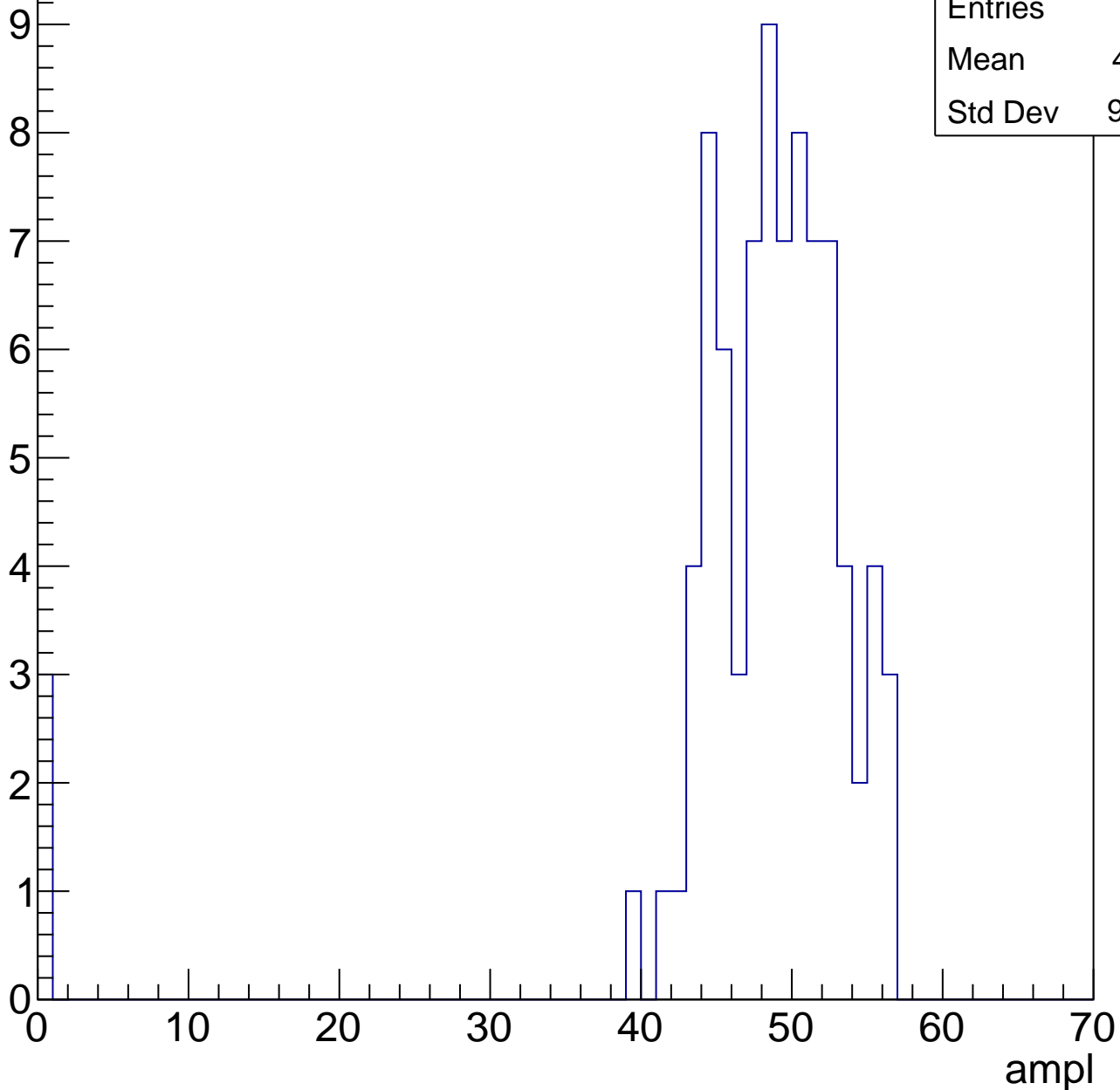


B1L103S, U18-ch95, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	46.91
Std Dev	9.732



B1L103S, U18-ch95, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 43

Mean 55.07

Std Dev 2.937

0

0

10

20

30

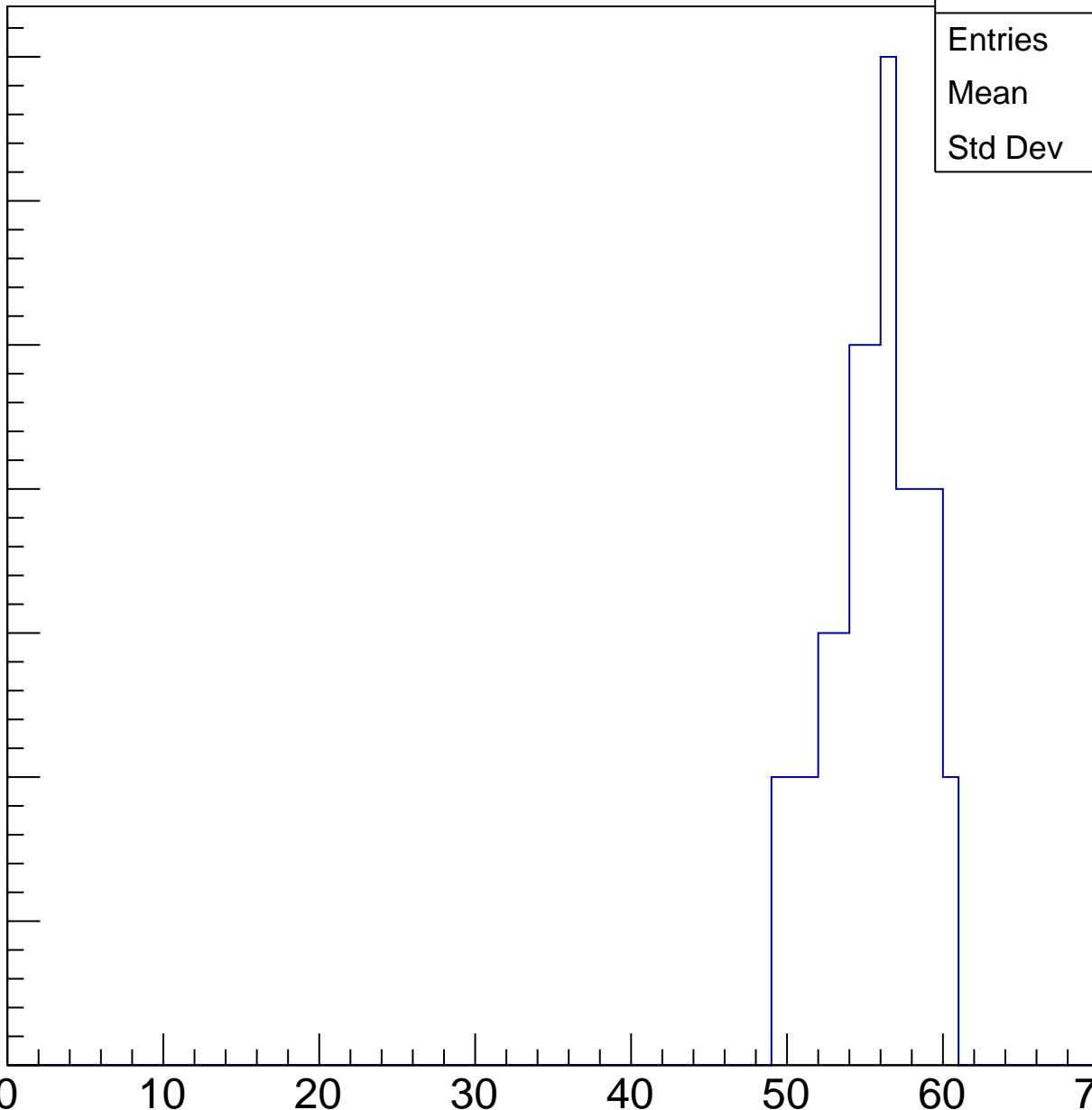
40

50

60

70

ampl

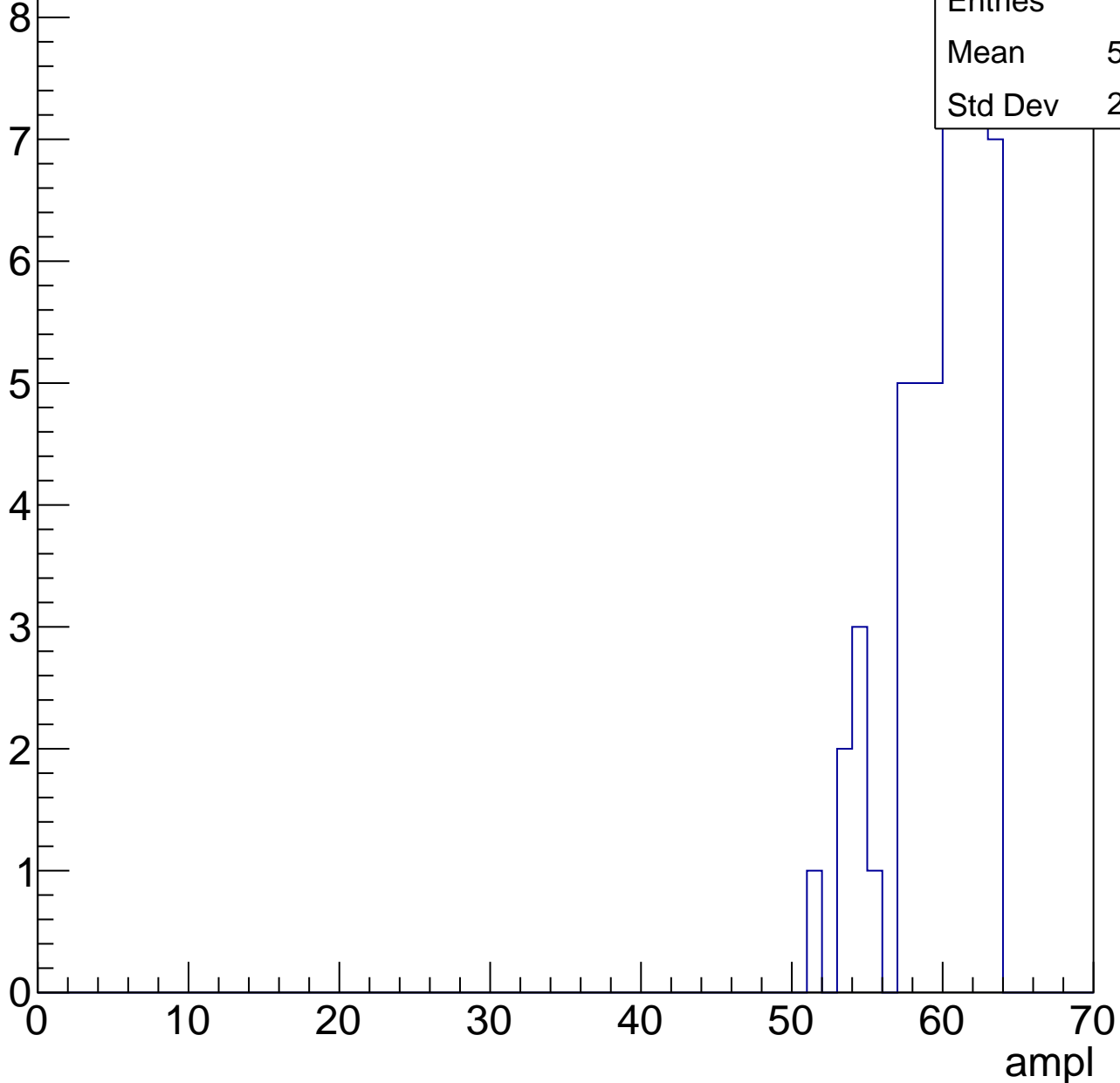


B1L103S, U18-ch95, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

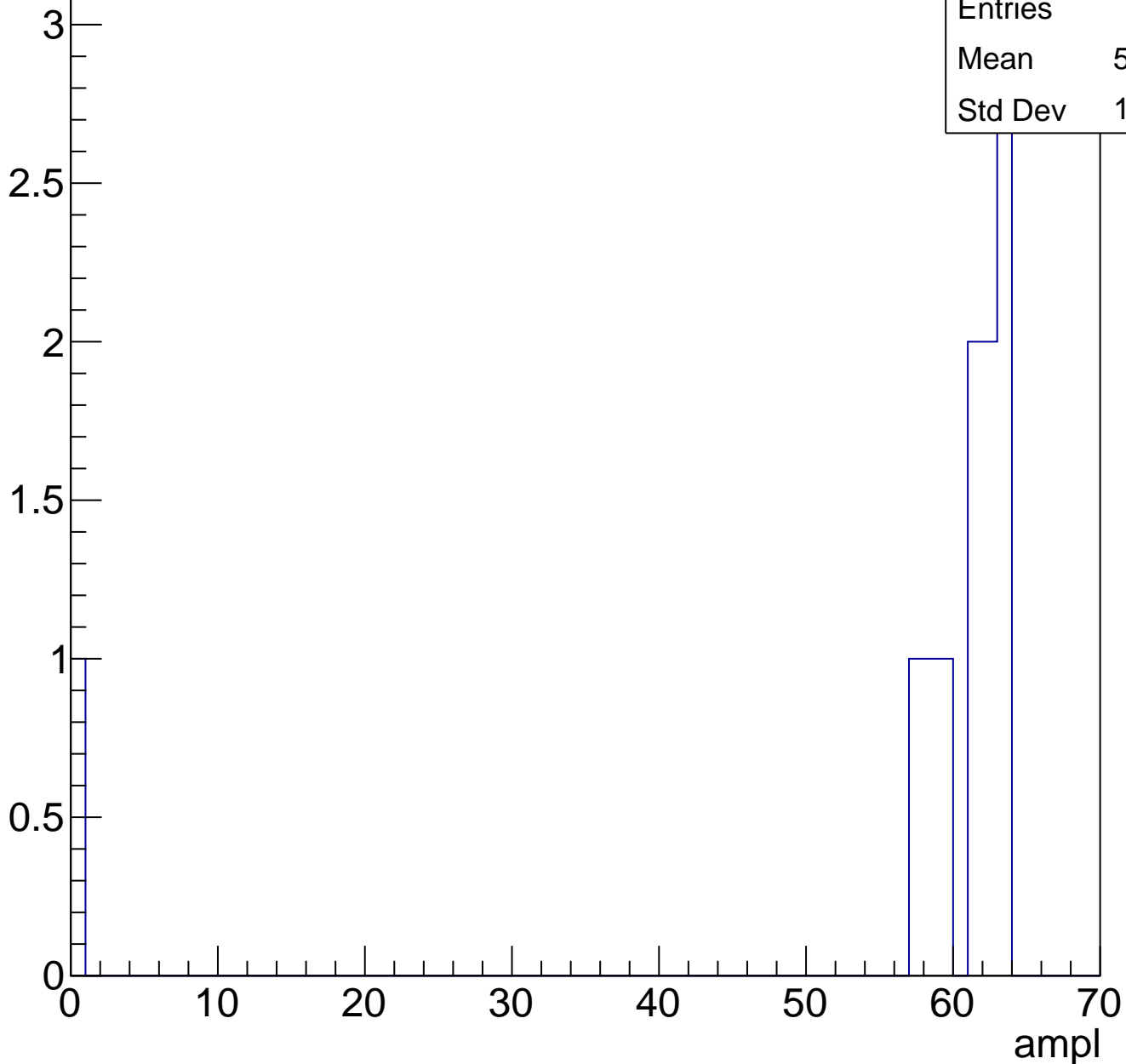
Entries	53
Mean	59.42
Std Dev	2.968



B1L103S, U18-ch95, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch95, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch96, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	79
Mean	23.15
Std Dev	10.87

Gaus mean : 28.6442

Gaus Width: 3.6469

Entry

12

10

8

6

4

2

0

0

10

20

30

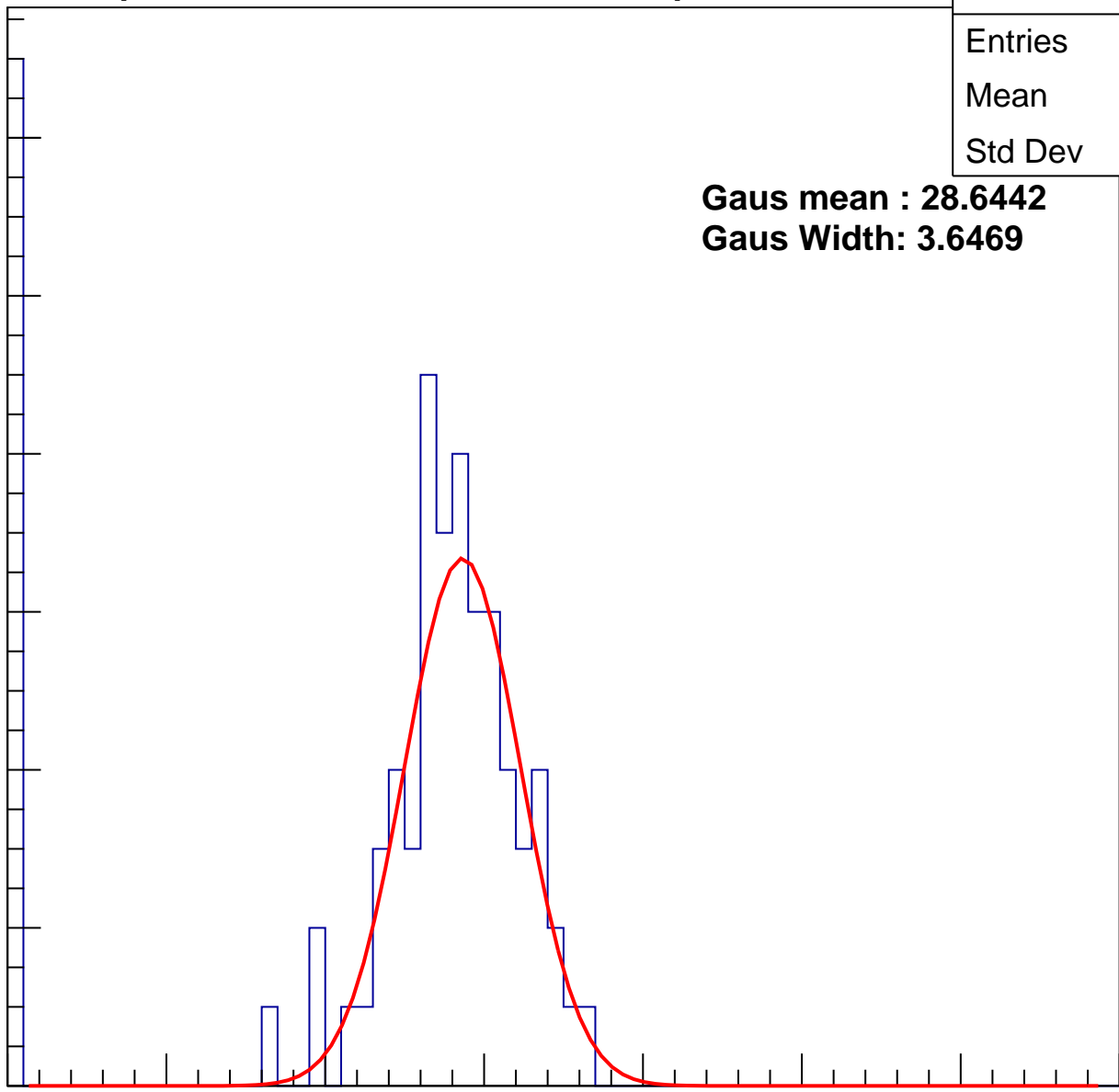
40

50

60

70

ampl



B1L103S, U18-ch96, adc1

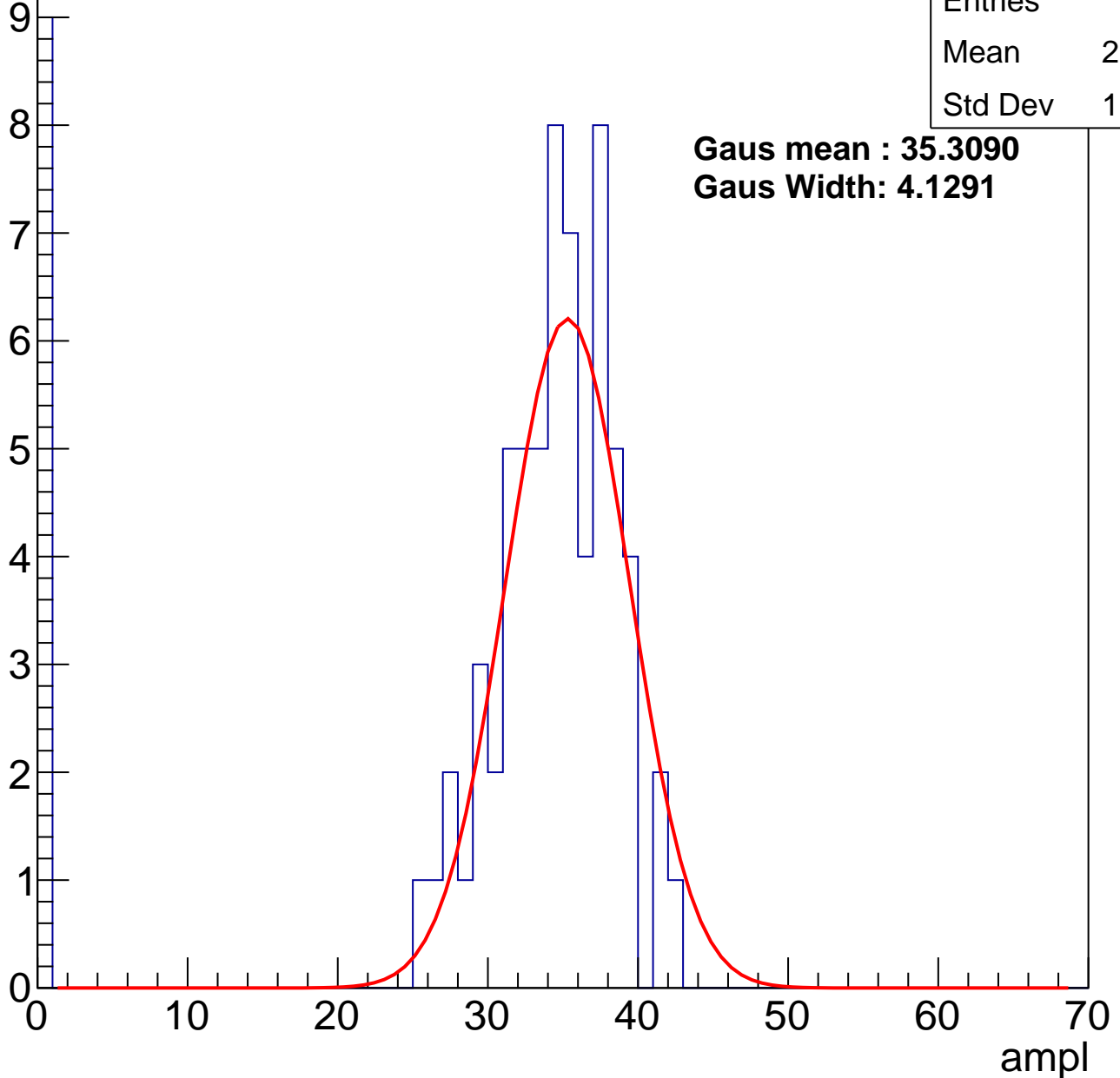
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	29.96
Std Dev	11.77

Gaus mean : 35.3090

Gaus Width: 4.1291



B1L103S, U18-ch96, adc2

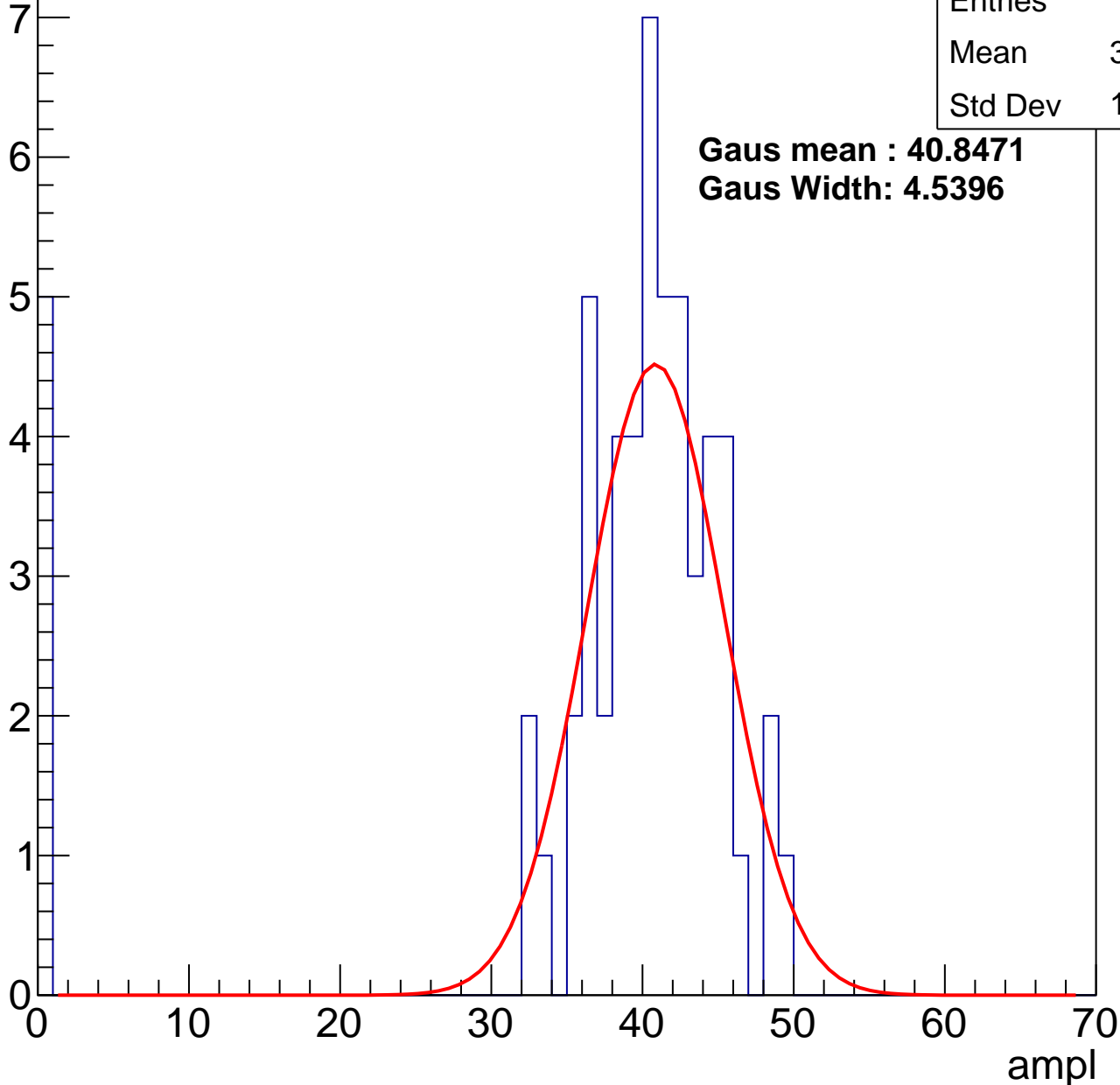
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	36.84
Std Dev	12.02

Gaus mean : 40.8471

Gaus Width: 4.5396

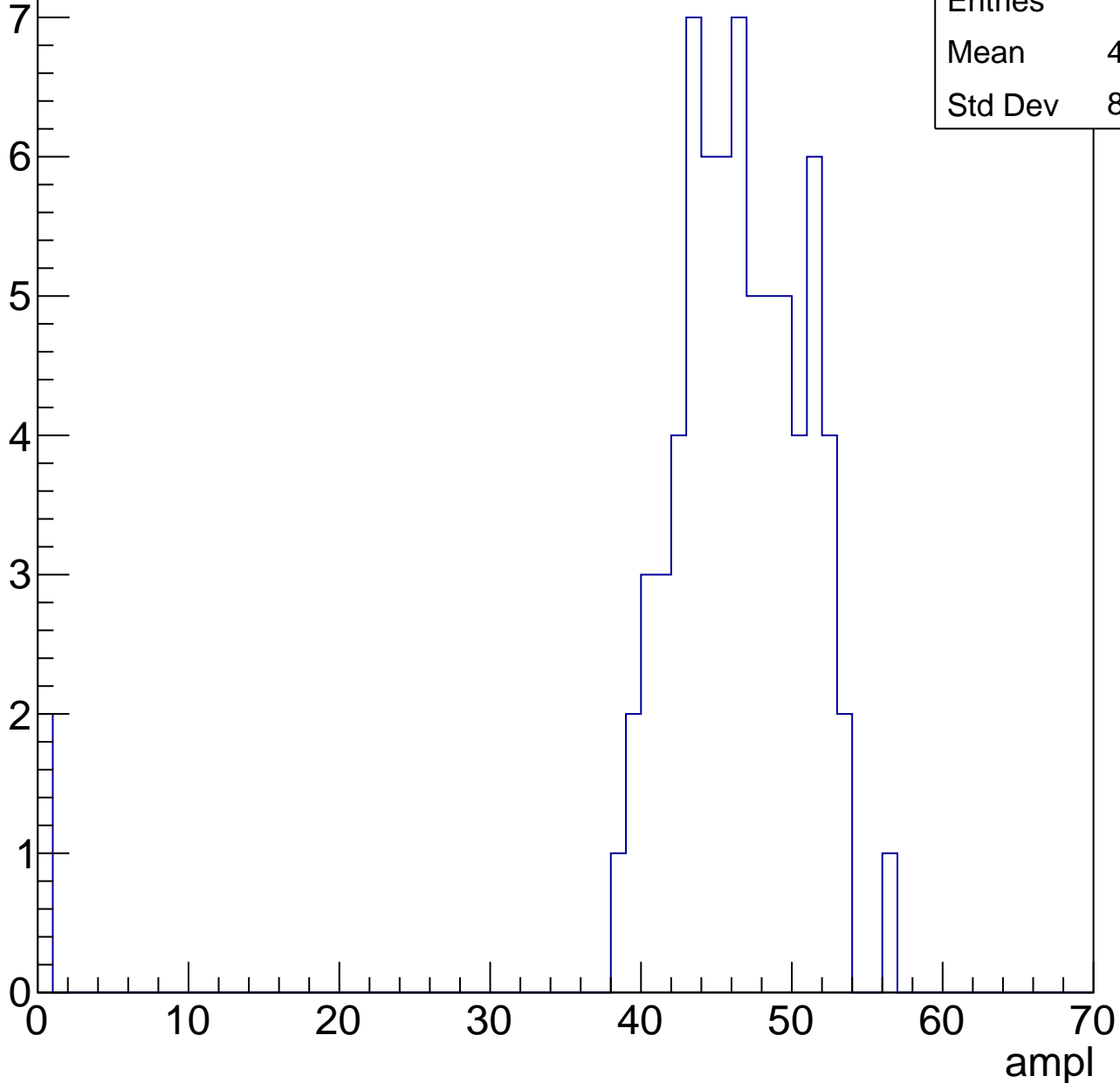


B1L103S, U18-ch96, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	44.93
Std Dev	8.502

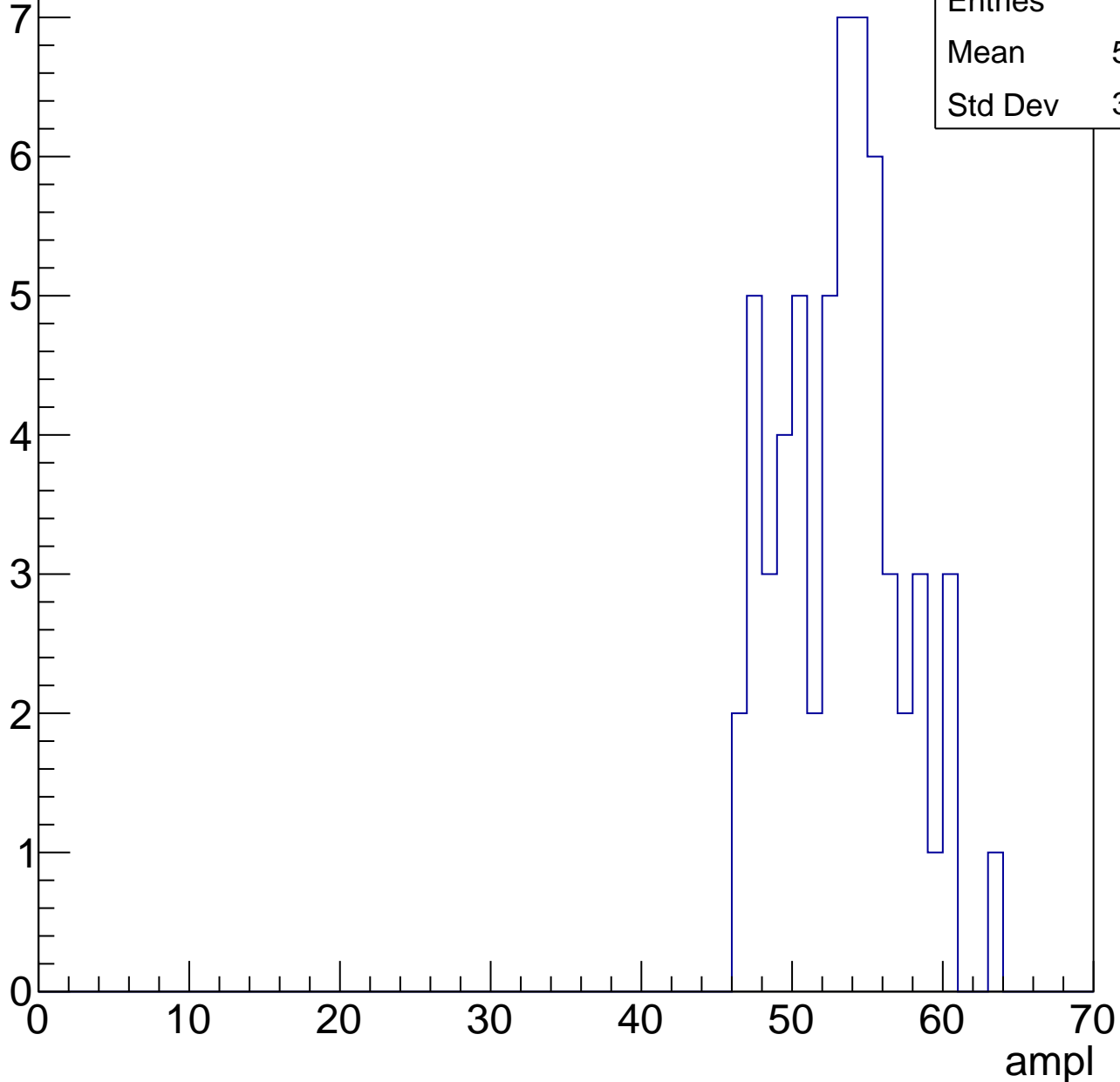


B1L103S, U18-ch96, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

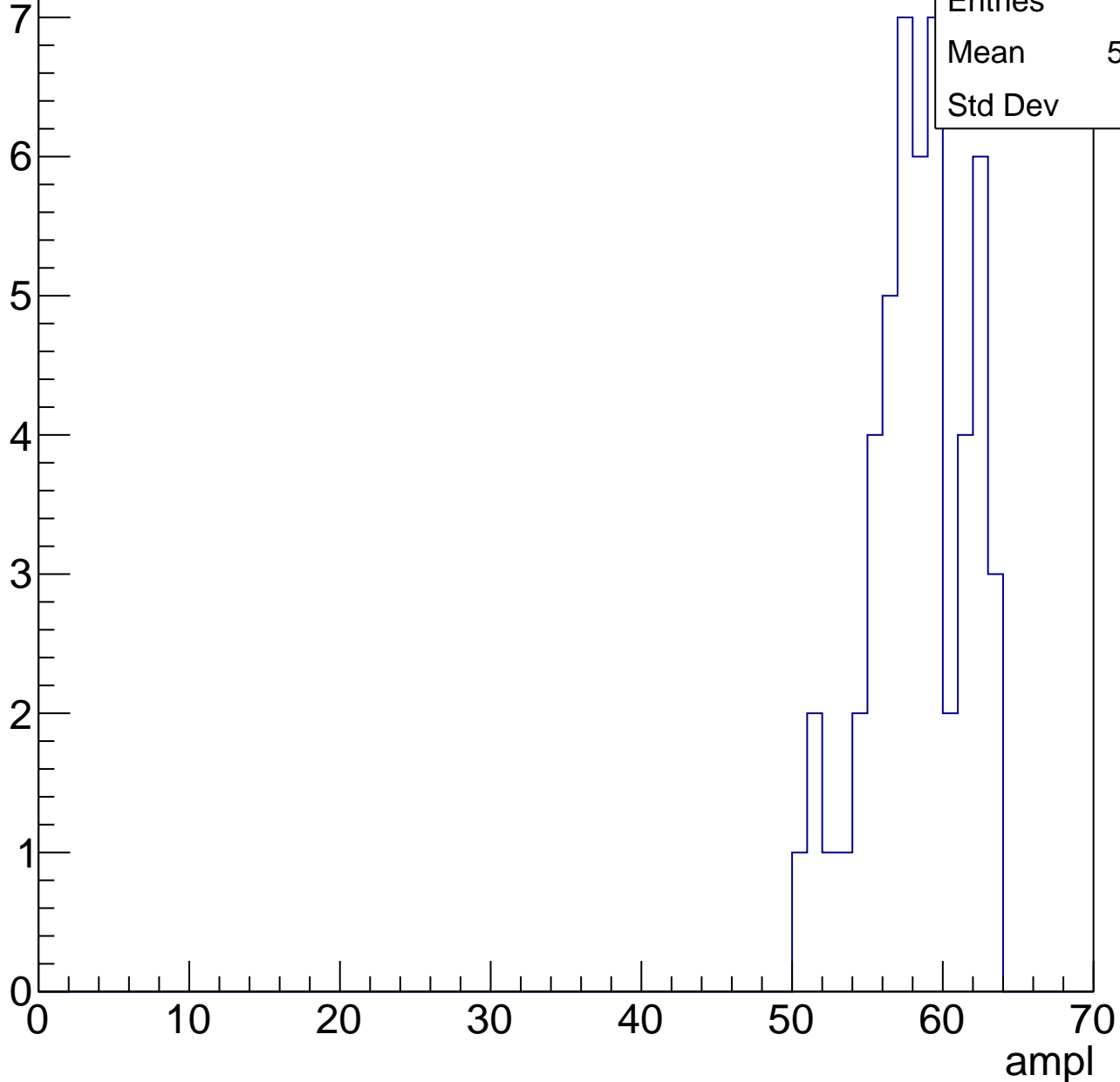
Entries	59
Mean	52.81
Std Dev	3.951



B1L103S, U18-ch96, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

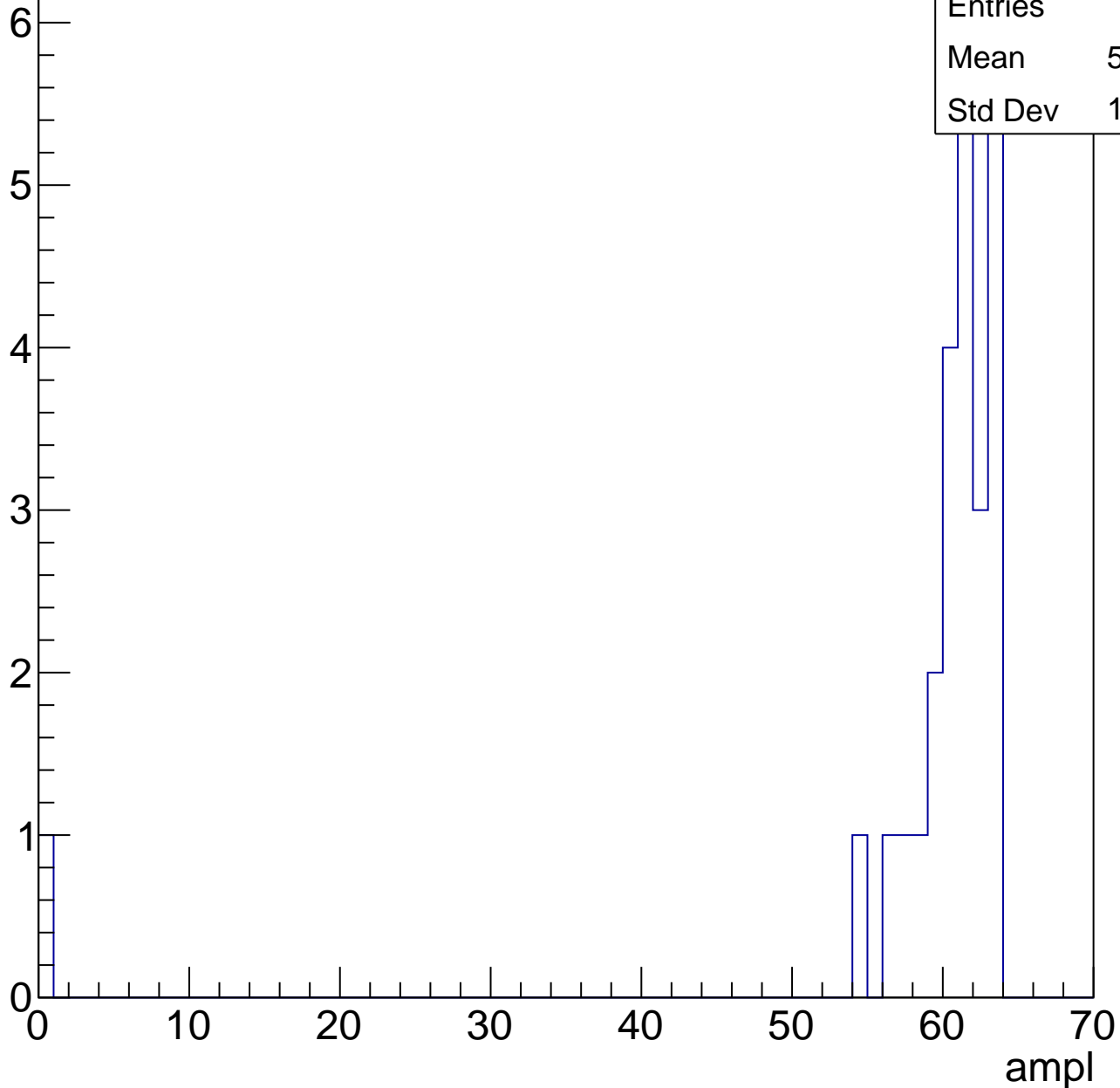


Entries	51
Mean	57.84
Std Dev	3.25

B1L103S, U18-ch96, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

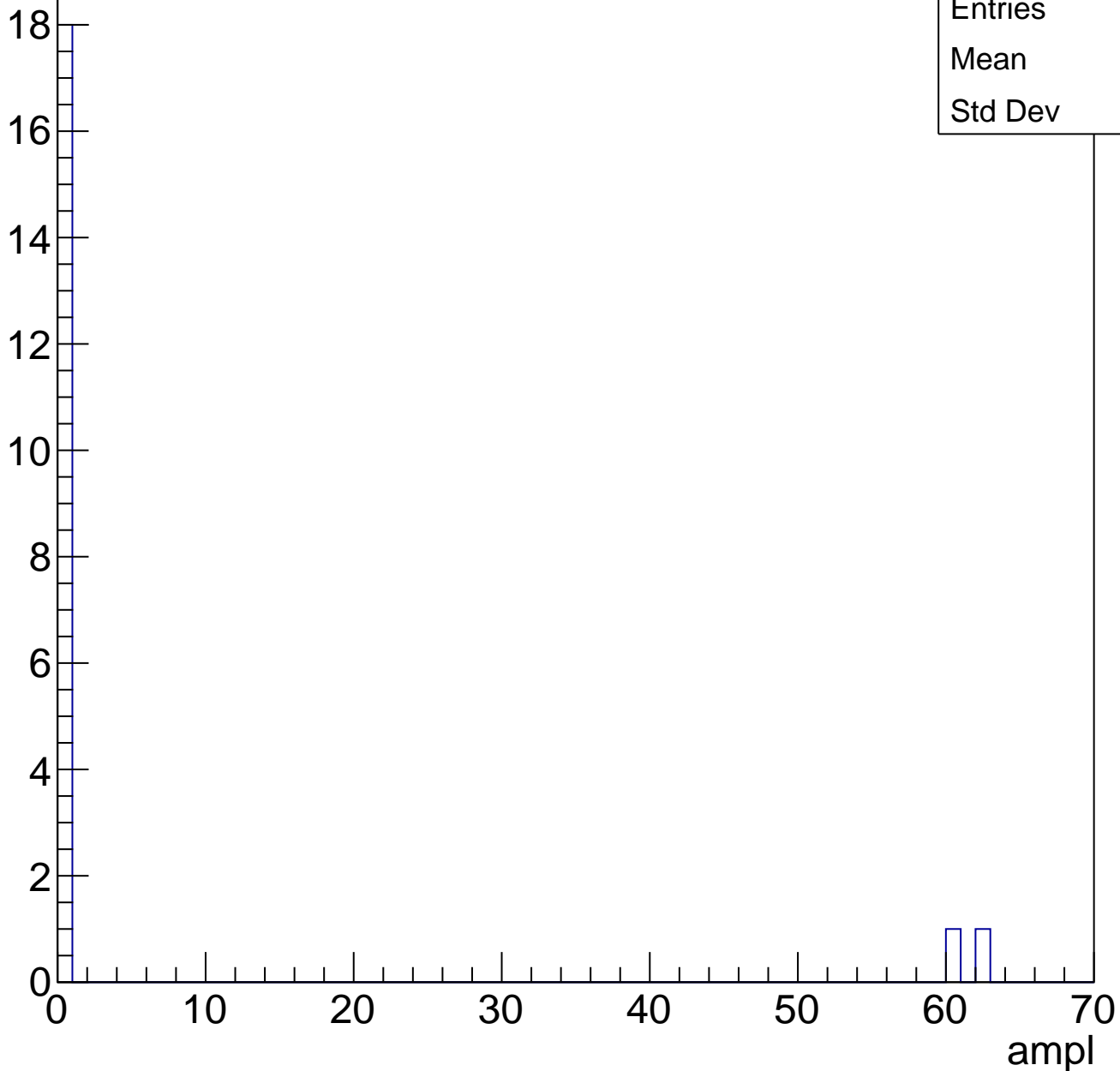


B1L103S, U18-ch96, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	6.1
Std Dev	18.3

Entry



B1L103S, U18-ch97, adc0

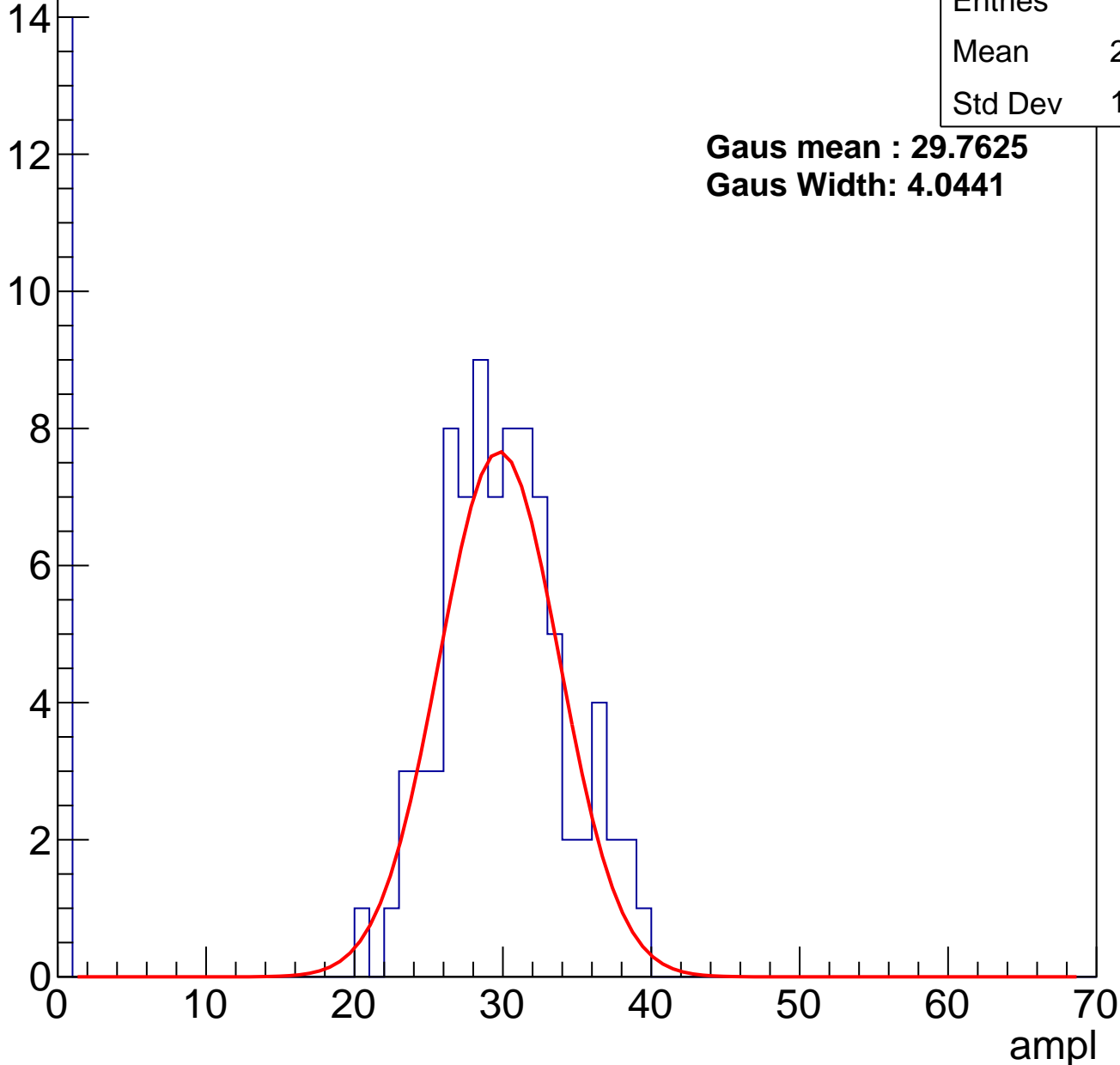
calib_packv5_041523_1651.root, FC#0, port C2

Entries	97
Mean	25.34
Std Dev	11.05

Gaus mean : 29.7625

Gaus Width: 4.0441

Entry



B1L103S, U18-ch97, adc1

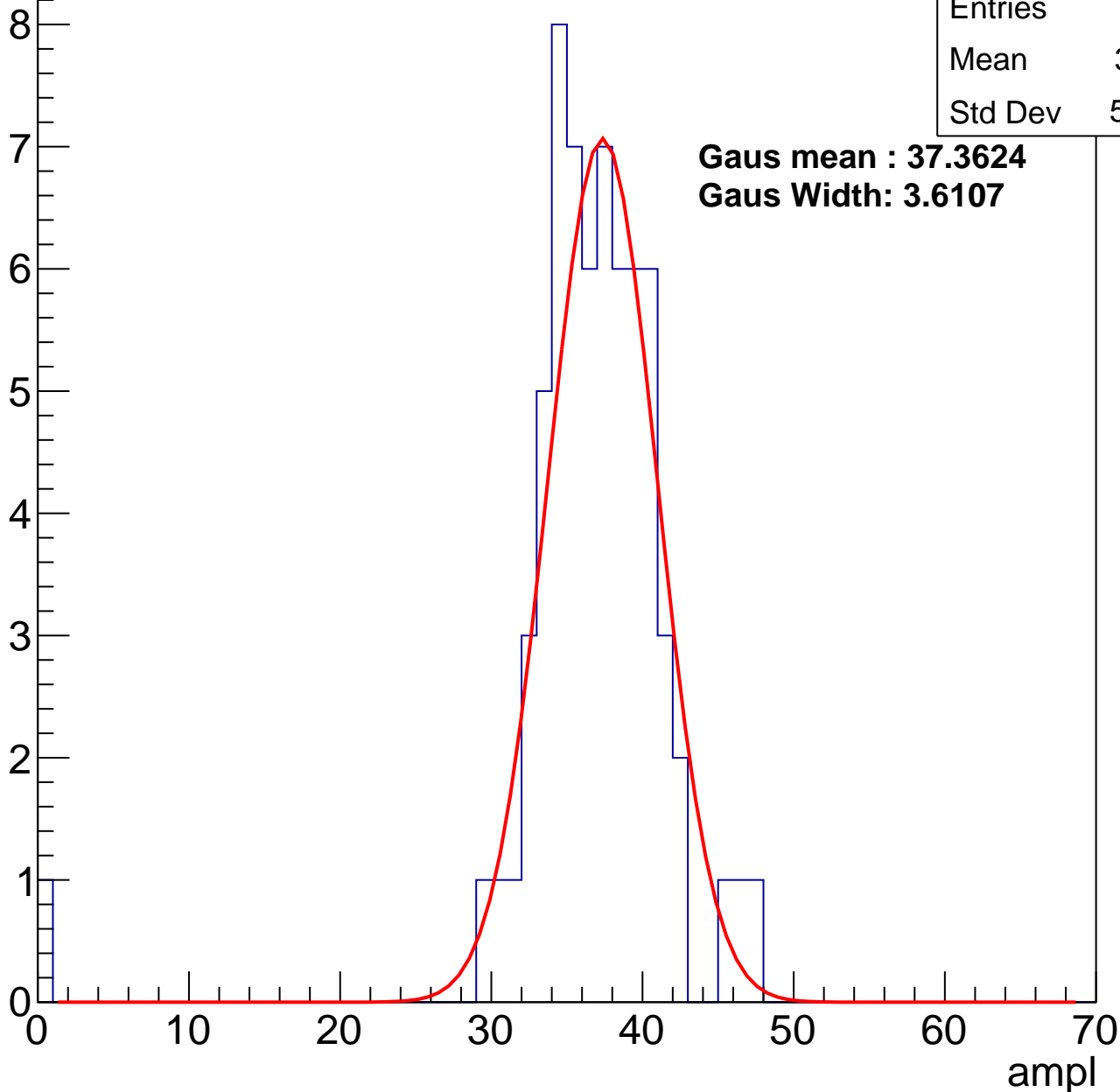
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.21
Std Dev	5.727

Gaus mean : 37.3624

Gaus Width: 3.6107



B1L103S, U18-ch97, adc2

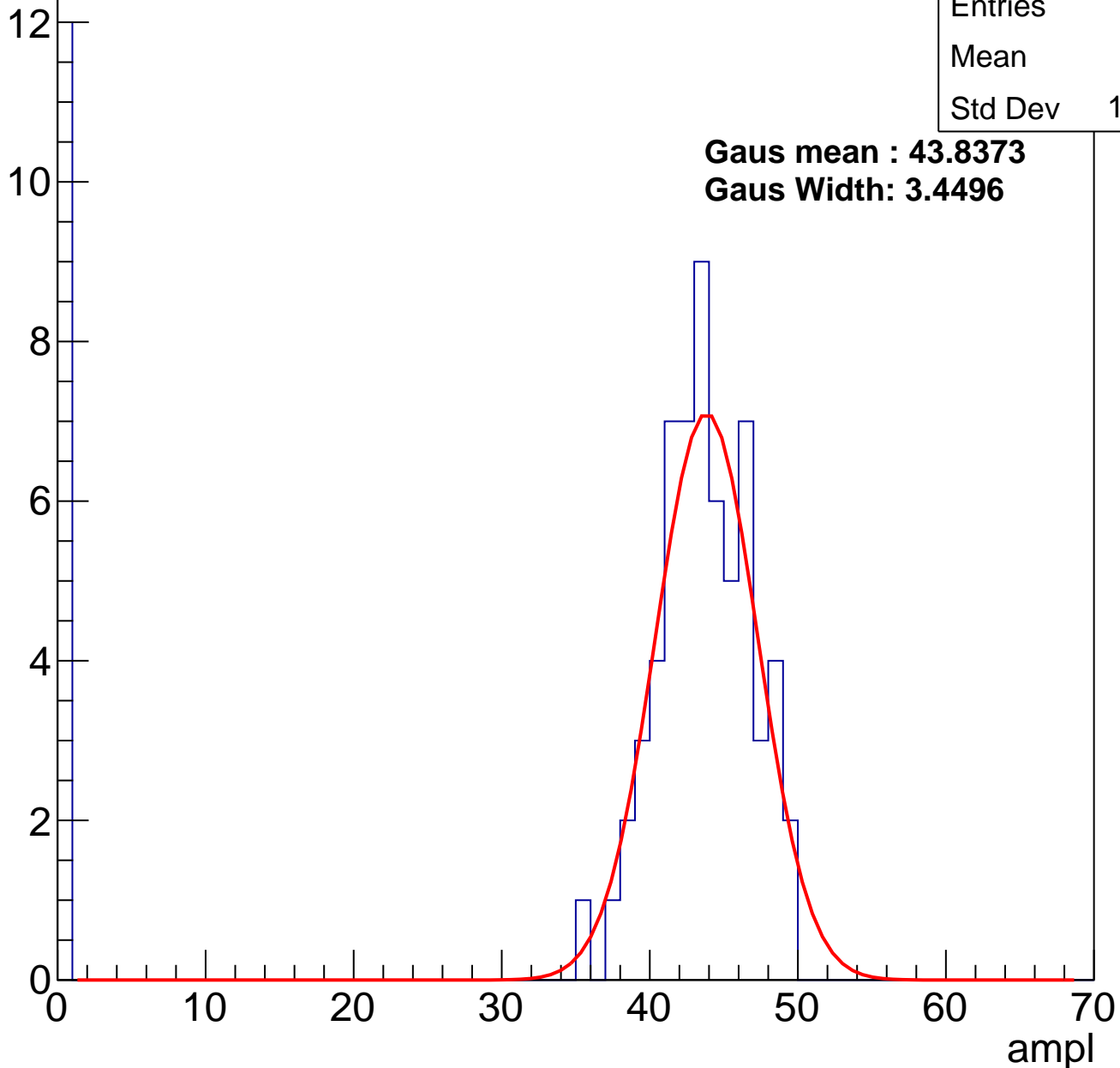
calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	36.1
Std Dev	16.25

Gaus mean : 43.8373

Gaus Width: 3.4496

Entry

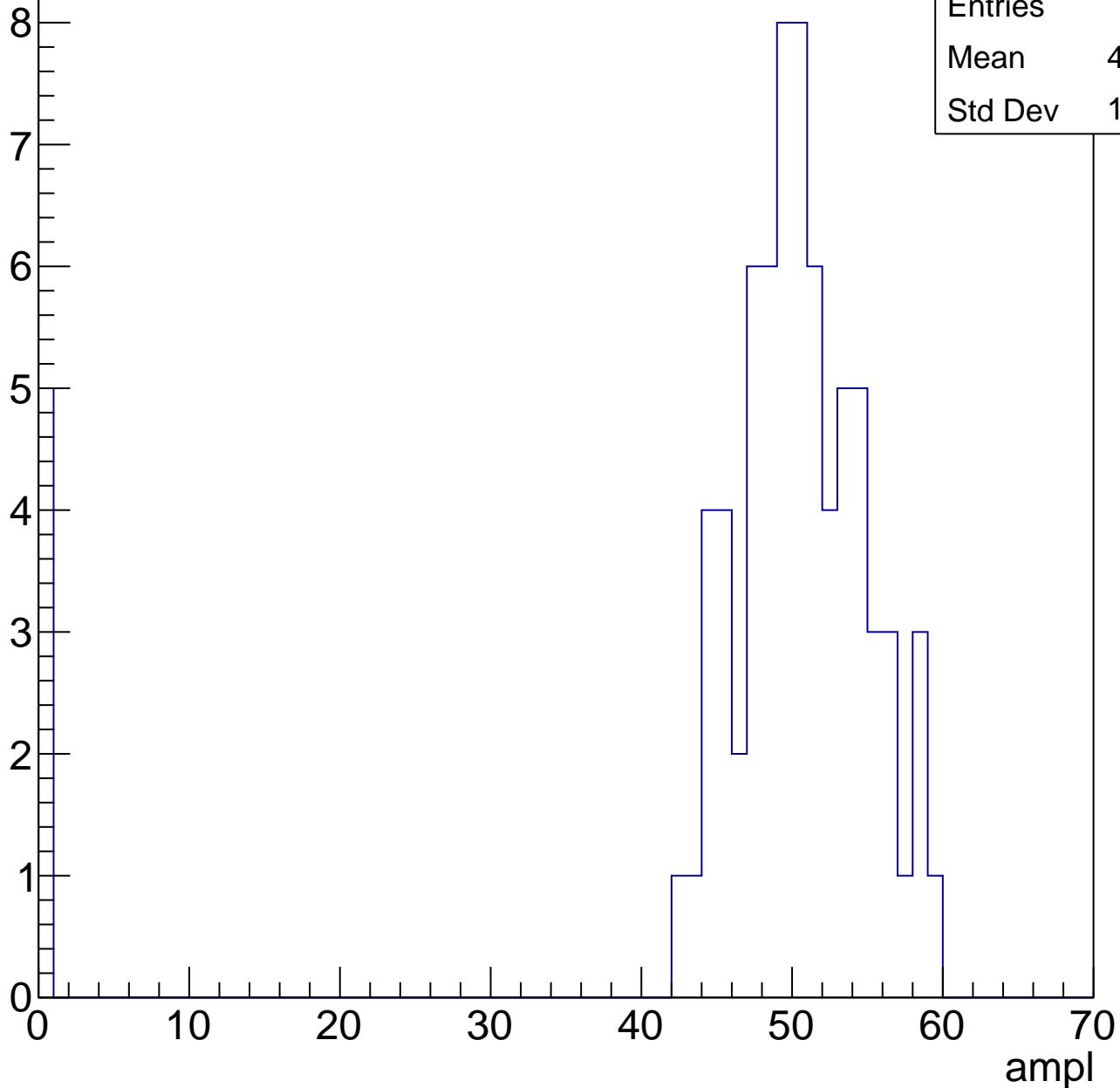


B1L103S, U18-ch97, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	46.93
Std Dev	13.03

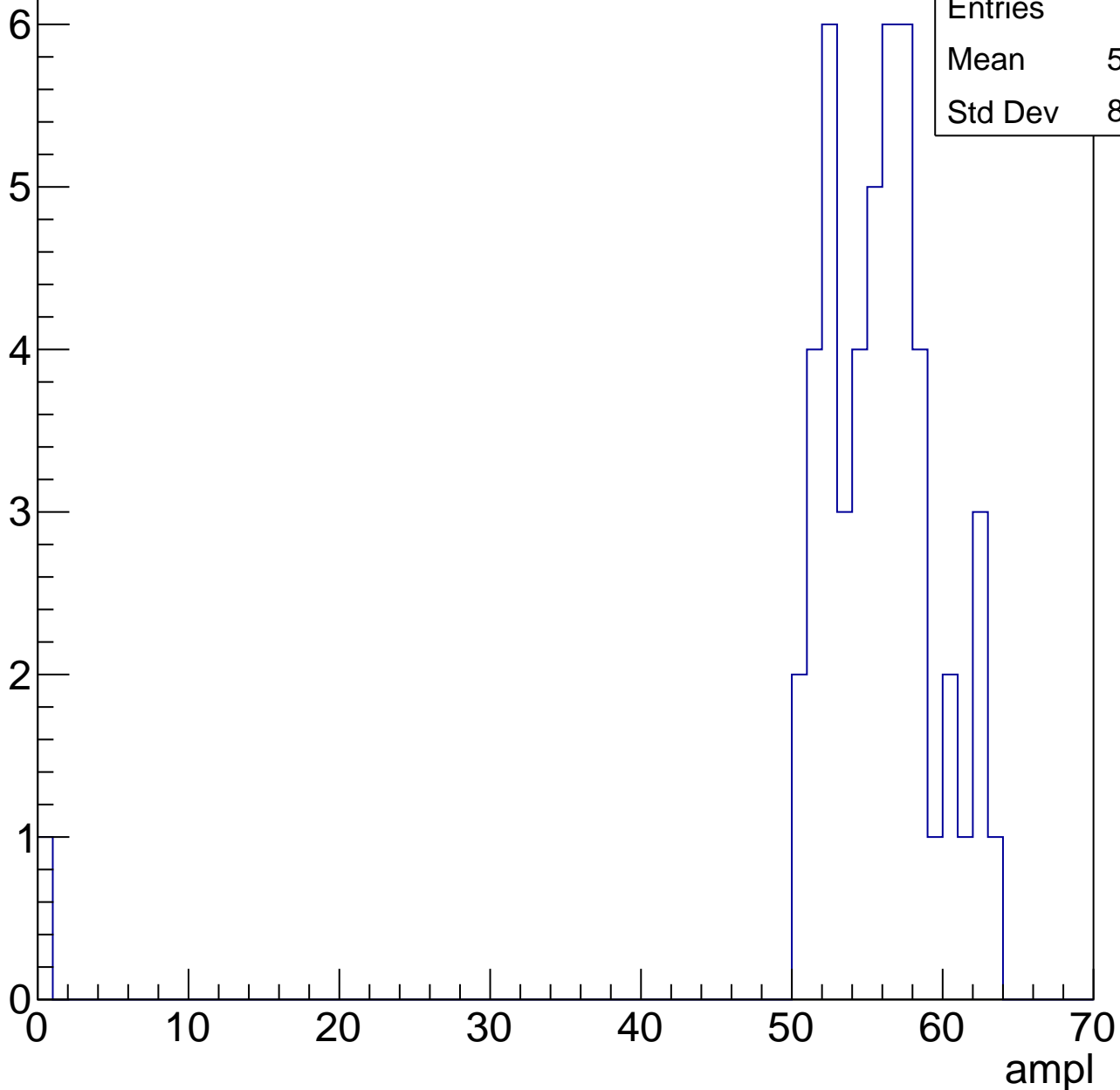


B1L103S, U18-ch97, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

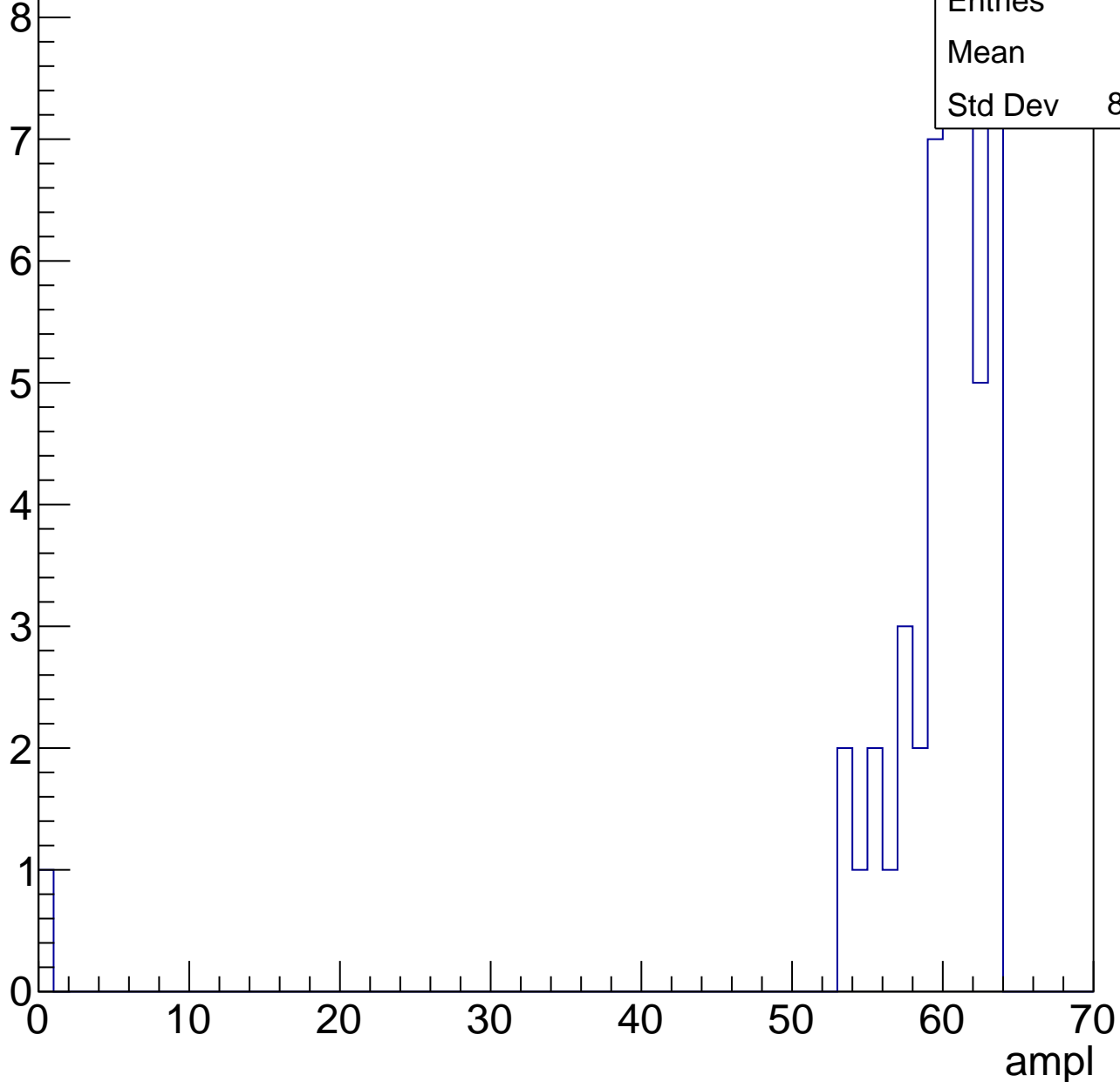
Entries	49
Mean	54.39
Std Dev	8.535



B1L103S, U18-ch97, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

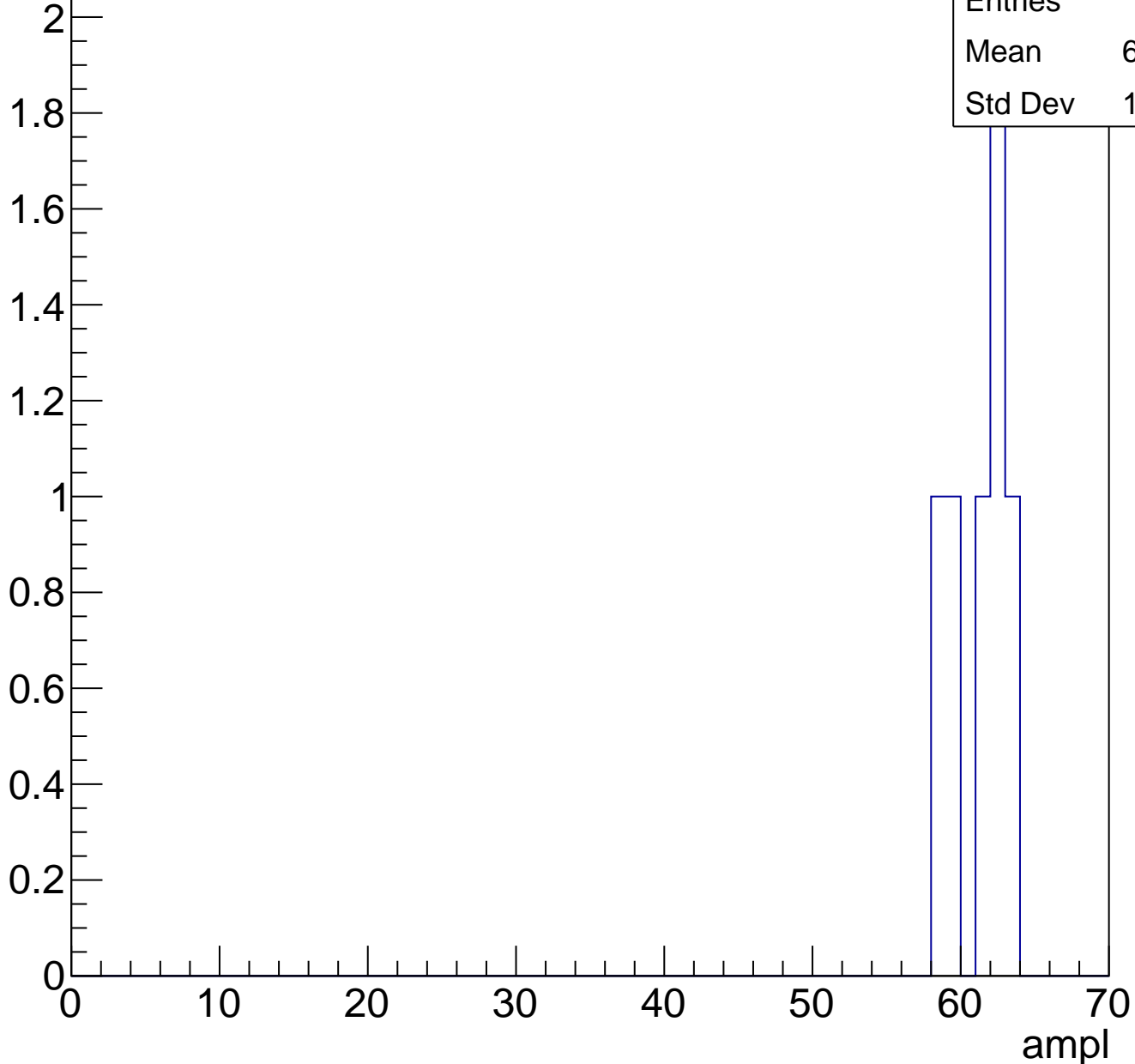


Entries	48
Mean	58.5
Std Dev	8.937

B1L103S, U18-ch97, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch97, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch98, adc0

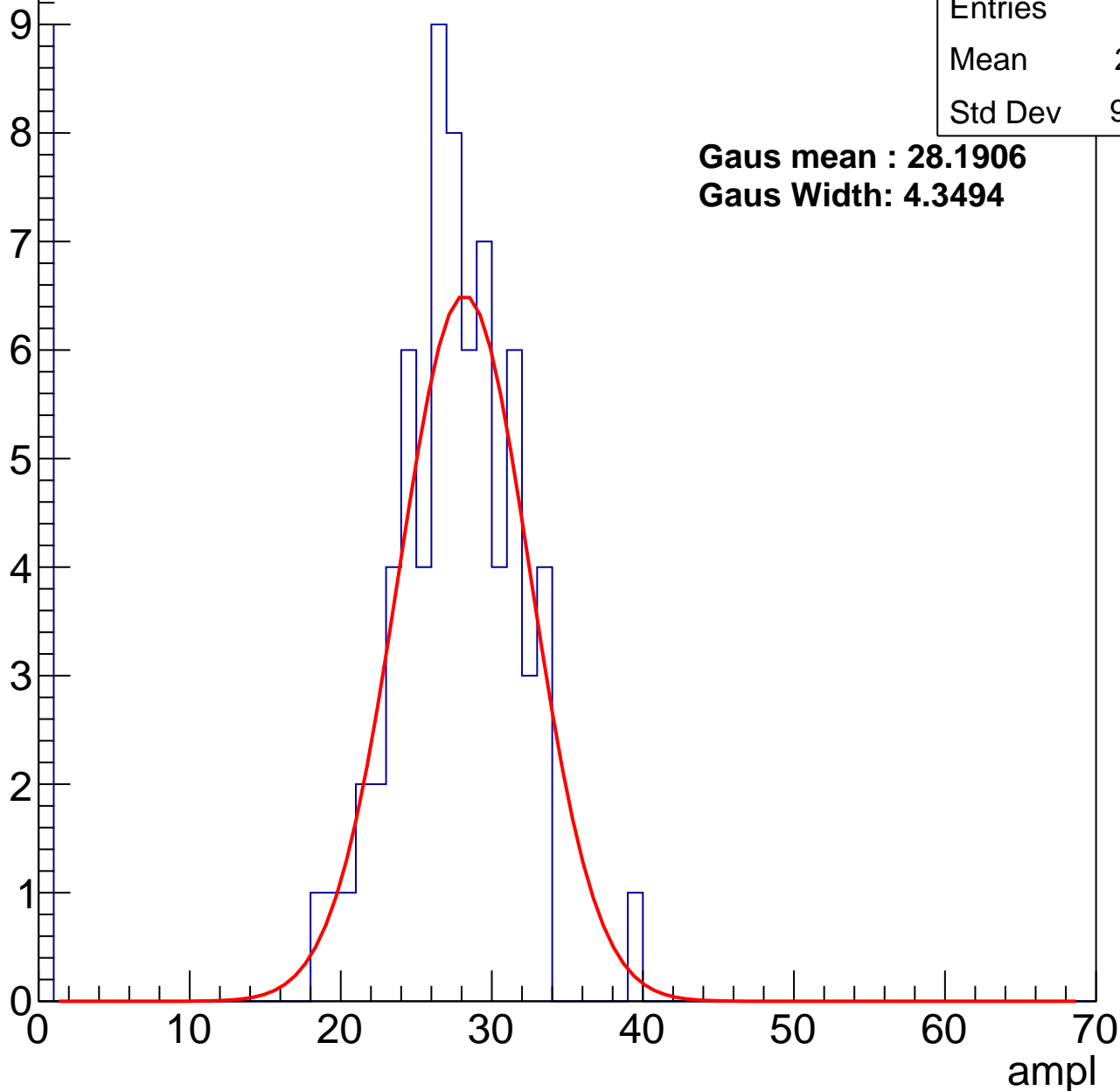
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	24.01
Std Dev	9.373

Gaus mean : 28.1906

Gaus Width: 4.3494



B1L103S, U18-ch98, adc1

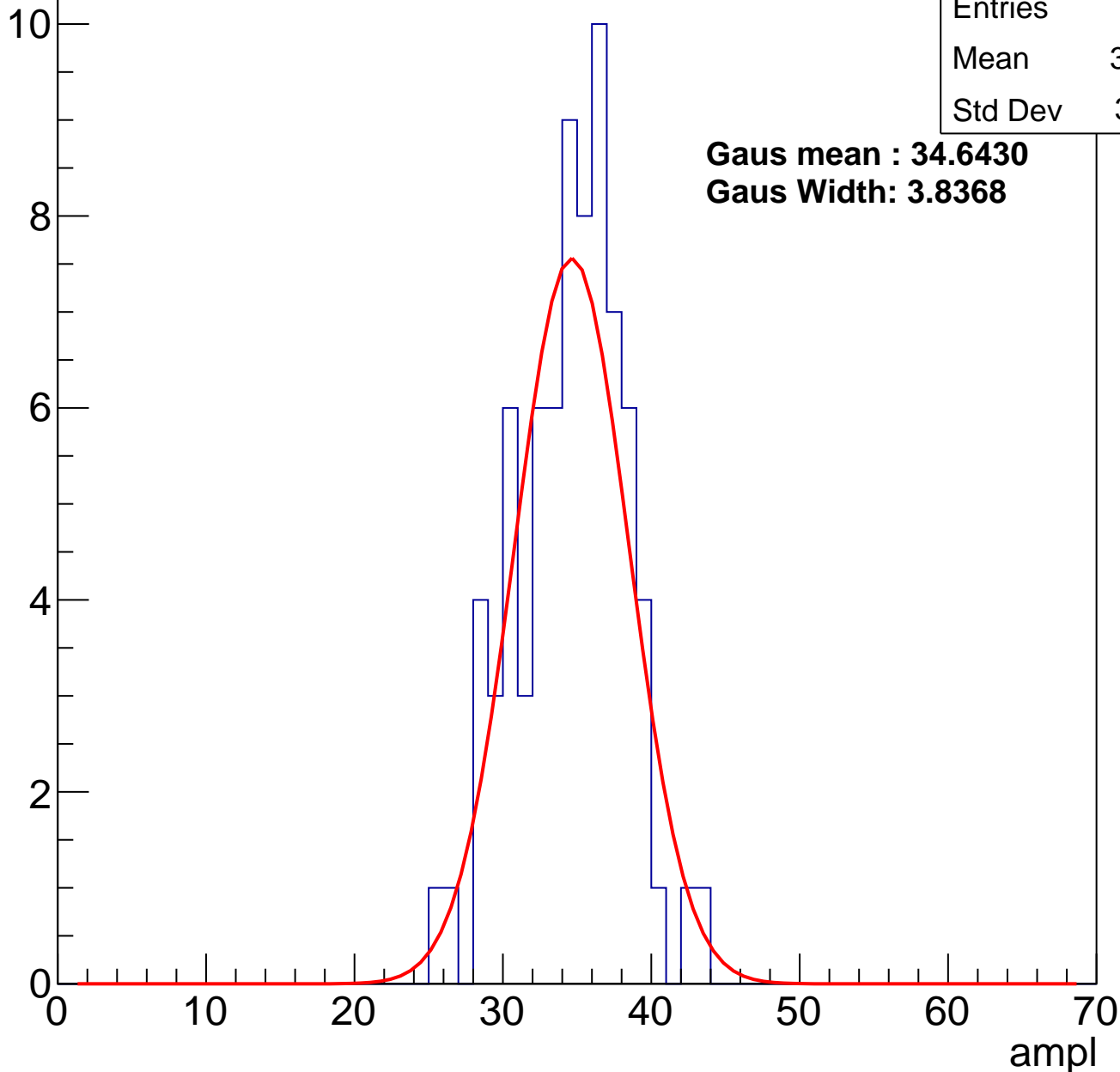
calib_packv5_041523_1651.root, FC#0, port C2

Entries	77
Mean	34.12
Std Dev	3.611

Gaus mean : 34.6430

Gaus Width: 3.8368

Entry



B1L103S, U18-ch98, adc2

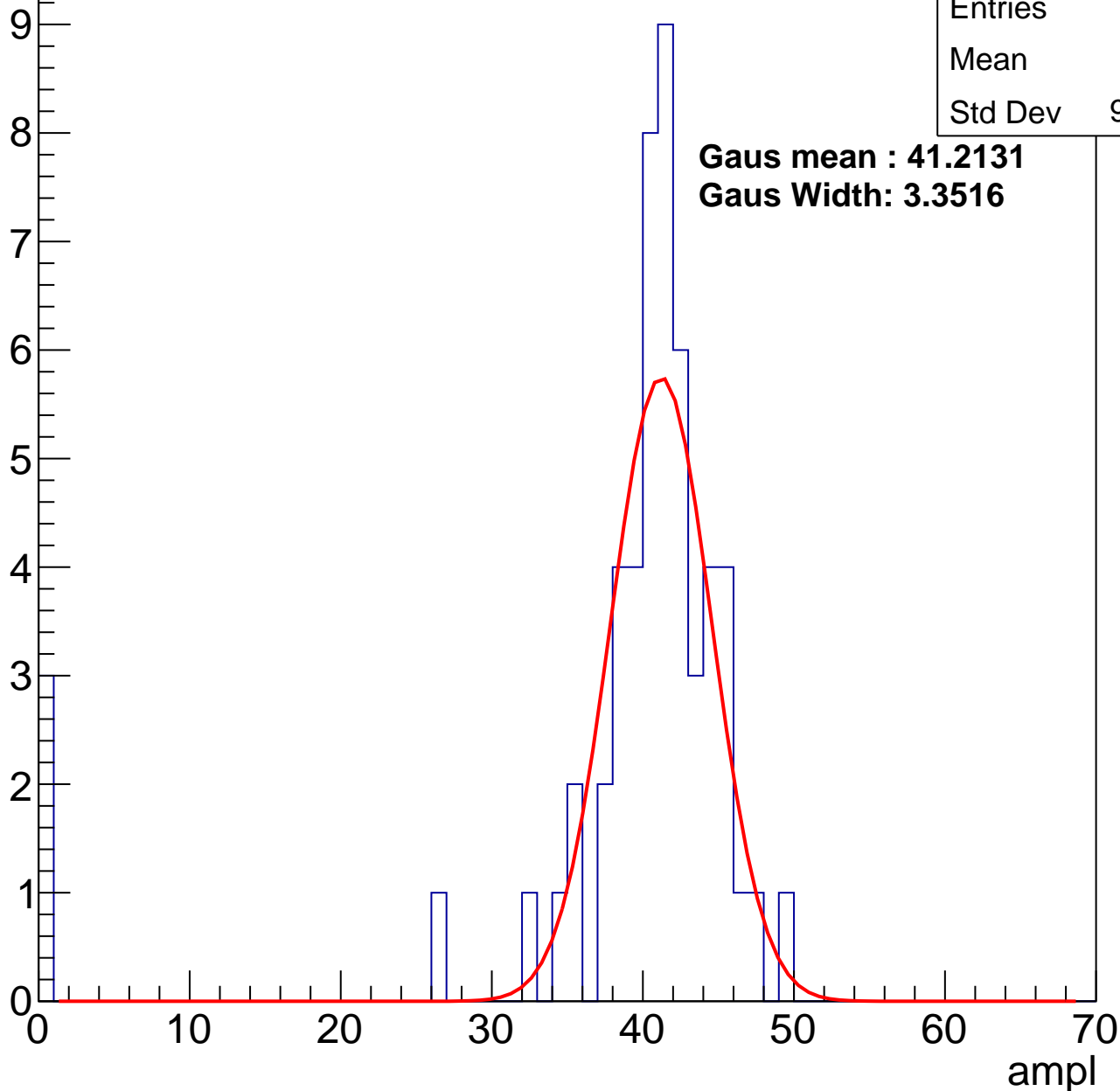
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	38.4
Std Dev	9.939

Gaus mean : 41.2131

Gaus Width: 3.3516

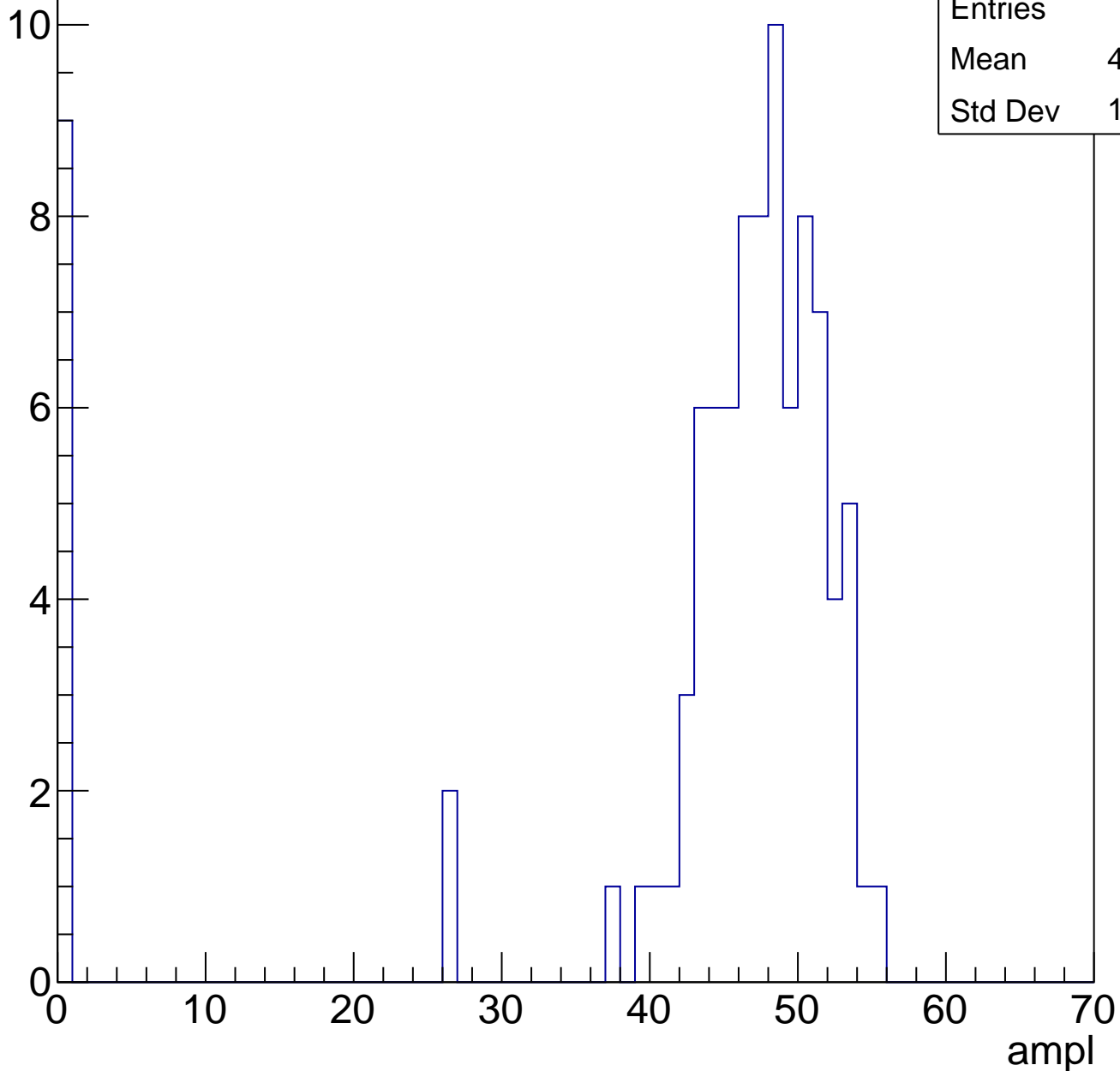


B1L103S, U18-ch98, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	42.38
Std Dev	14.54

Entry

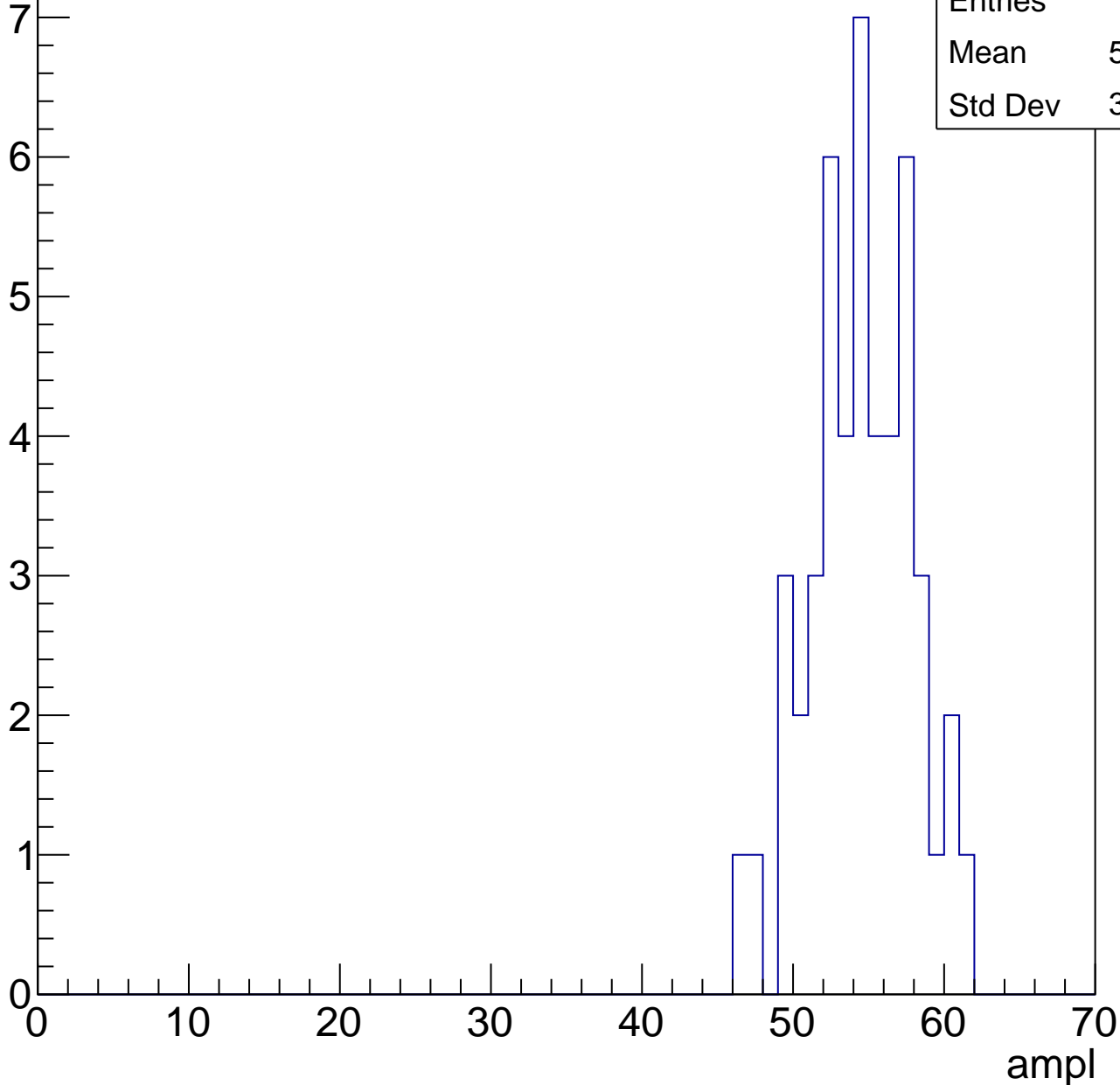


B1L103S, U18-ch98, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

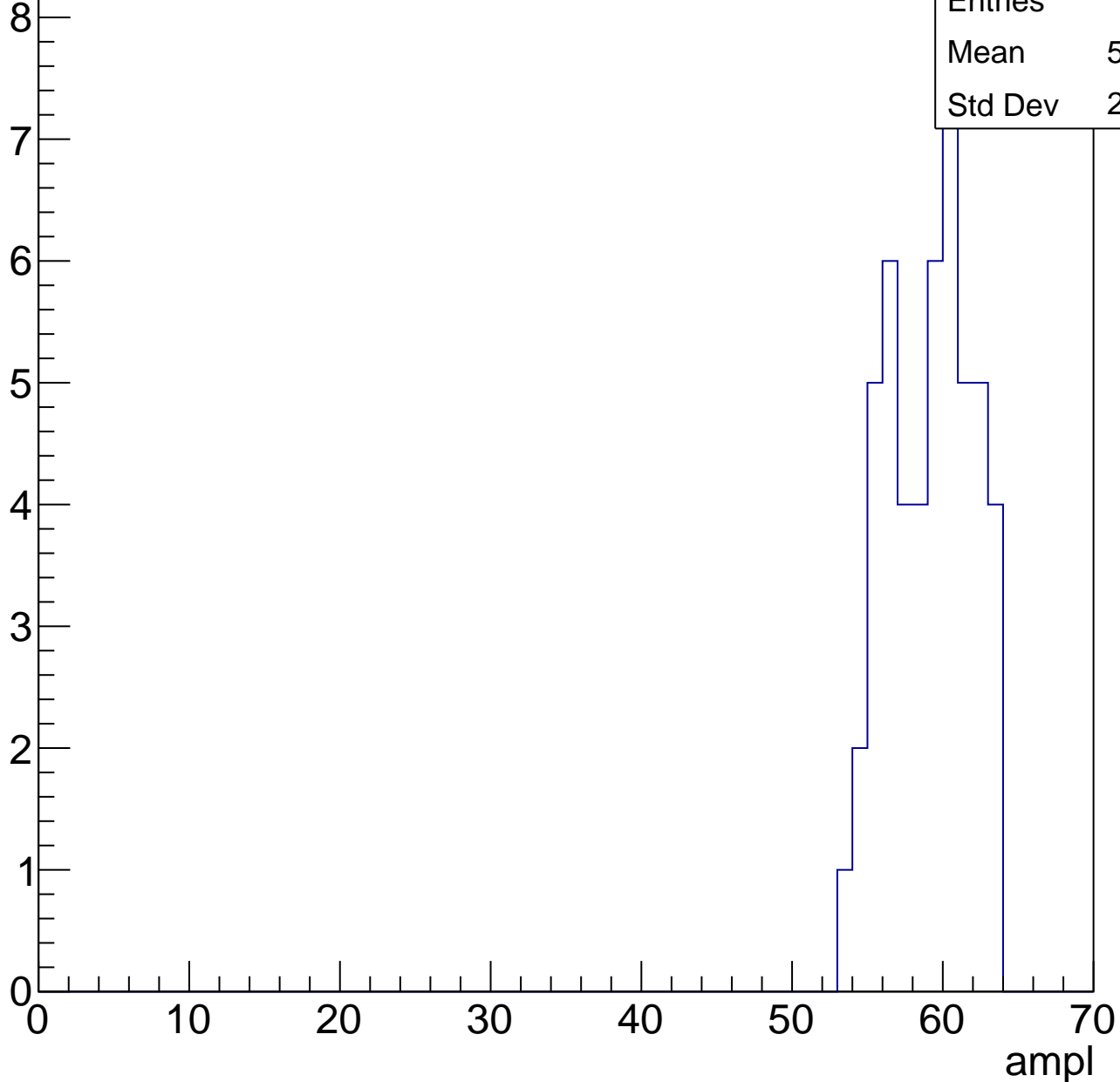
Entries	48
Mean	54.06
Std Dev	3.363



B1L103S, U18-ch98, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

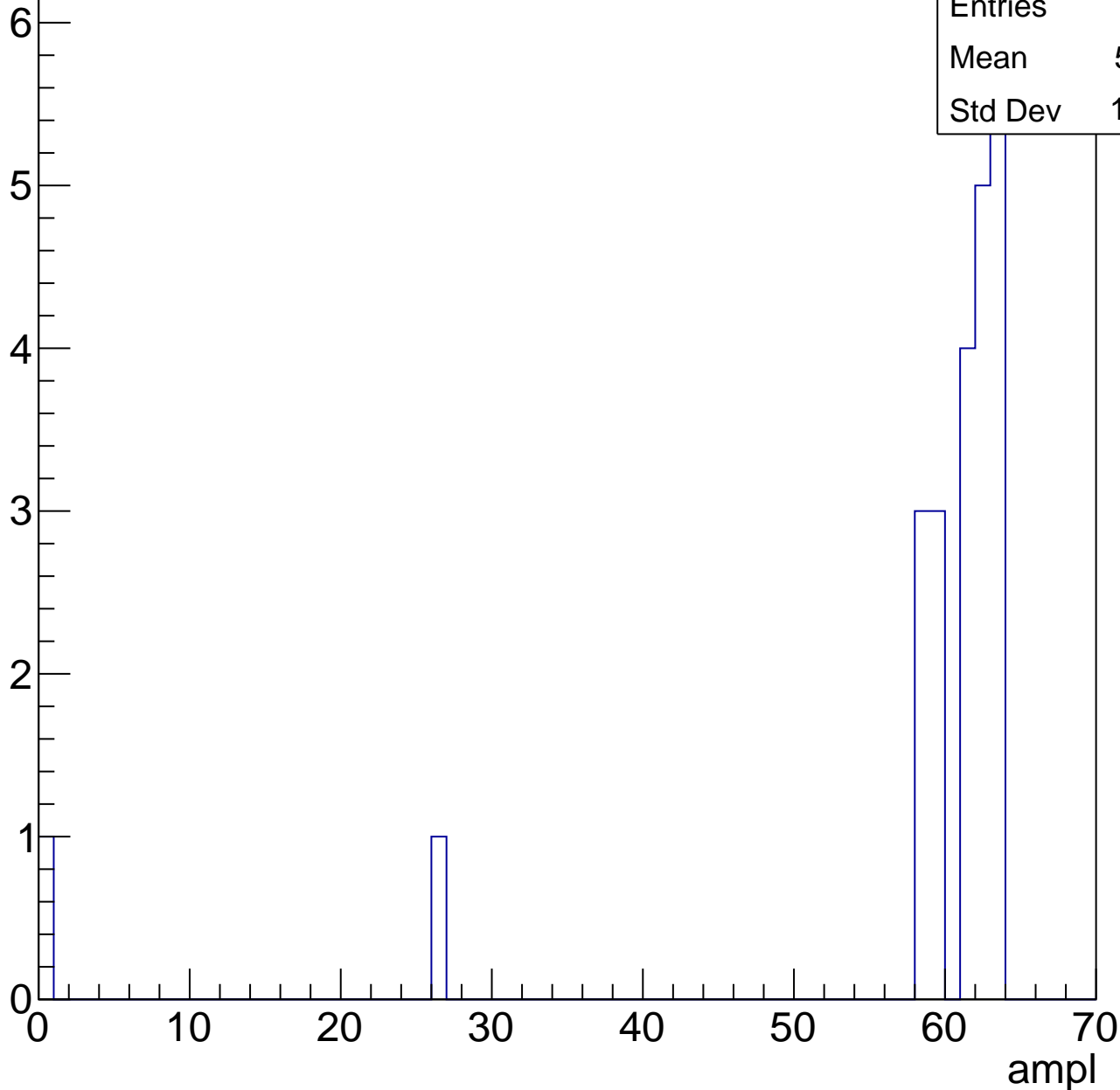


B1L103S, U18-ch98, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	56.91
Std Dev	14.19

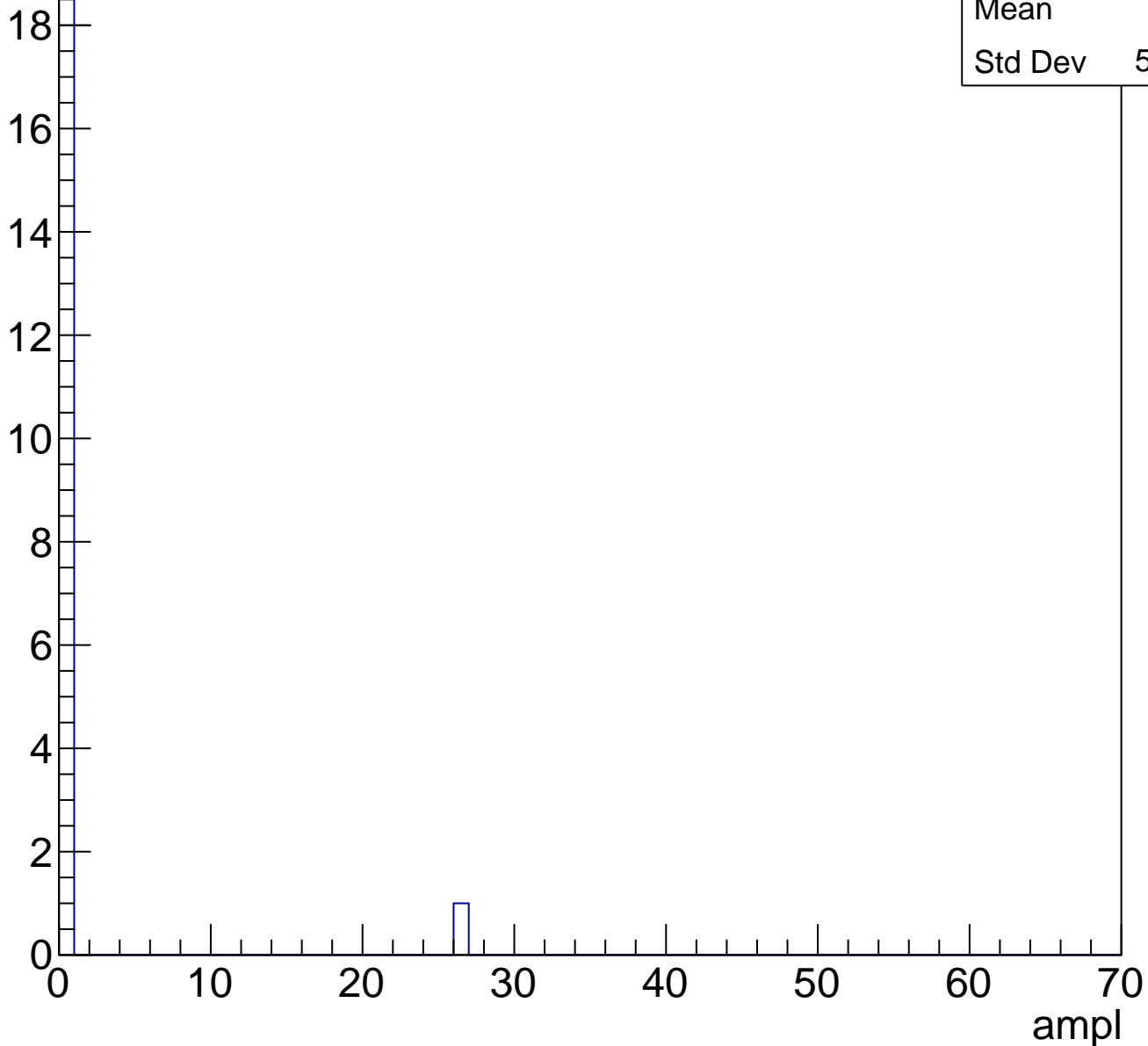


B1L103S, U18-ch98, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	1.3
Std Dev	5.667

Entry



B1L103S, U18-ch99, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	24.22
Std Dev	10.91

Gaus mean : 29.1101

Gaus Width: 4.7388

Entry

12

10

8

6

4

2

0

0

10

20

30

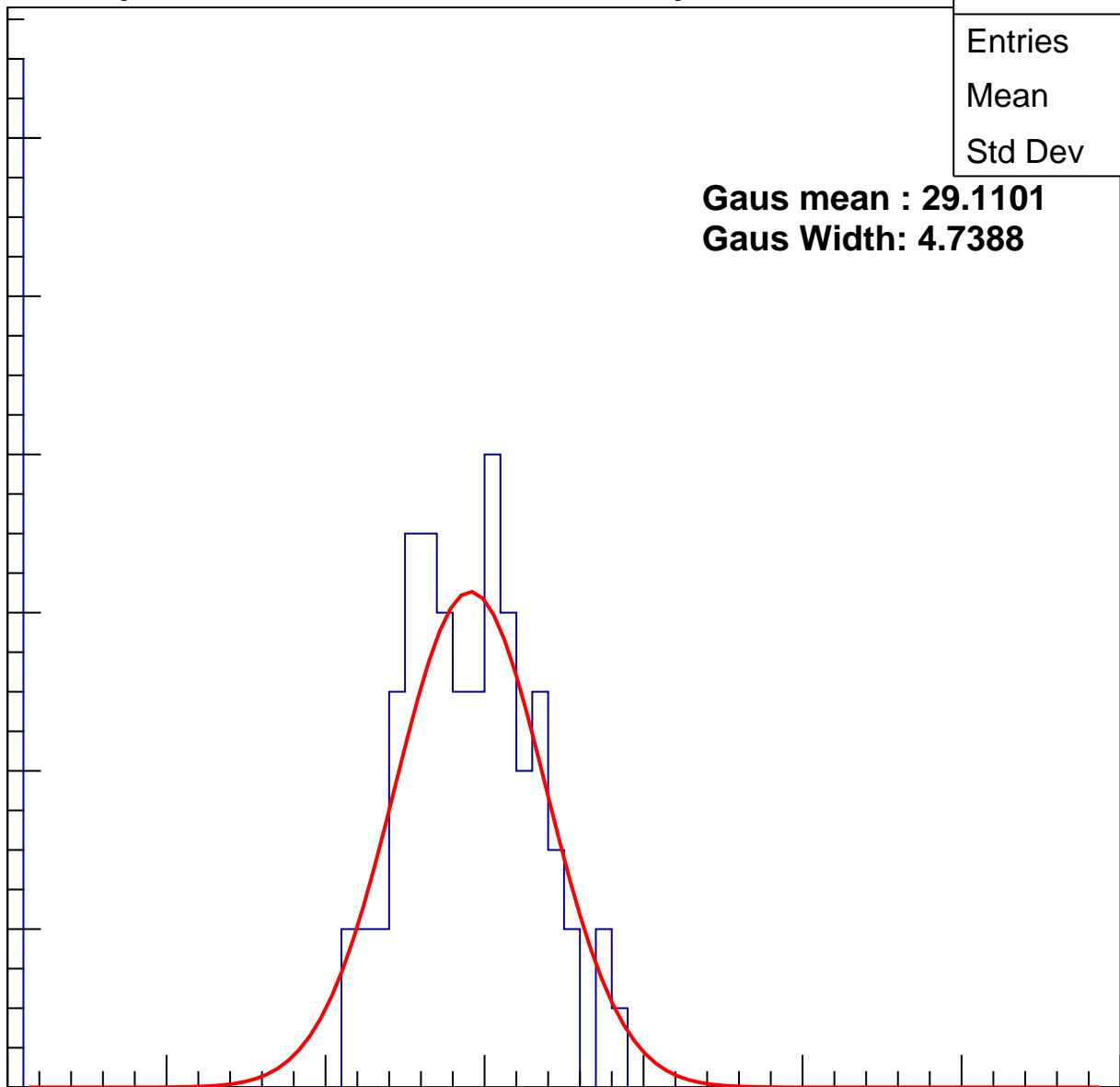
40

50

60

70

ampl



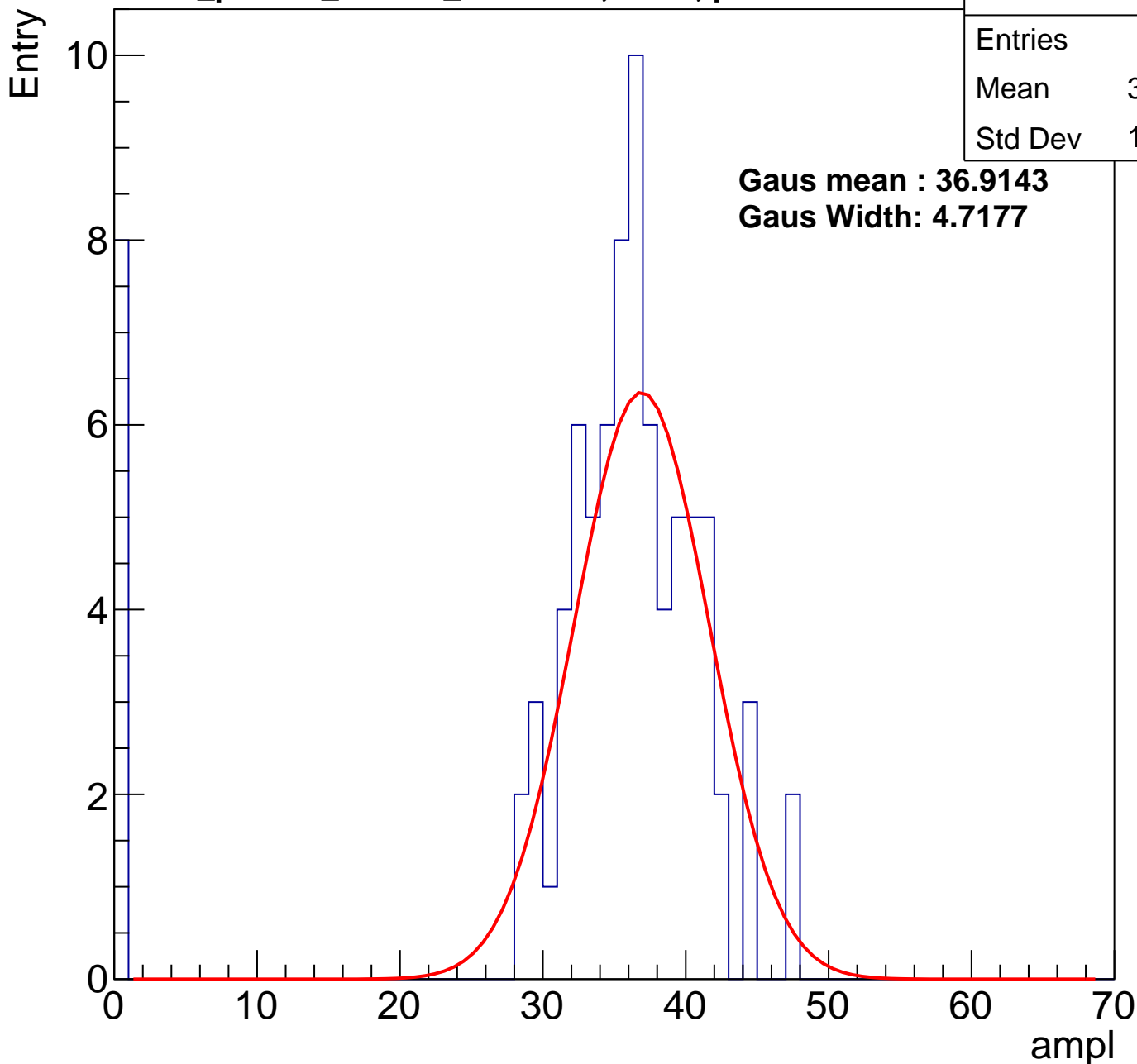
B1L103S, U18-ch99, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	32.73
Std Dev	11.28

Gaus mean : 36.9143

Gaus Width: 4.7177



B1L103S, U18-ch99, adc2

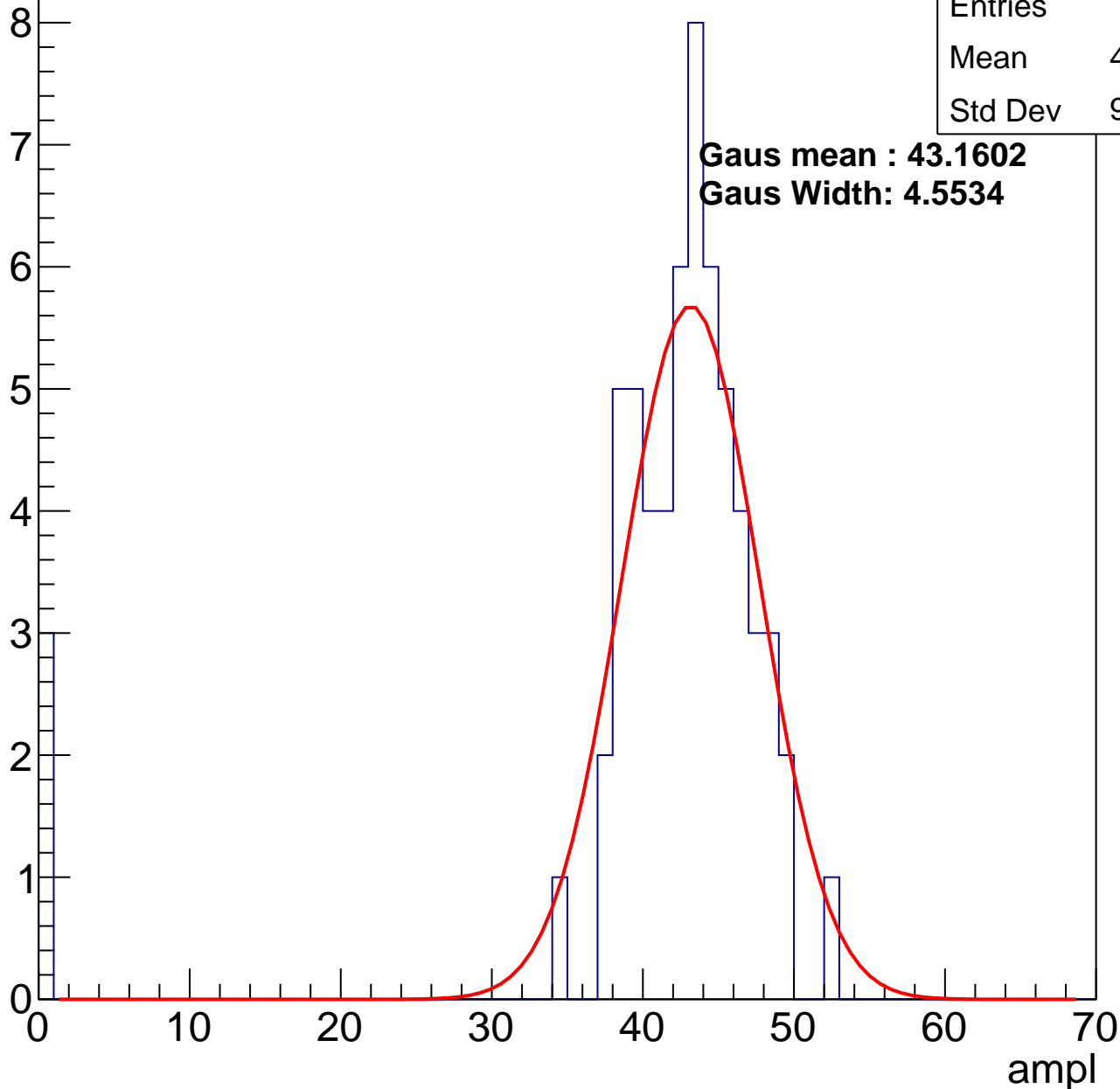
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	62
Mean	40.66
Std Dev	9.806

Gaus mean : 43.1602

Gaus Width: 4.5534

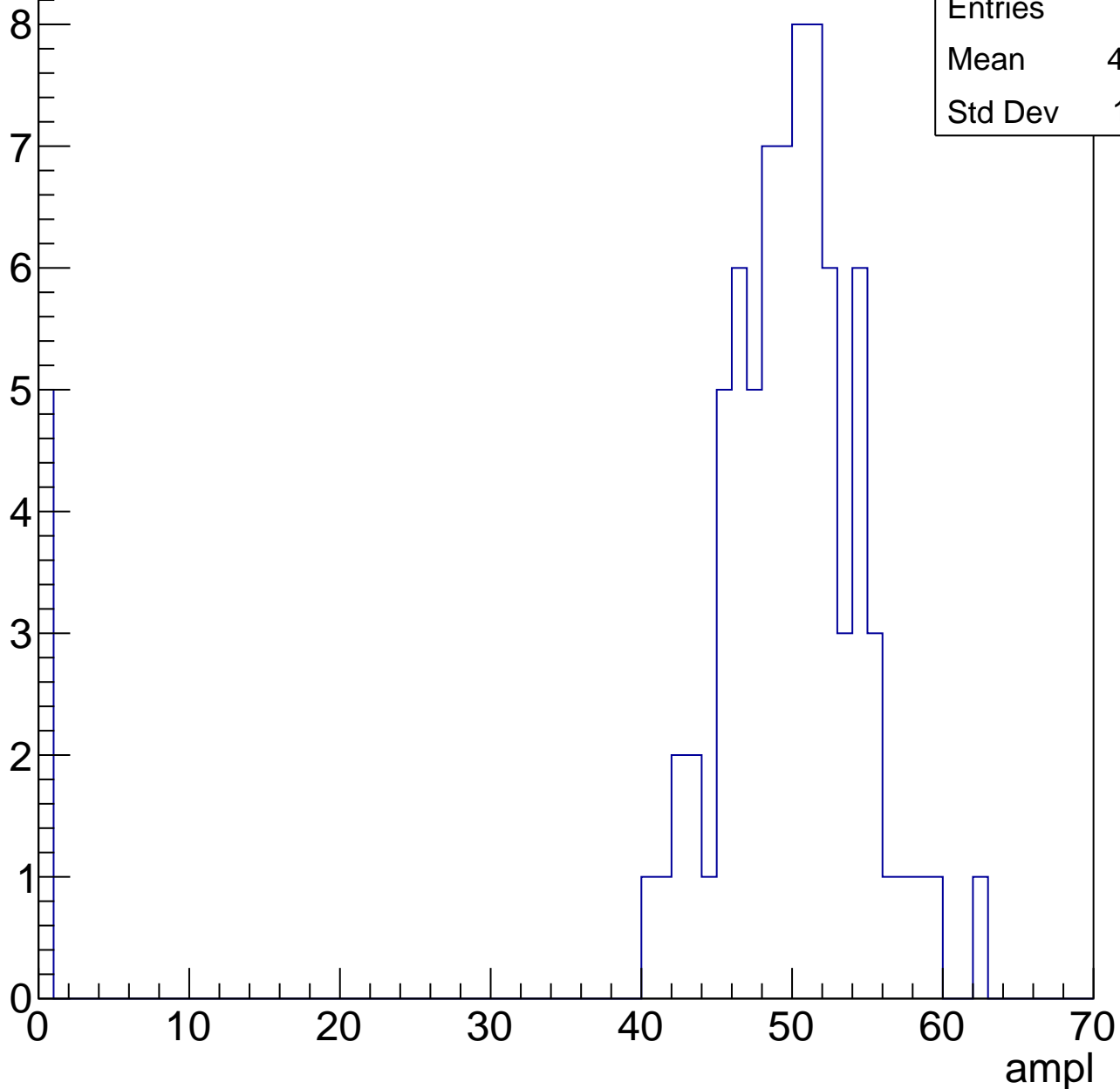


B1L103S, U18-ch99, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	46.54
Std Dev	12.61

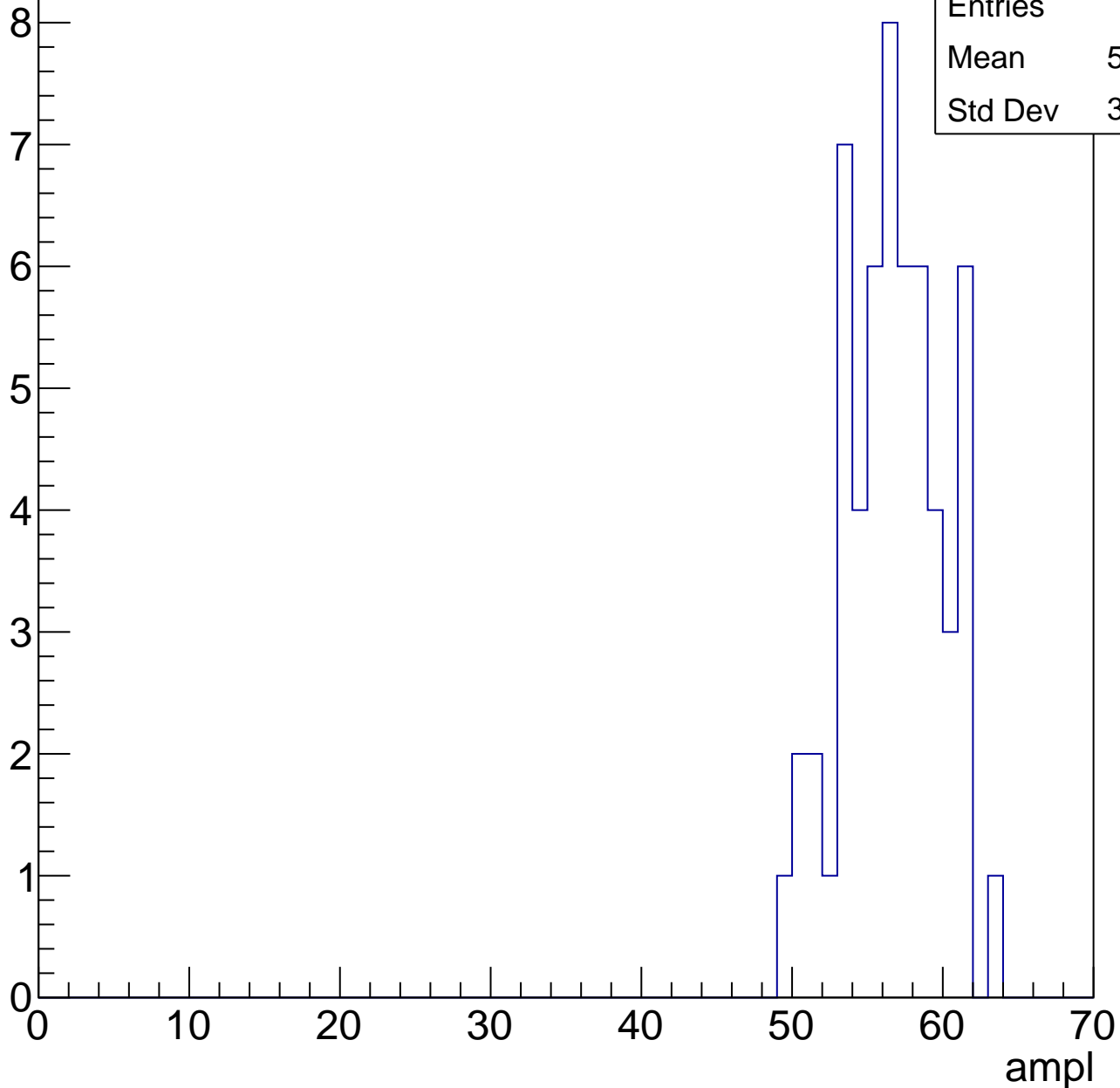


B1L103S, U18-ch99, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

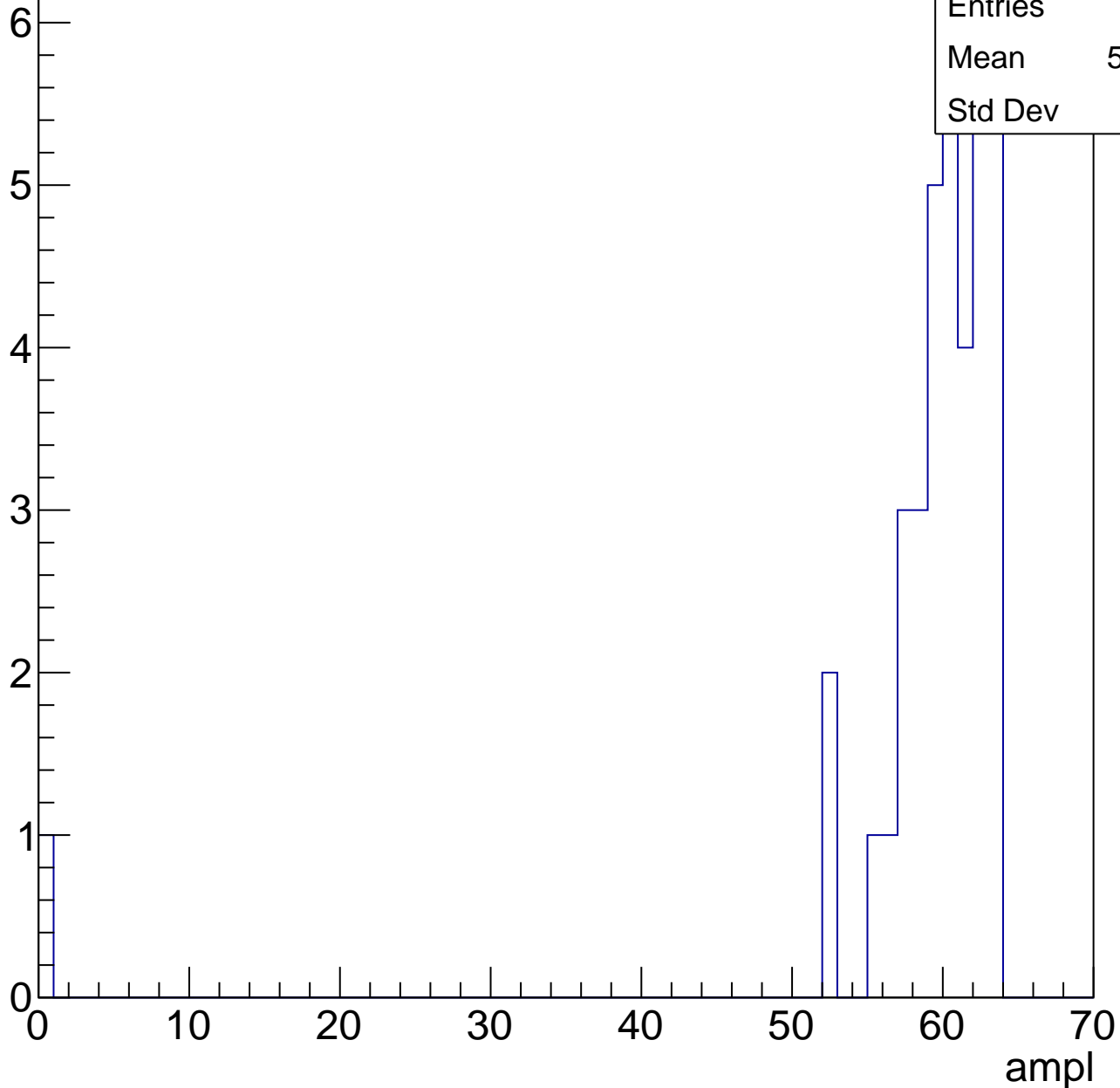
Entries	57
Mean	56.19
Std Dev	3.198



B1L103S, U18-ch99, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch99, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

6

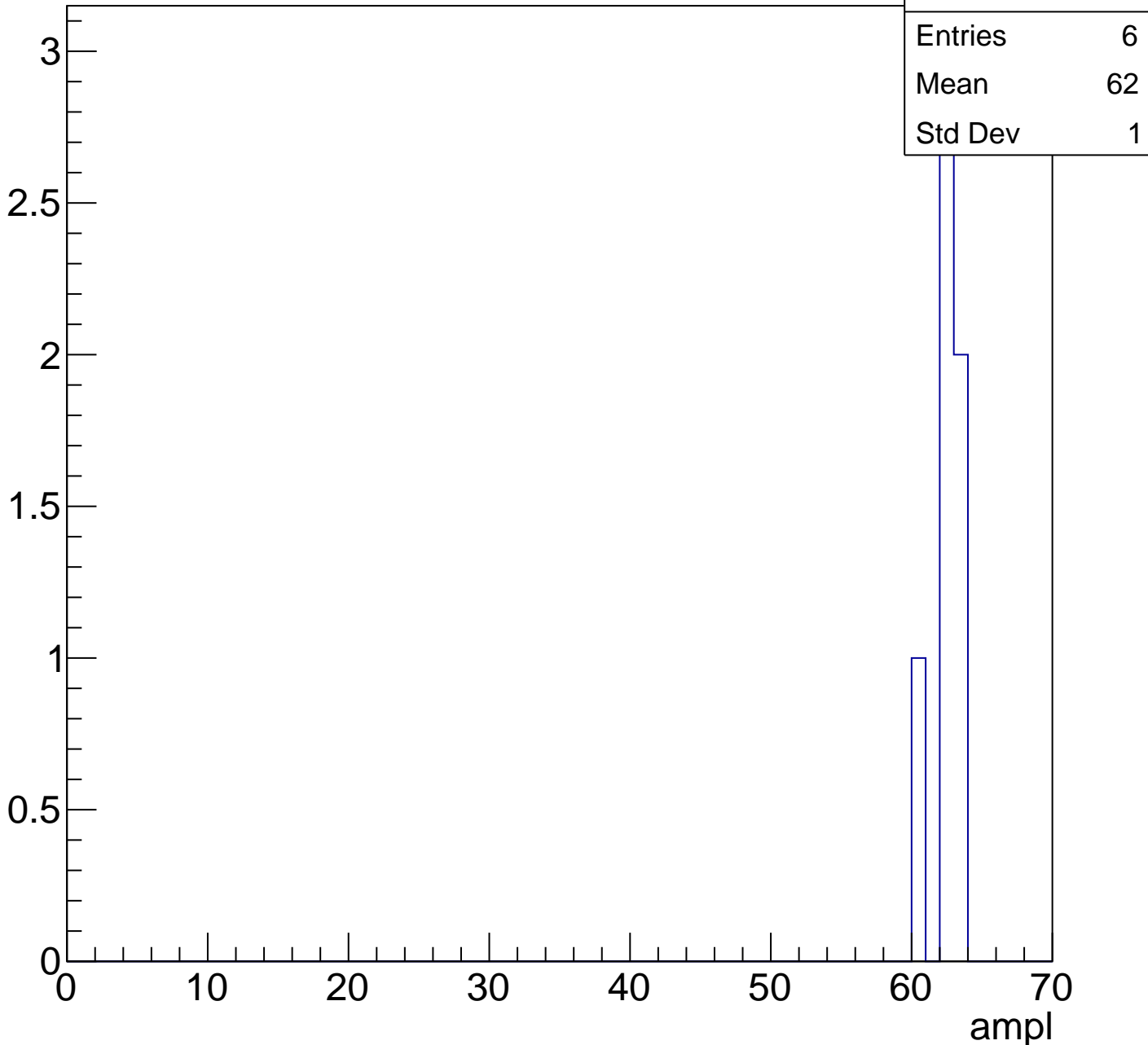
Mean

62

Std Dev

1

ampl

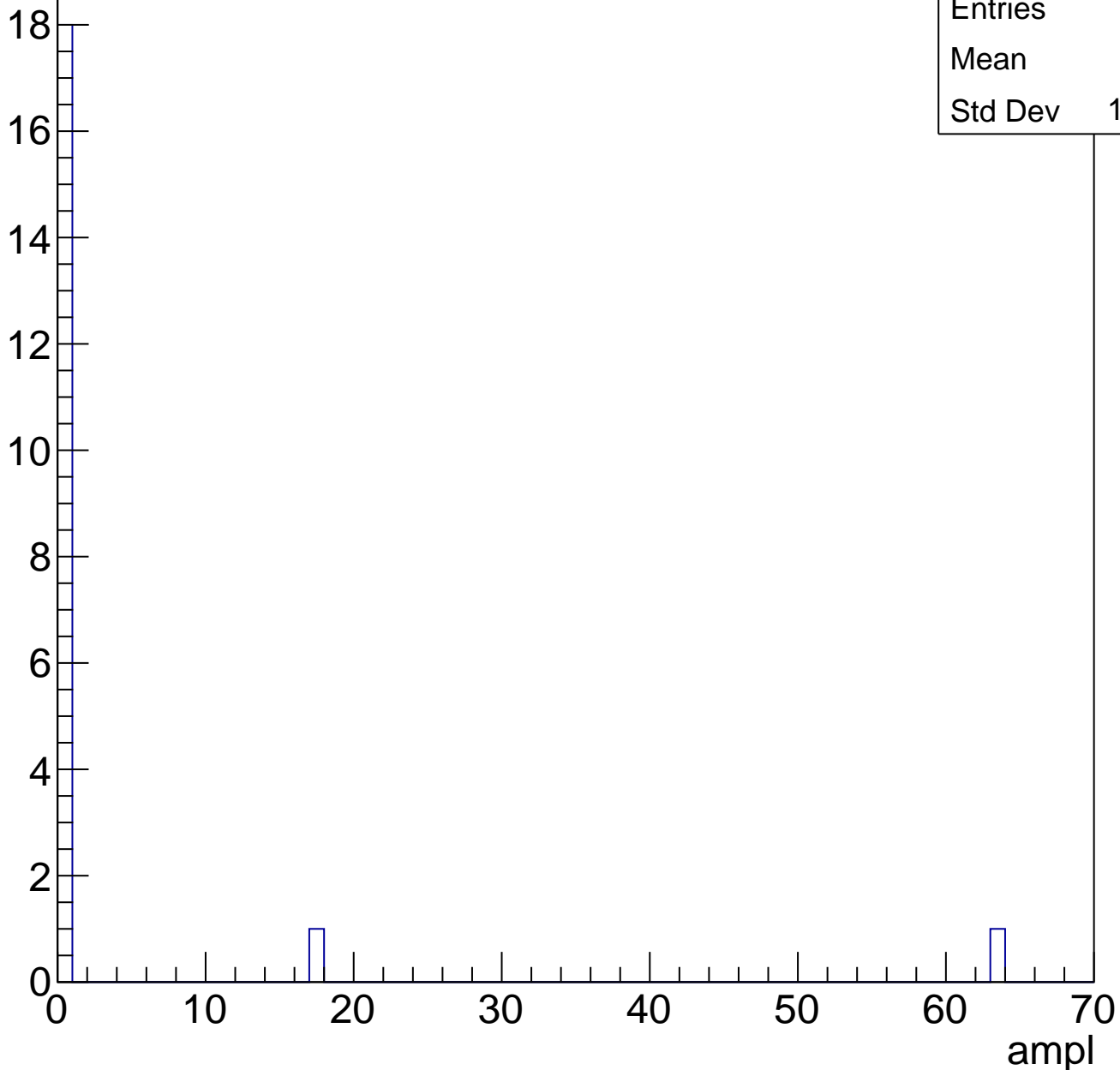


B1L103S, U18-ch99, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4
Std Dev	14.03

Entry



B1L103S, U18-ch100, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	99
Mean	18.8
Std Dev	11.71

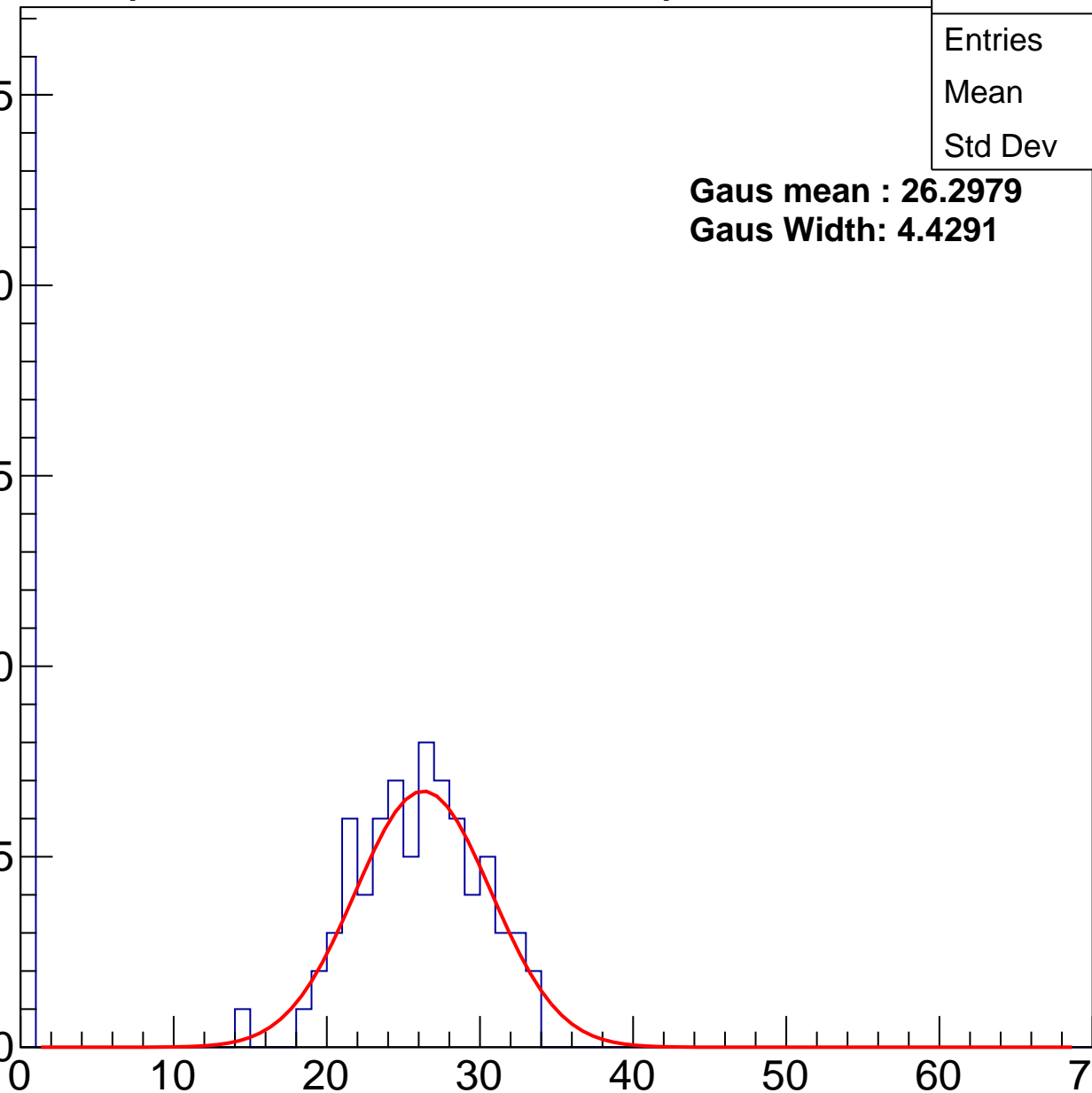
Gaus mean : 26.2979

Gaus Width: 4.4291

Entry

25
20
15
10
5
0

ampl



B1L103S, U18-ch100, adc1

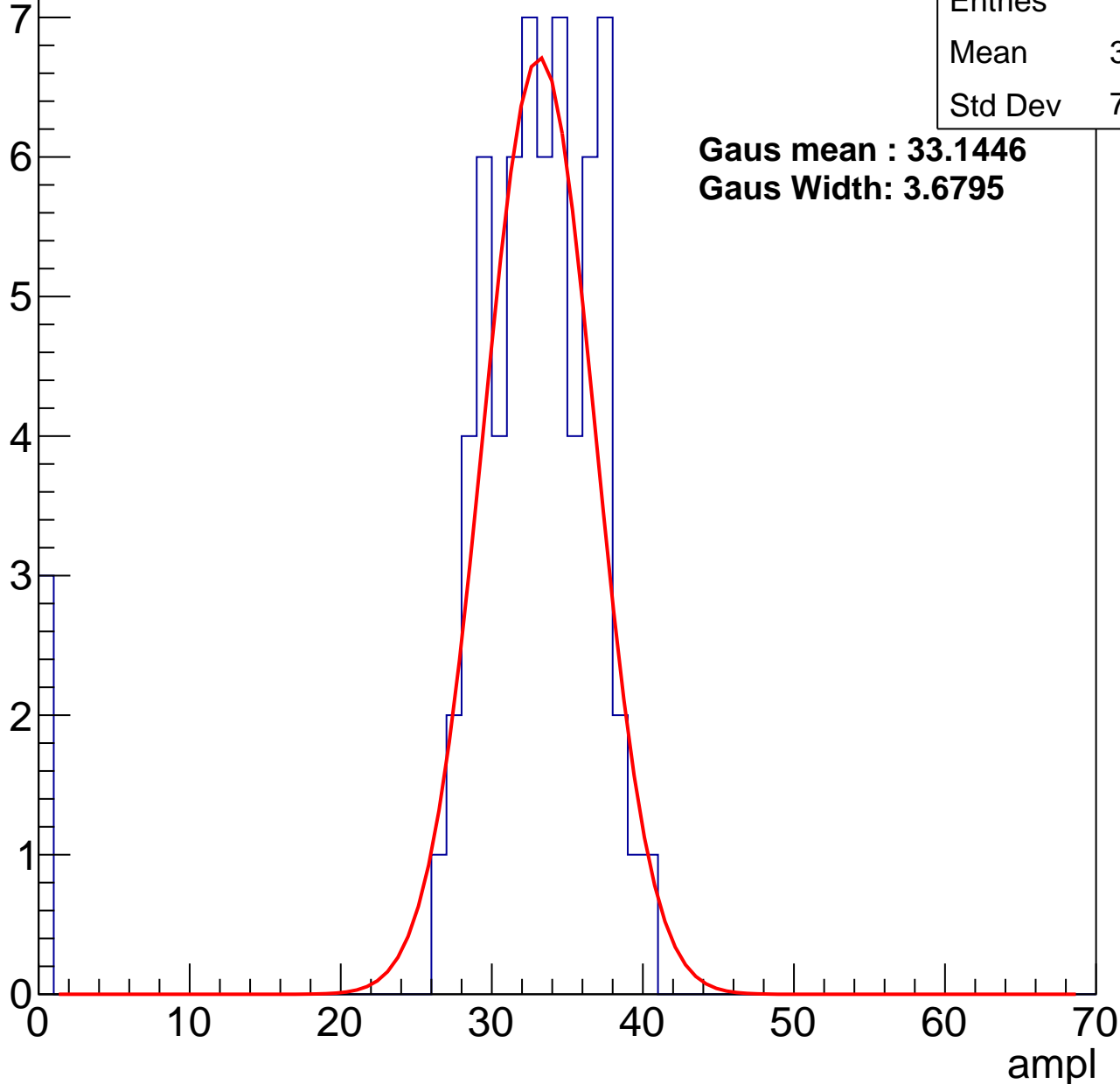
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	31.37
Std Dev	7.529

Gaus mean : 33.1446

Gaus Width: 3.6795



B1L103S, U18-ch100, adc2

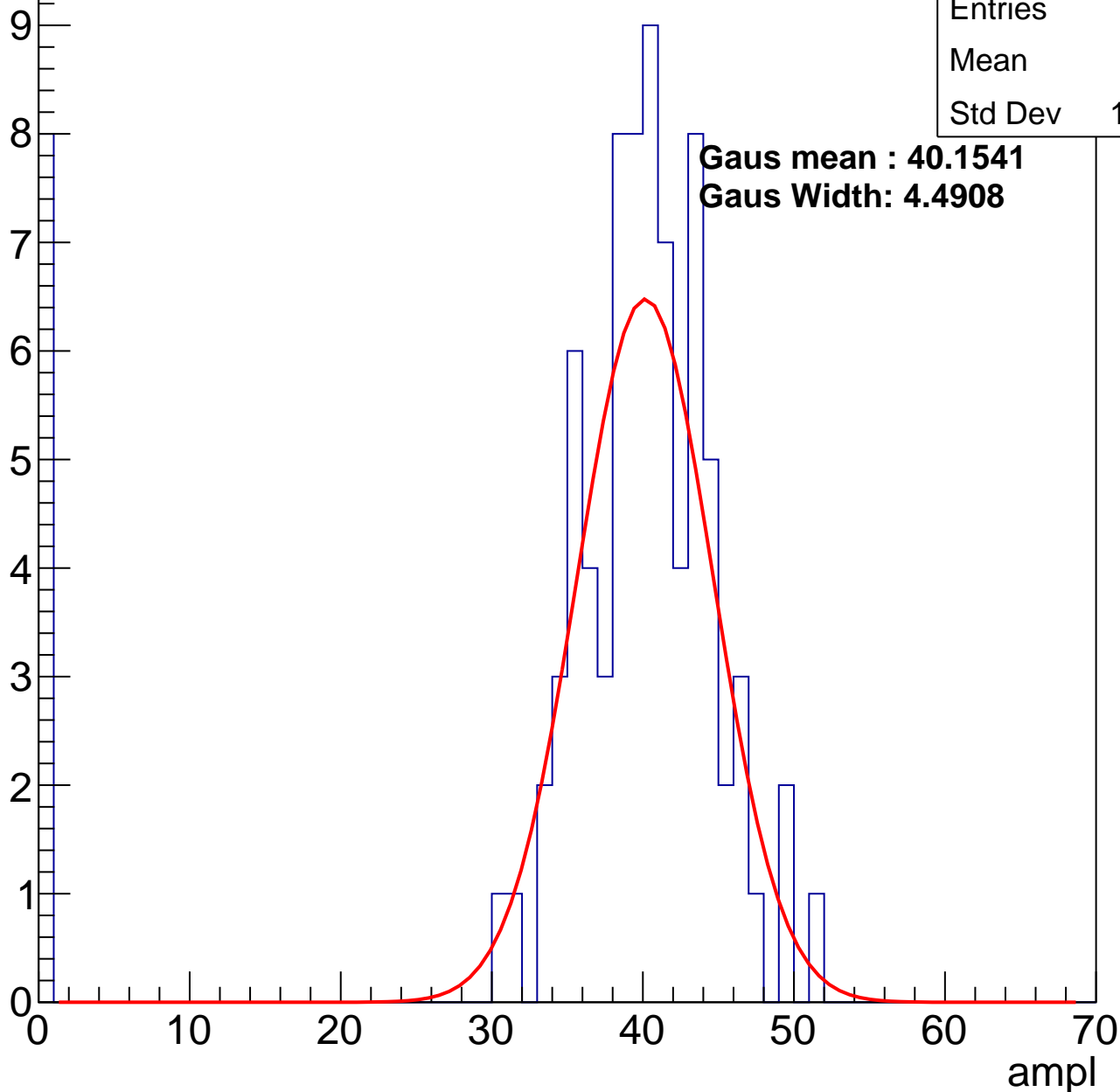
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	36.2
Std Dev	12.24

Gaus mean : 40.1541

Gaus Width: 4.4908

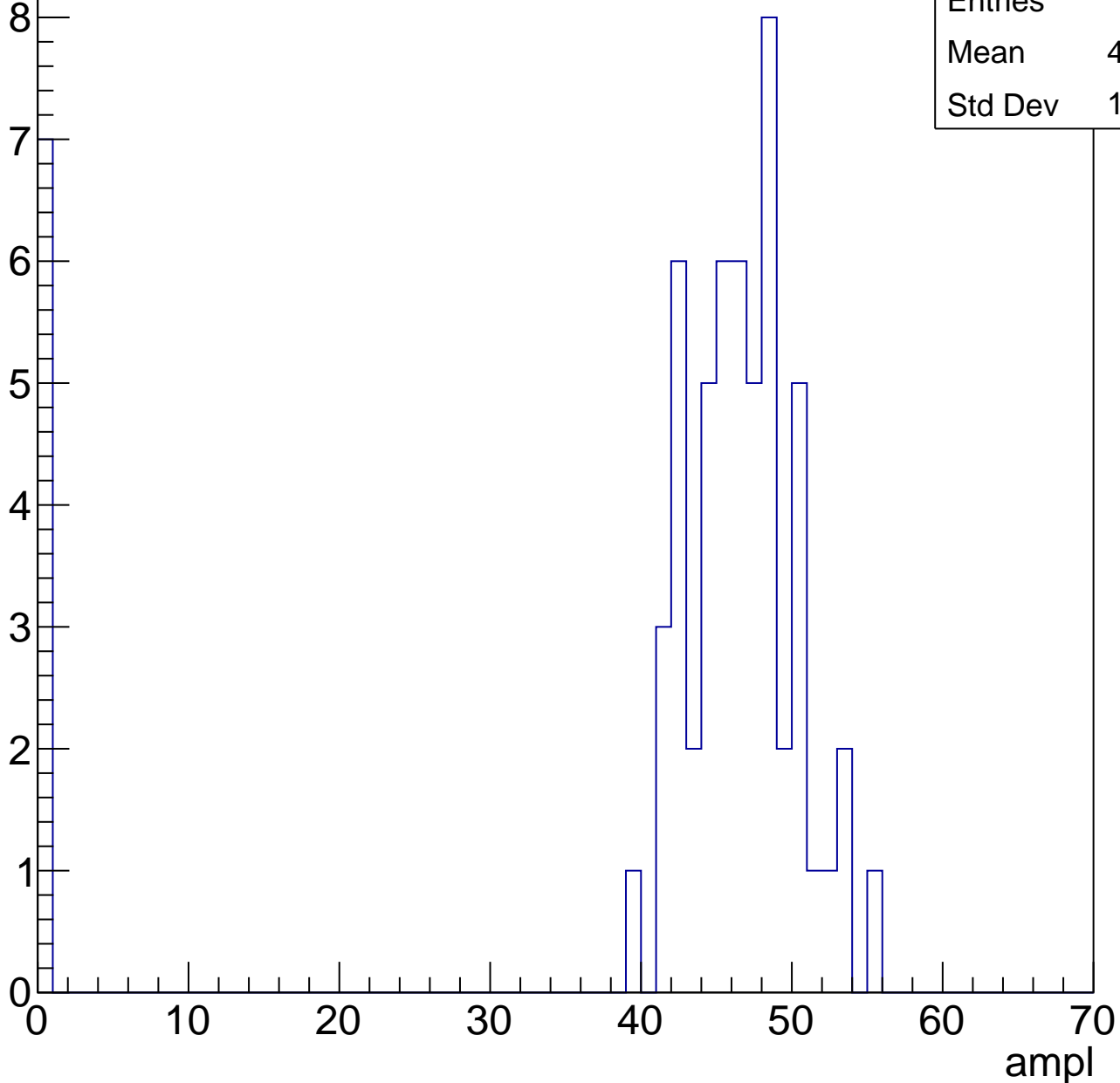


B1L103S, U18-ch100, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	40.93
Std Dev	15.09

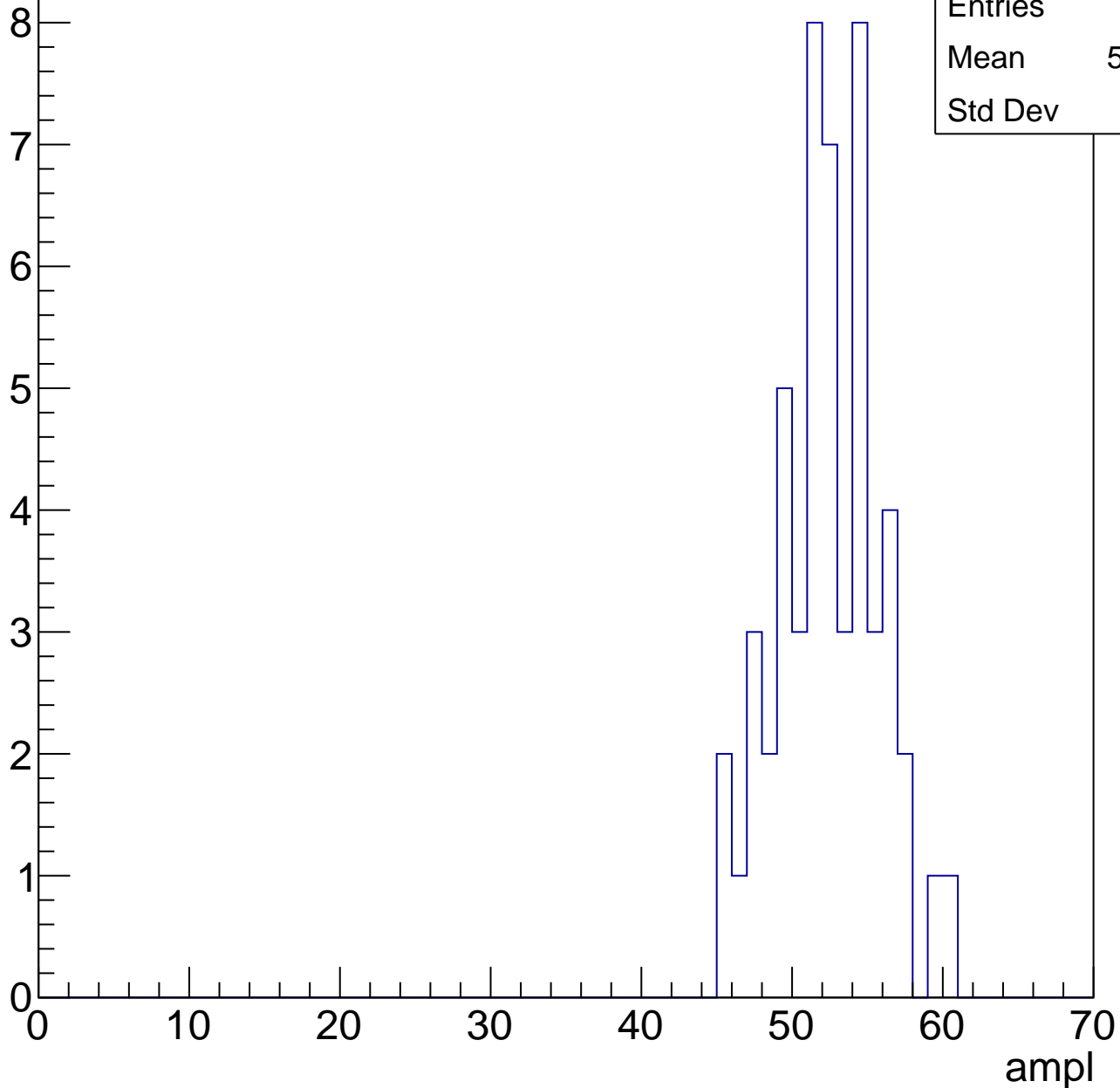


B1L103S, U18-ch100, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	51.94
Std Dev	3.35



B1L103S, U18-ch100, adc5

calib_packv5_041523_1651.root, FC#0, port C2

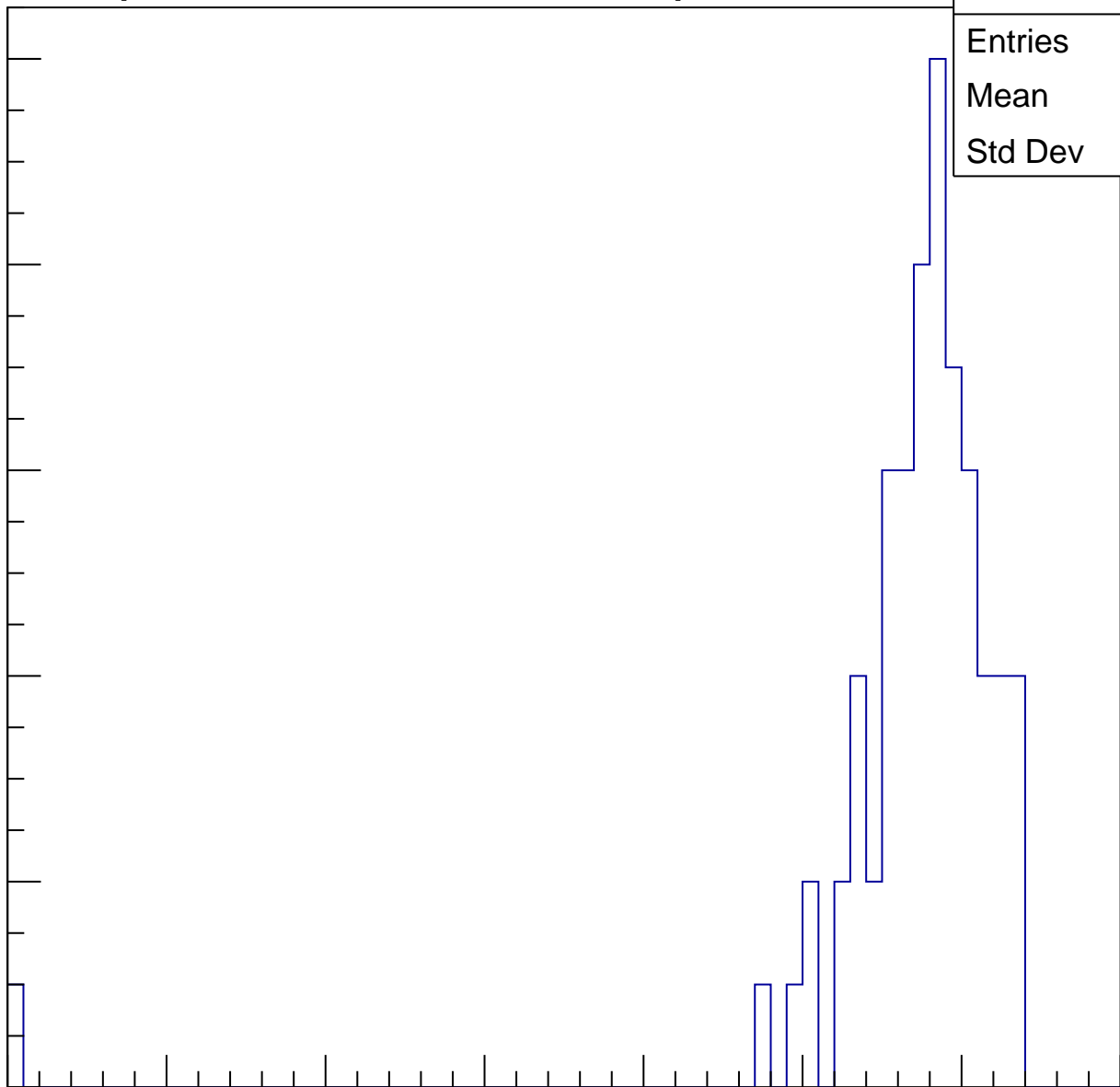
Entries	68
Mean	56.46
Std Dev	7.72

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

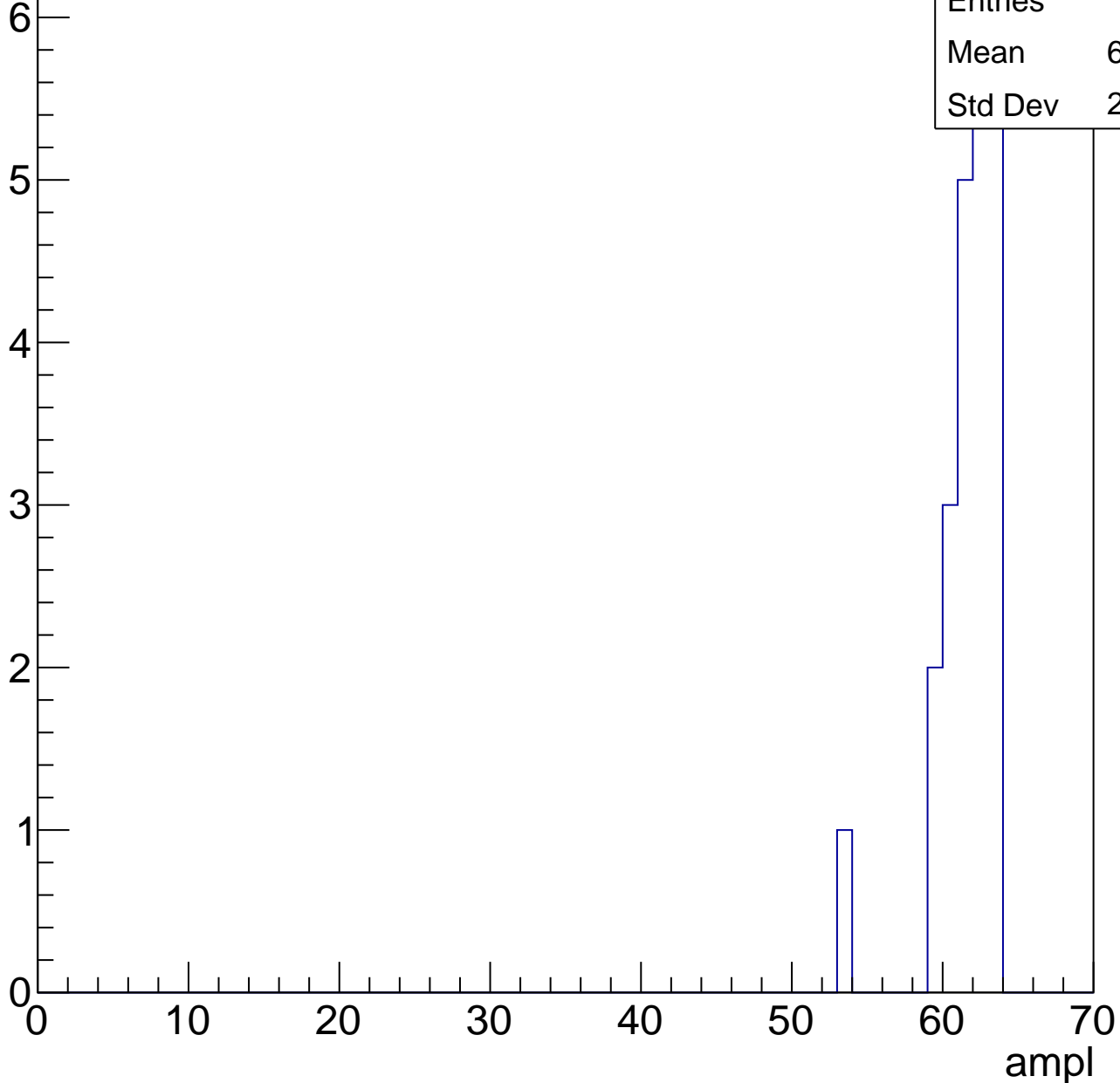


B1L103S, U18-ch100, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	23
Mean	61.13
Std Dev	2.133



B1L103S, U18-ch100, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



B1L103S, U18-ch101, adc0

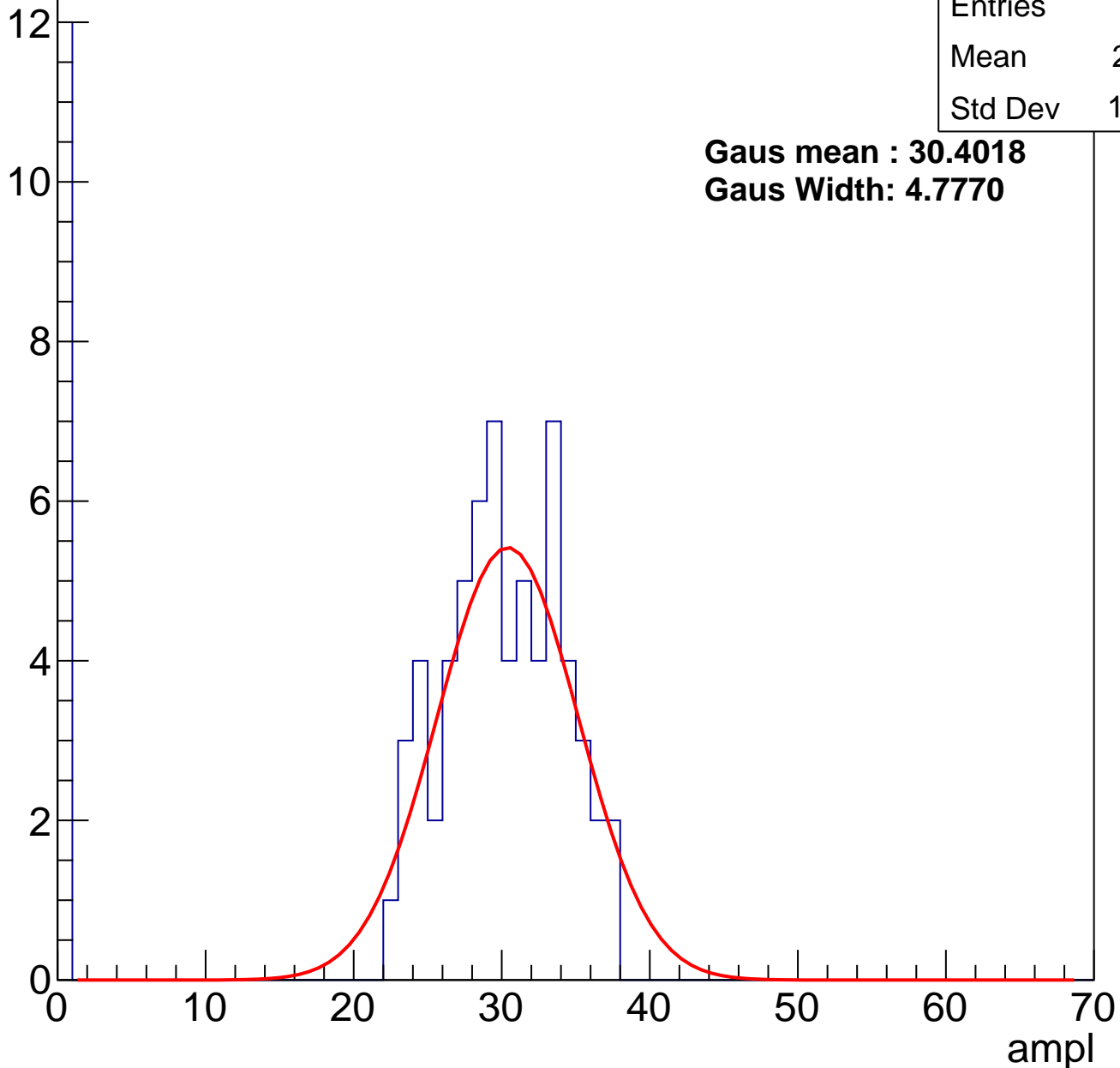
calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	24.91
Std Dev	11.43

Gaus mean : 30.4018

Gaus Width: 4.7770

Entry



B1L103S, U18-ch101, adc1

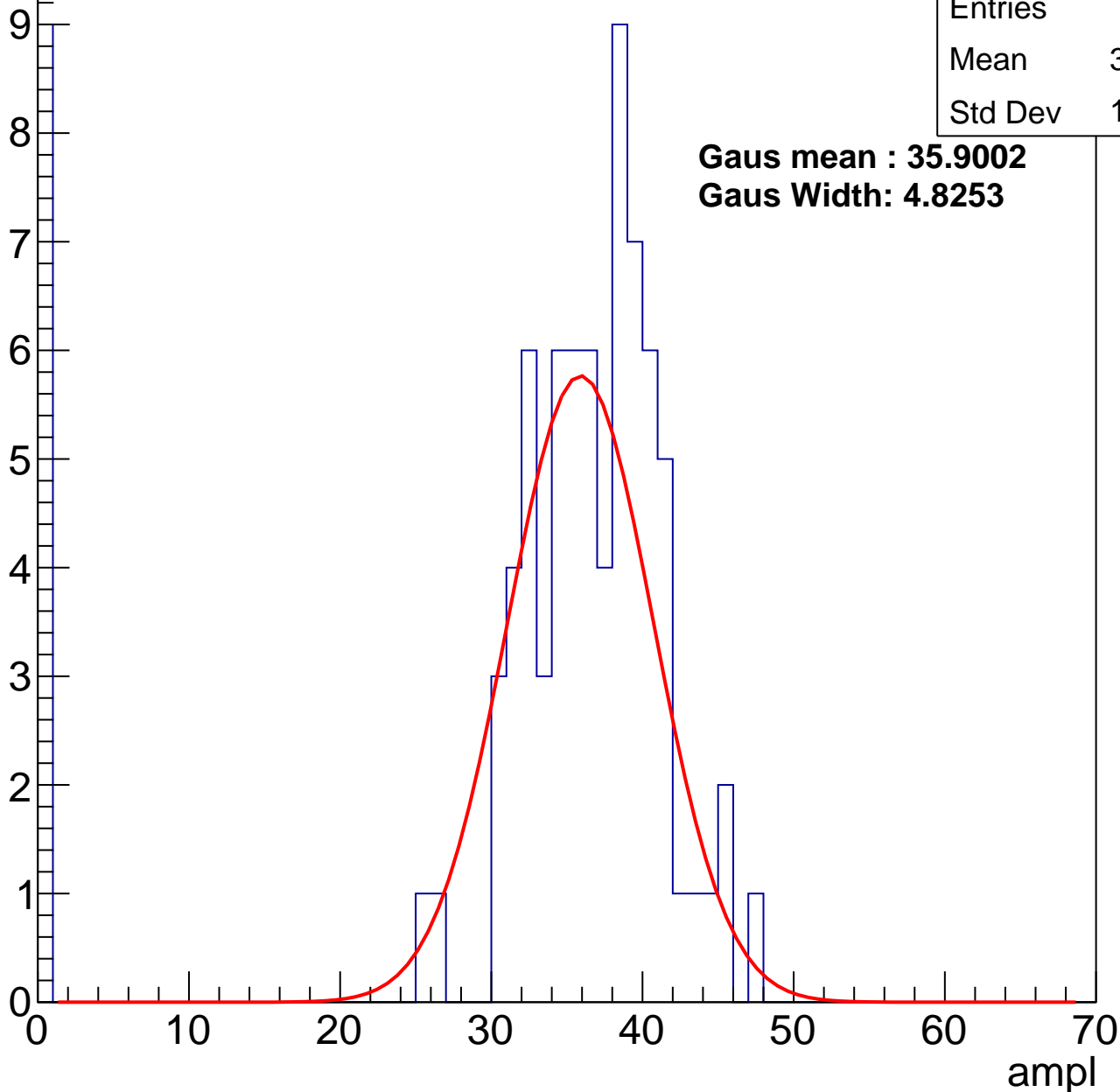
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	32.44
Std Dev	12.08

Gaus mean : 35.9002

Gaus Width: 4.8253



B1L103S, U18-ch101, adc2

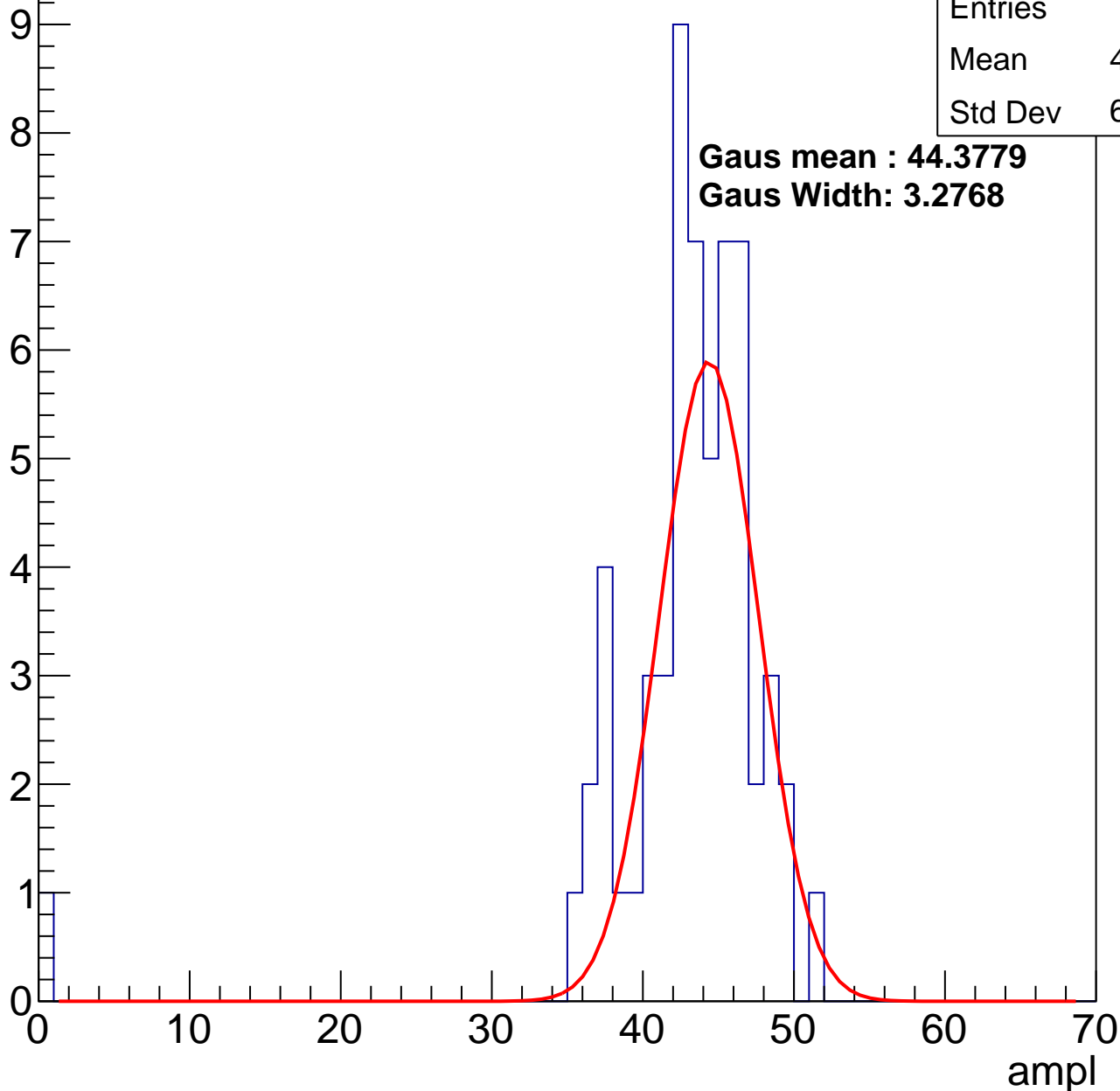
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	42.34
Std Dev	6.584

Gaus mean : 44.3779

Gaus Width: 3.2768

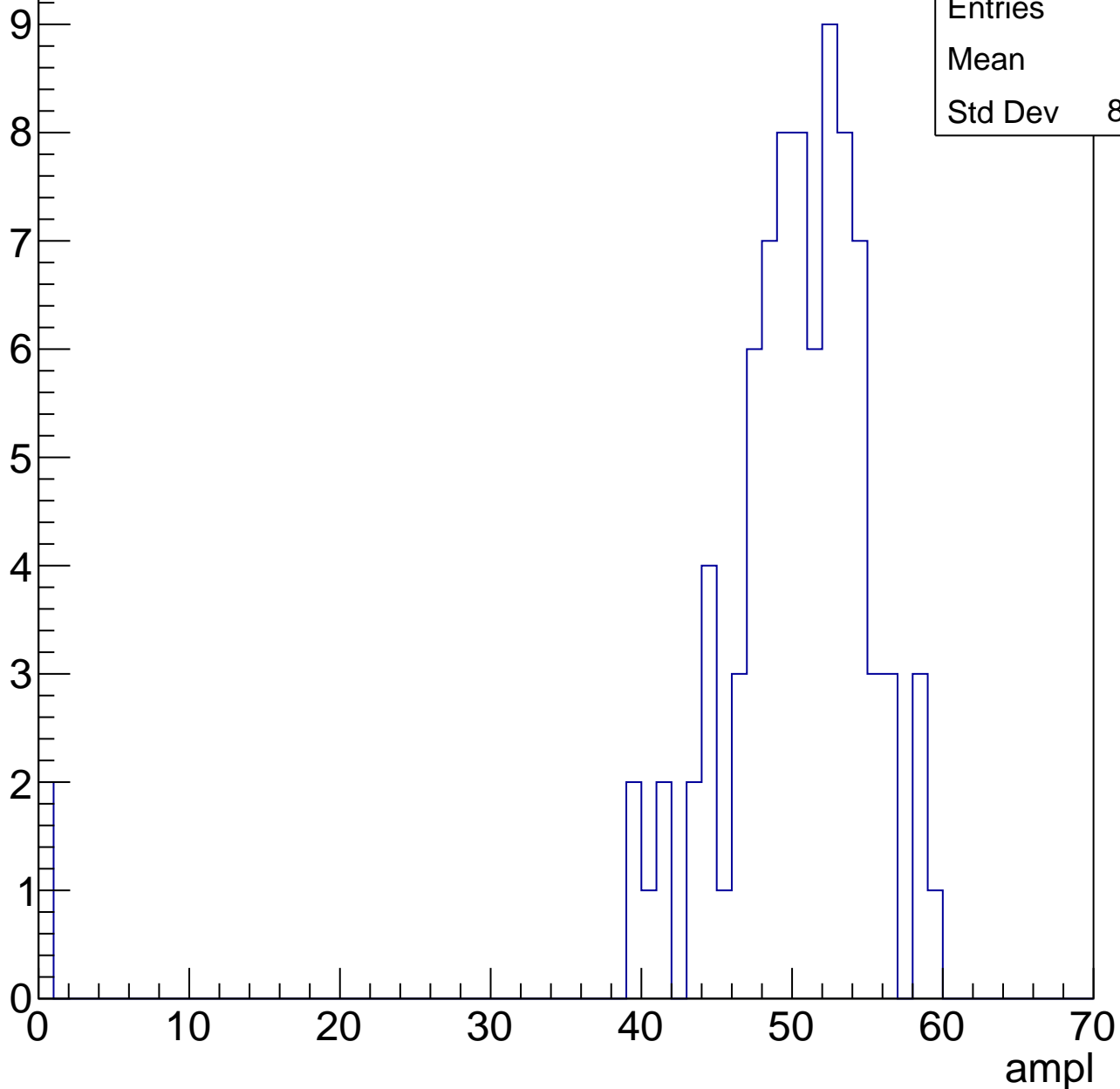


B1L103S, U18-ch101, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	86
Mean	48.8
Std Dev	8.672

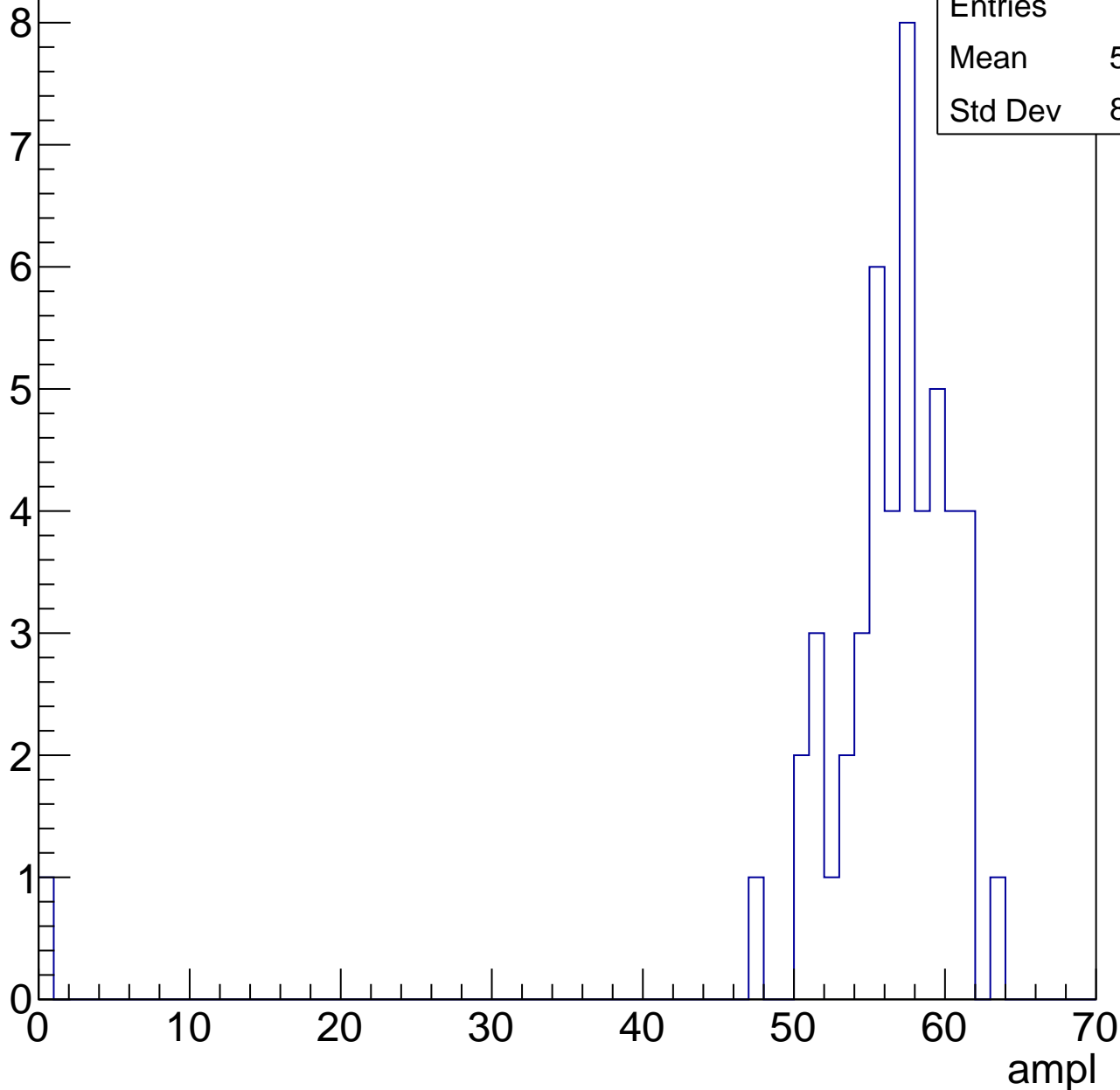


B1L103S, U18-ch101, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	55.18
Std Dev	8.649

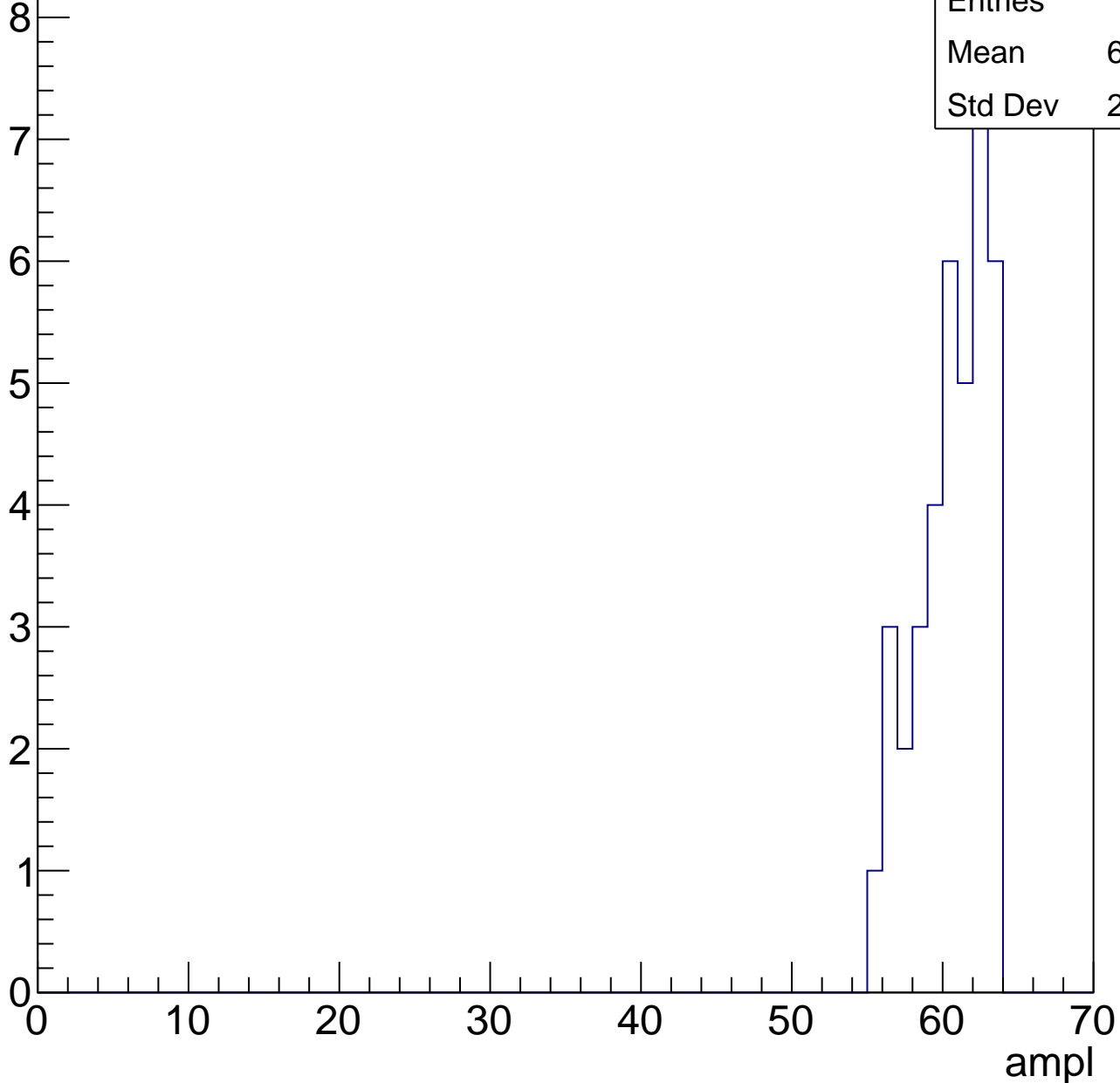


B1L103S, U18-ch101, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	38
Mean	60.16
Std Dev	2.277



B1L103S, U18-ch101, adc6

calib_packv5_041523_1651.root, FC#0, port C2

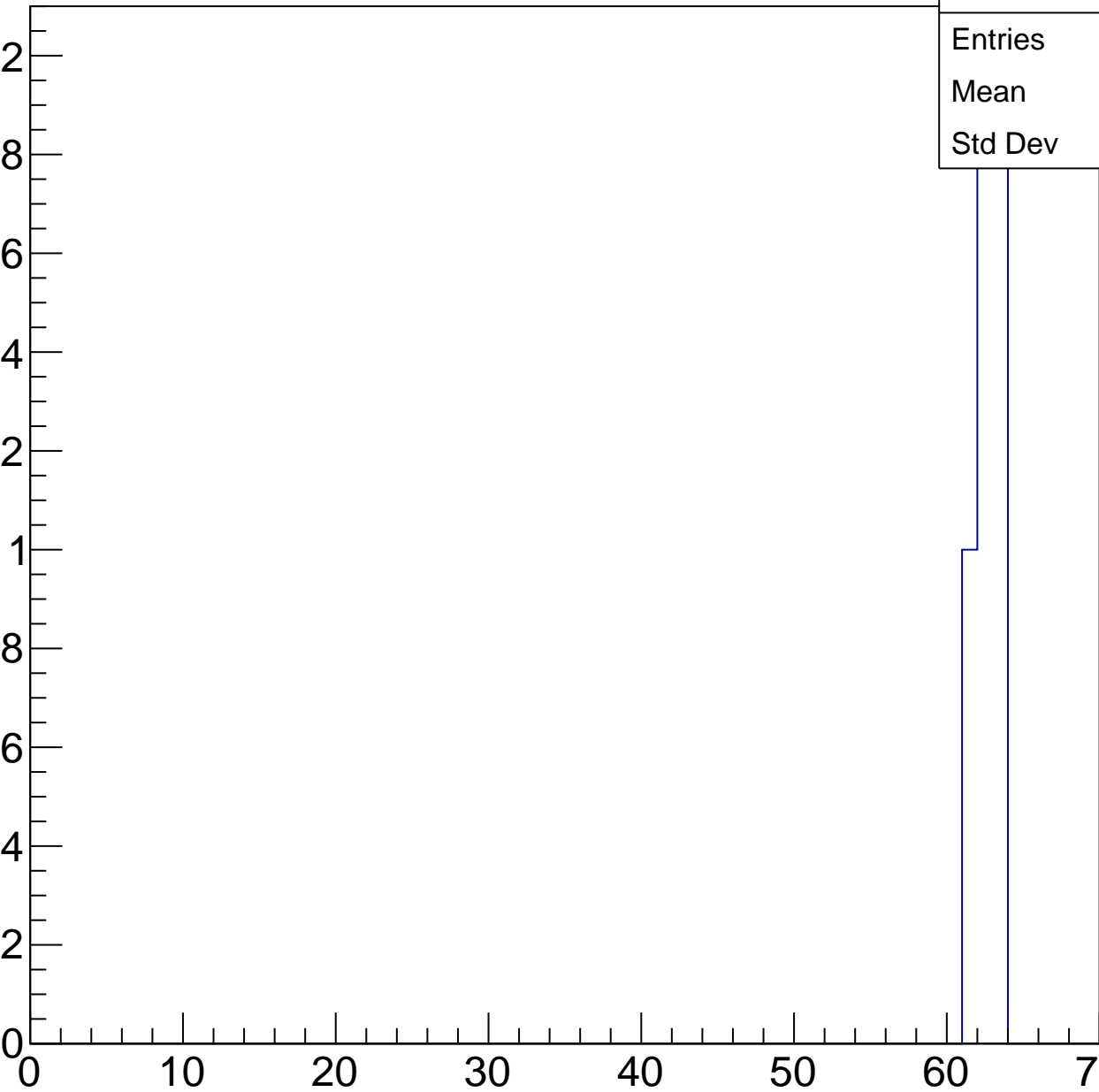
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl

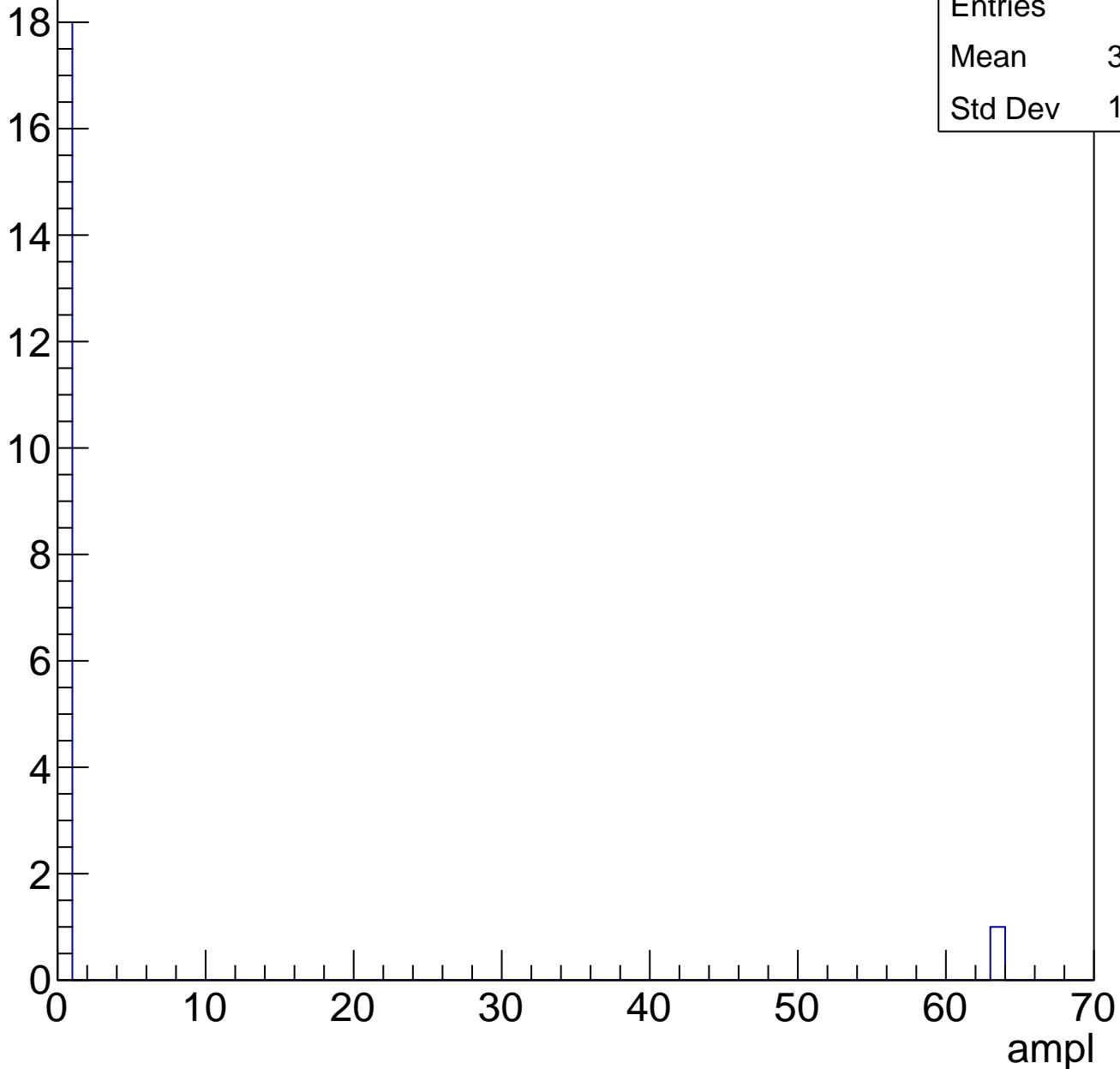


B1L103S, U18-ch101, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U18-ch102, adc0

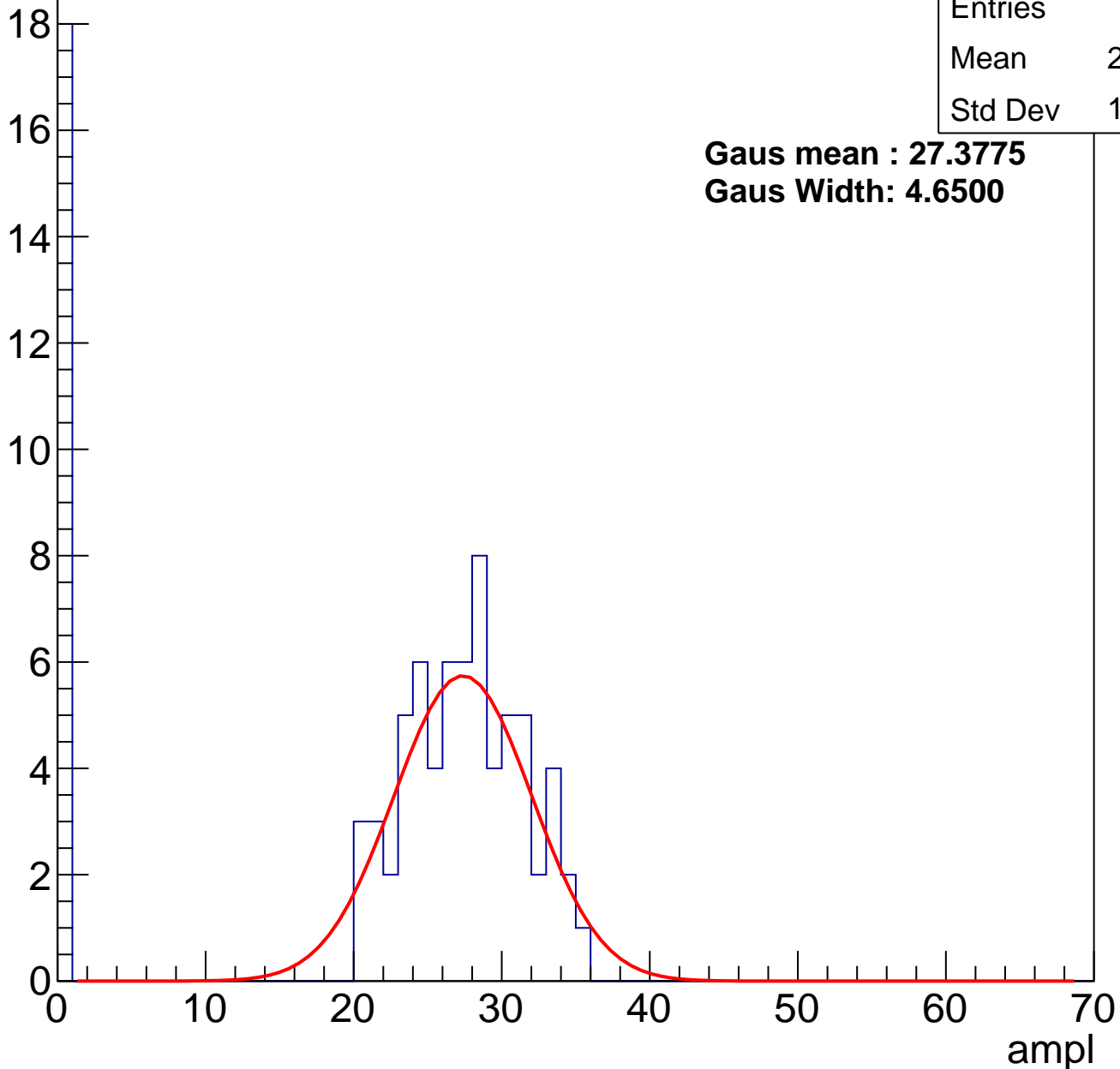
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	21.29
Std Dev	11.62

Gaus mean : 27.3775

Gaus Width: 4.6500

Entry



B1L103S, U18-ch102, adc1

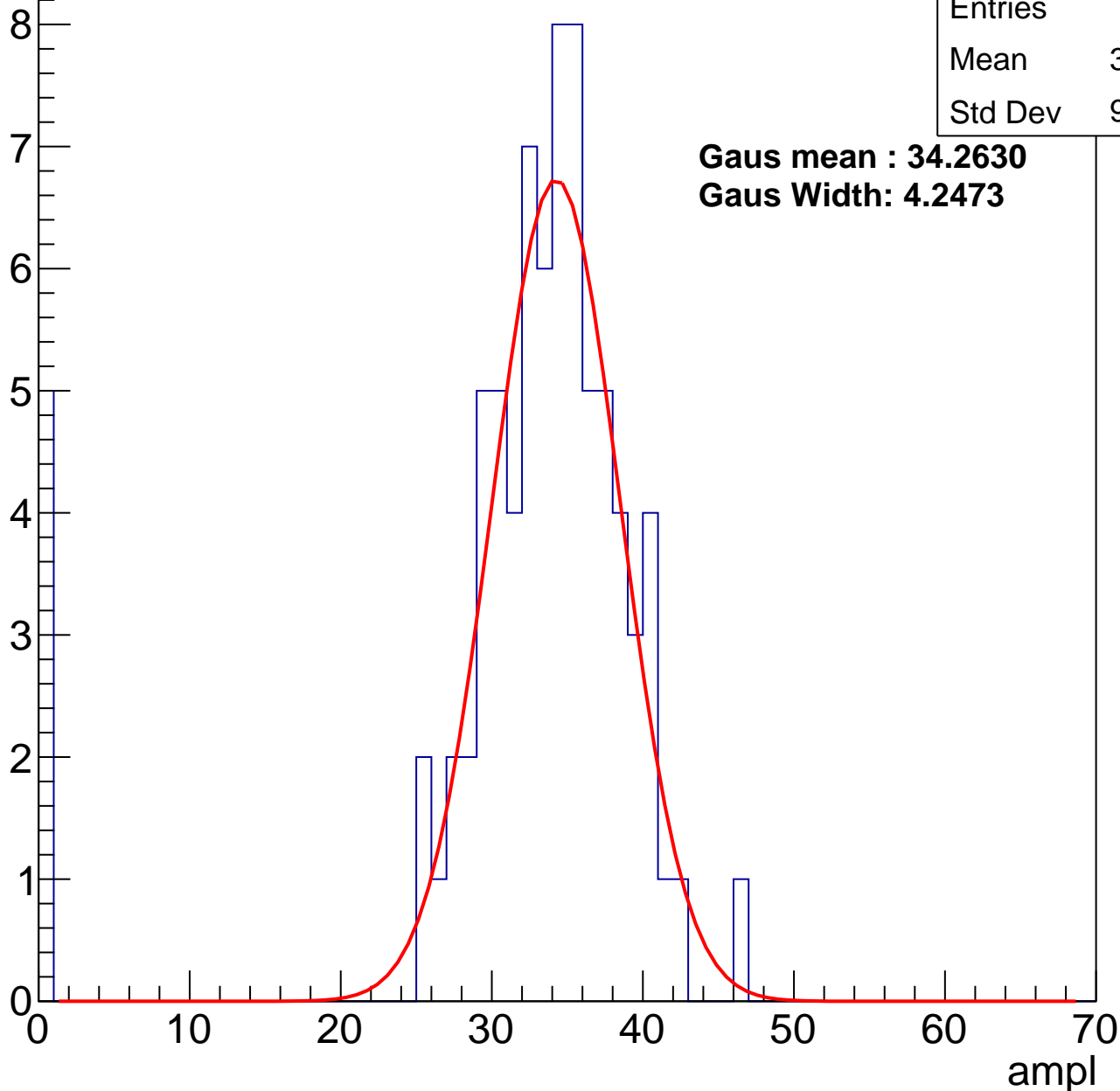
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	79
Mean	31.67
Std Dev	9.165

Gaus mean : 34.2630

Gaus Width: 4.2473



B1L103S, U18-ch102, adc2

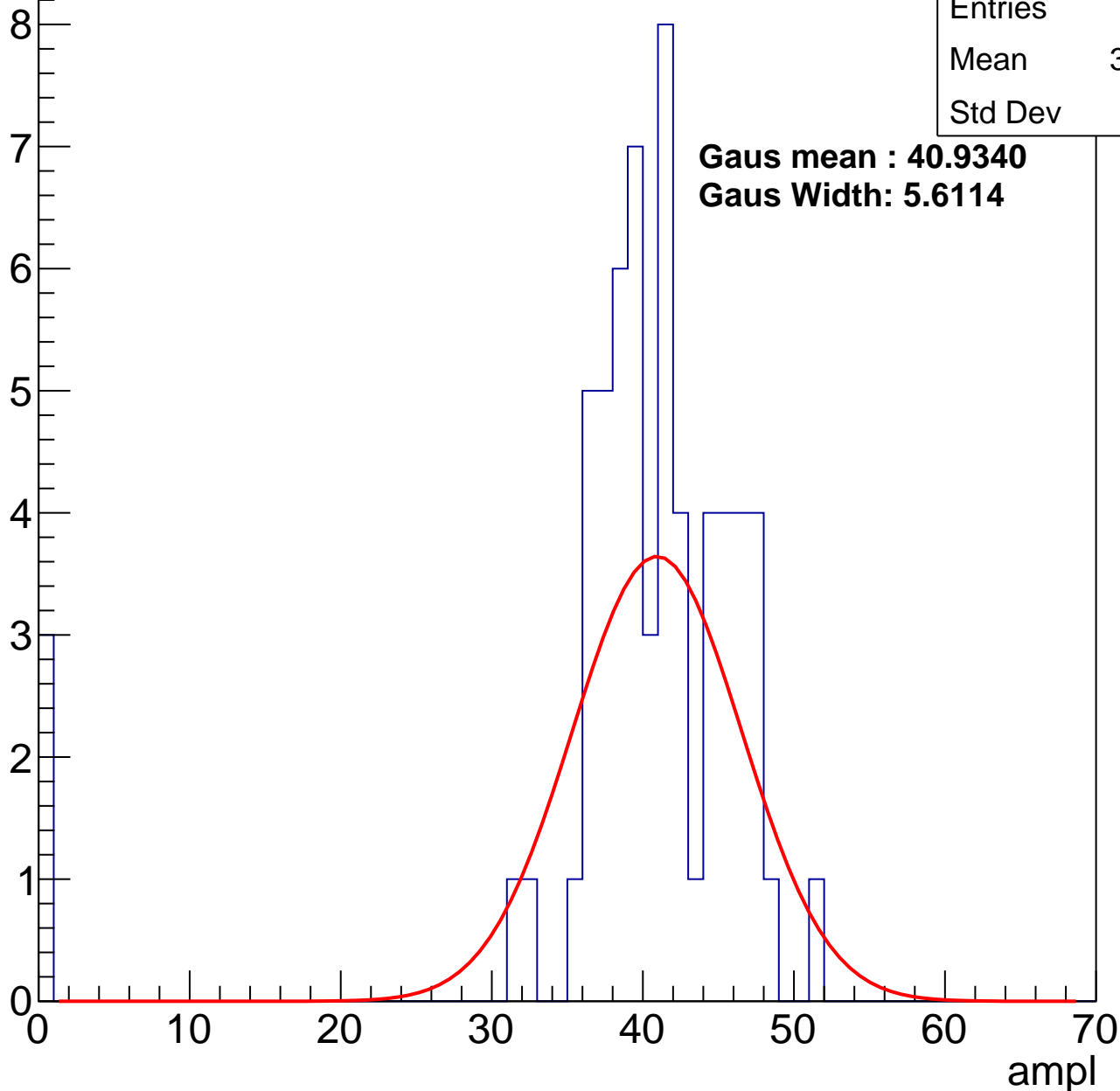
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	38.89
Std Dev	9.57

Gaus mean : 40.9340

Gaus Width: 5.6114



B1L103S, U18-ch102, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	70
Mean	38.77
Std Dev	17.06

Entry

10

8

6

4

2

0

0

10

20

30

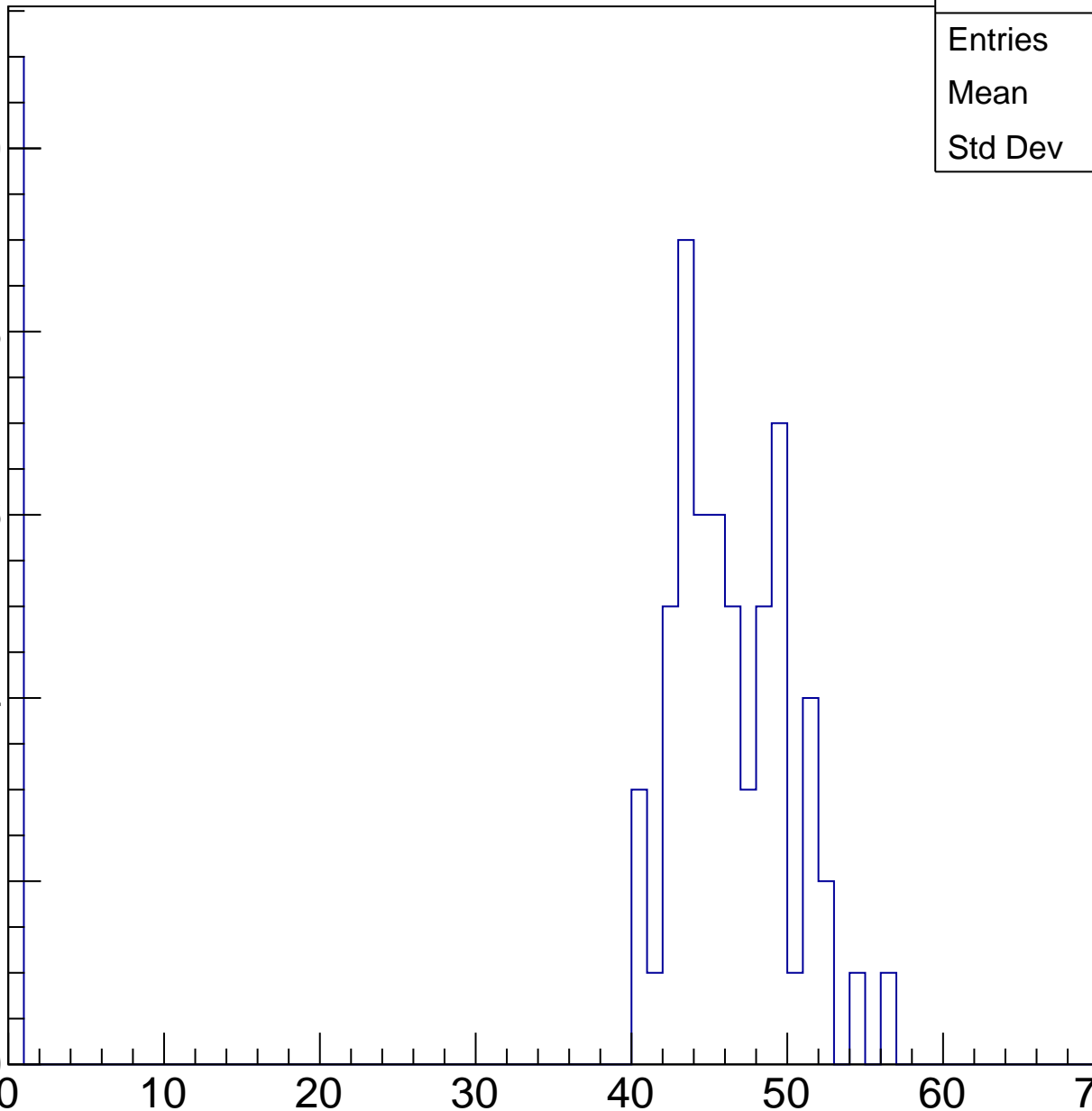
40

50

60

70

ampl

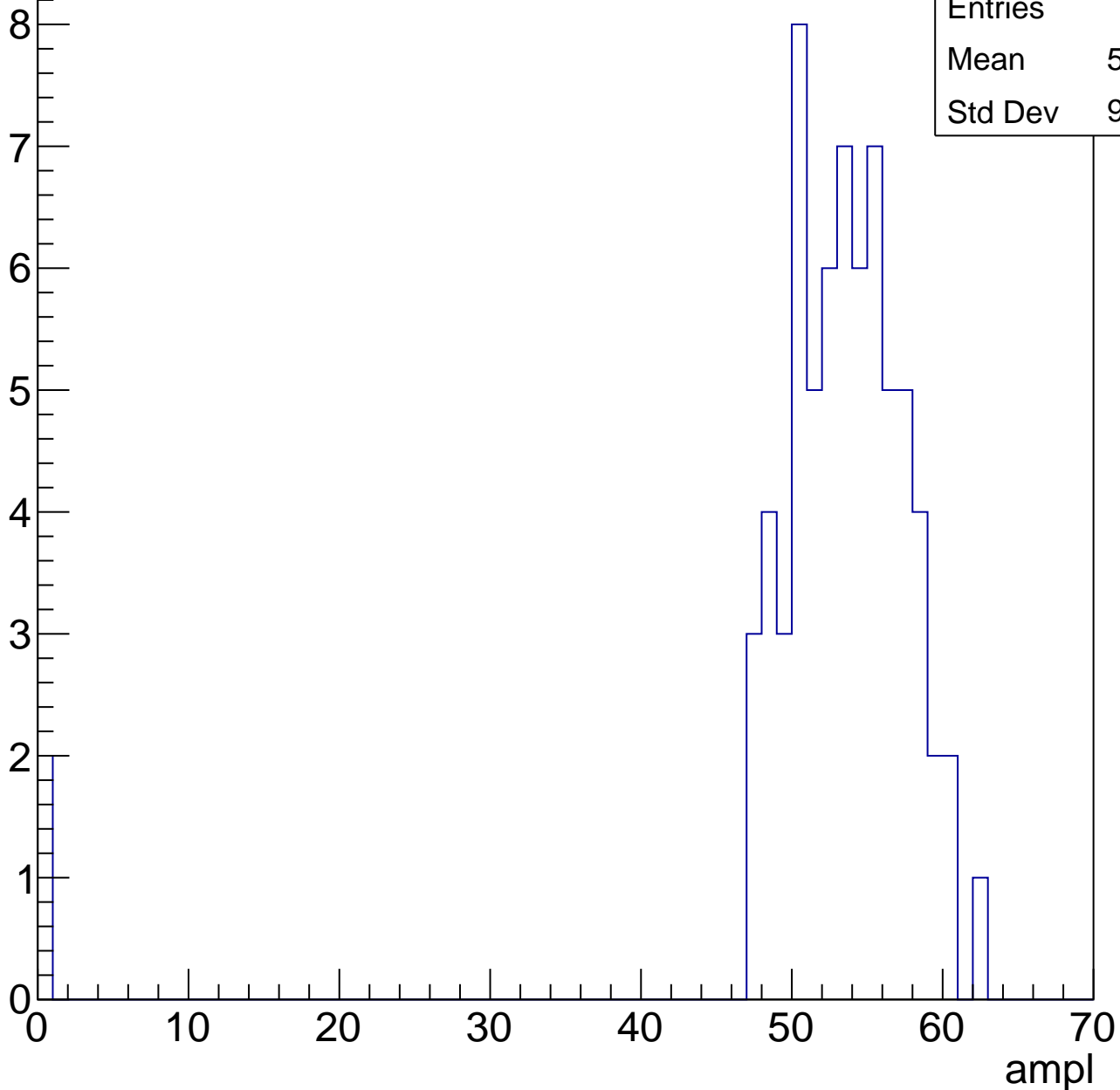


B1L103S, U18-ch102, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

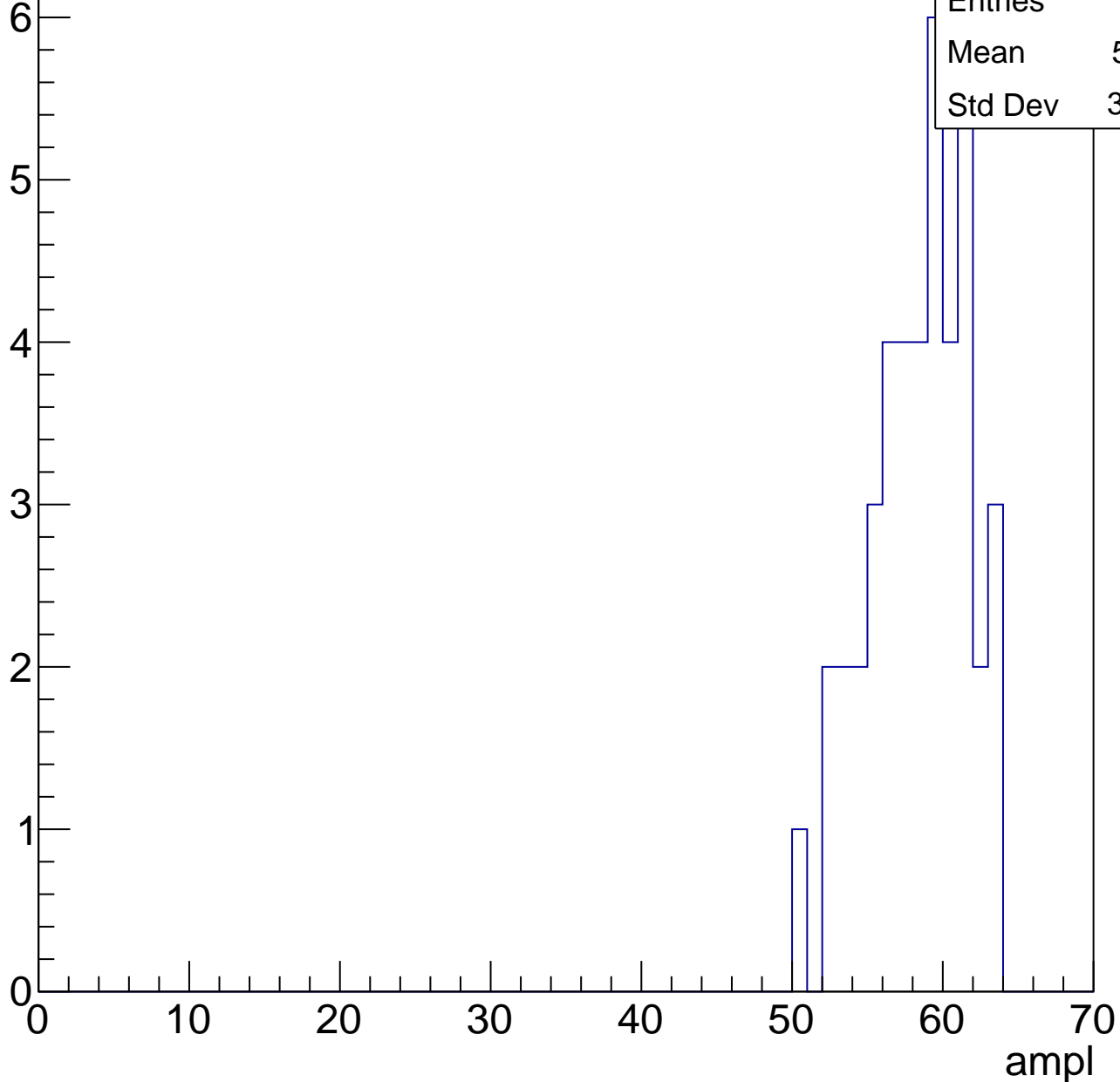
Entries	70
Mean	51.77
Std Dev	9.544



B1L103S, U18-ch102, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

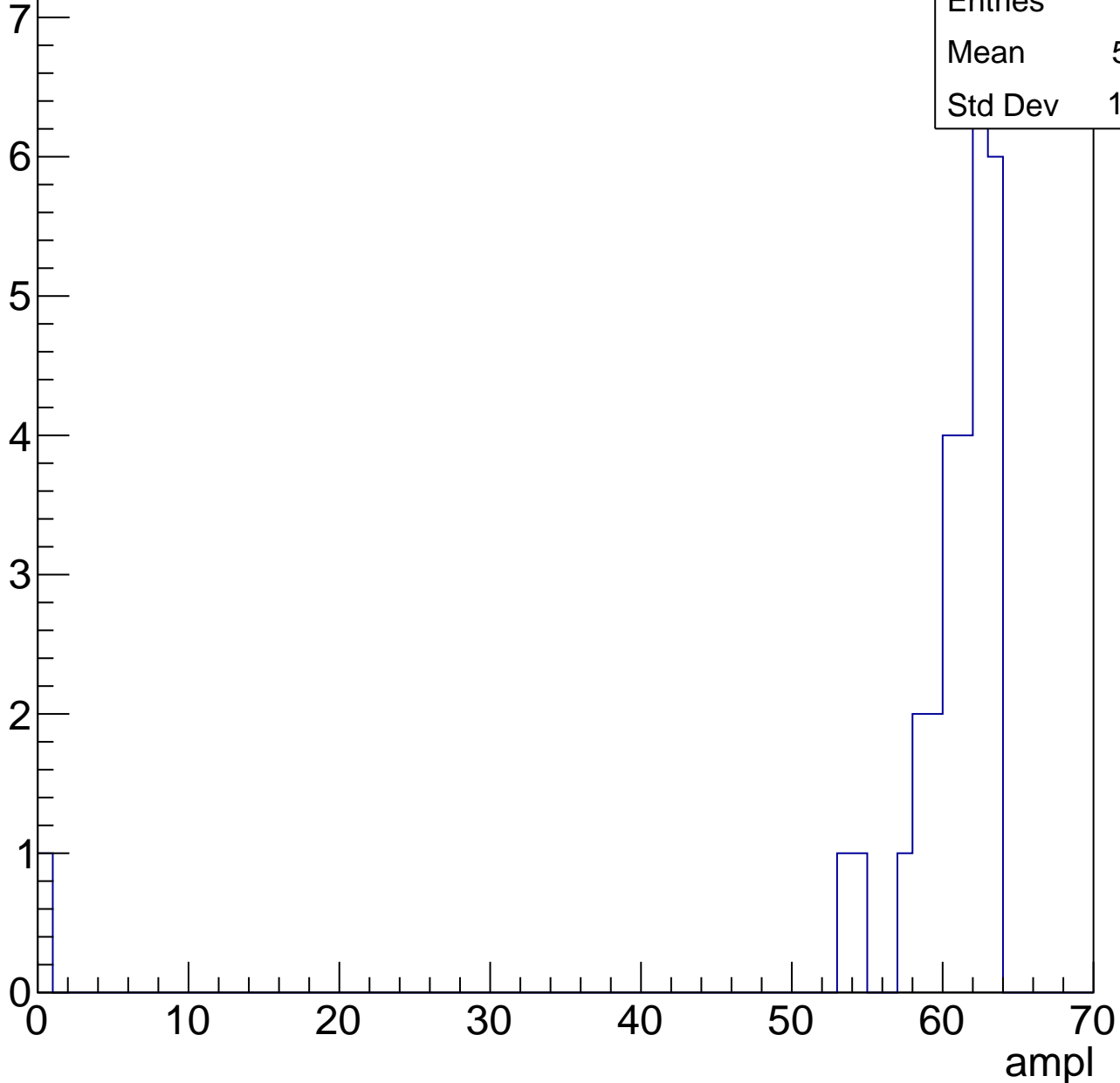


B1L103S, U18-ch102, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	29
Mean	58.41
Std Dev	11.32

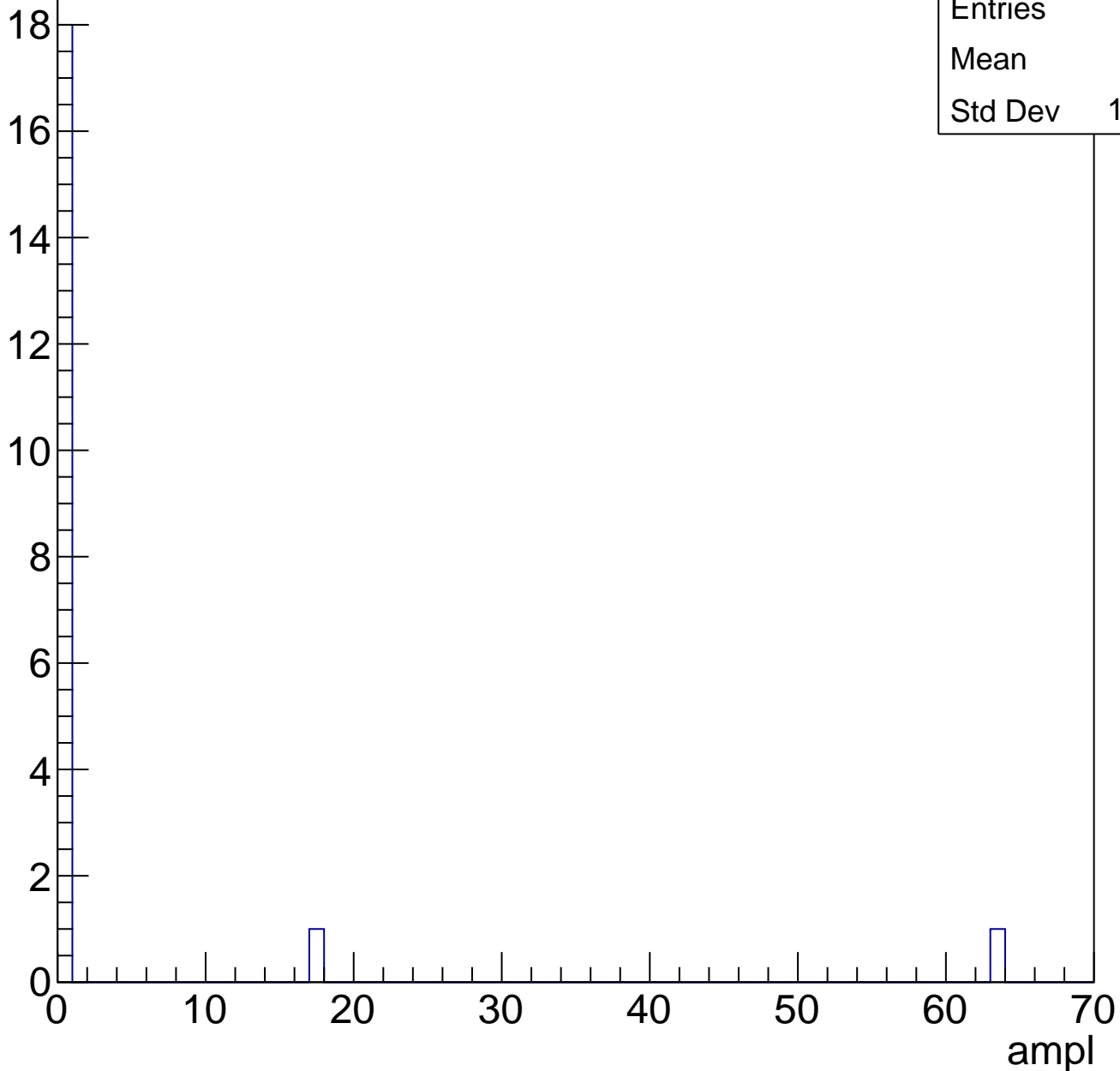


B1L103S, U18-ch102, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	4
Std Dev	14.03

Entry



B1L103S, U18-ch103, adc0

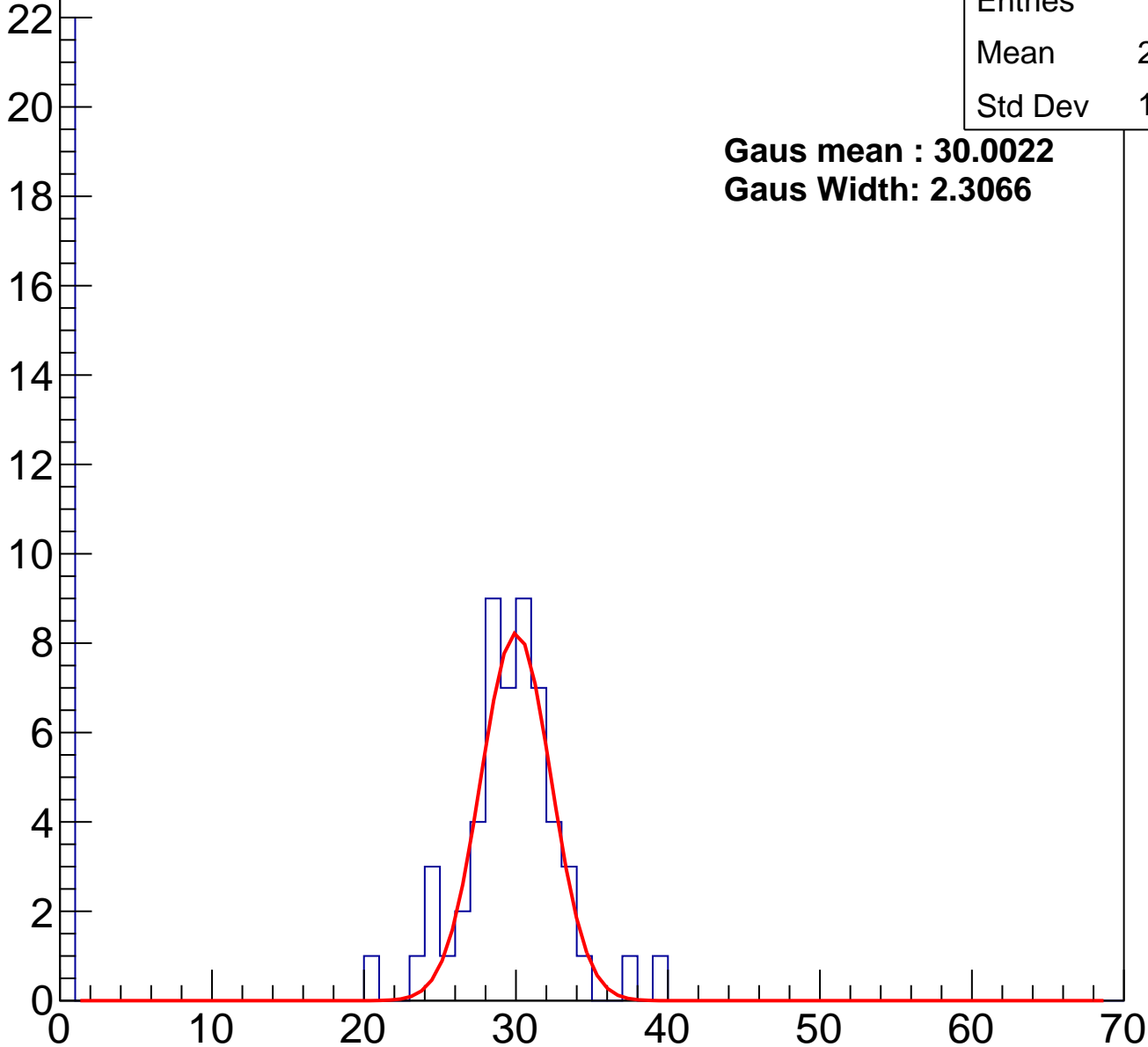
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	20.78
Std Dev	13.54

Gaus mean : 30.0022

Gaus Width: 2.3066

Entry



ampl

B1L103S, U18-ch103, adc1

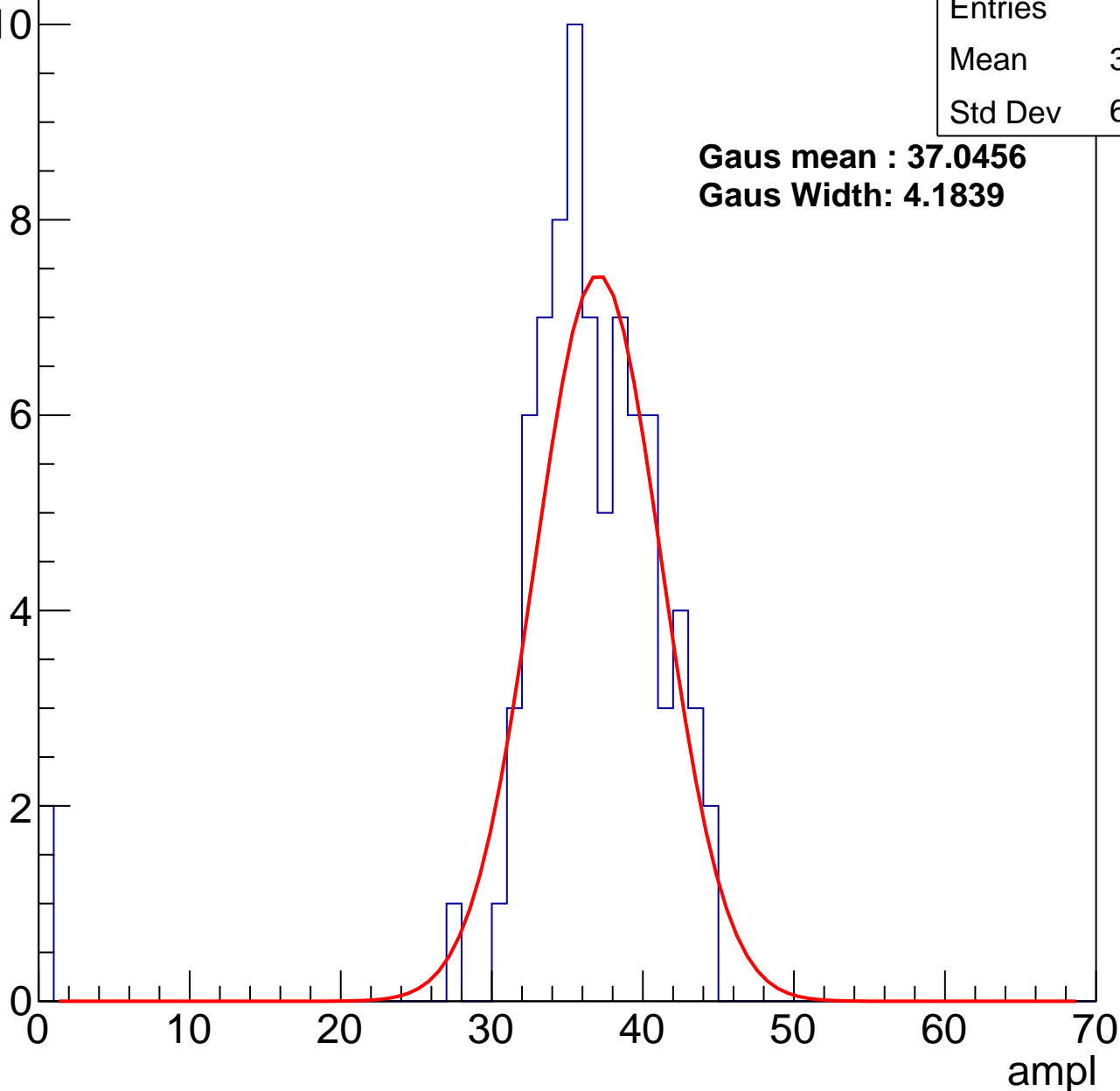
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	81
Mean	35.56
Std Dev	6.709

Gaus mean : 37.0456

Gaus Width: 4.1839



B1L103S, U18-ch103, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	64
Mean	34.61
Std Dev	16.07

Gaus mean : 42.6321

Gaus Width: 3.9619

Entry

10

8

6

4

2

0

0

10

20

30

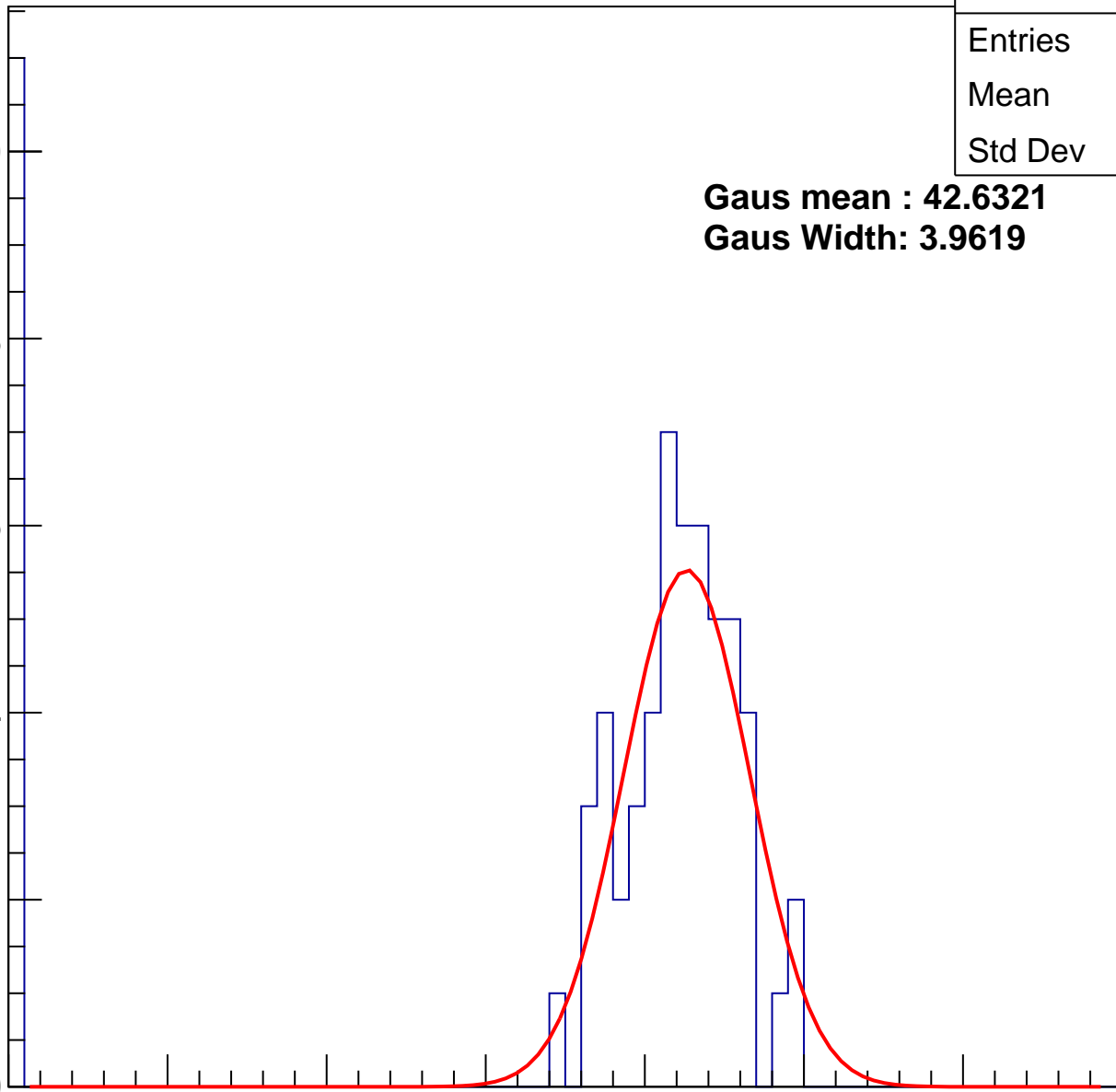
40

50

60

70

ampl

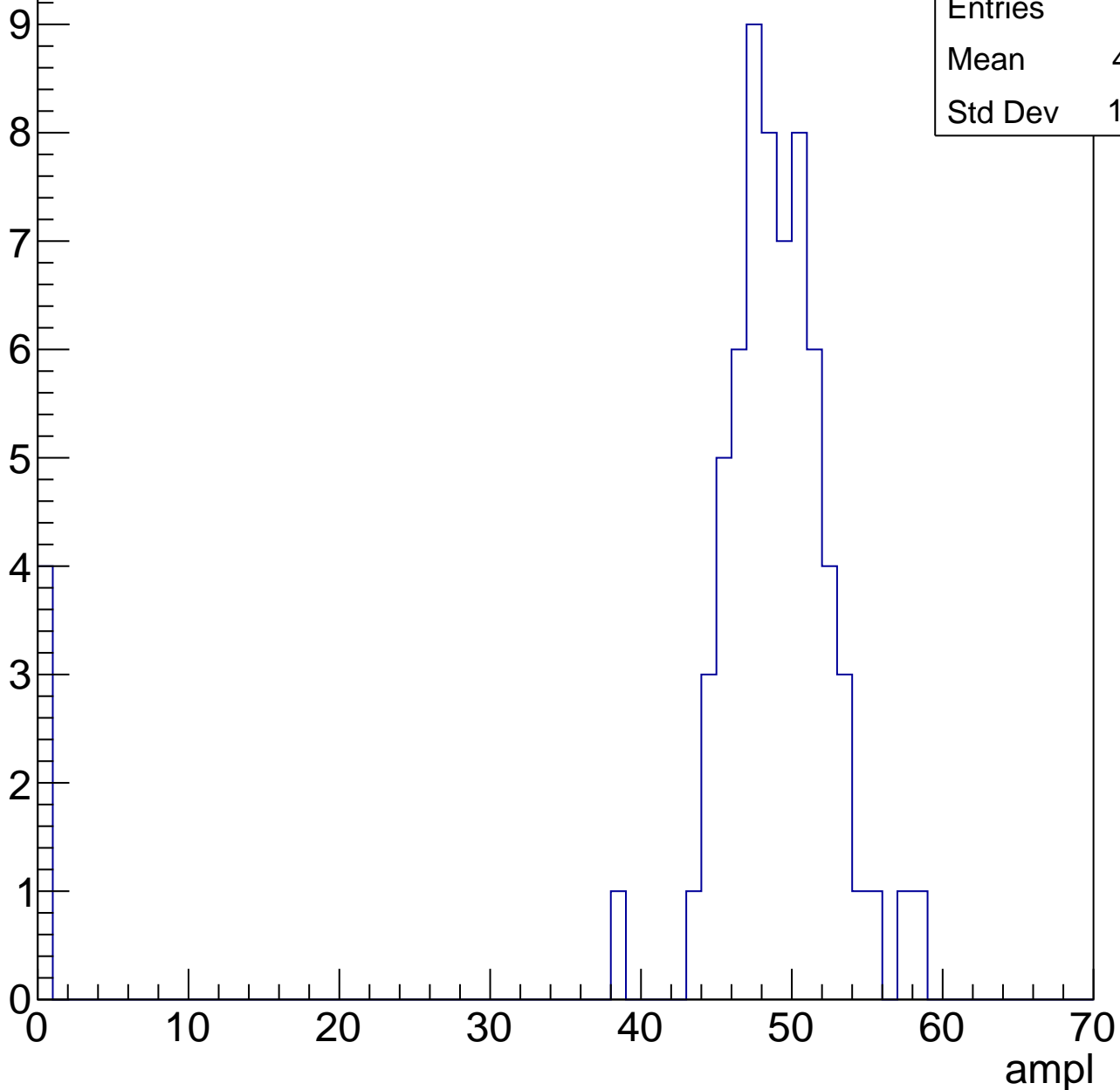


B1L103S, U18-ch103, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	69
Mean	45.81
Std Dev	11.82

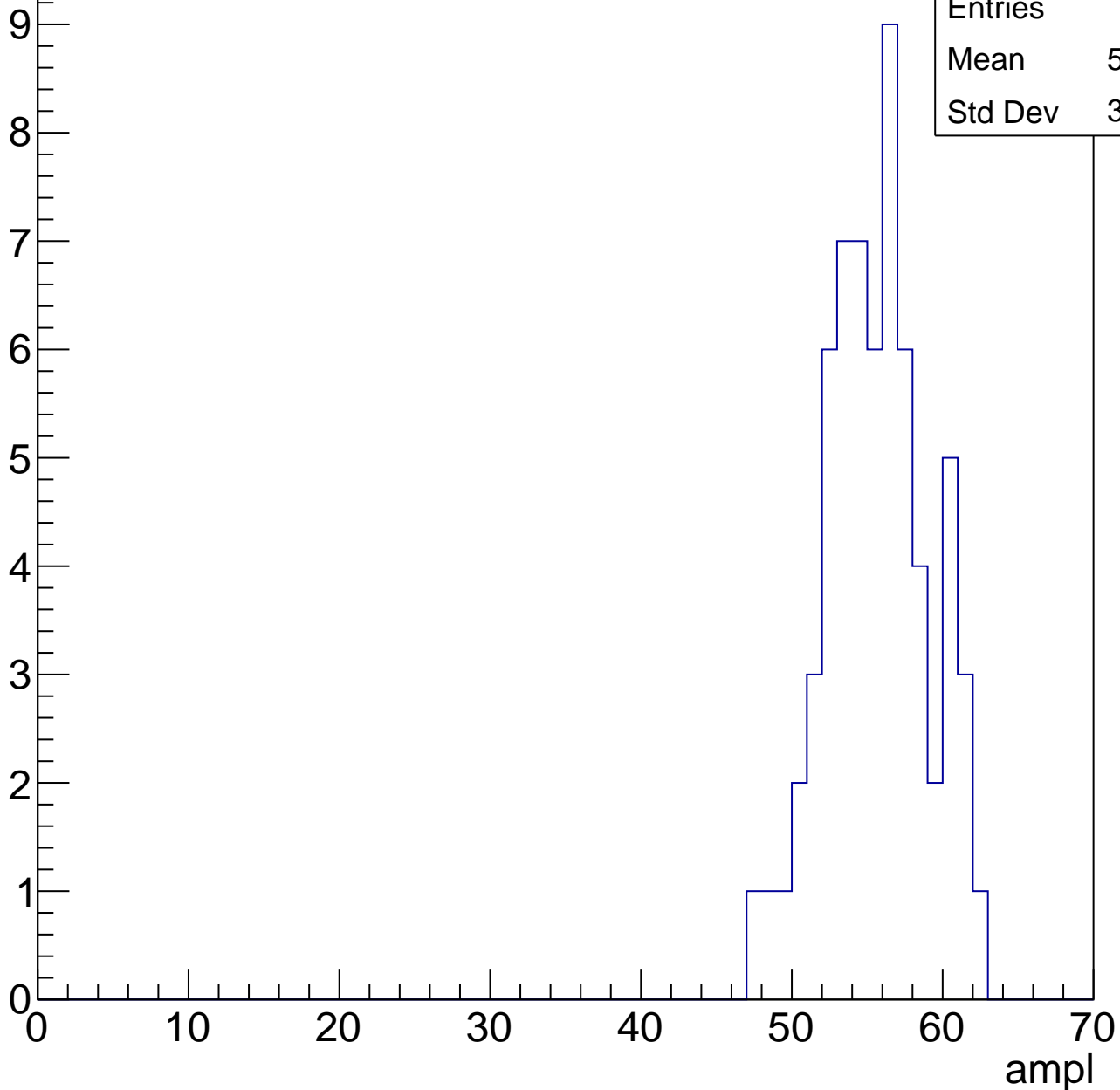


B1L103S, U18-ch103, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	55.14
Std Dev	3.349

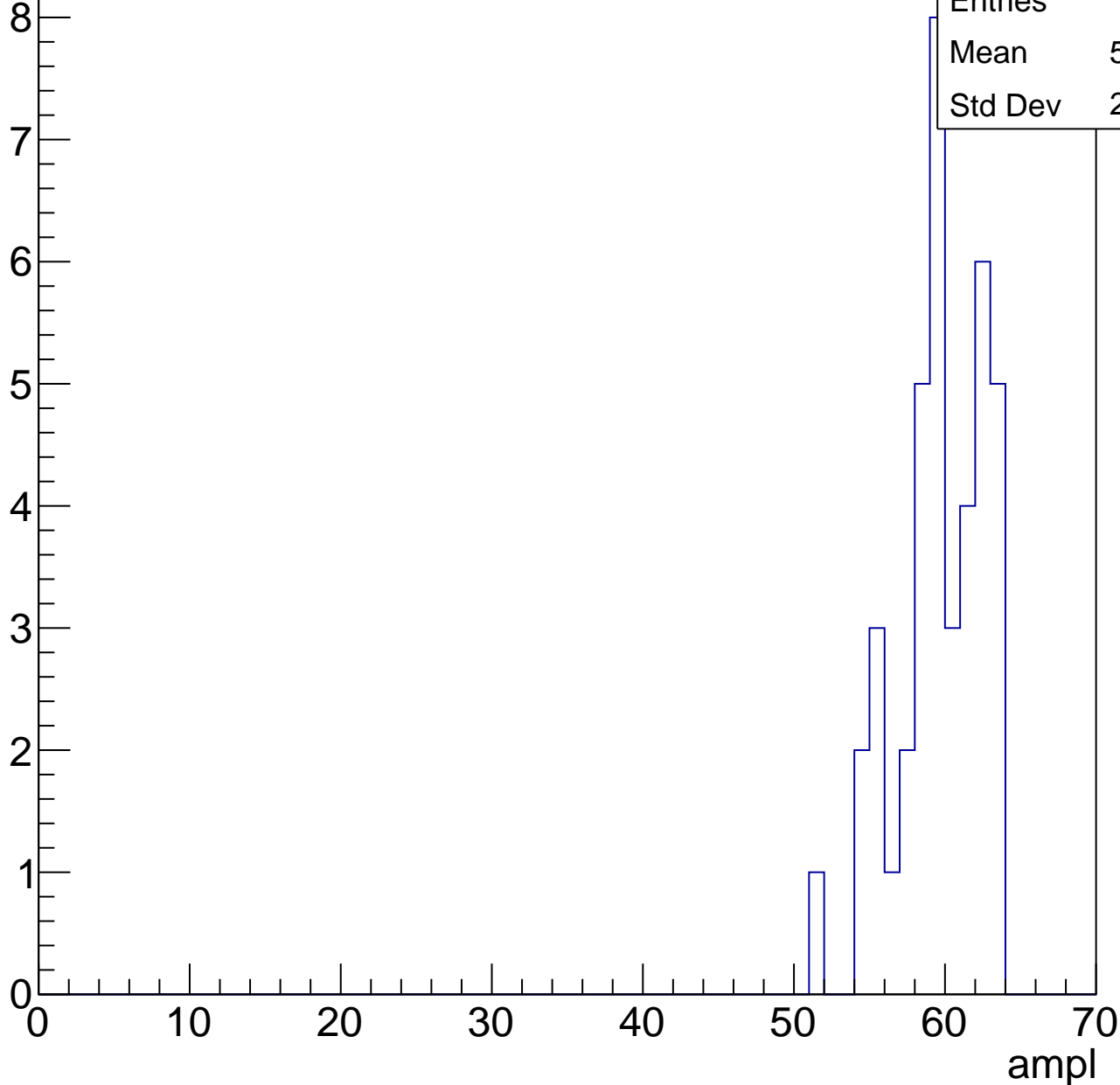


B1L103S, U18-ch103, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	59.17
Std Dev	2.897

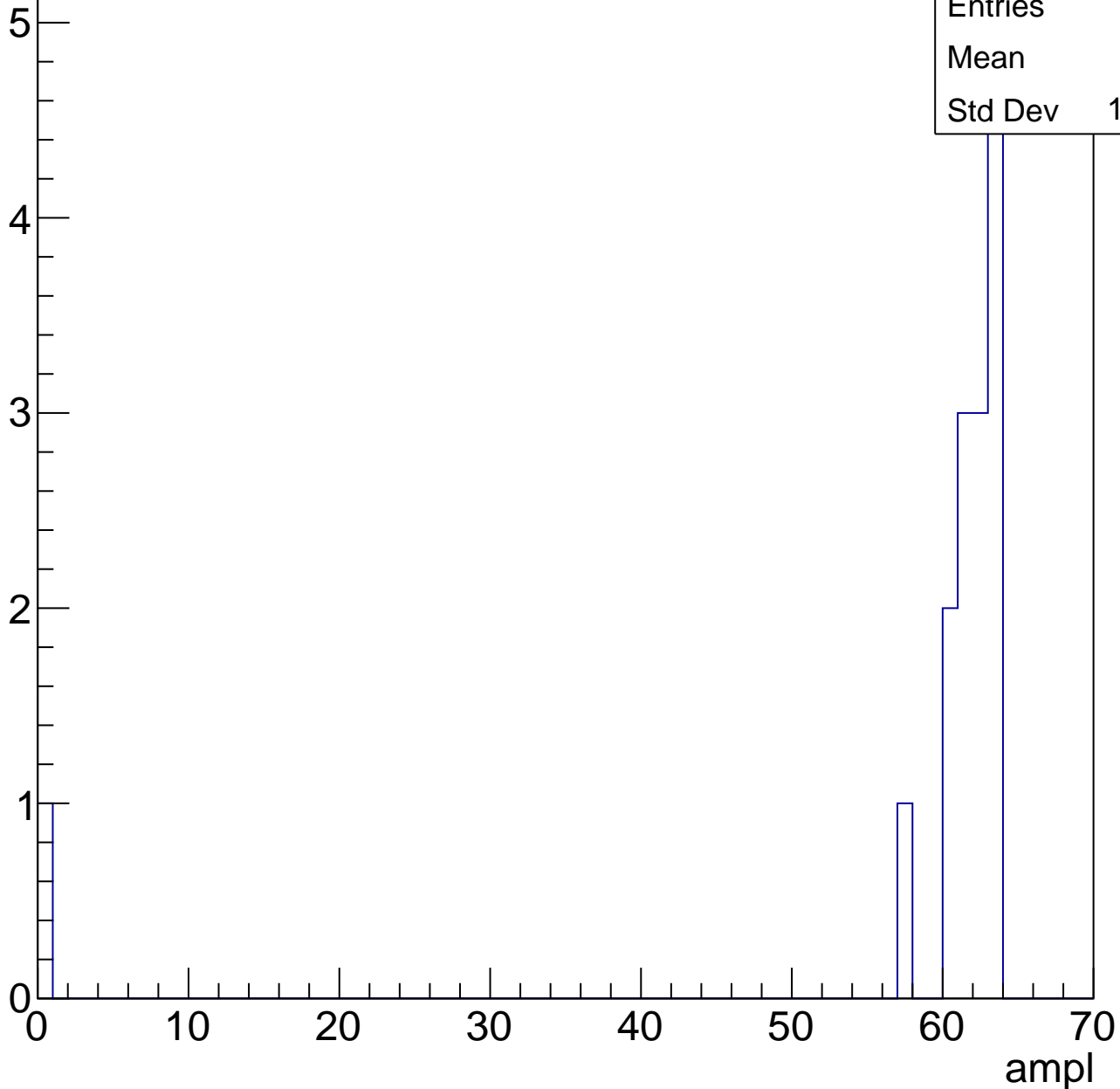


B1L103S, U18-ch103, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

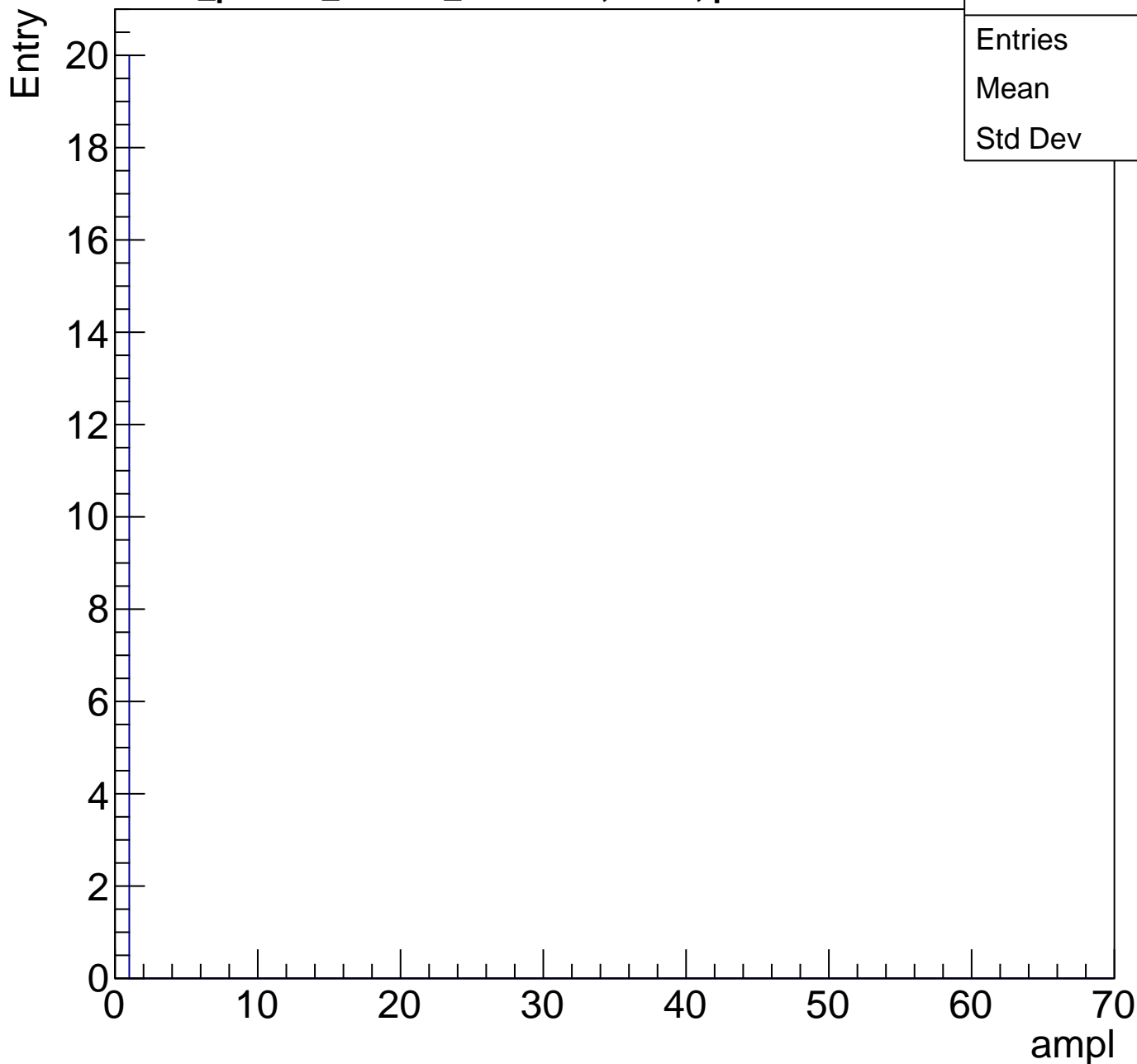
Entries	15
Mean	57.4
Std Dev	15.42



B1L103S, U18-ch103, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	0
Std Dev	0



B1L103S, U18-ch104, adc0

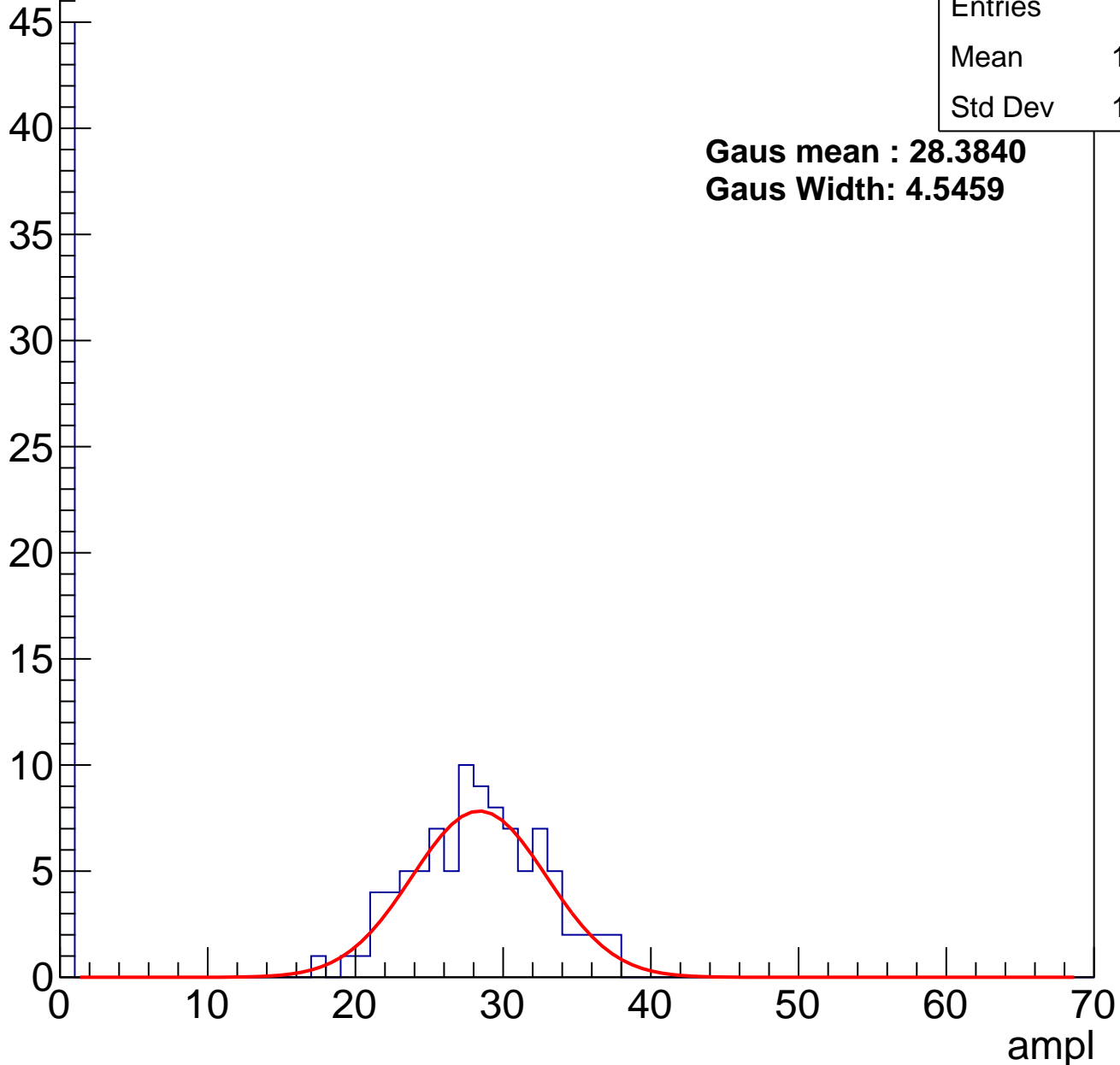
calib_packv5_041523_1651.root, FC#0, port C2

Entries	137
Mean	18.69
Std Dev	13.53

Gaus mean : 28.3840

Gaus Width: 4.5459

Entry



B1L103S, U18-ch104, adc1

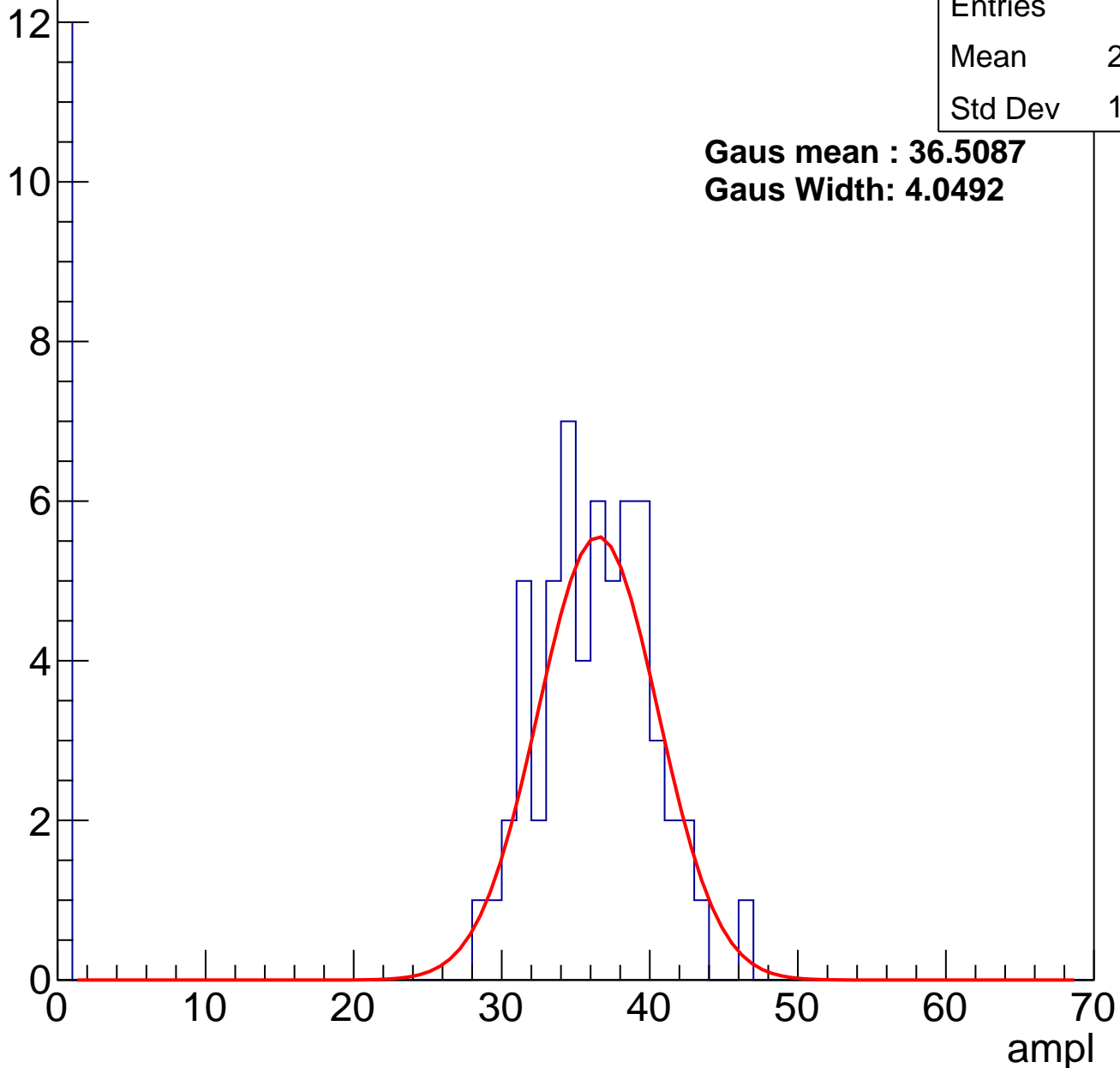
calib_packv5_041523_1651.root, FC#0, port C2

Entries	71
Mean	29.82
Std Dev	13.87

Gaus mean : 36.5087

Gaus Width: 4.0492

Entry



B1L103S, U18-ch104, adc2

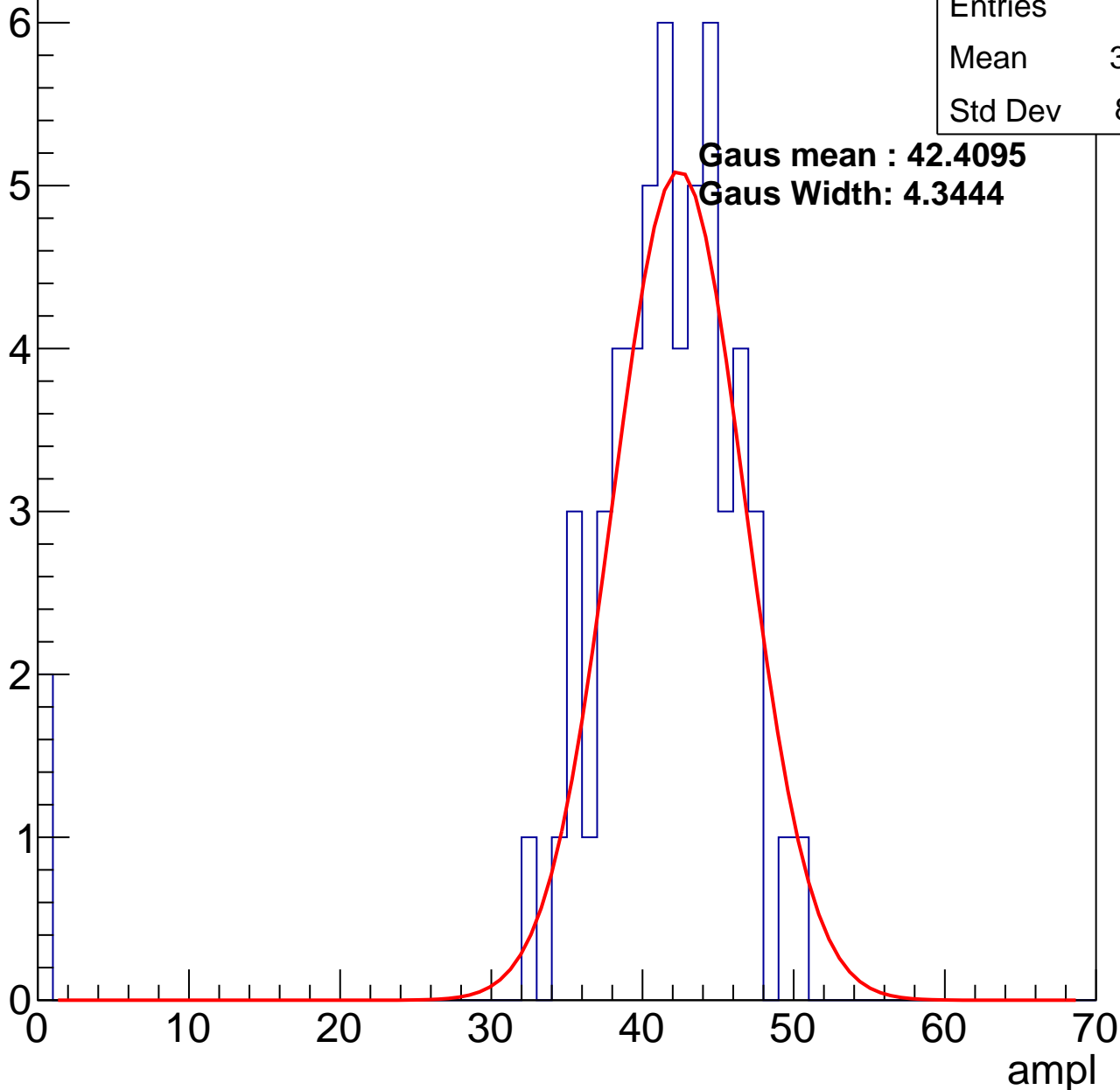
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	39.96
Std Dev	8.531

Gaus mean : 42.4095

Gaus Width: 4.3444

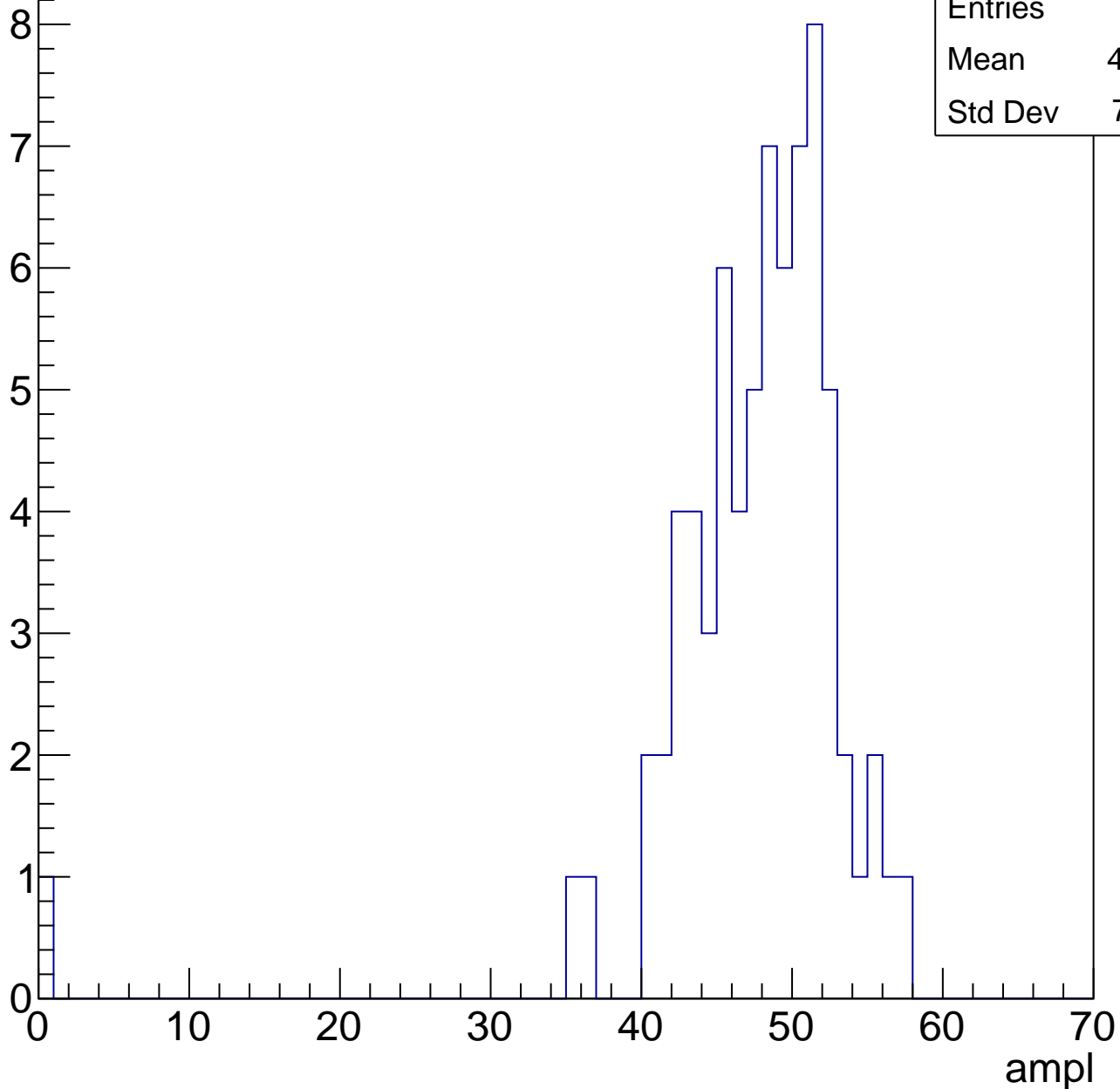


B1L103S, U18-ch104, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	46.92
Std Dev	7.061

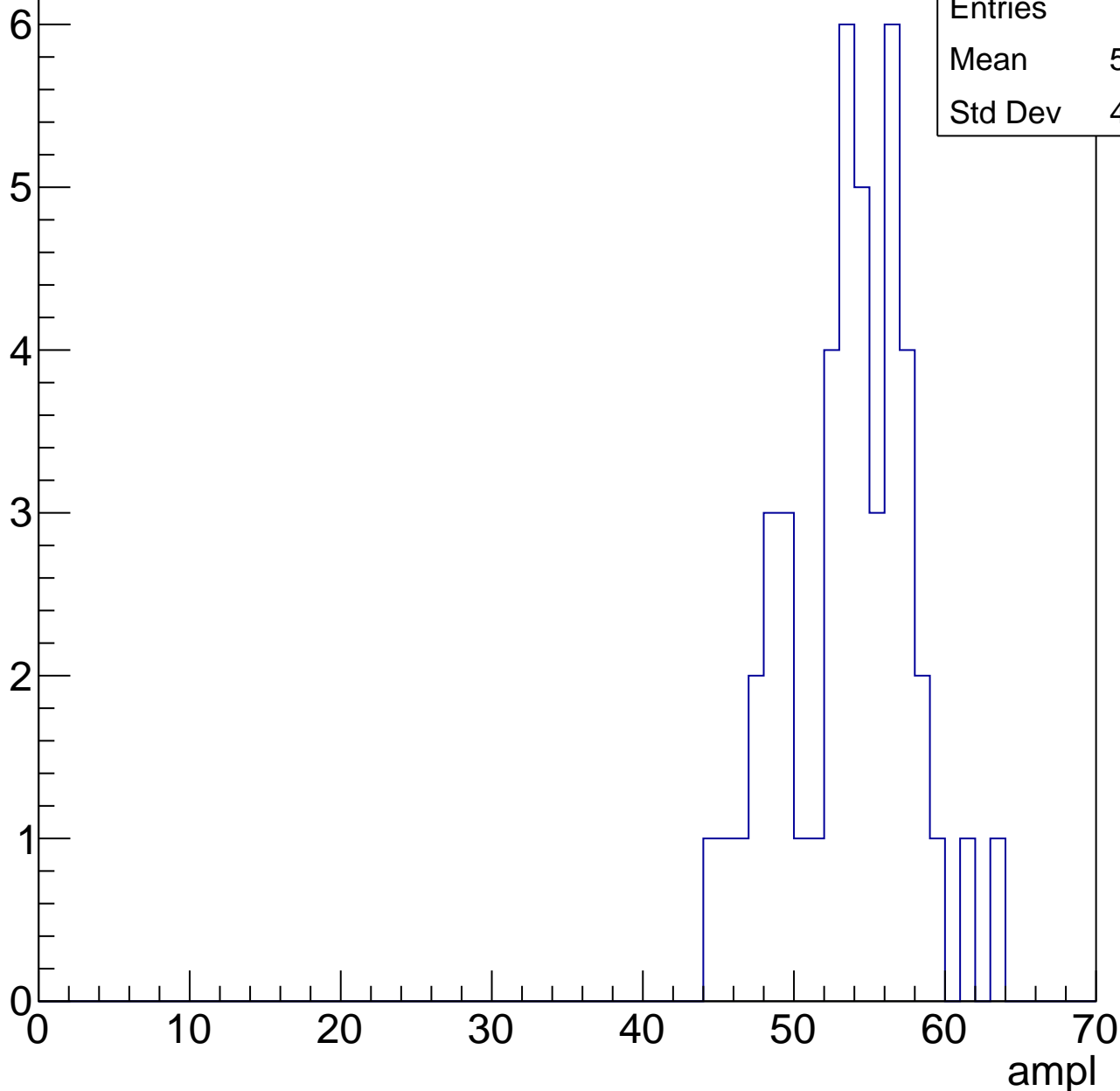


B1L103S, U18-ch104, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	53.15
Std Dev	4.144

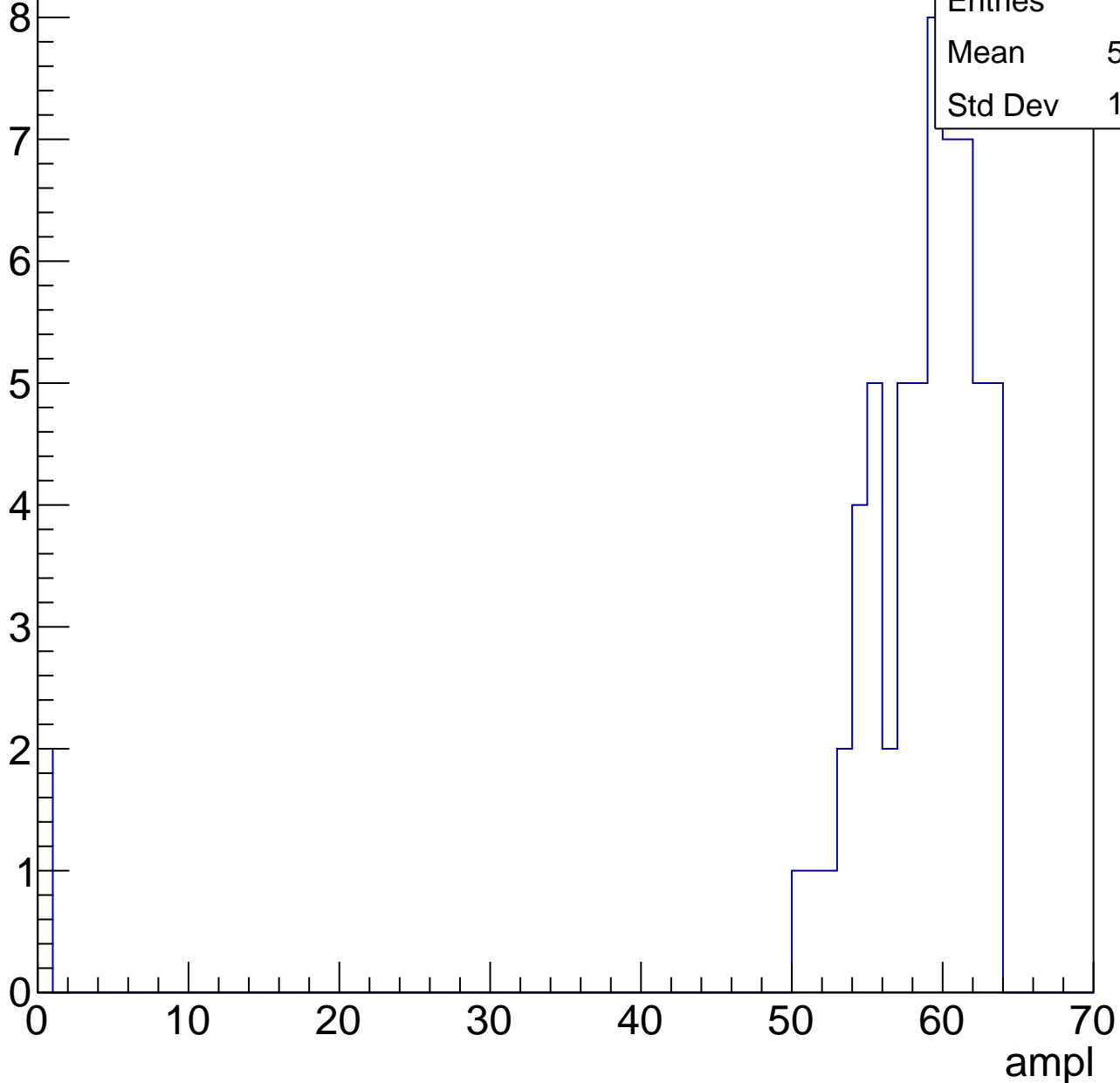


B1L103S, U18-ch104, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

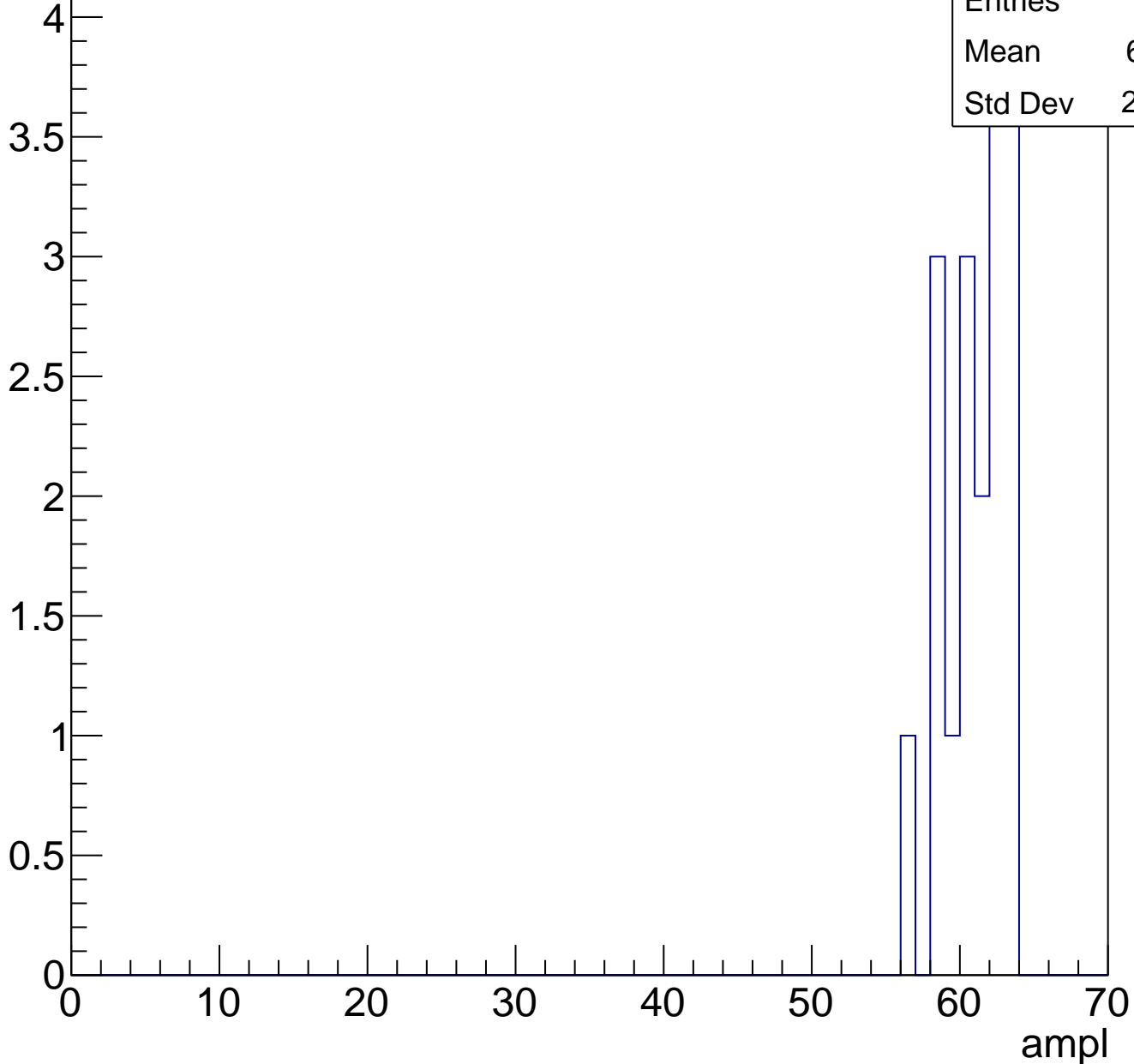
Entries	60
Mean	56.35
Std Dev	10.95



B1L103S, U18-ch104, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

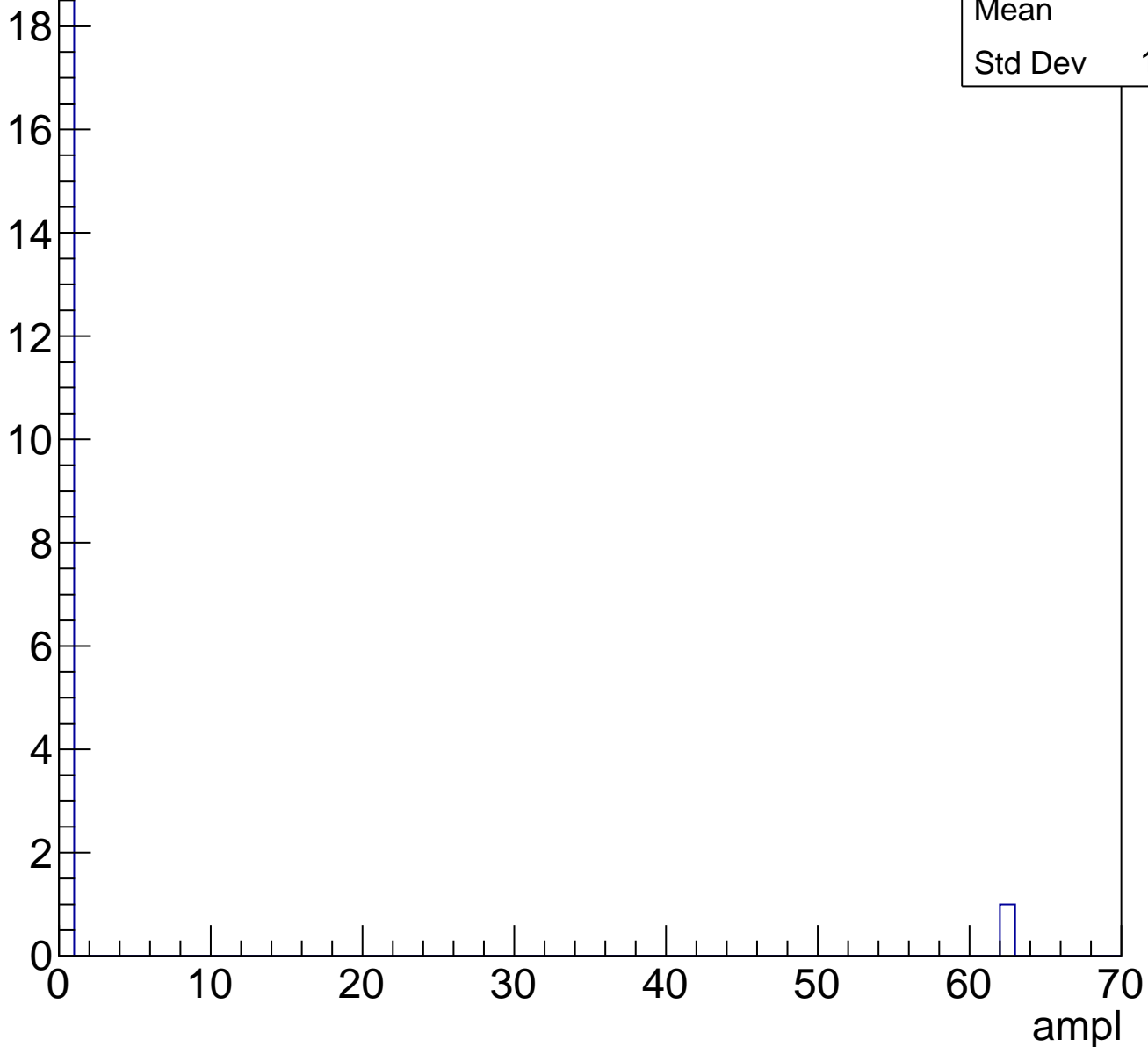


B1L103S, U18-ch104, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U18-ch105, adc0

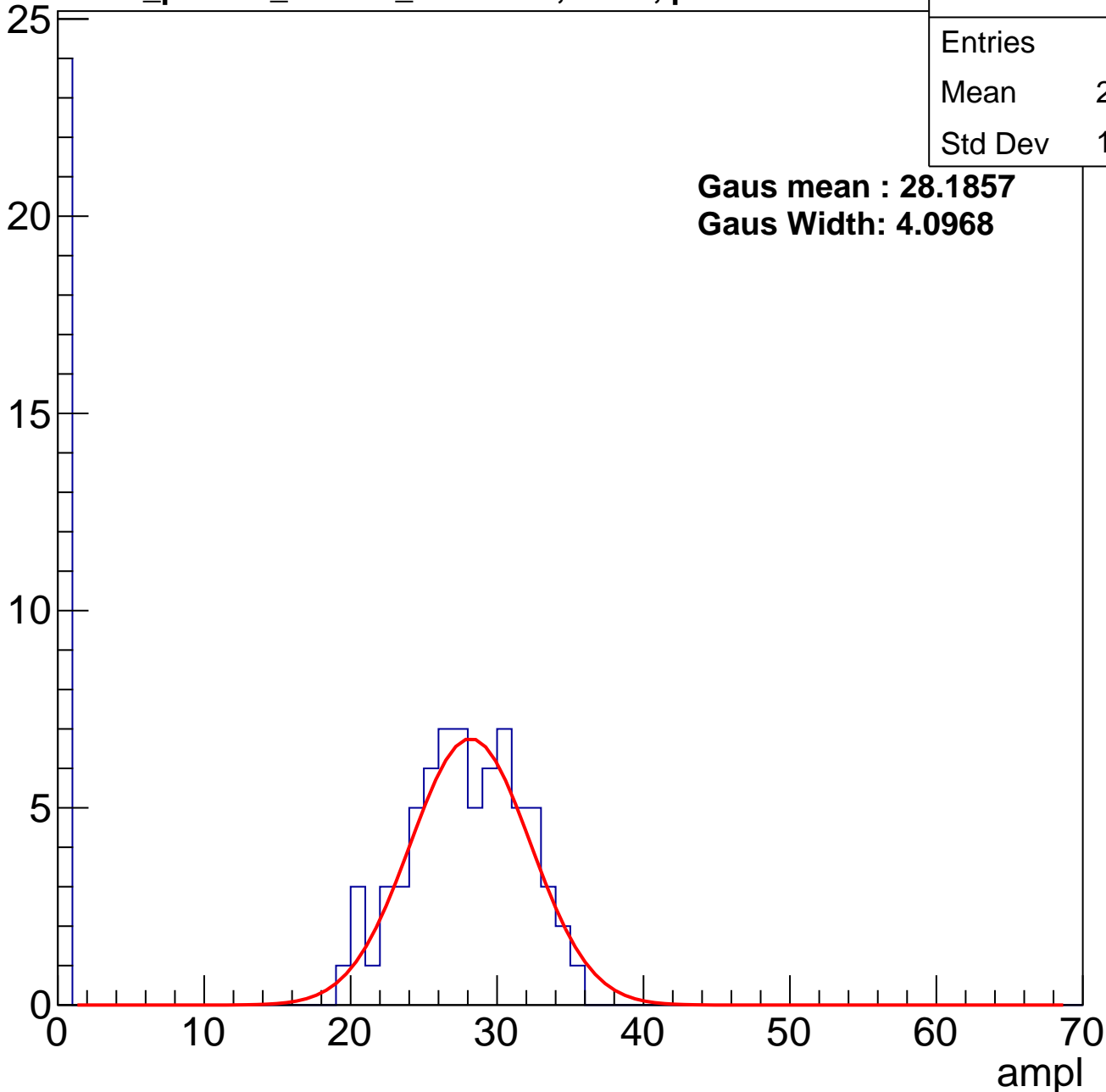
calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	20.39
Std Dev	12.38

Gaus mean : 28.1857

Gaus Width: 4.0968

Entry



B1L103S, U18-ch105, adc1

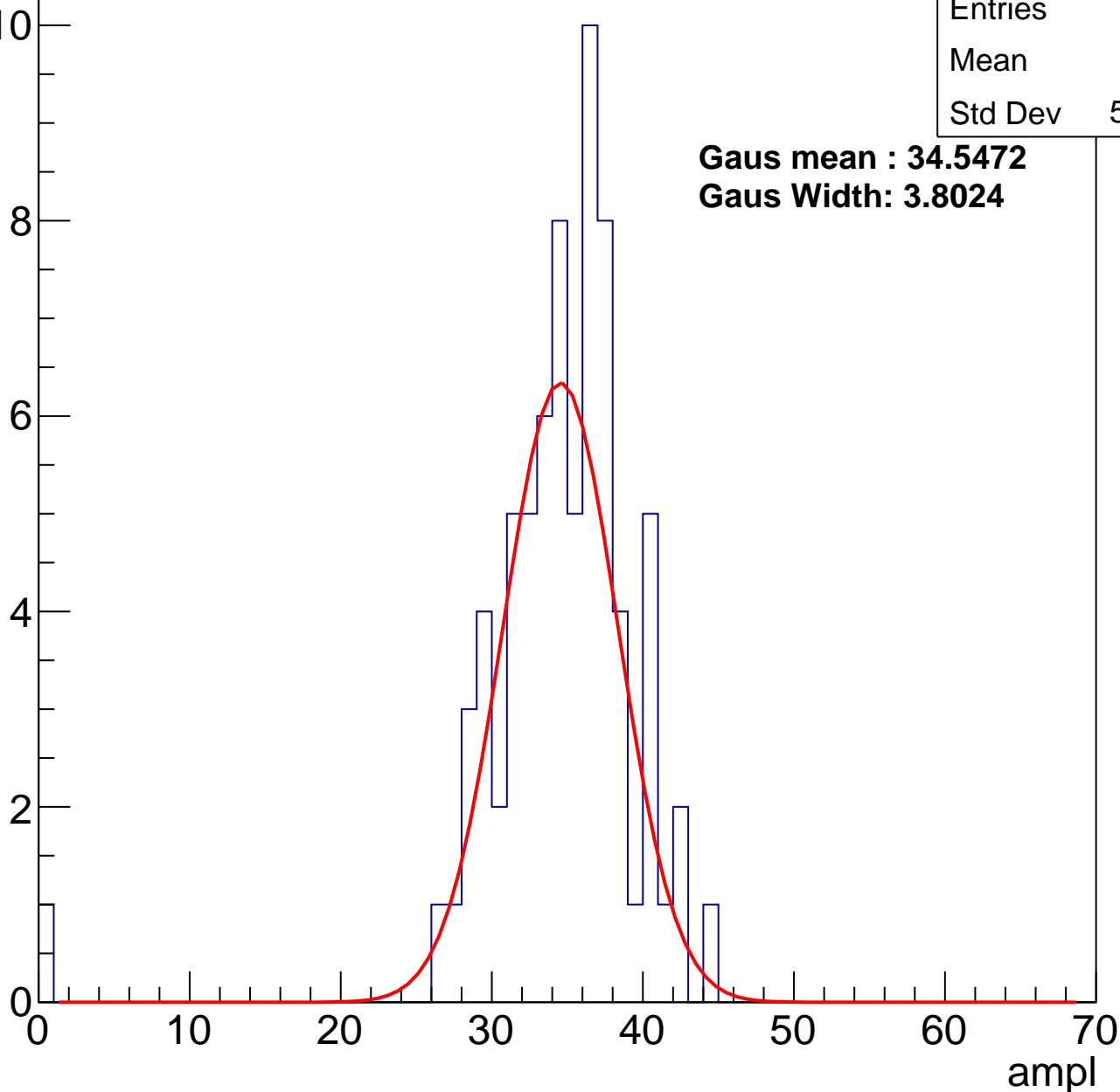
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	34.1
Std Dev	5.542

Gaus mean : 34.5472

Gaus Width: 3.8024

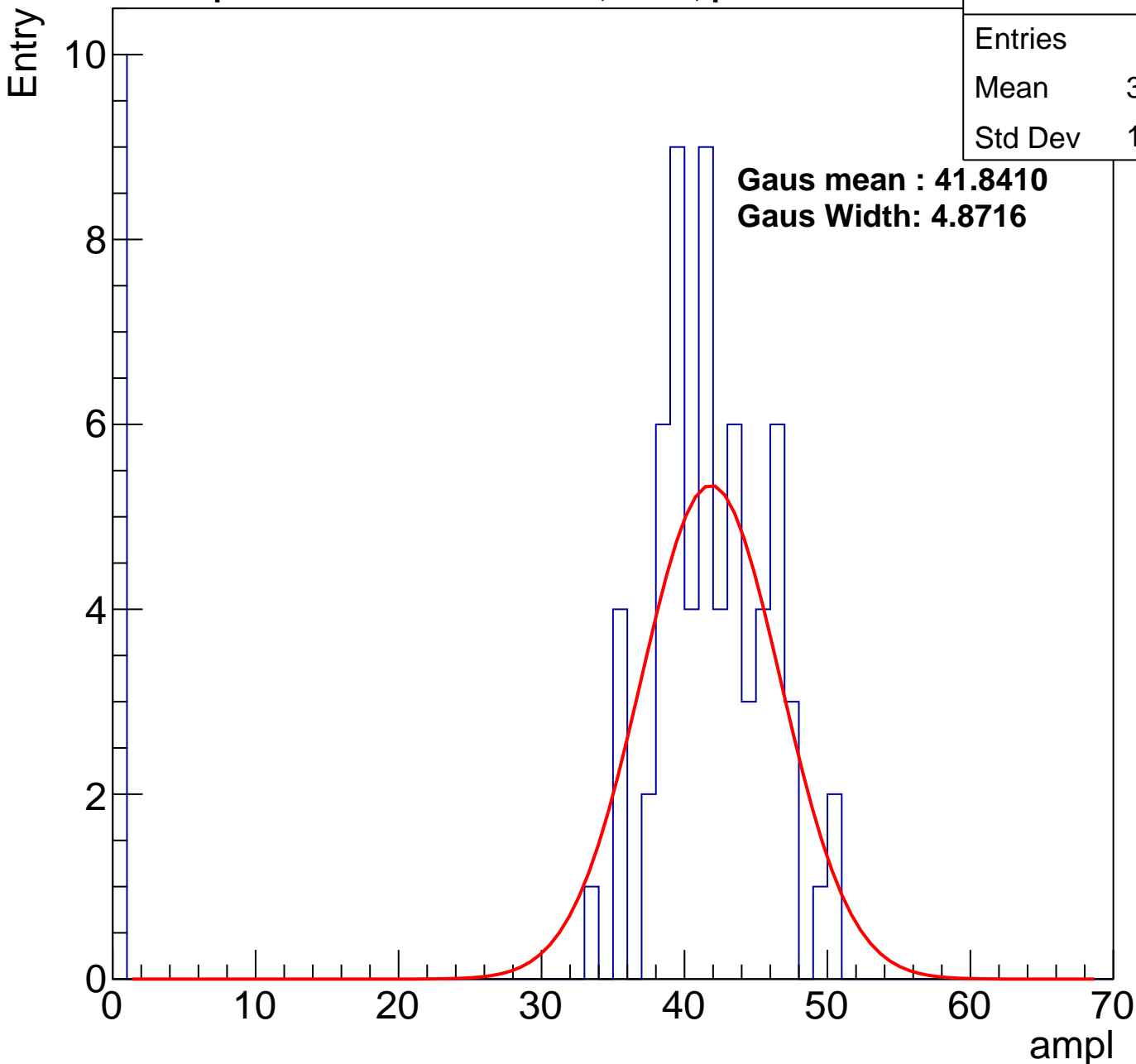


B1L103S, U18-ch105, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	74
Mean	35.93
Std Dev	14.63

Gaus mean : 41.8410
Gaus Width: 4.8716

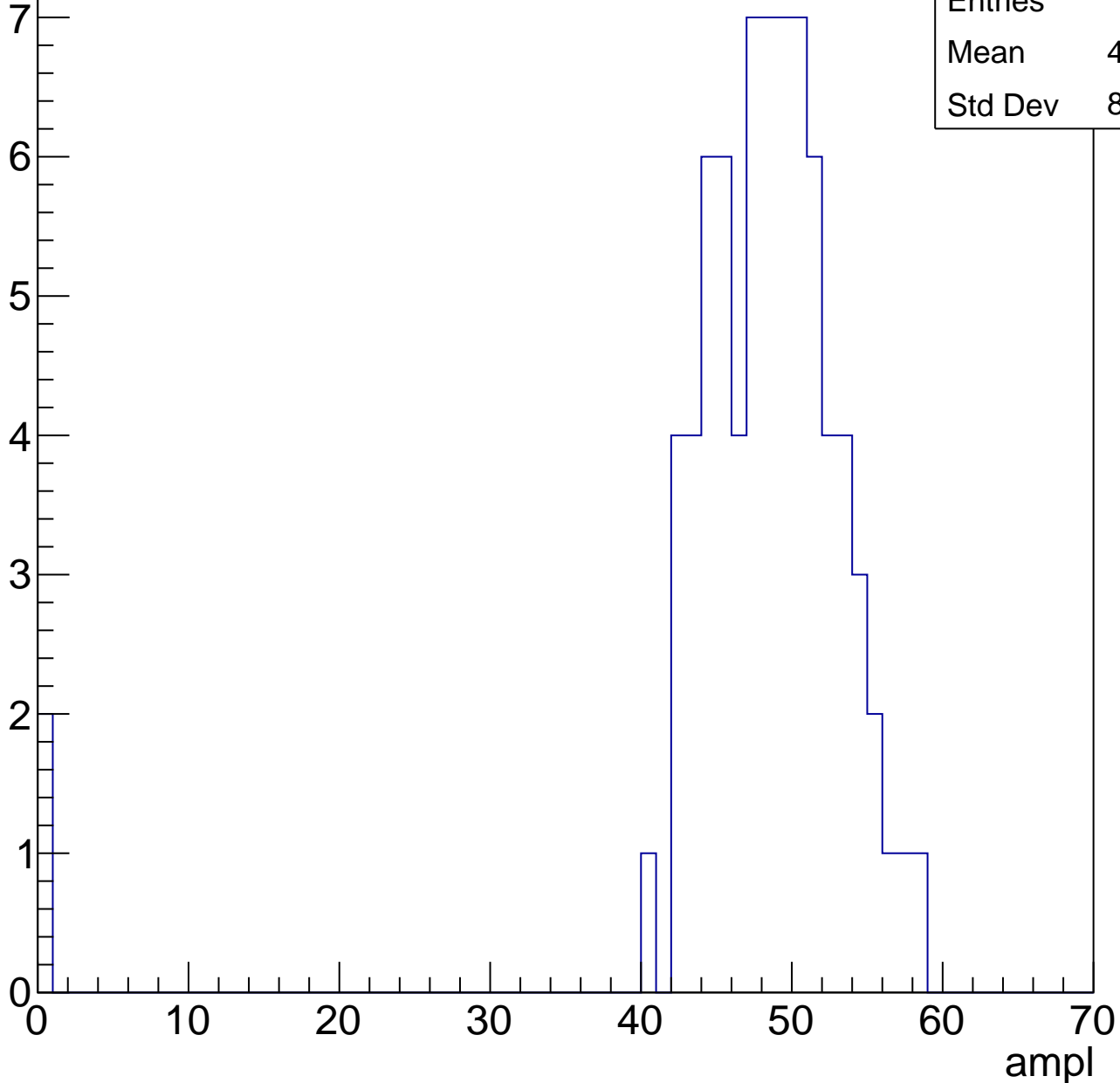


B1L103S, U18-ch105, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	77
Mean	47.08
Std Dev	8.625

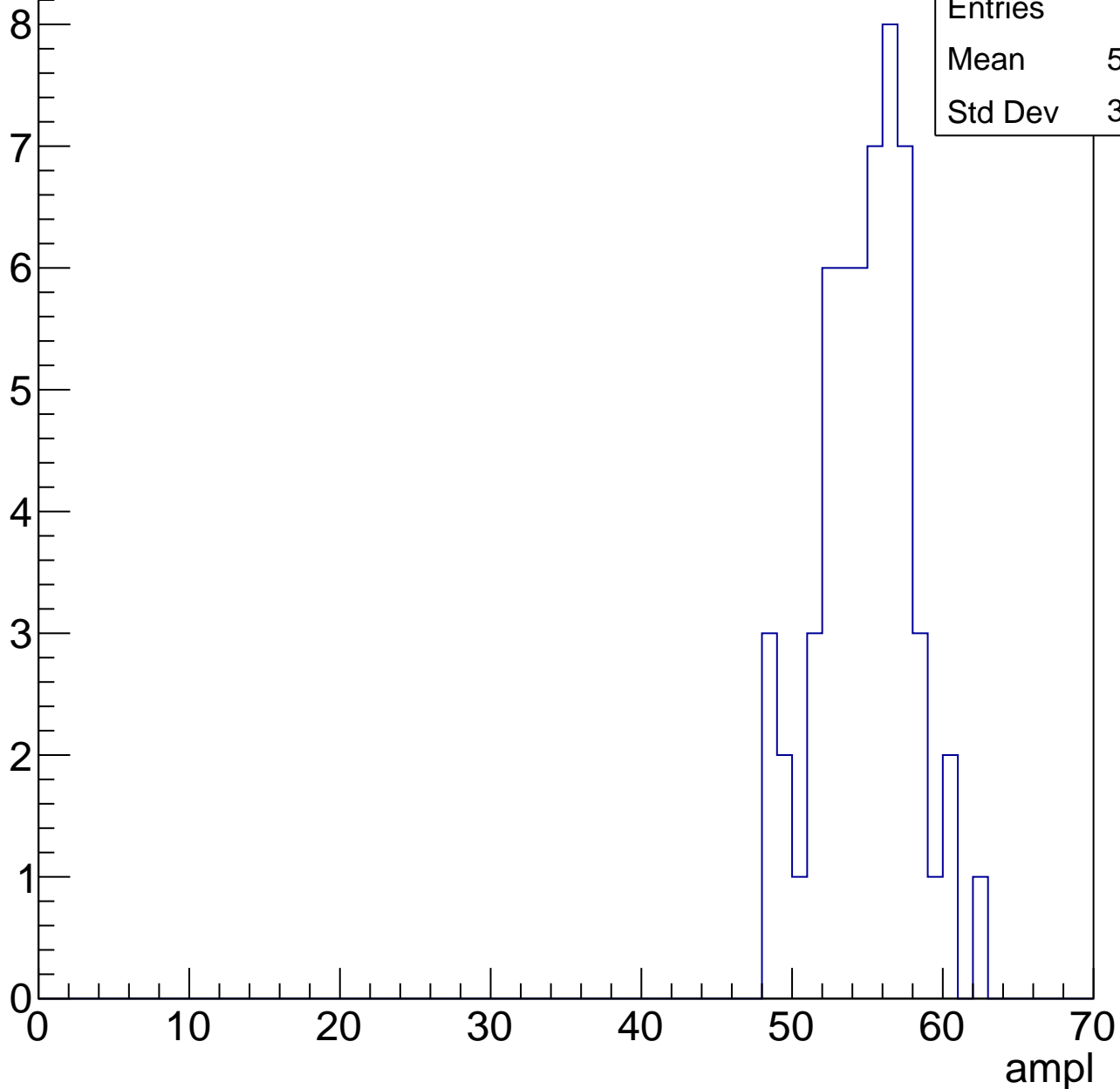


B1L103S, U18-ch105, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	54.39
Std Dev	3.086

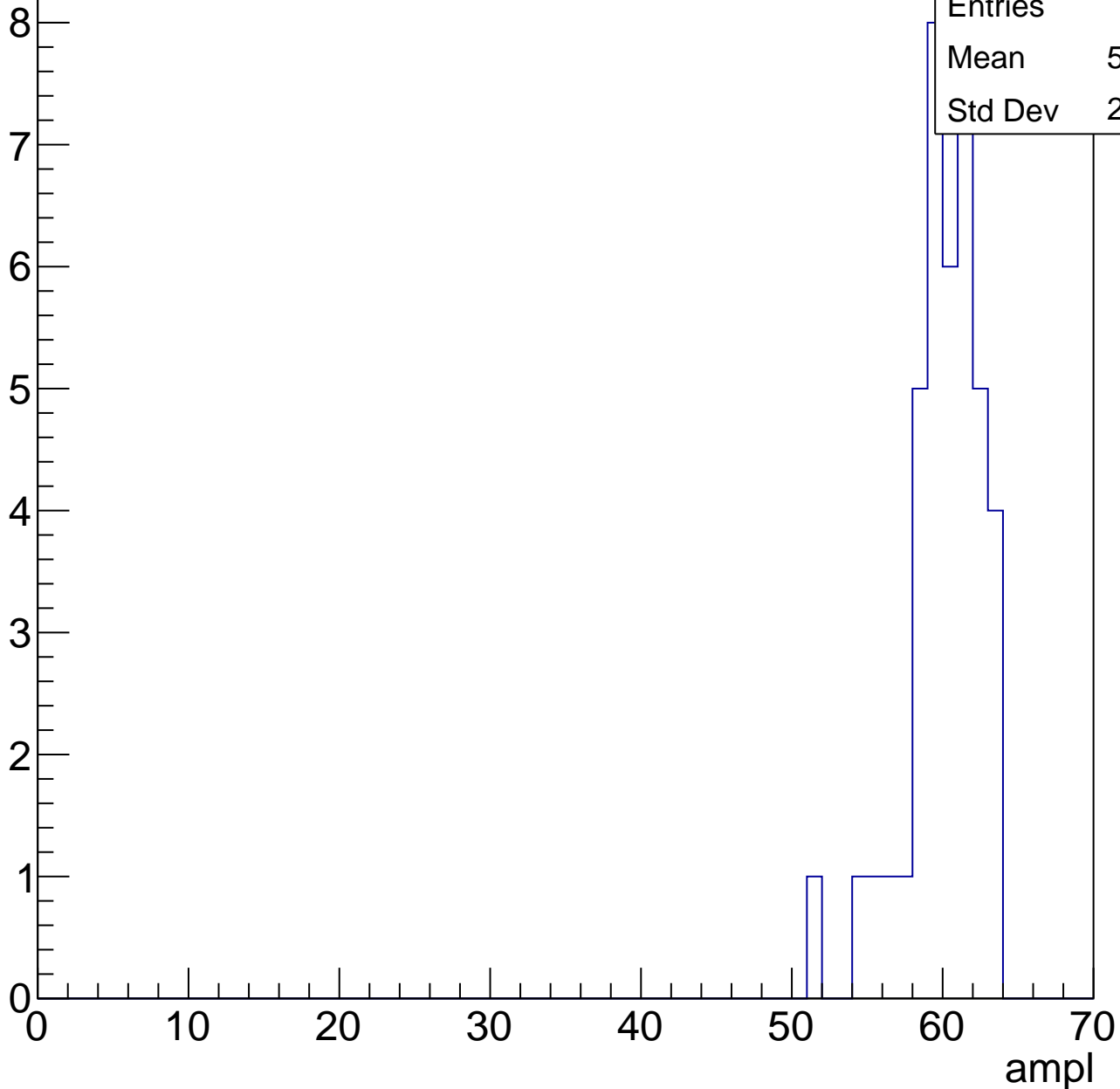


B1L103S, U18-ch105, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

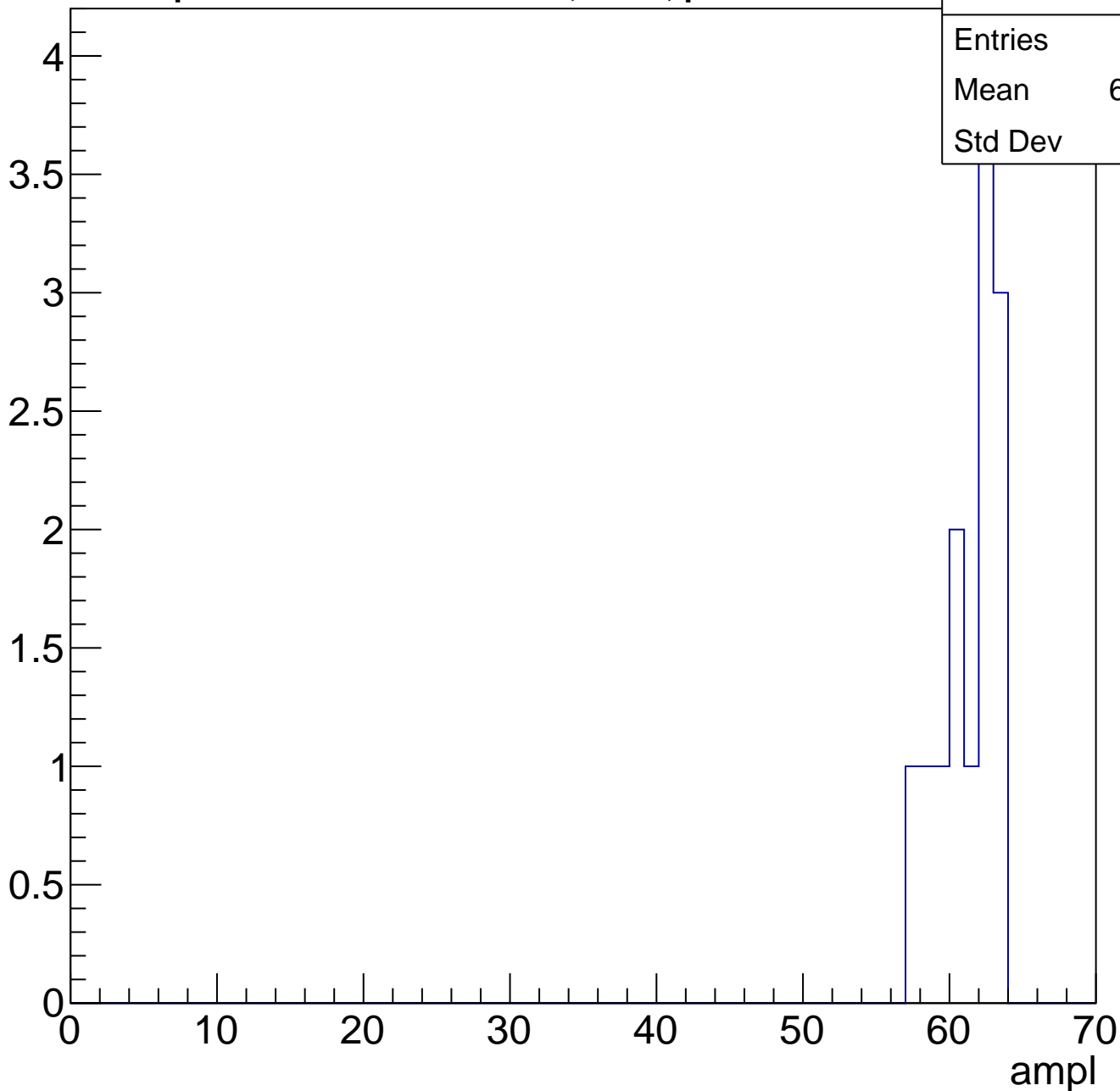
Entries	41
Mean	59.63
Std Dev	2.487



B1L103S, U18-ch105, adc6

calib_packv5_041523_1651.root, FC#0, port C2

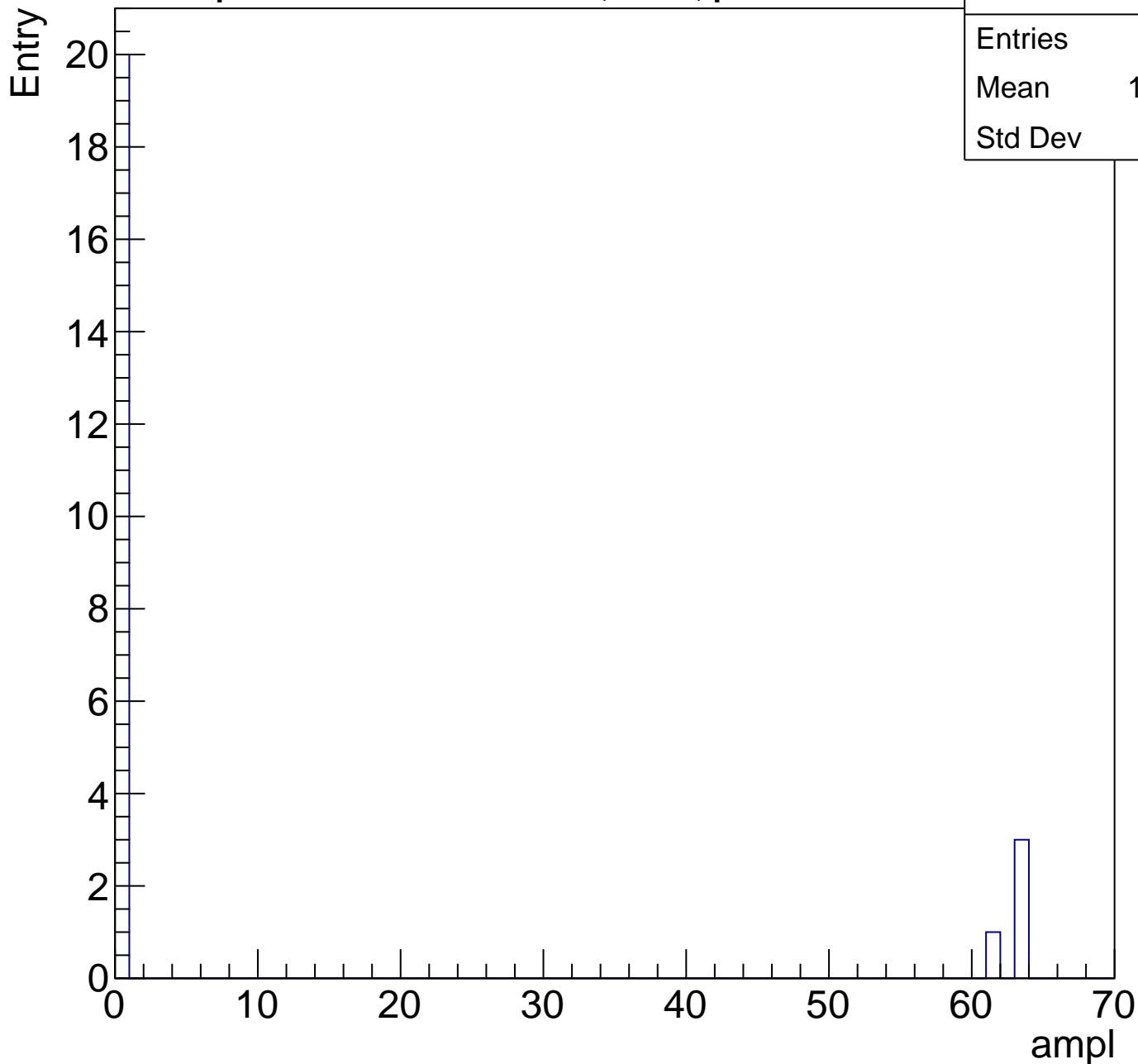
Entry



B1L103S, U18-ch105, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	24
Mean	10.42
Std Dev	23.3



B1L103S, U18-ch106, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	102
Mean	26.25
Std Dev	7.692

Gaus mean : 28.4248

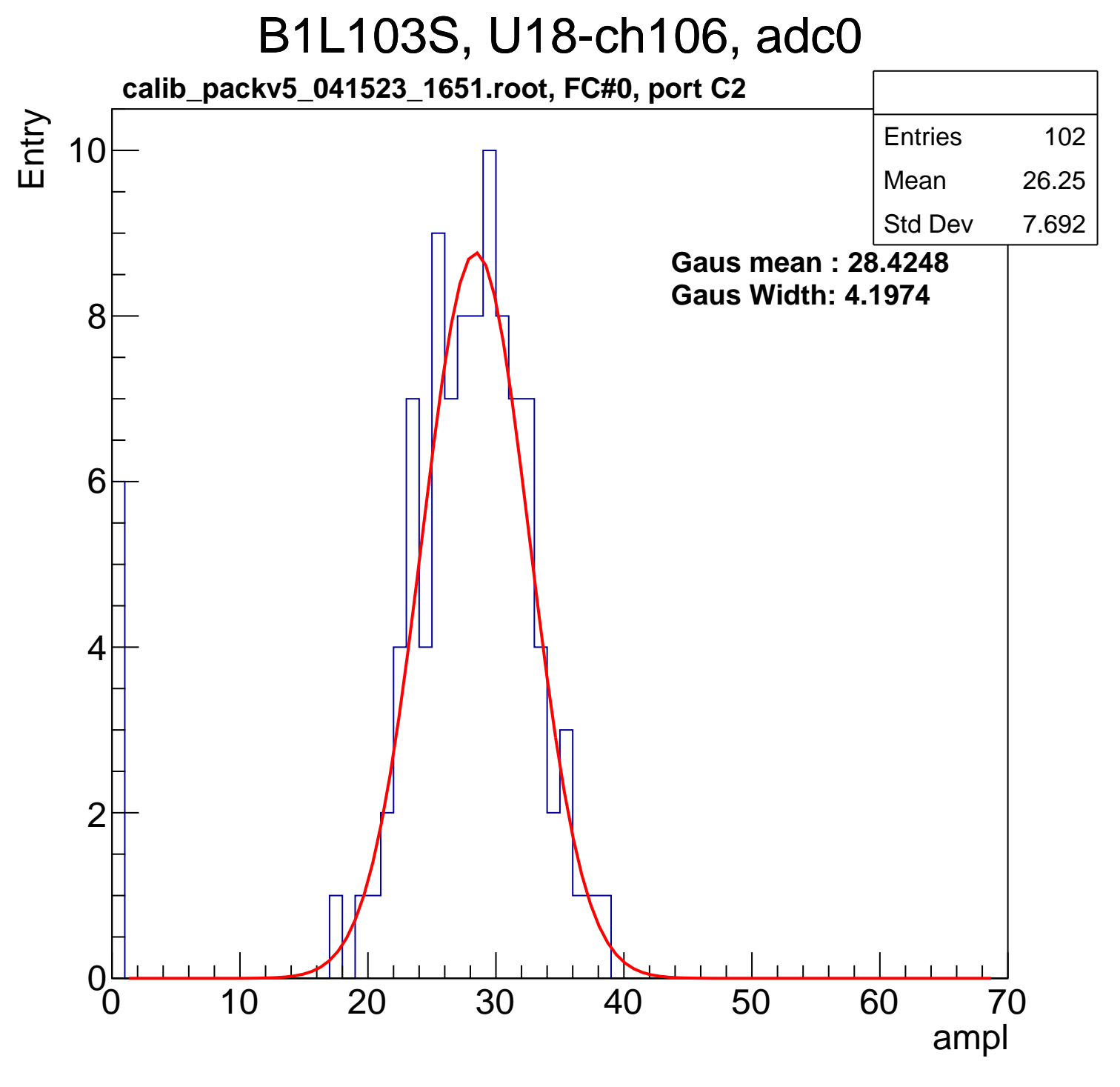
Gaus Width: 4.1974

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch106, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	29.2
Std Dev	13.99

Gaus mean : 35.6817

Gaus Width: 3.9452

Entry

10

8

6

4

2

0

0

10

20

30

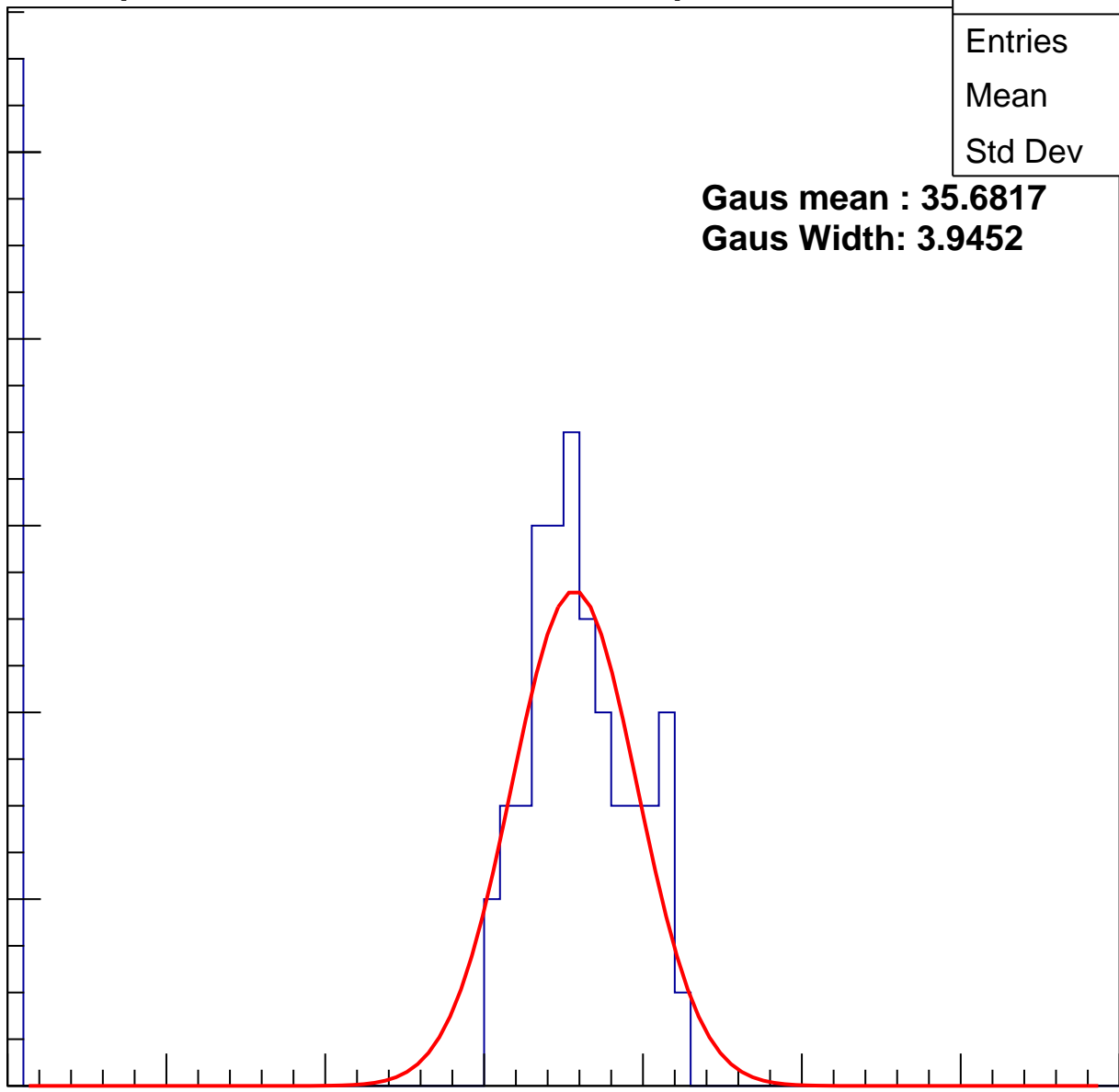
40

50

60

70

ampl



B1L103S, U18-ch106, adc2

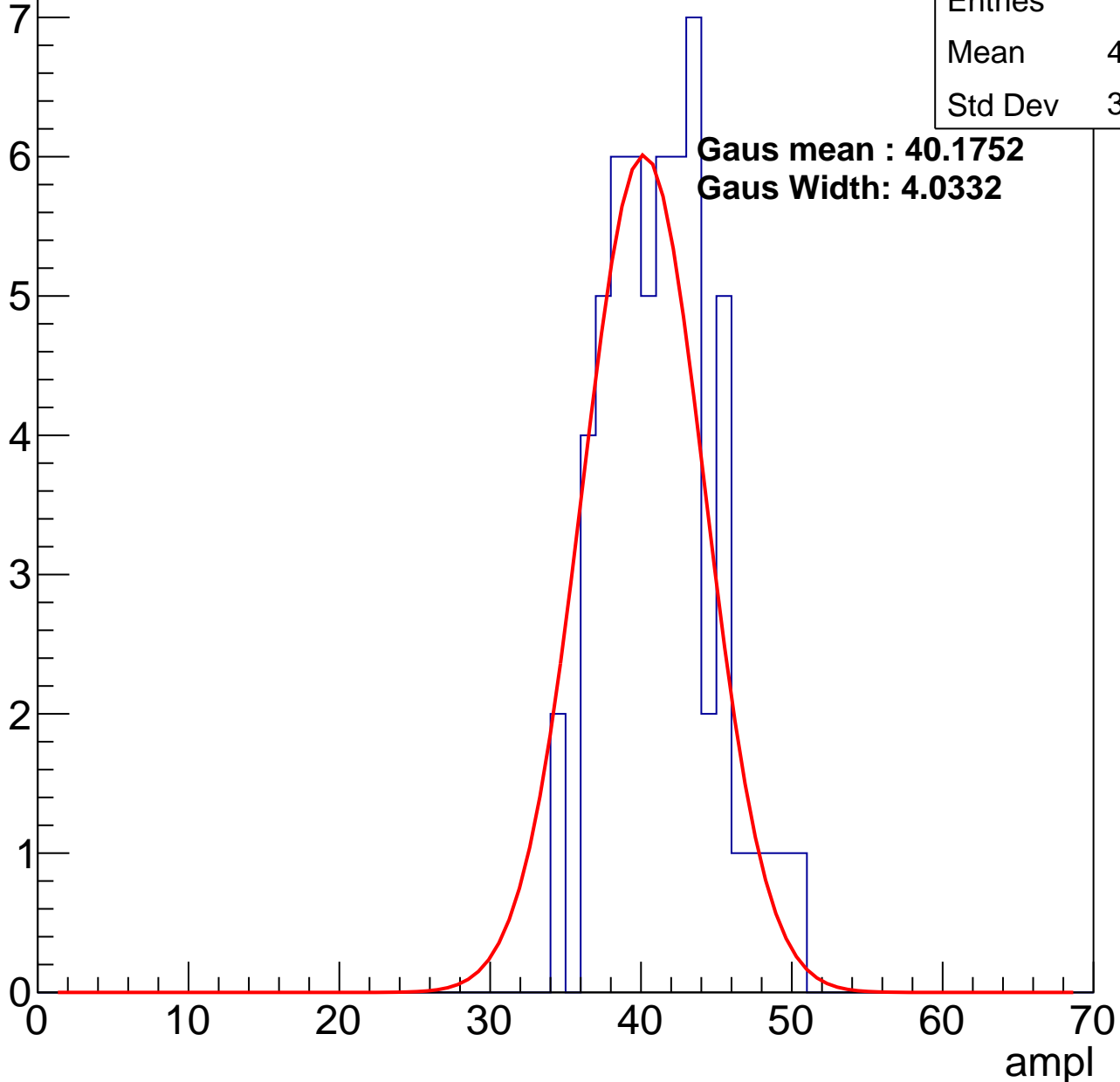
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	40.86
Std Dev	3.558

Gaus mean : 40.1752

Gaus Width: 4.0332

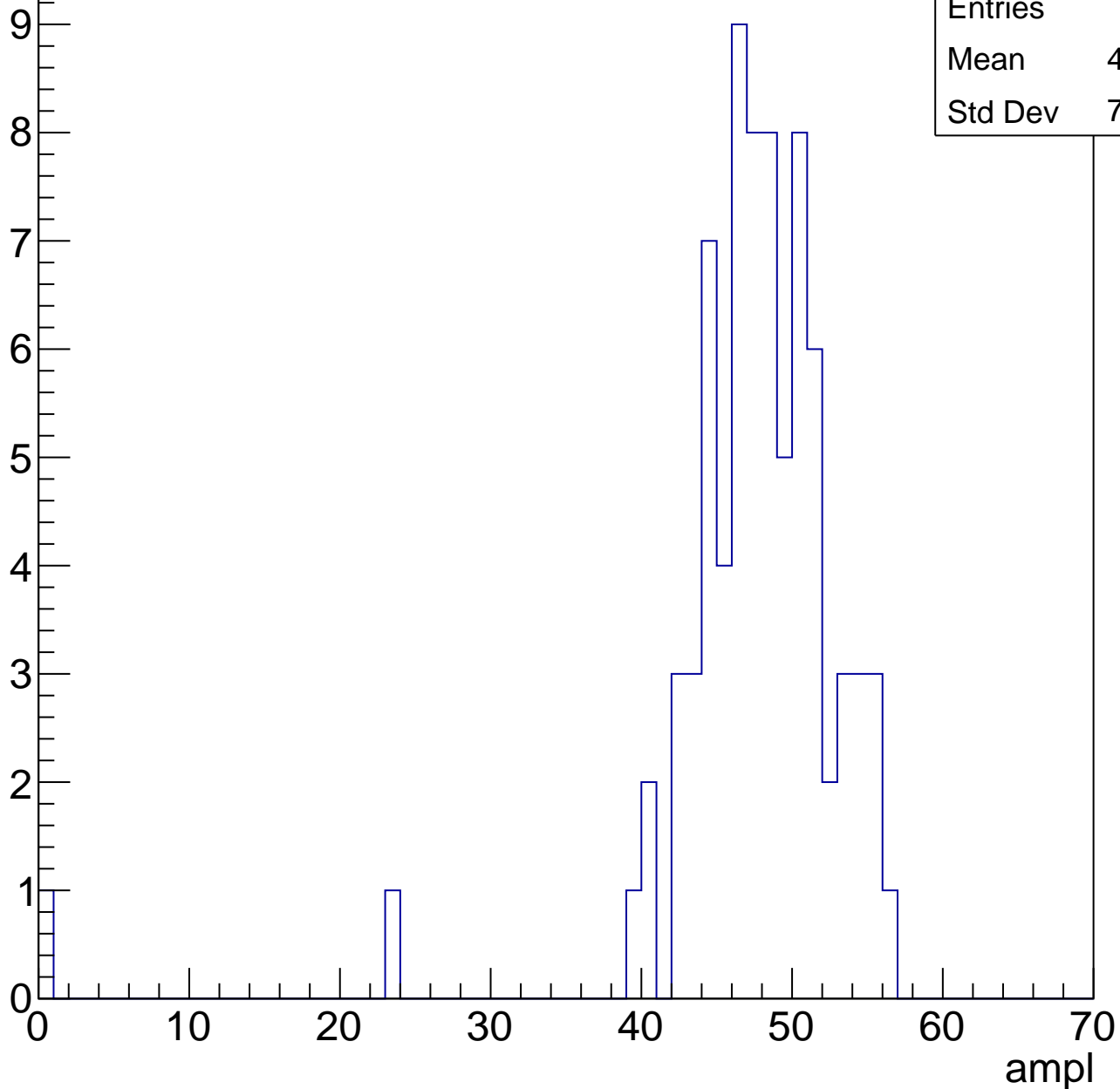


B1L103S, U18-ch106, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	78
Mean	46.87
Std Dev	7.099

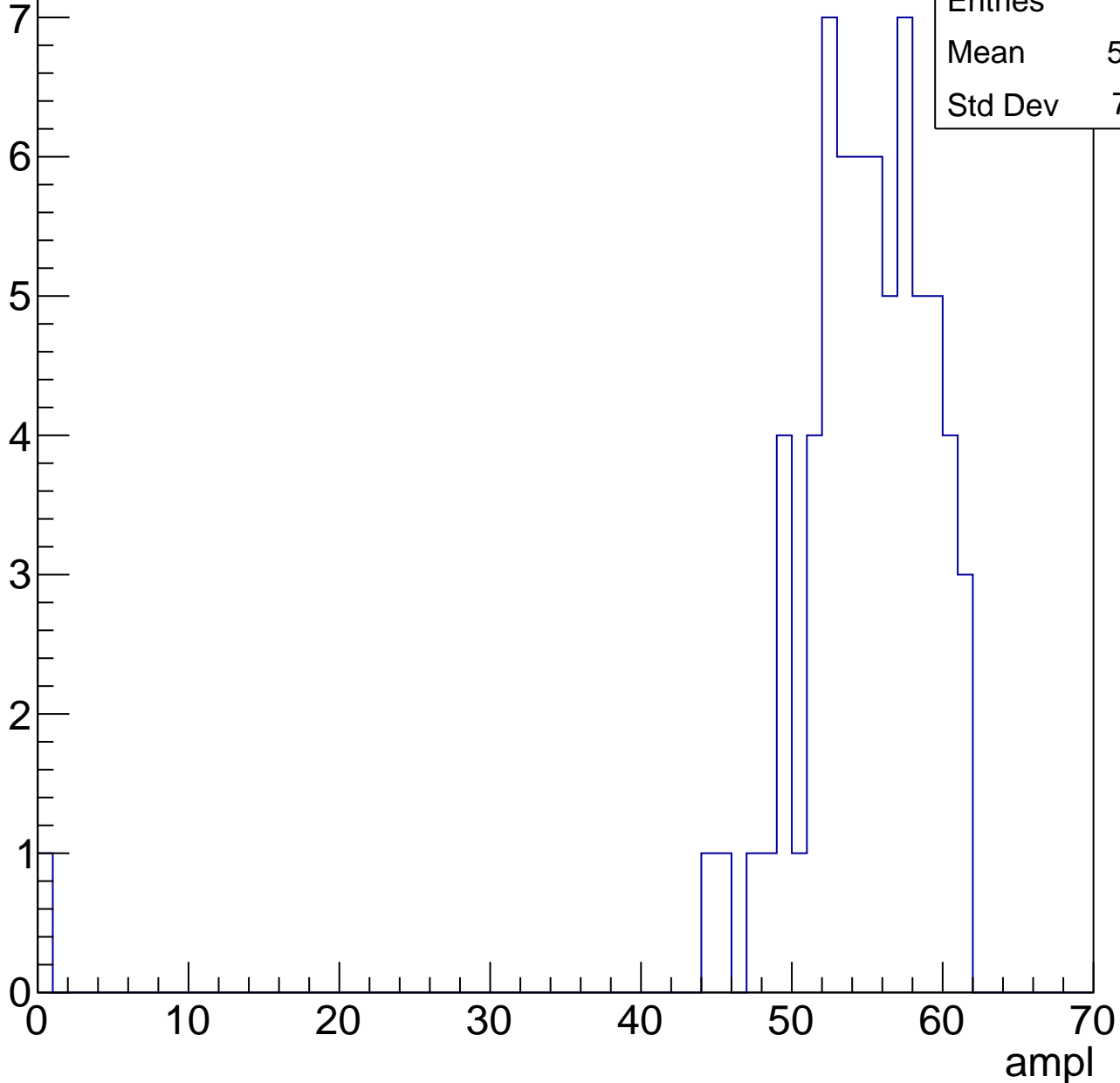


B1L103S, U18-ch106, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	53.78
Std Dev	7.621

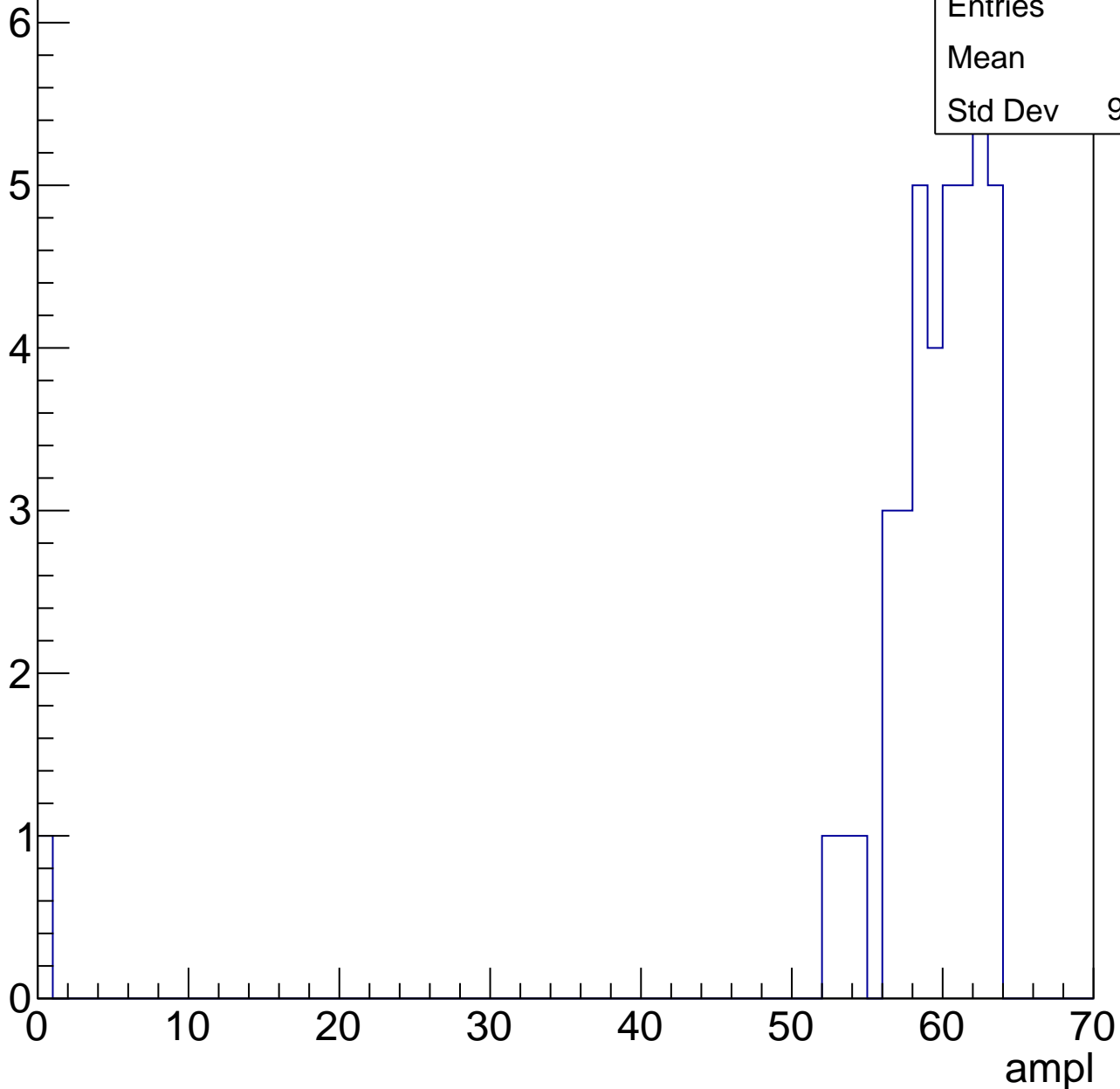


B1L103S, U18-ch106, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

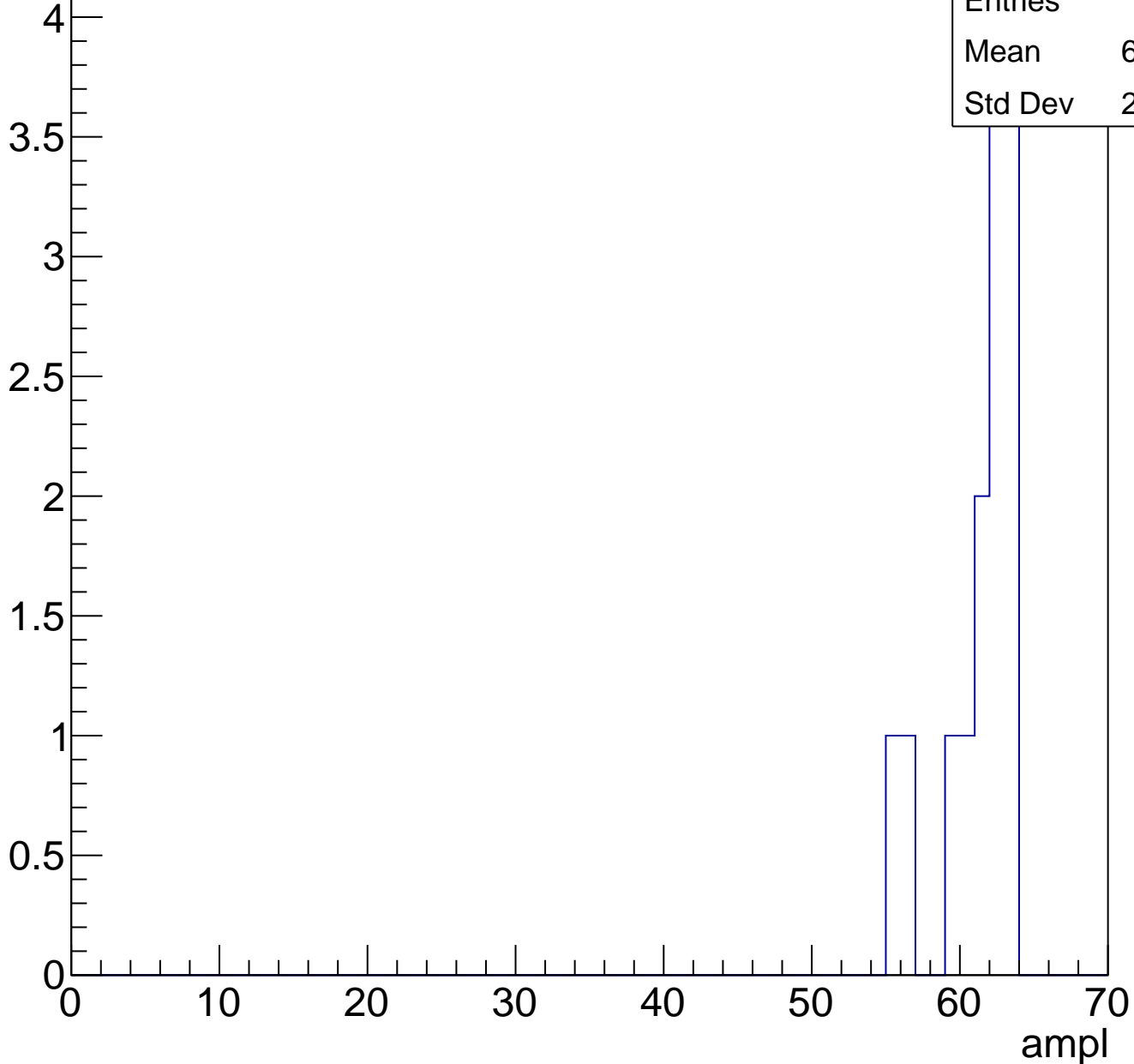
Entries	40
Mean	57.9
Std Dev	9.677



B1L103S, U18-ch106, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch106, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



B1L103S, U18-ch107, adc0

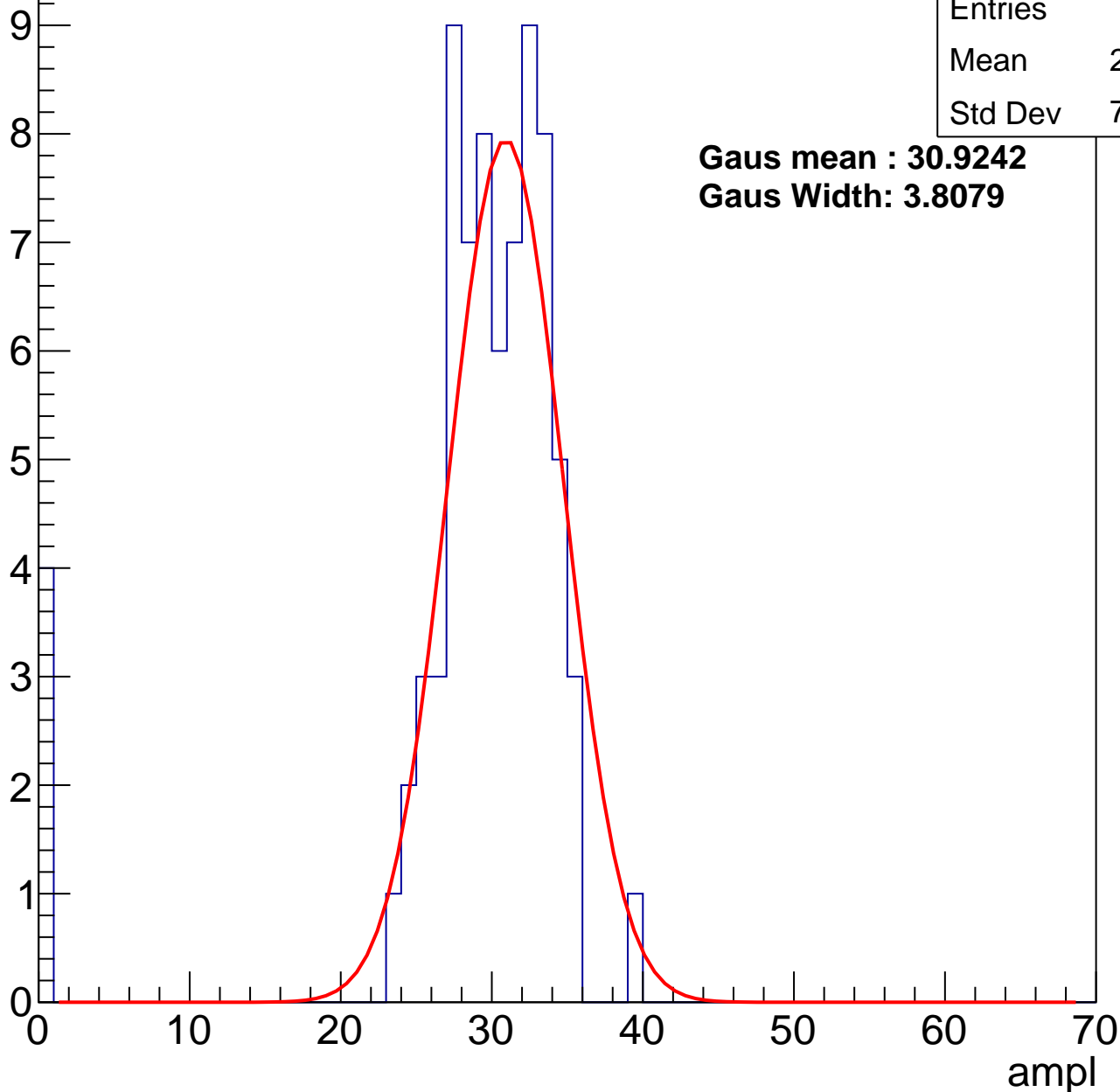
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	28.39
Std Dev	7.359

Gaus mean : 30.9242

Gaus Width: 3.8079



B1L103S, U18-ch107, adc1

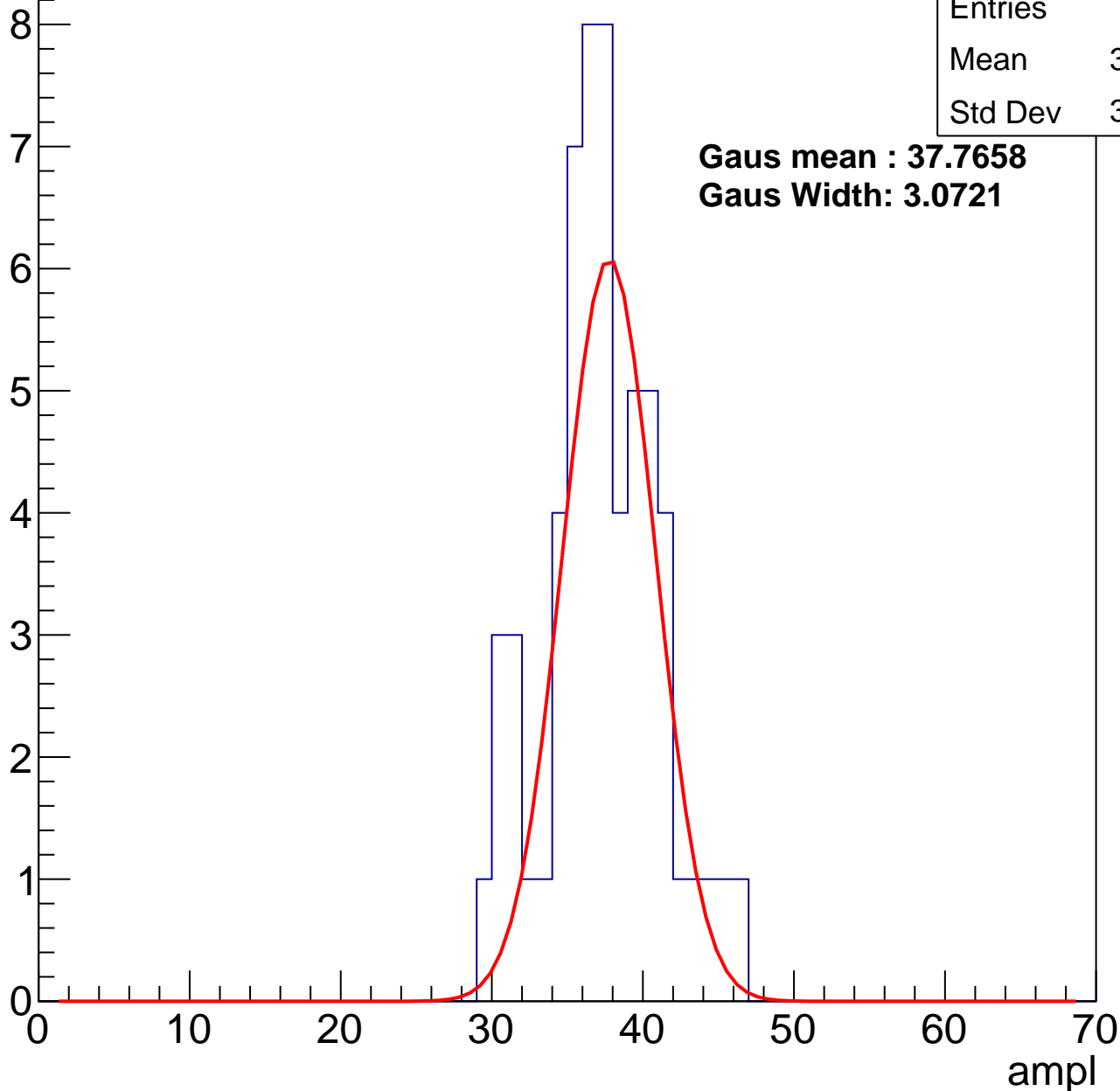
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	36.83
Std Dev	3.724

Gaus mean : 37.7658

Gaus Width: 3.0721



B1L103S, U18-ch107, adc2

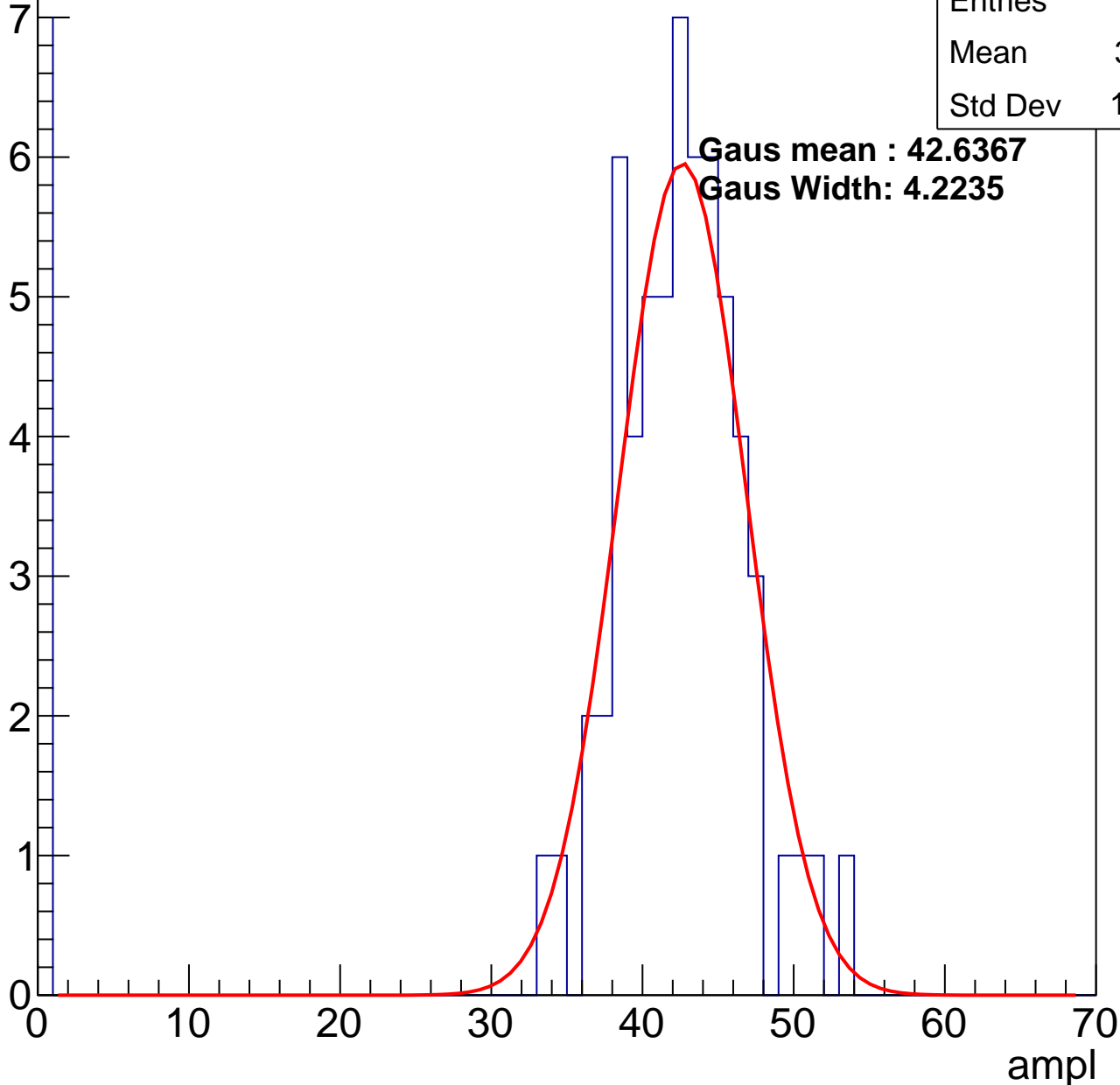
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	37.81
Std Dev	13.34

Gaus mean : 42.6367

Gaus Width: 4.2235



B1L103S, U18-ch107, adc3

calib_packv5_041523_1651.root, FC#0, port C2

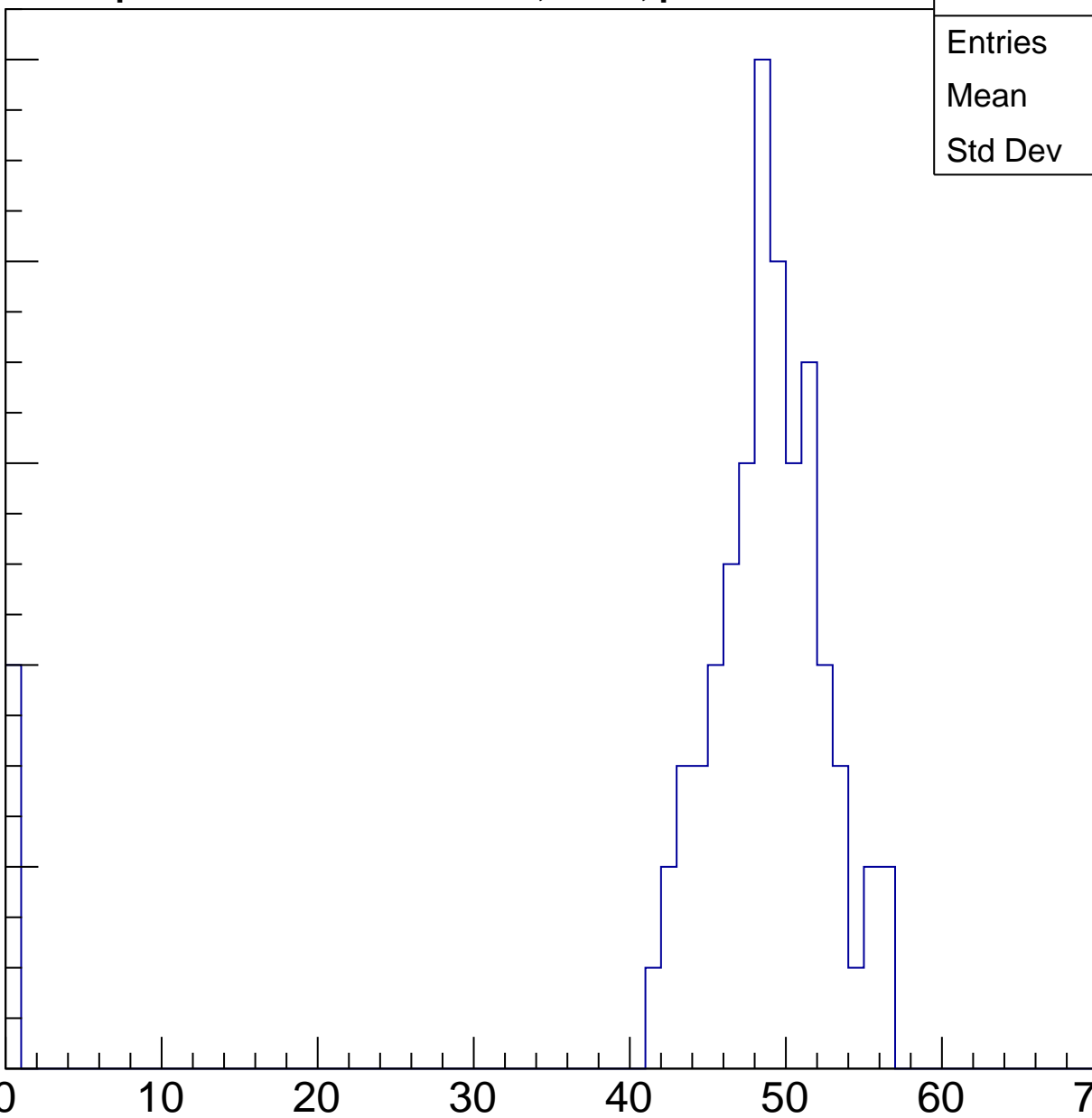
Entries	71
Mean	45.77
Std Dev	11.67

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

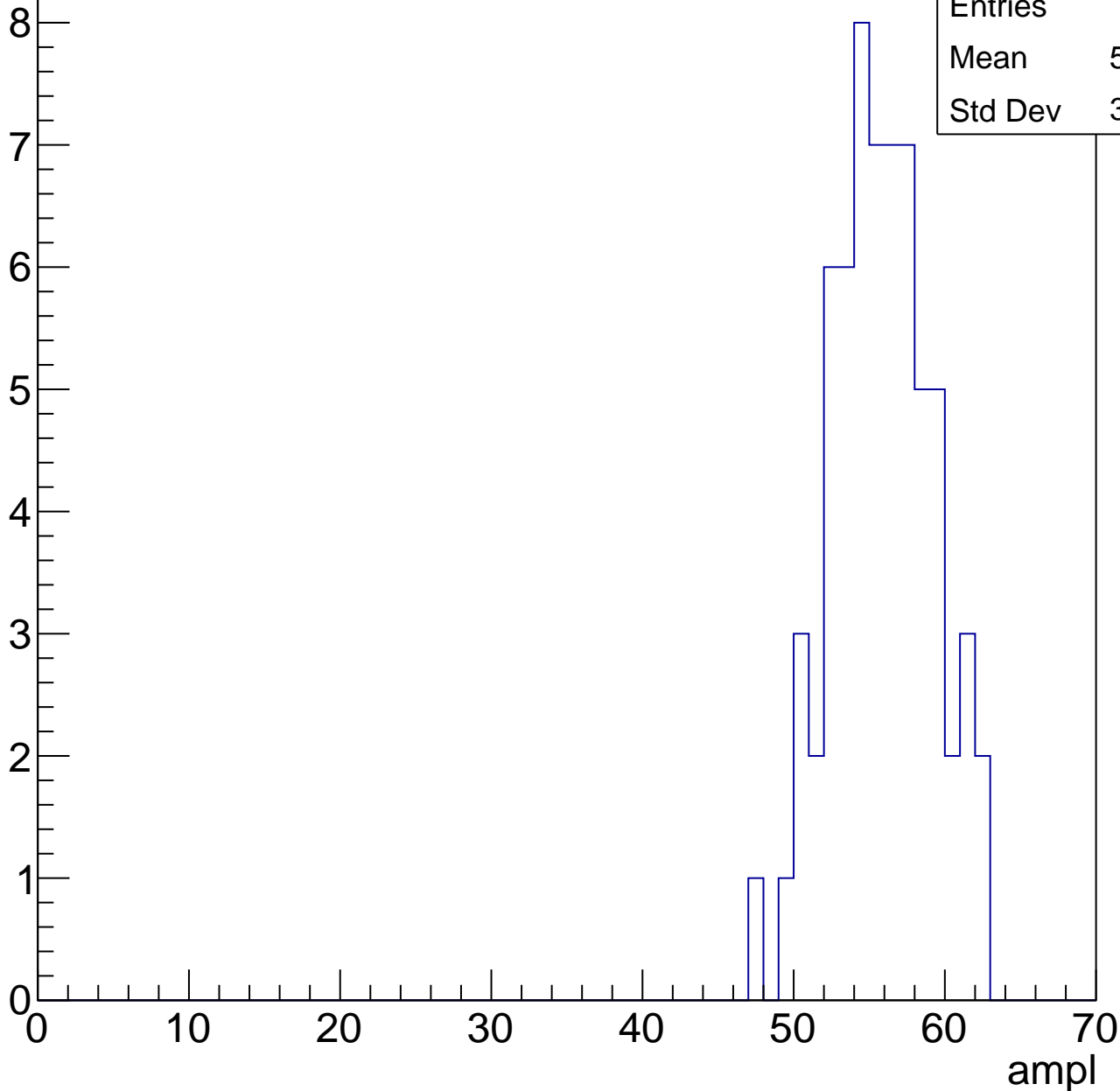


B1L103S, U18-ch107, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	55.35
Std Dev	3.284

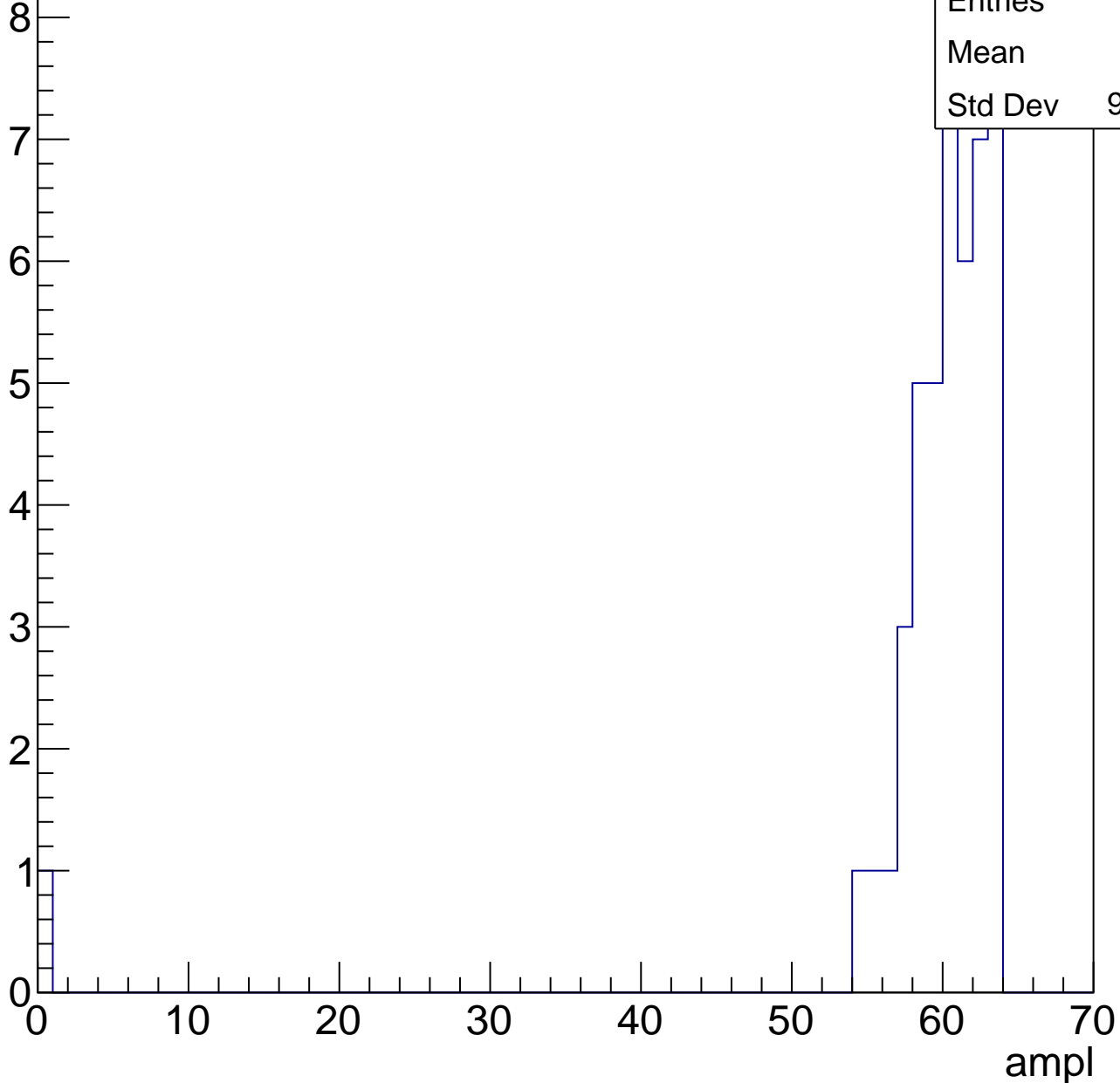


B1L103S, U18-ch107, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.8
Std Dev	9.052



B1L103S, U18-ch107, adc6

calib_packv5_041523_1651.root, FC#0, port C2

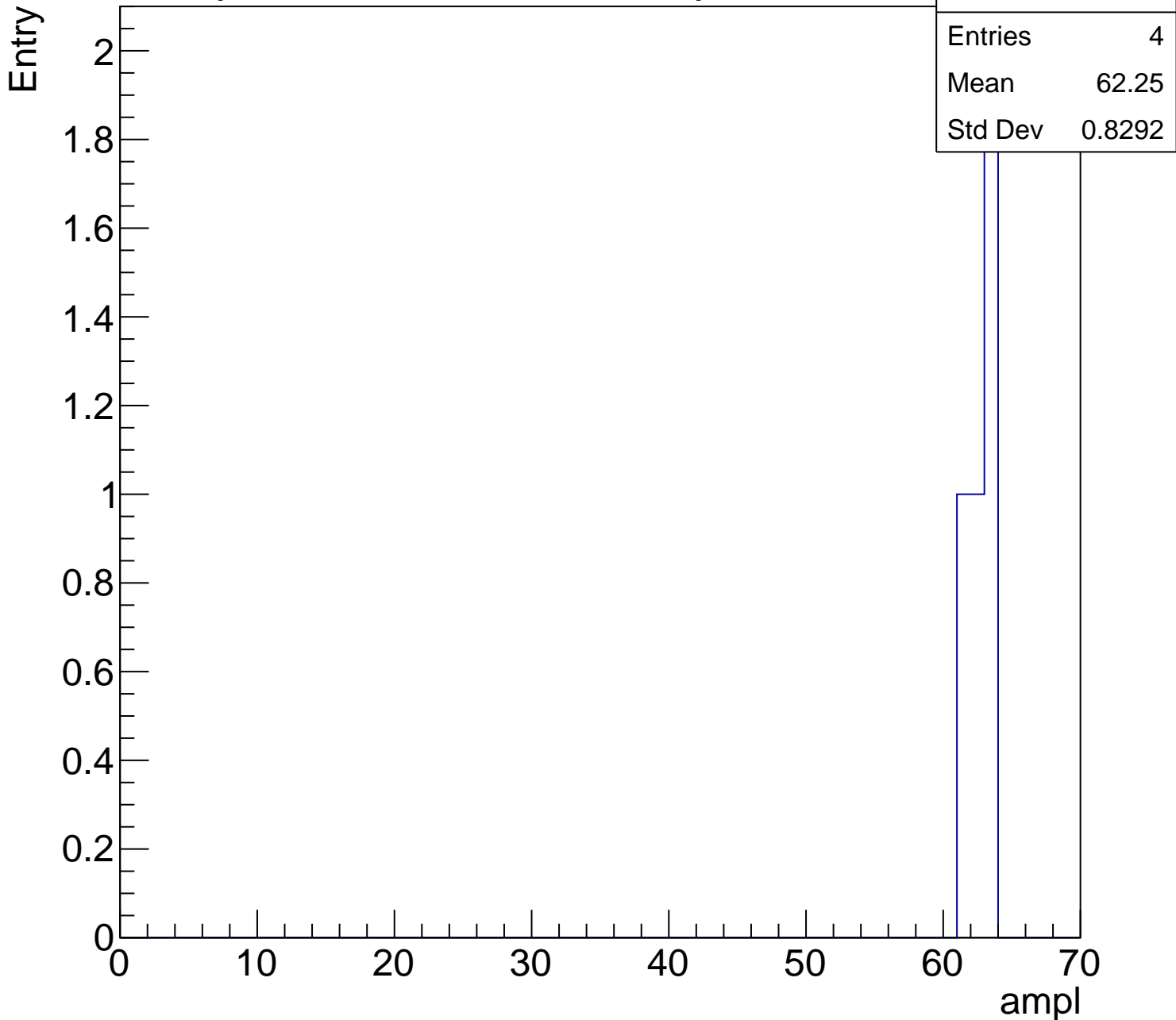
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl

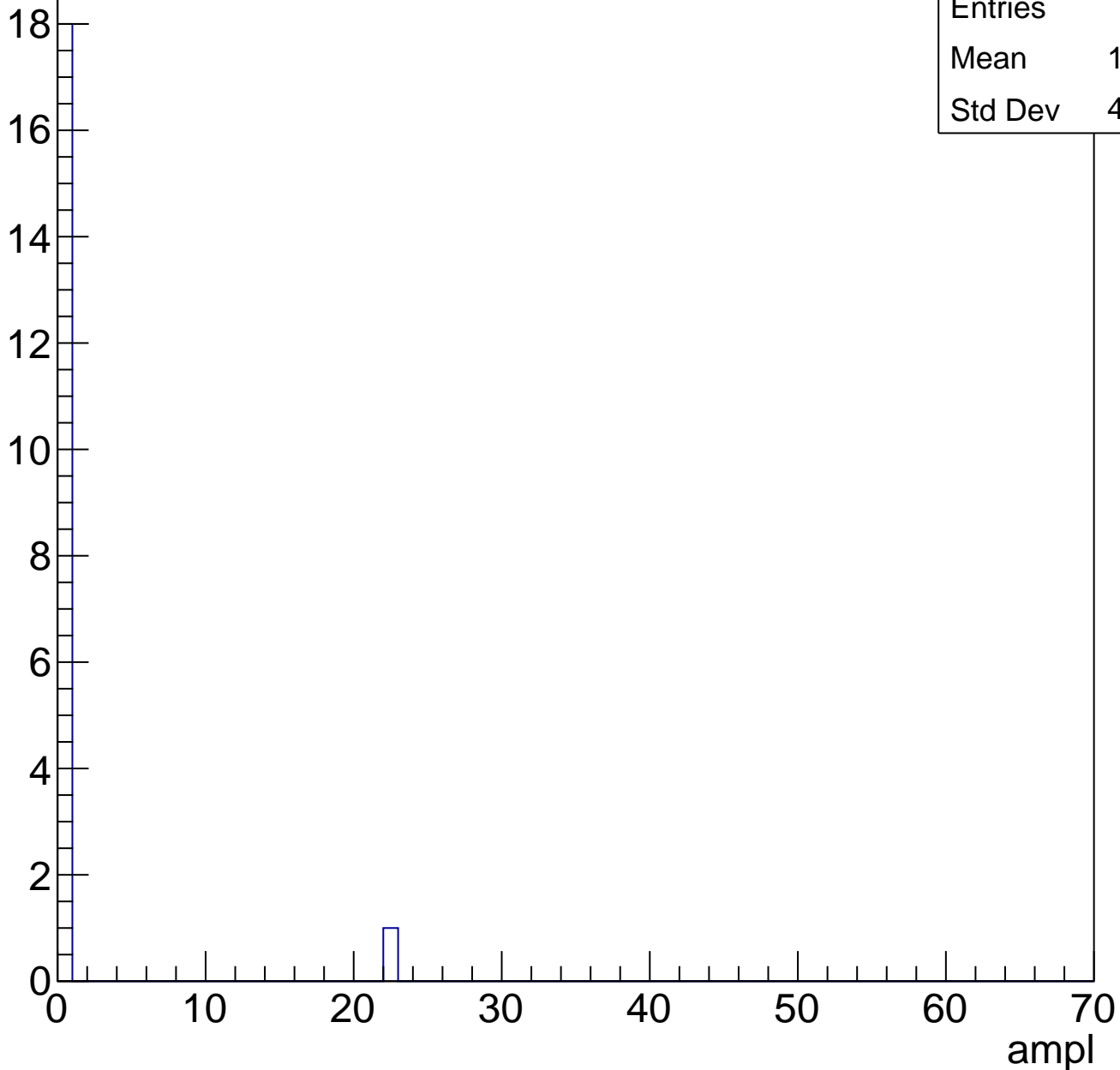


B1L103S, U18-ch107, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



B1L103S, U18-ch108, adc0

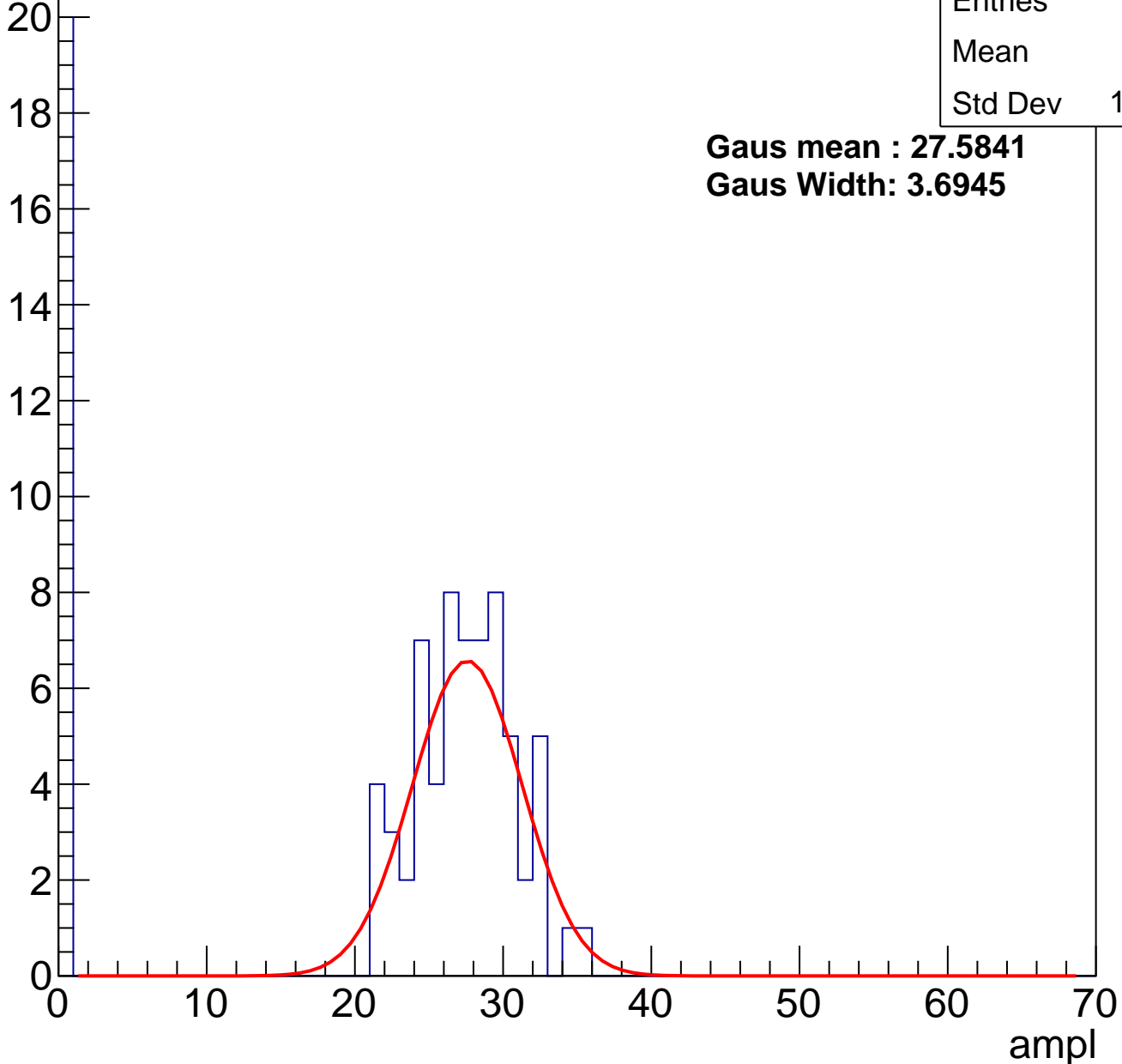
calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	20.6
Std Dev	11.87

Gaus mean : 27.5841

Gaus Width: 3.6945

Entry



B1L103S, U18-ch108, adc1

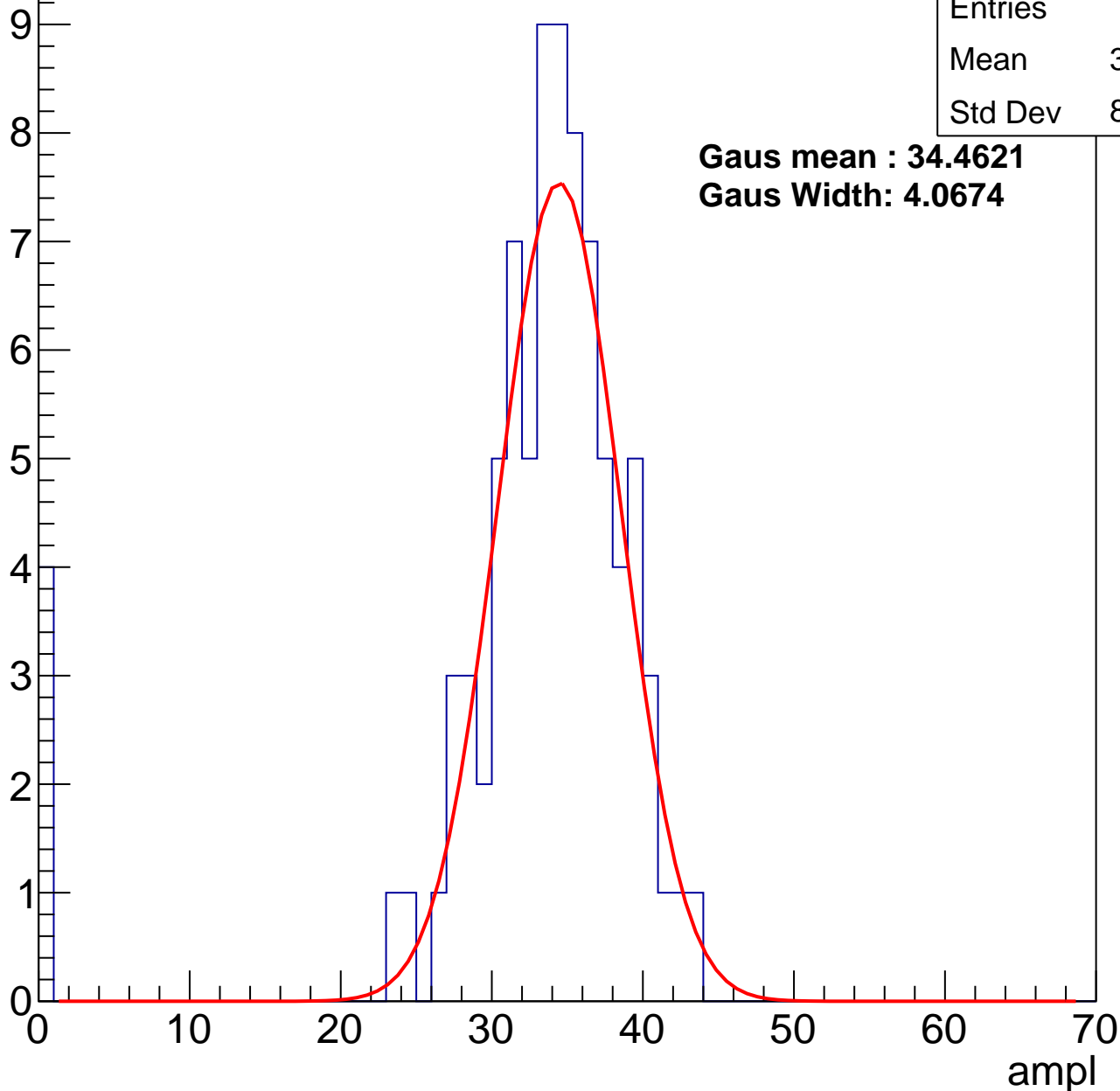
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	32.19
Std Dev	8.174

Gaus mean : 34.4621

Gaus Width: 4.0674



B1L103S, U18-ch108, adc2

calib_packv5_041523_1651.root, FC#0, port C2

Entries	61
Mean	33.92
Std Dev	15.31

Gaus mean : 40.9304

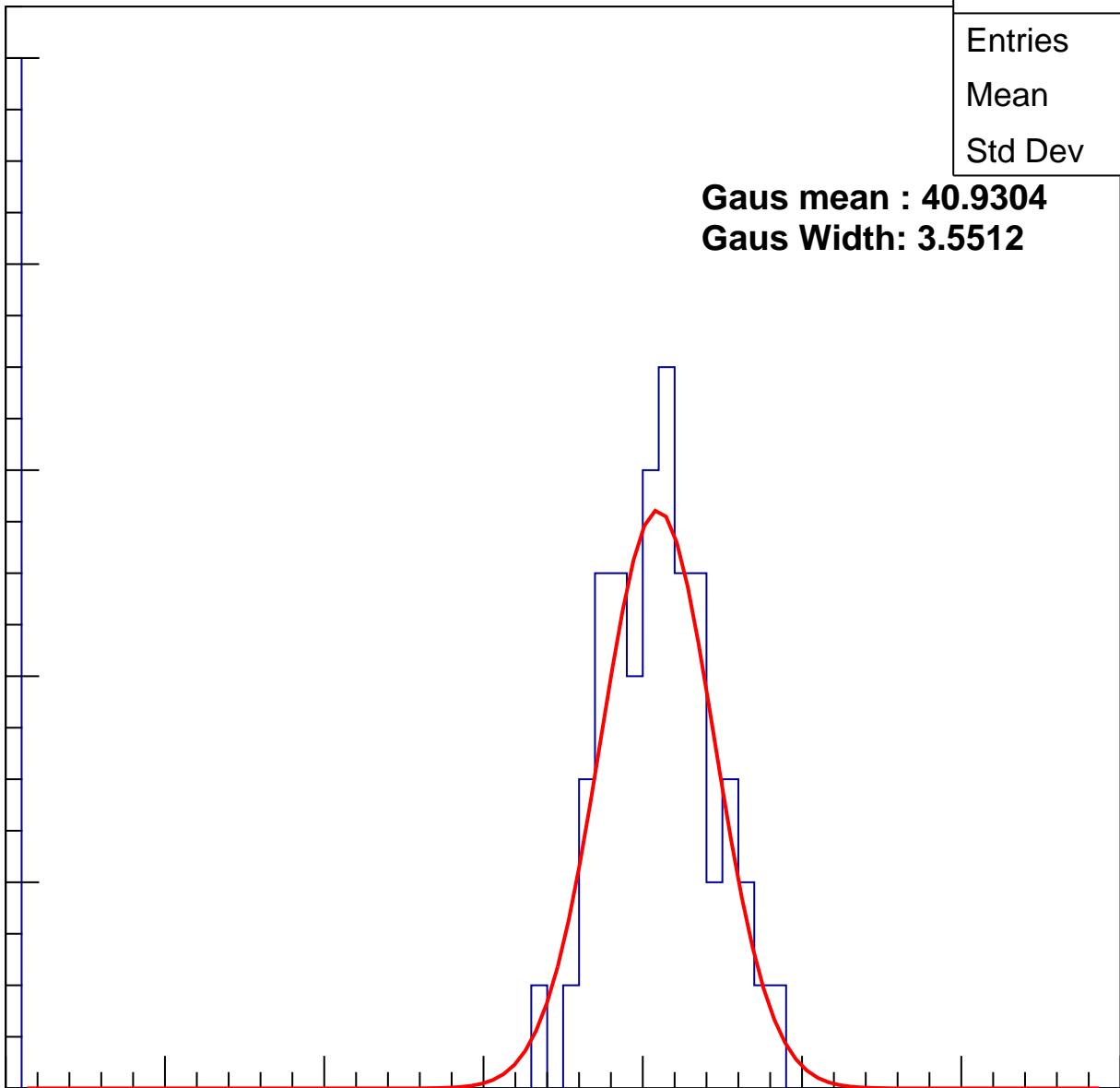
Gaus Width: 3.5512

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

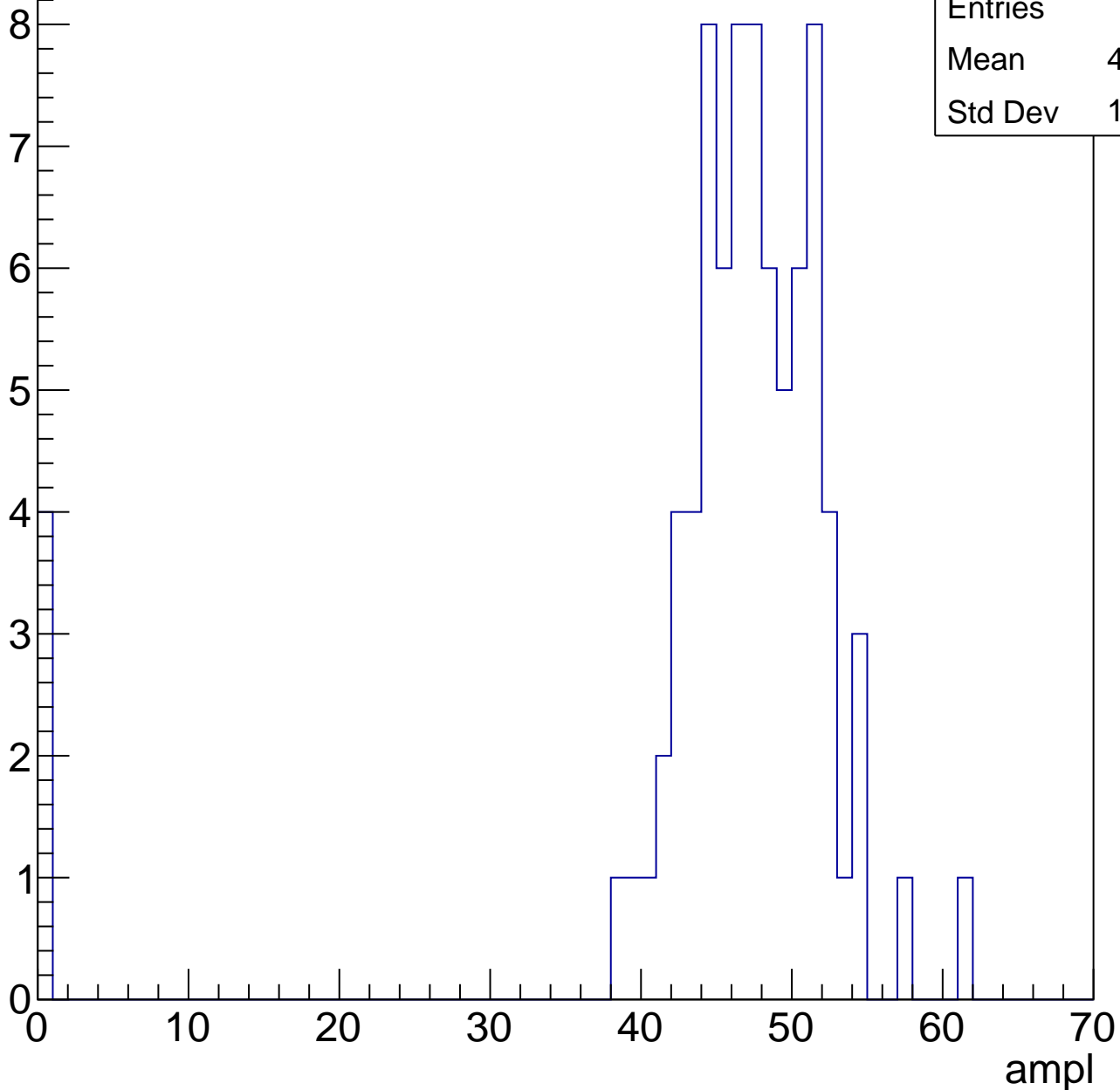


B1L103S, U18-ch108, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	82
Mean	44.96
Std Dev	10.94

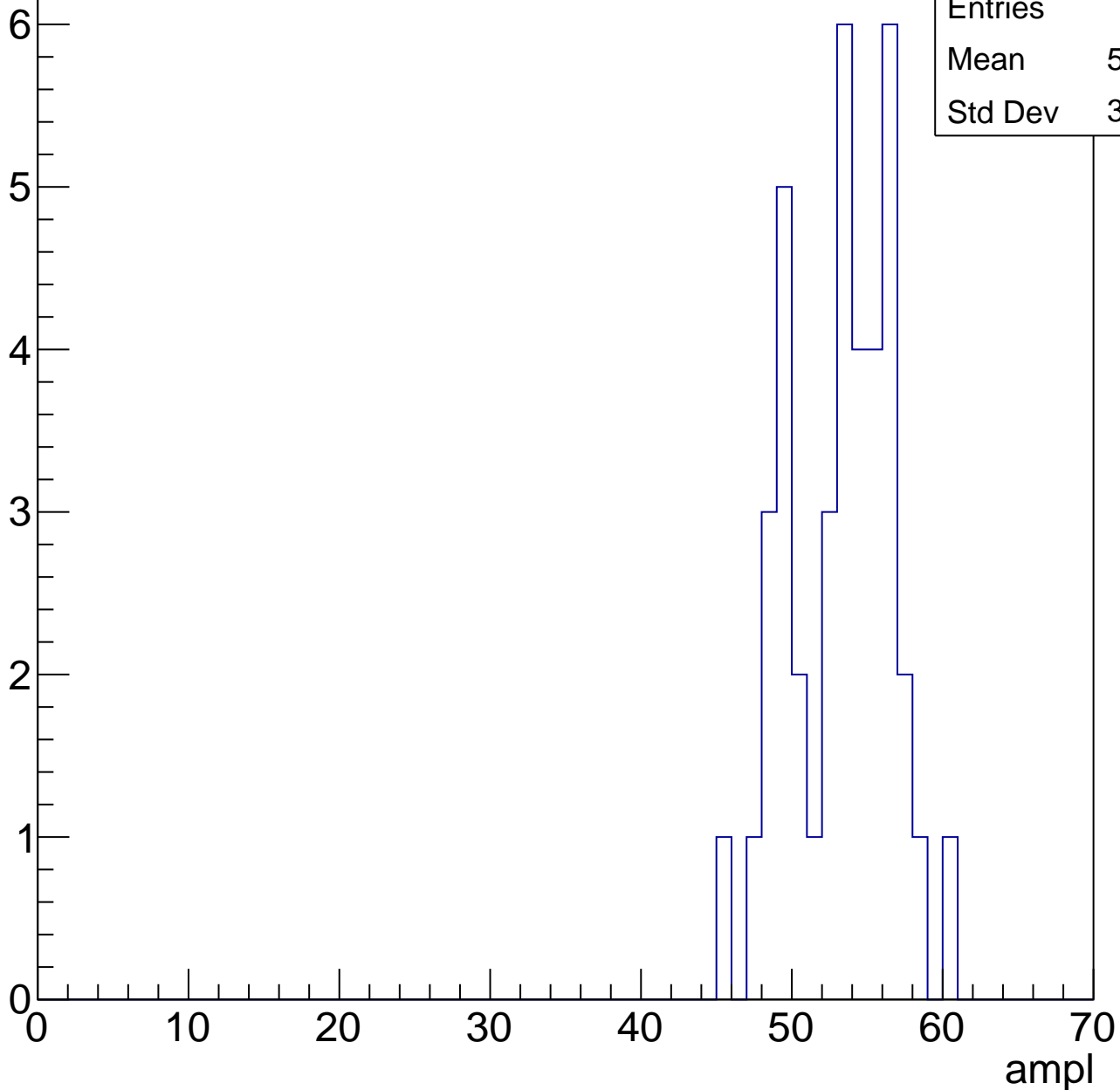


B1L103S, U18-ch108, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	40
Mean	52.75
Std Dev	3.382

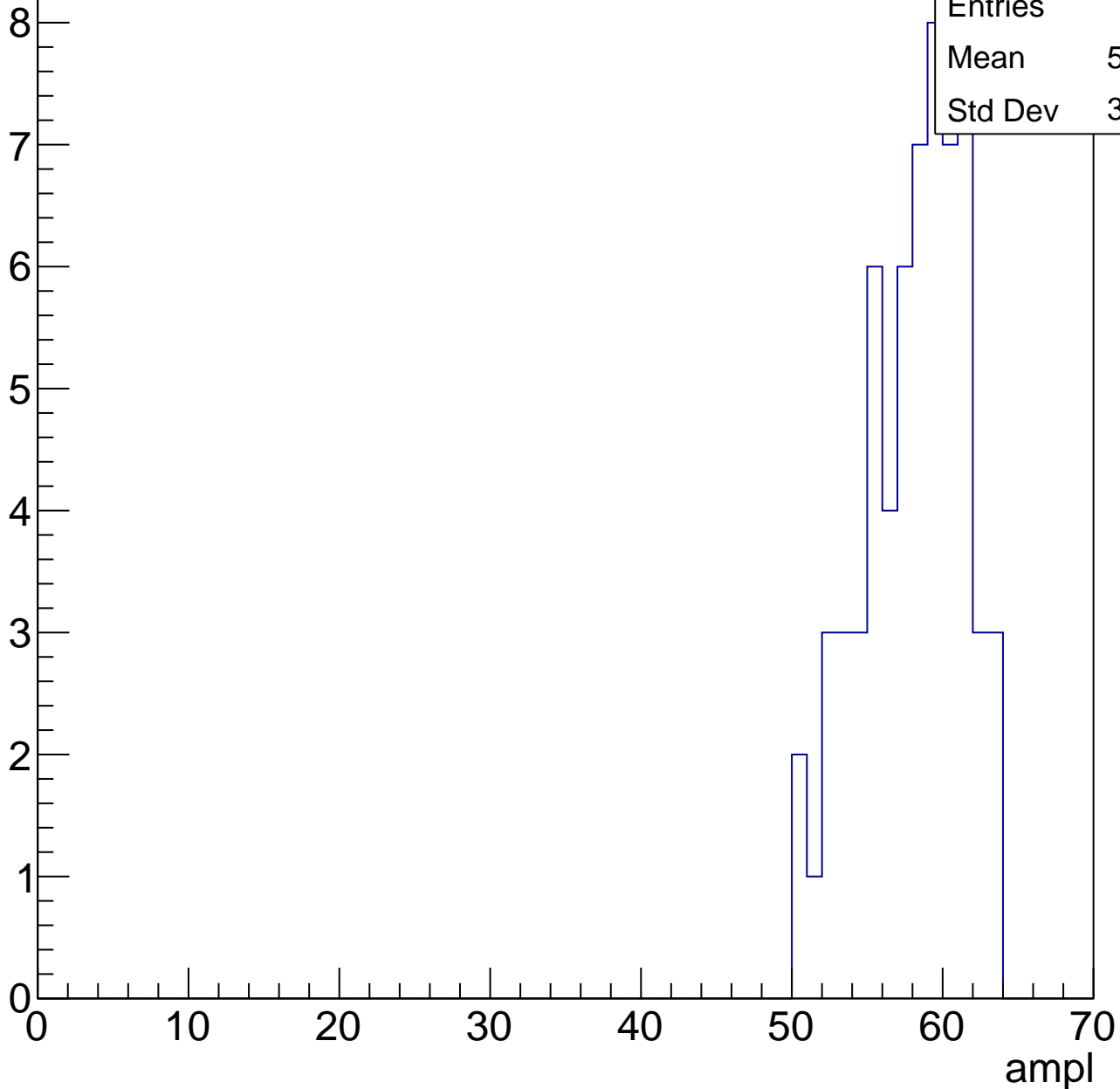


B1L103S, U18-ch108, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	57.58
Std Dev	3.316

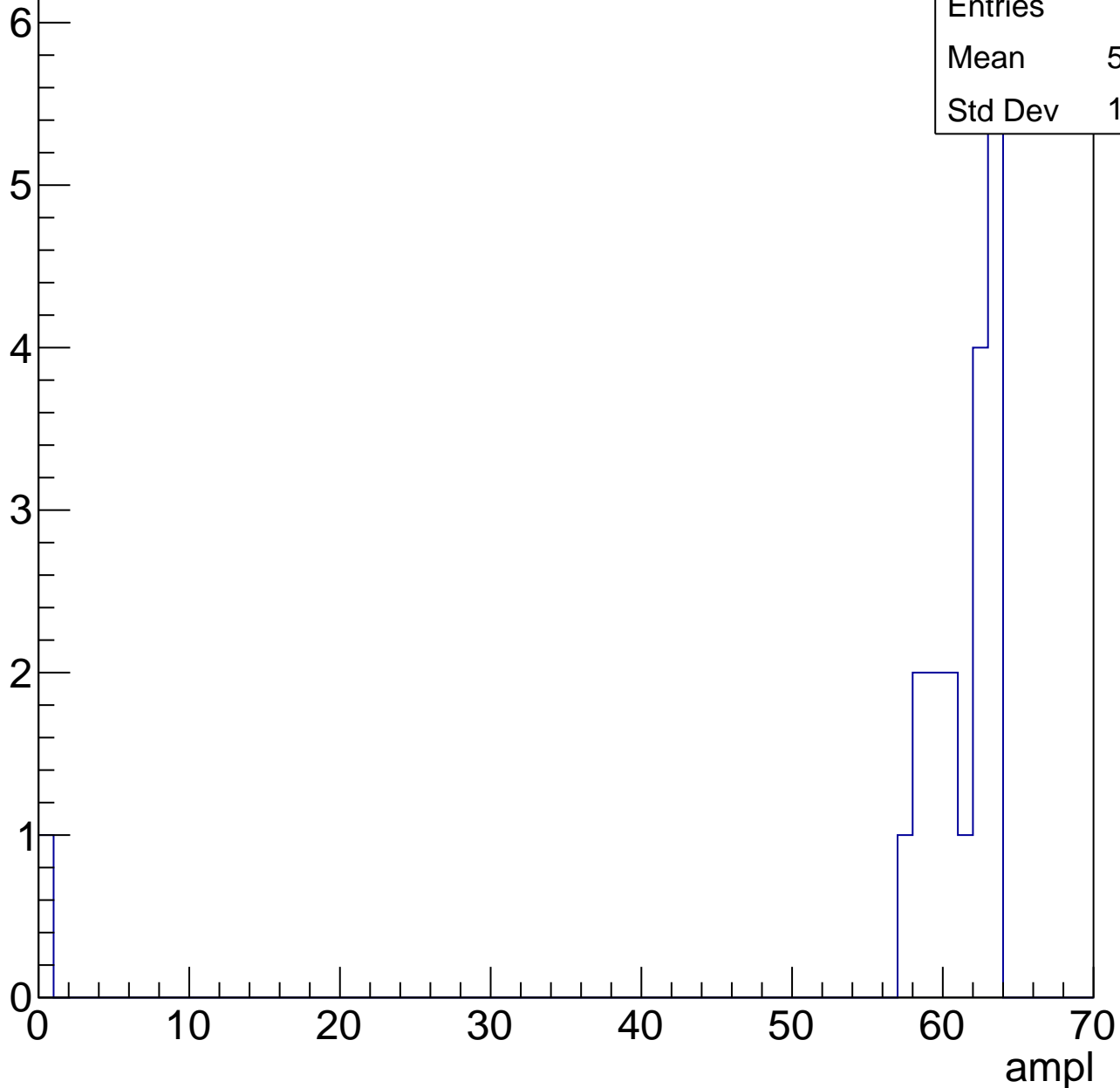


B1L103S, U18-ch108, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	19
Mean	57.79
Std Dev	13.76

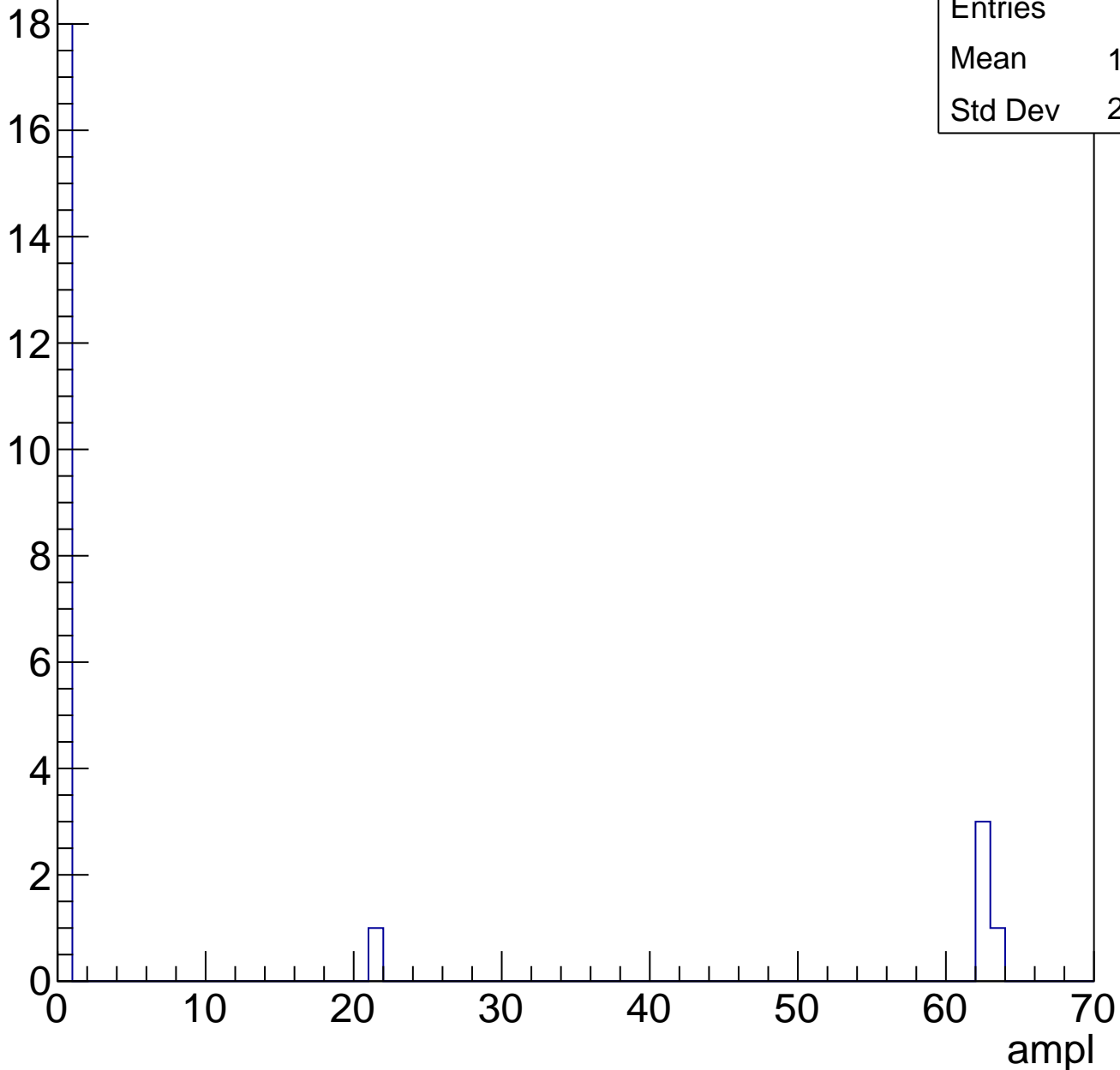


B1L103S, U18-ch108, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	11.74
Std Dev	23.57

Entry



B1L103S, U18-ch109, adc0

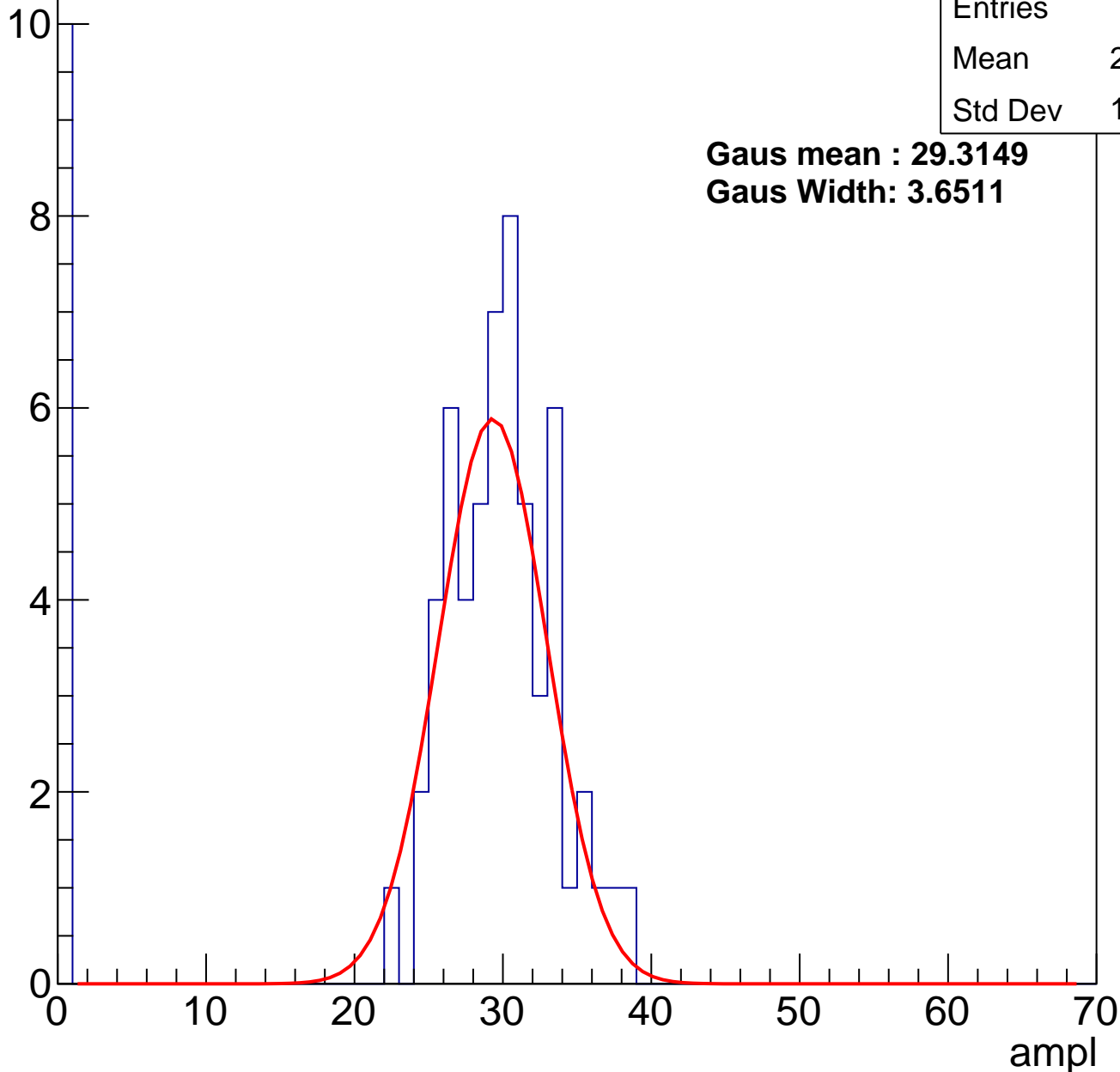
calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	25.09
Std Dev	10.97

Gaus mean : 29.3149

Gaus Width: 3.6511

Entry



B1L103S, U18-ch109, adc1

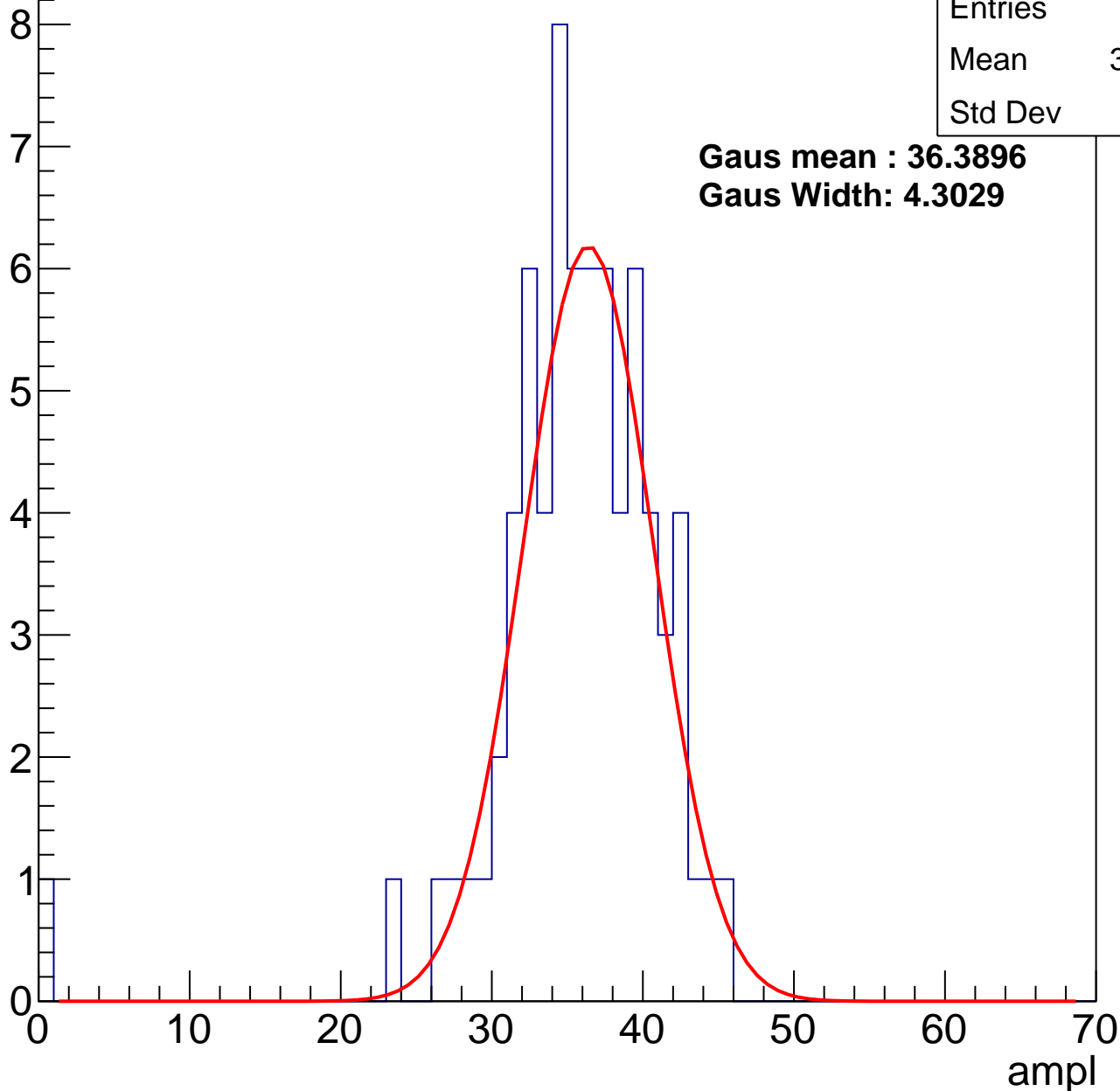
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	35.14
Std Dev	6.01

Gaus mean : 36.3896

Gaus Width: 4.3029



B1L103S, U18-ch109, adc2

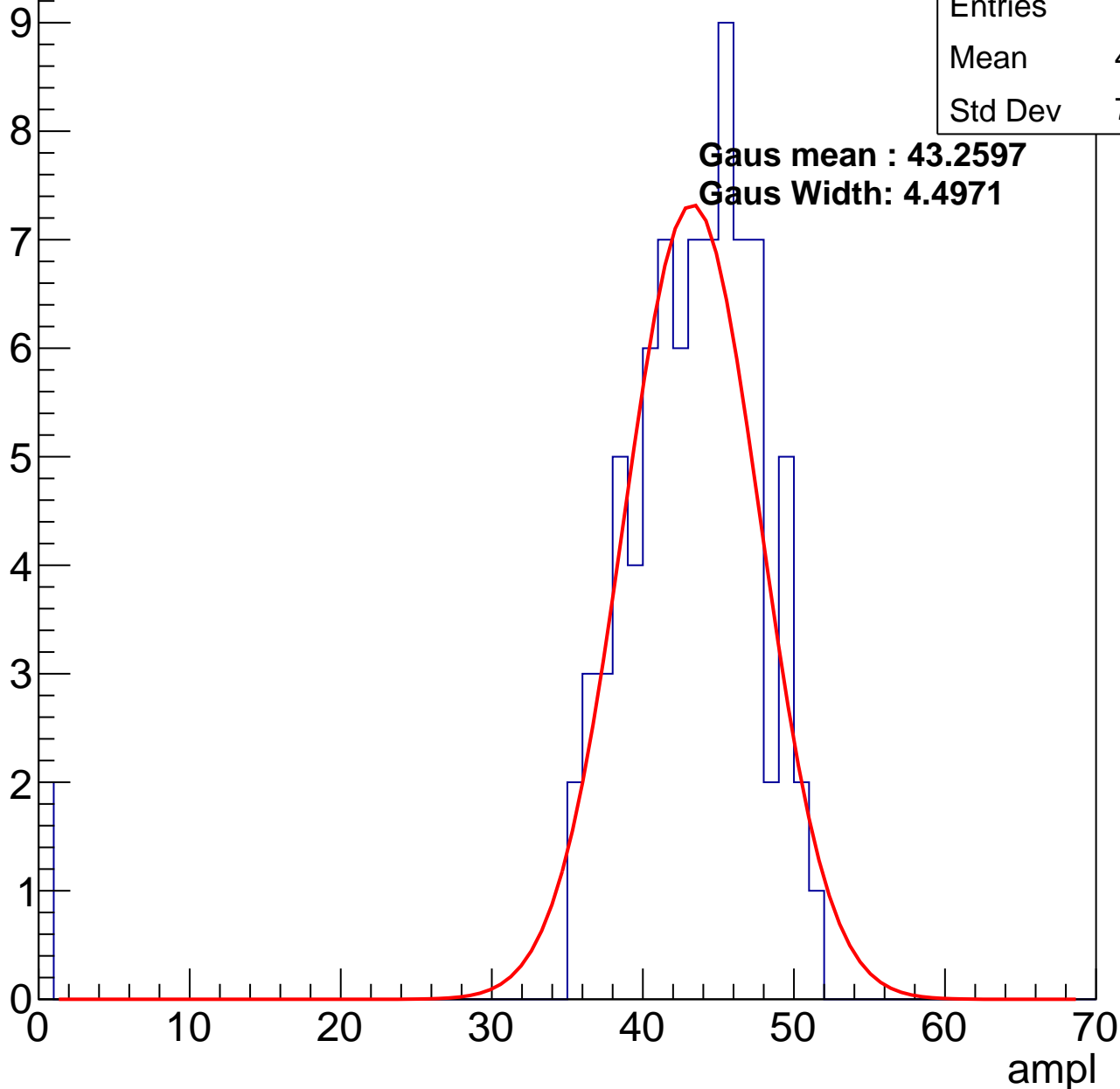
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	85
Mean	42.01
Std Dev	7.581

Gaus mean : 43.2597

Gaus Width: 4.4971



B1L103S, U18-ch109, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	67
Mean	42.34
Std Dev	19.01

Entry

10

8

6

4

2

0

0

10

20

30

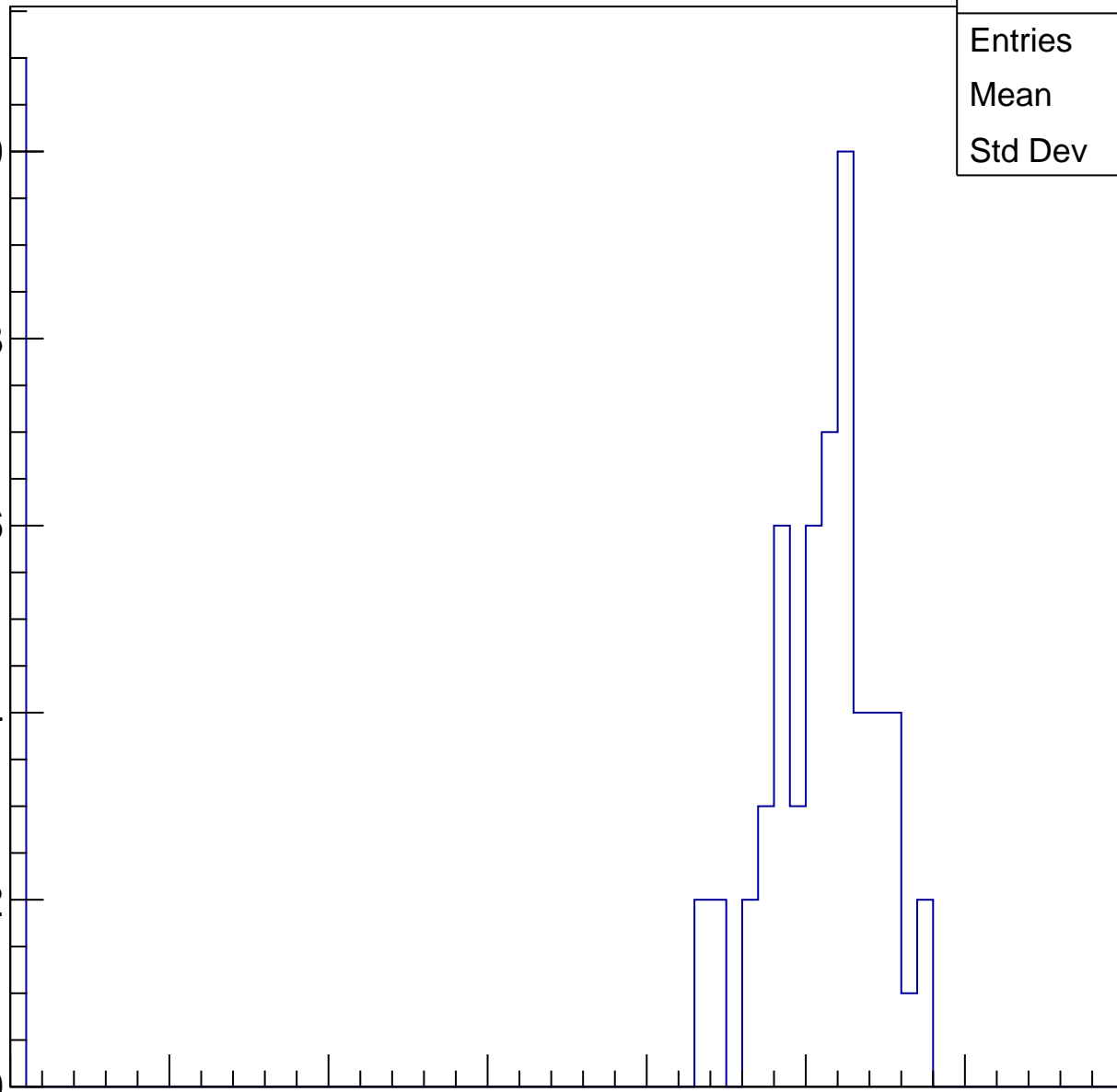
40

50

60

70

ampl

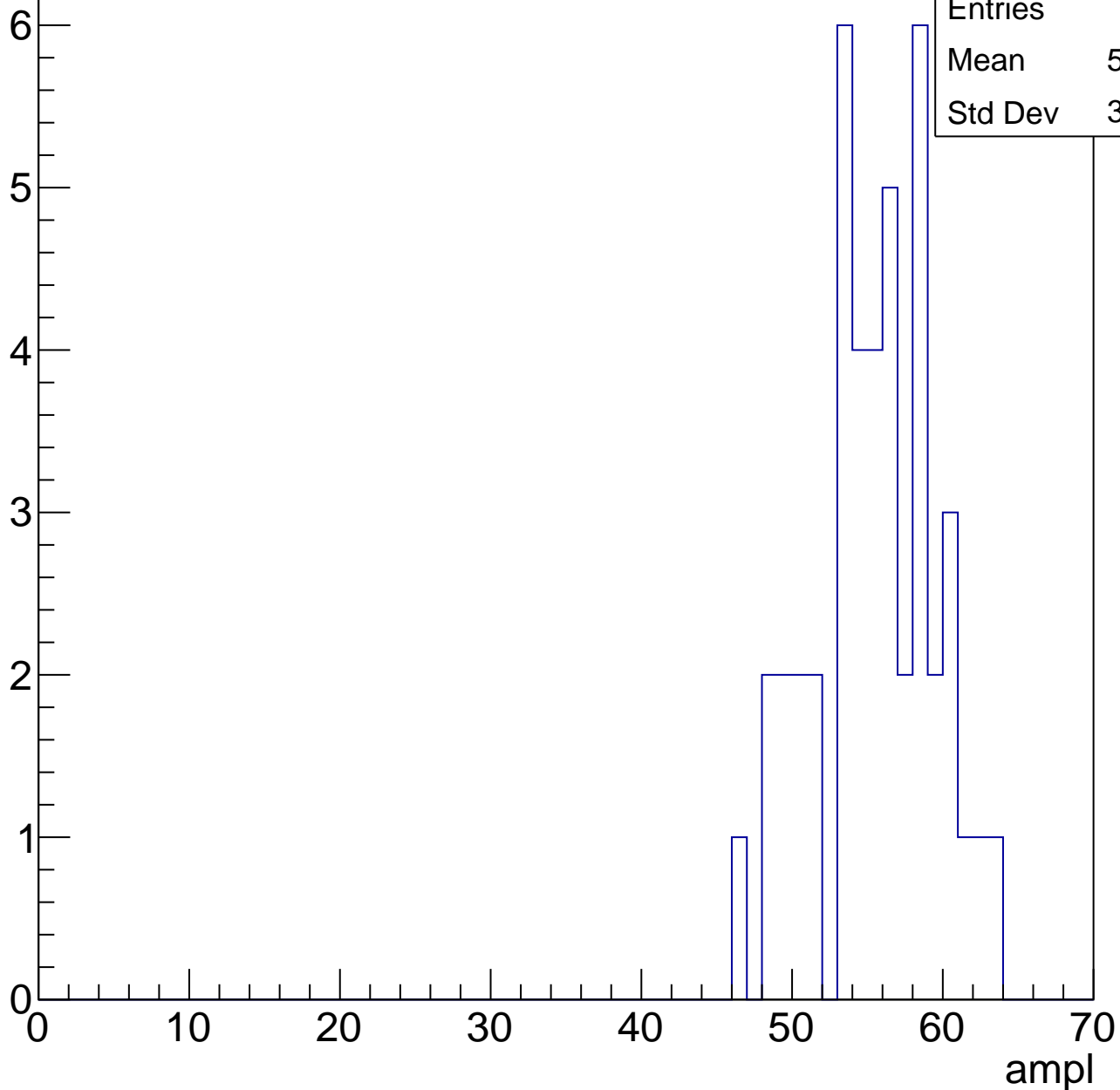


B1L103S, U18-ch109, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

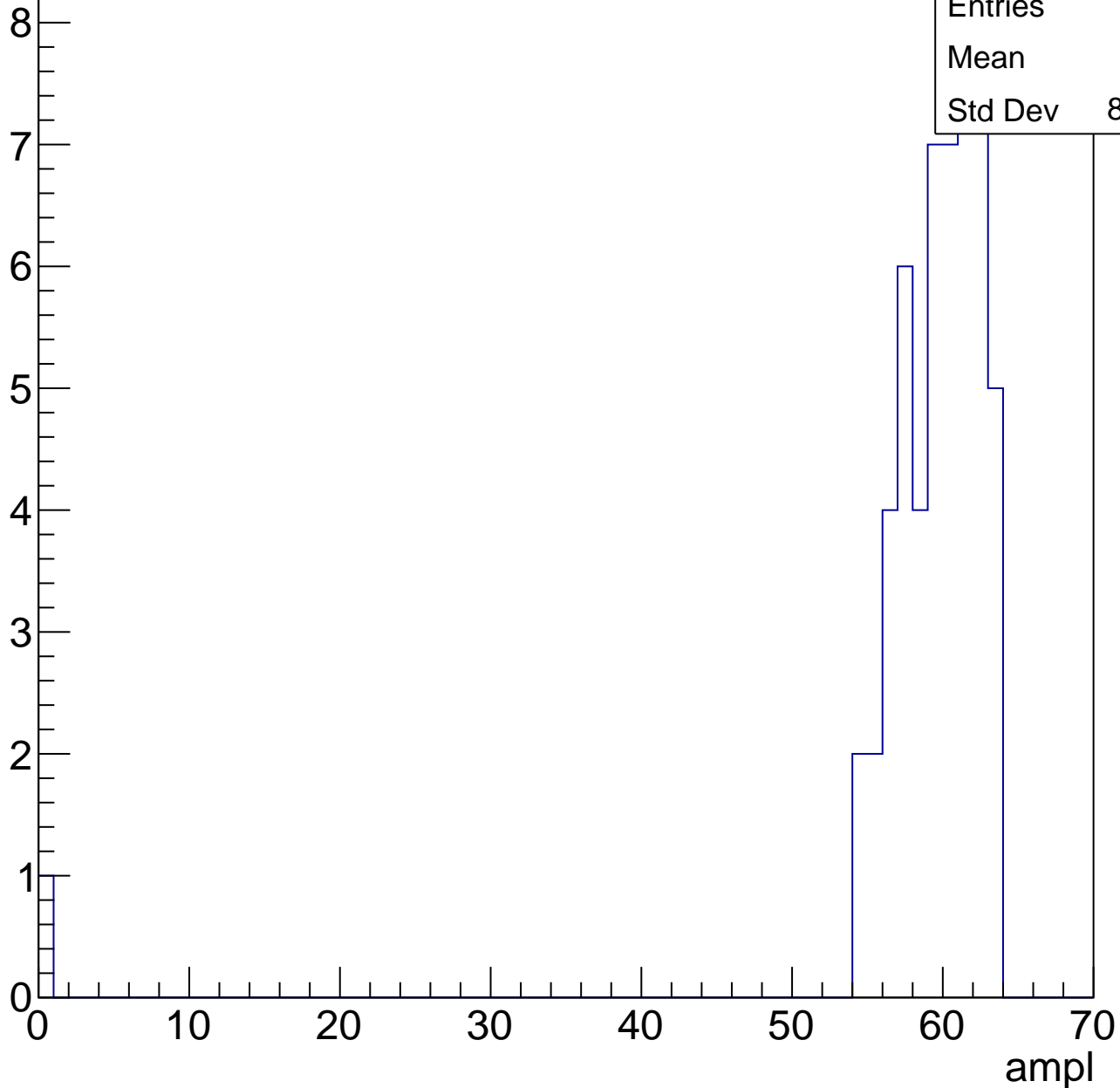
Entries	44
Mean	55.05
Std Dev	3.943



B1L103S, U18-ch109, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch109, adc6

calib_packv5_041523_1651.root, FC#0, port C2

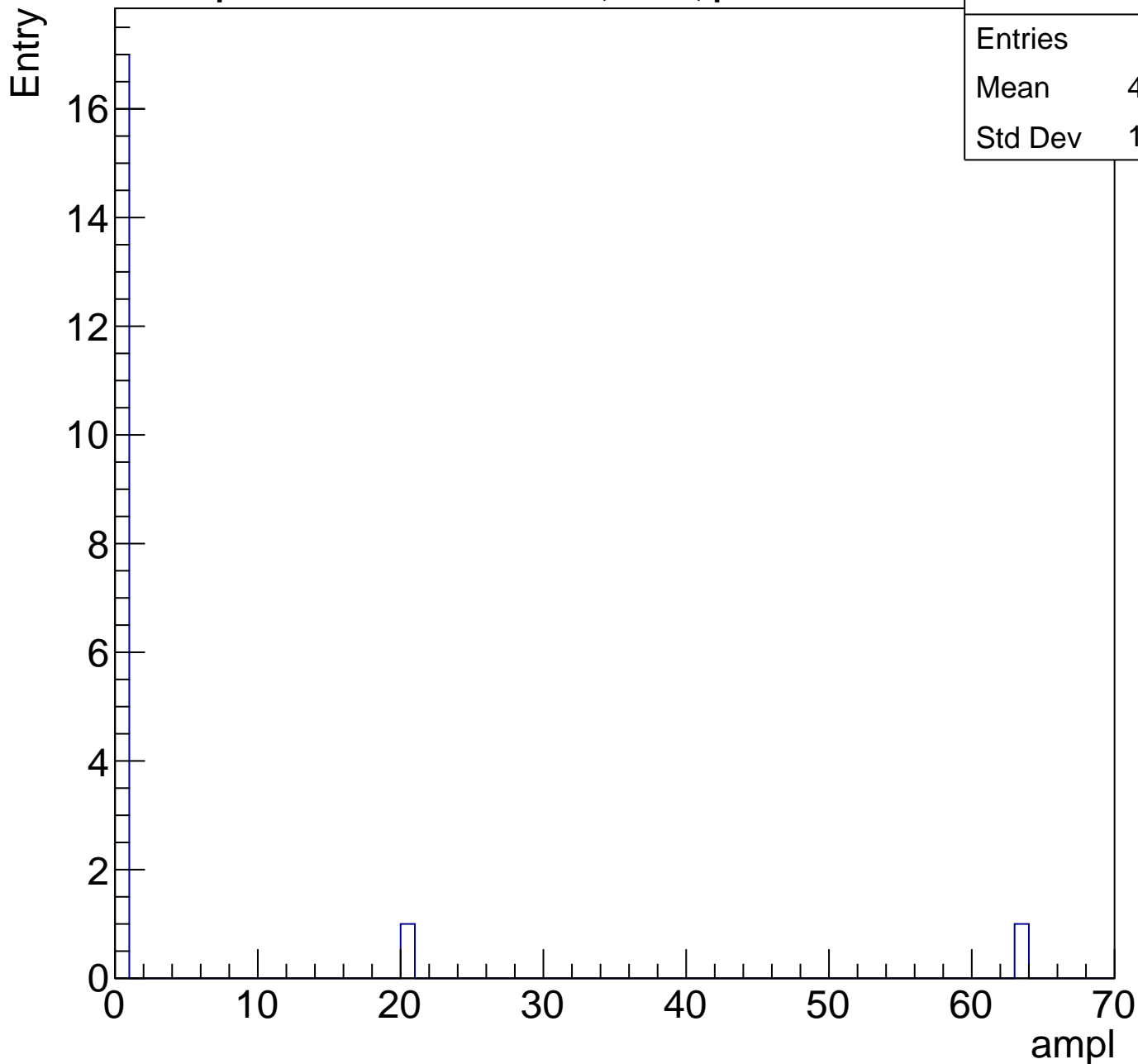
Entry



B1L103S, U18-ch109, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	4.368
Std Dev	14.52



B1L103S, U18-ch110, adc0

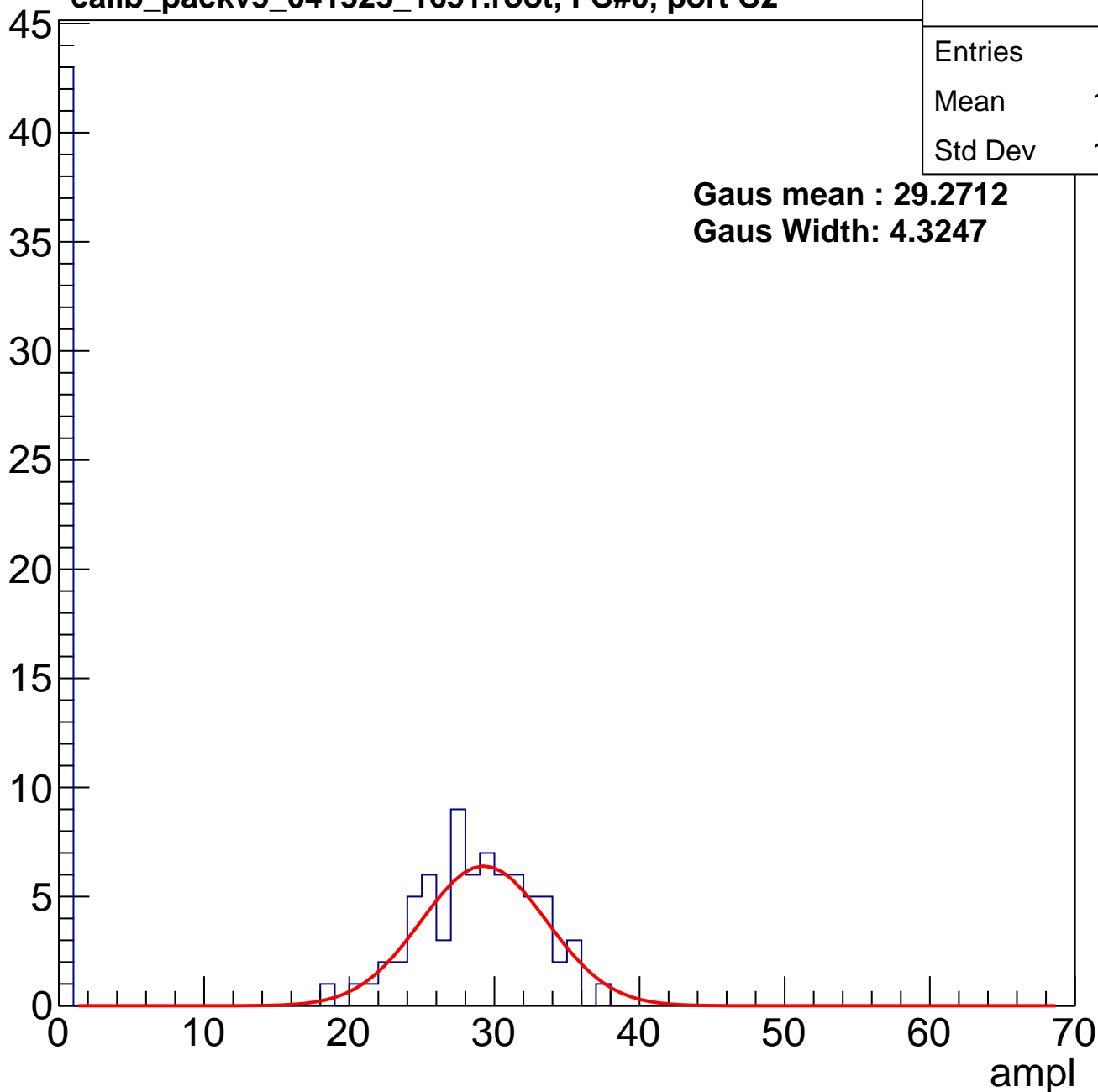
calib_packv5_041523_1651.root, FC#0, port C2

Entries	114
Mean	17.65
Std Dev	14.07

Gaus mean : 29.2712

Gaus Width: 4.3247

Entry



B1L103S, U18-ch110, adc1

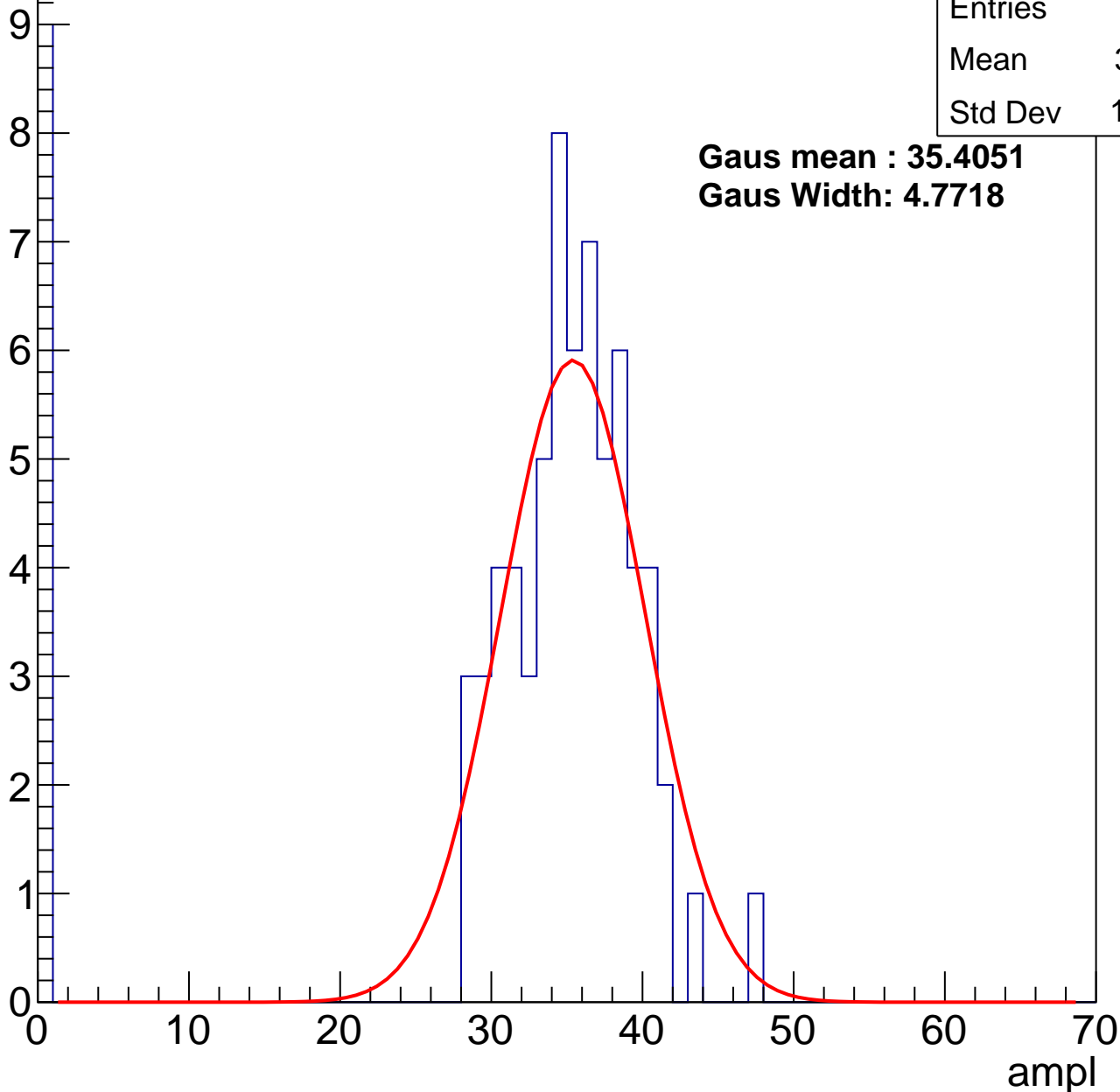
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	30.81
Std Dev	11.95

Gaus mean : 35.4051

Gaus Width: 4.7718



B1L103S, U18-ch110, adc2

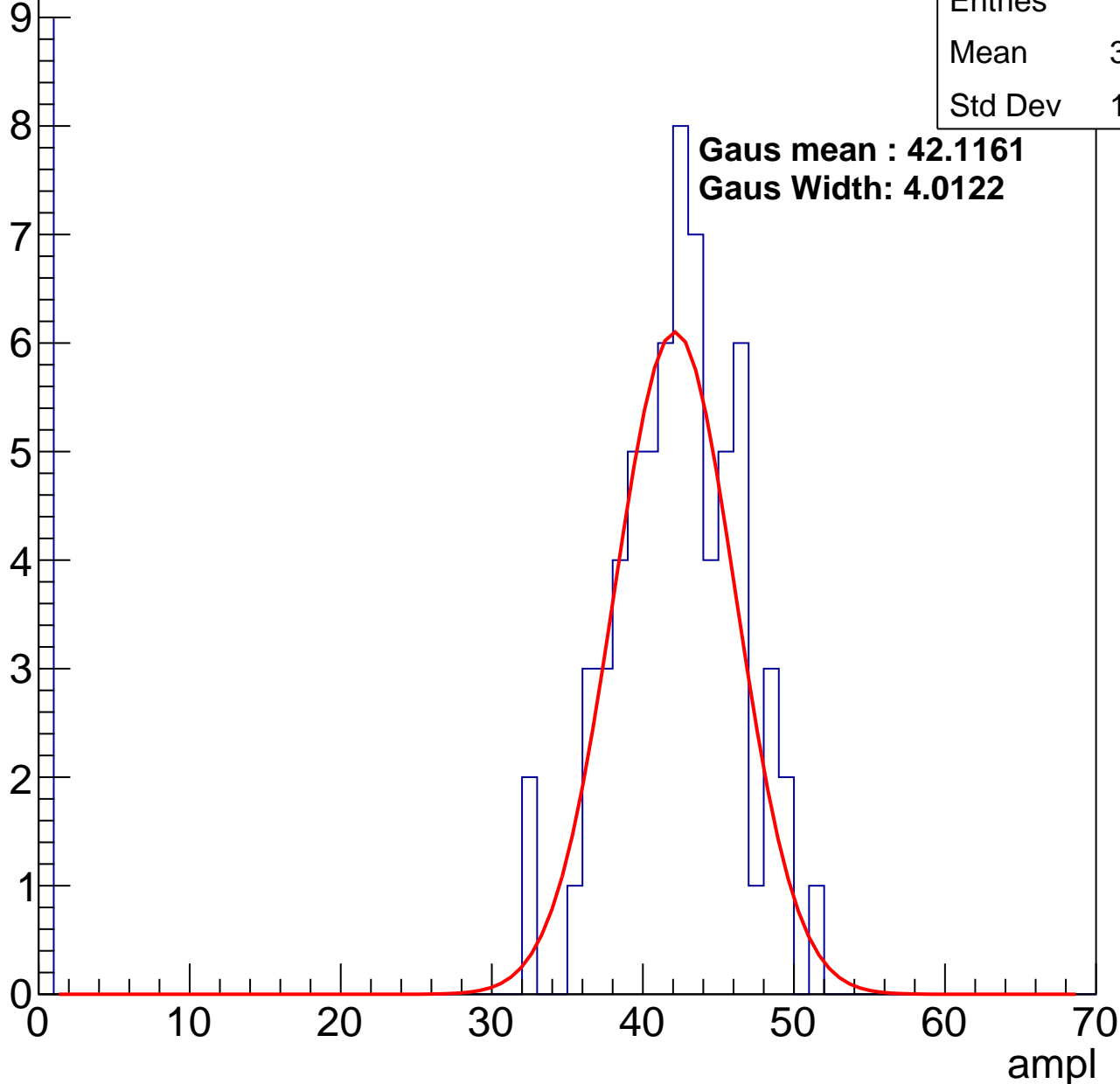
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	36.87
Std Dev	14.12

Gaus mean : 42.1161

Gaus Width: 4.0122

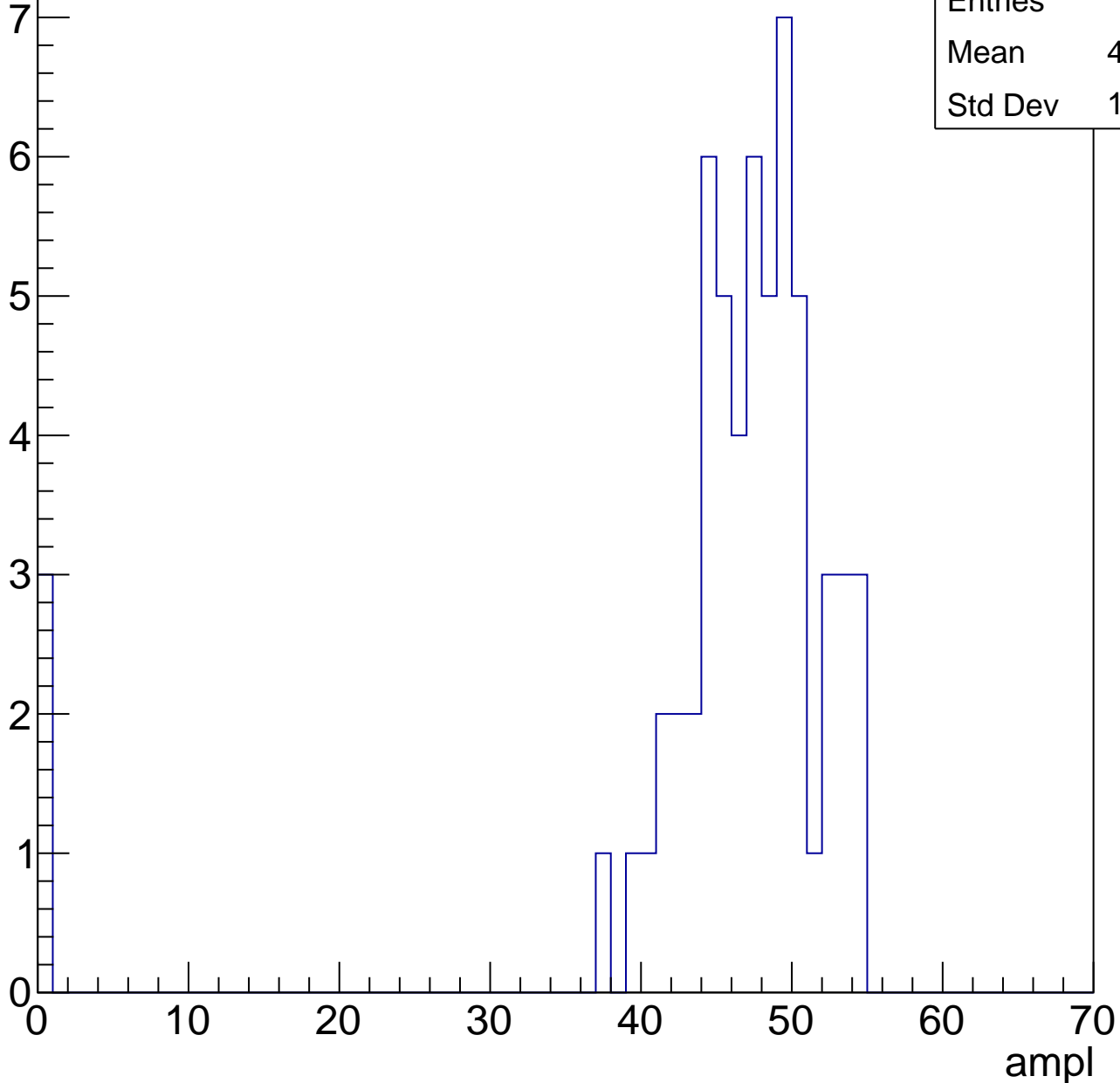


B1L103S, U18-ch110, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	44.73
Std Dev	10.95

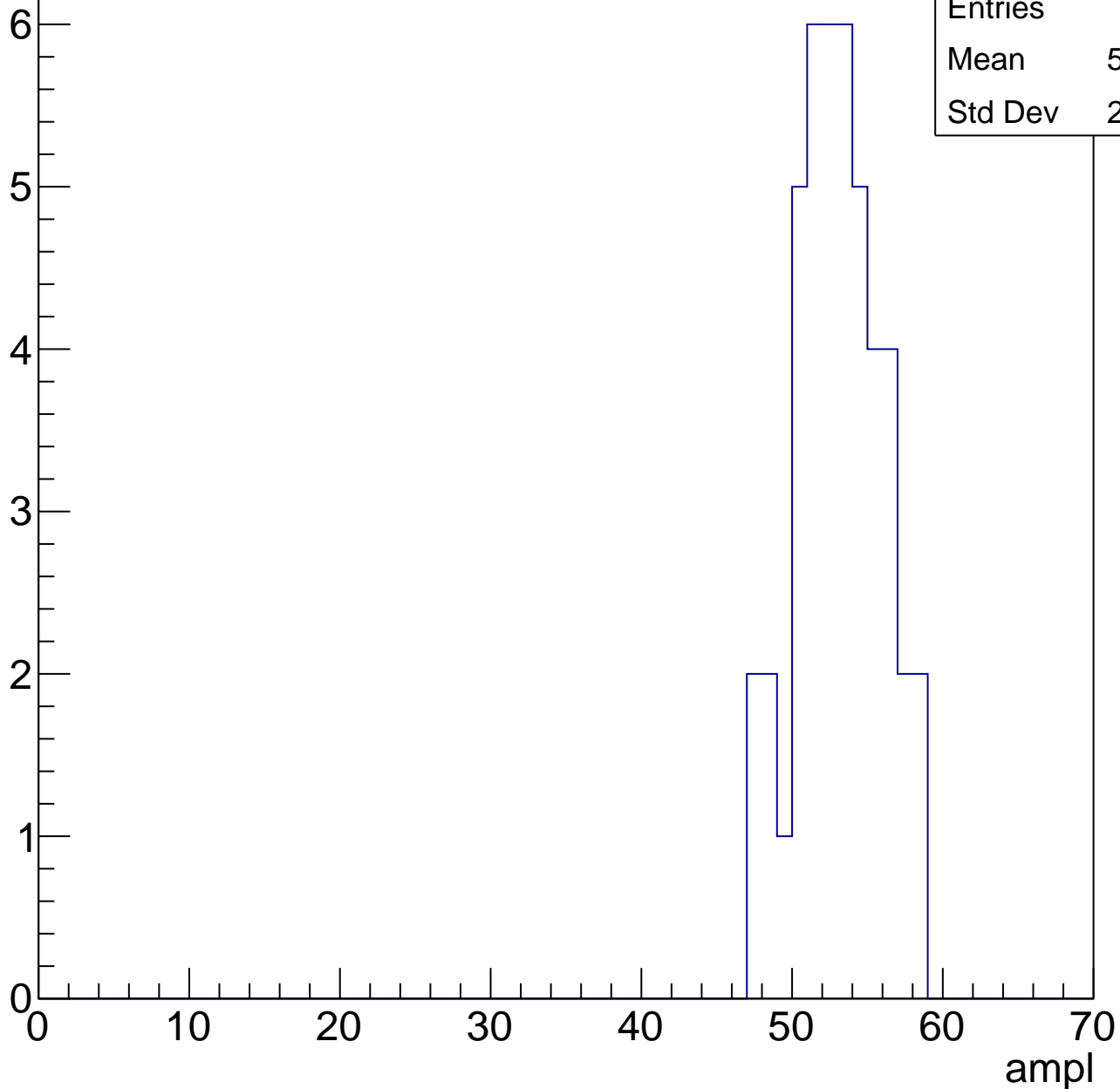


B1L103S, U18-ch110, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	52.64
Std Dev	2.774

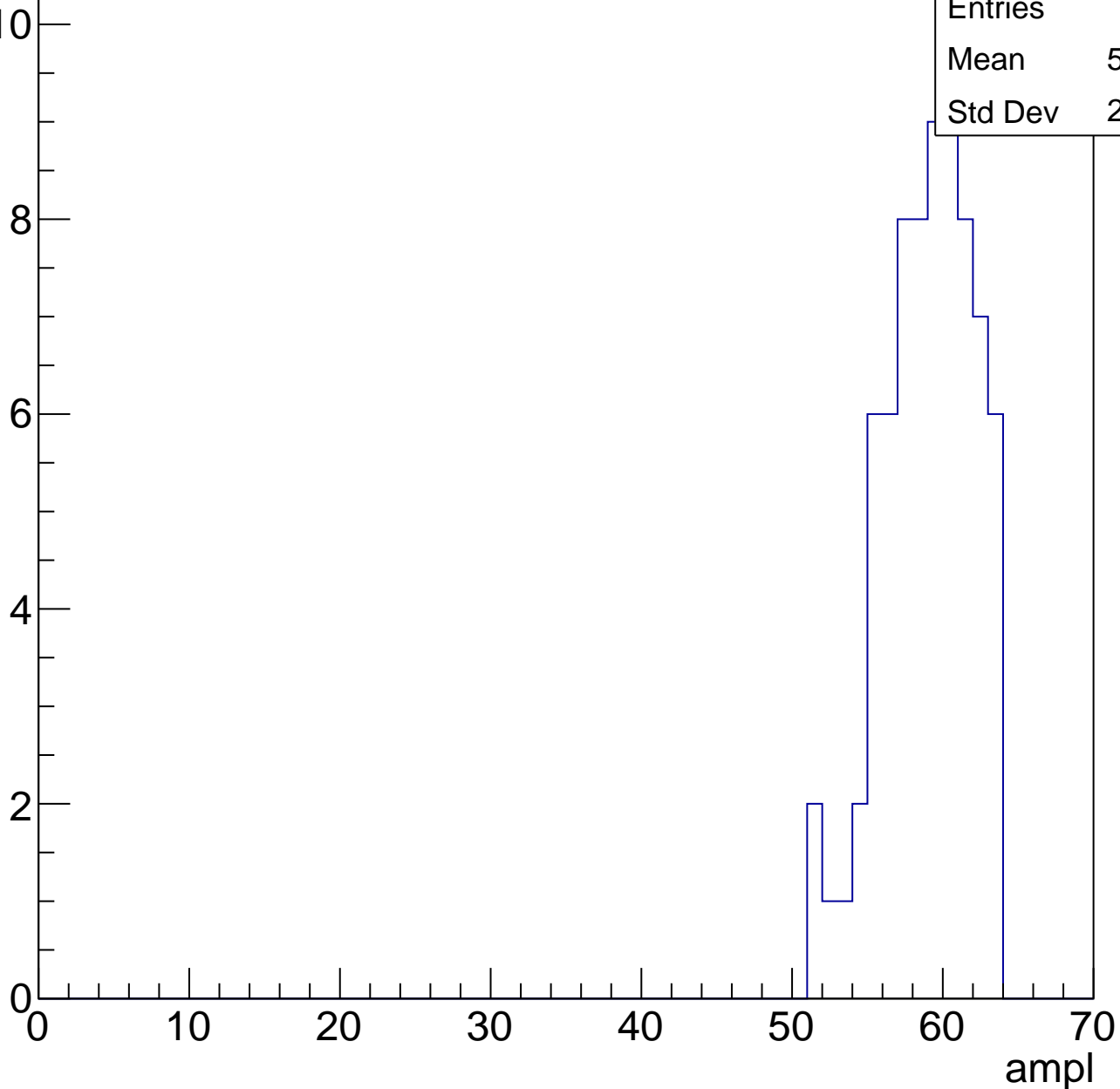


B1L103S, U18-ch110, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

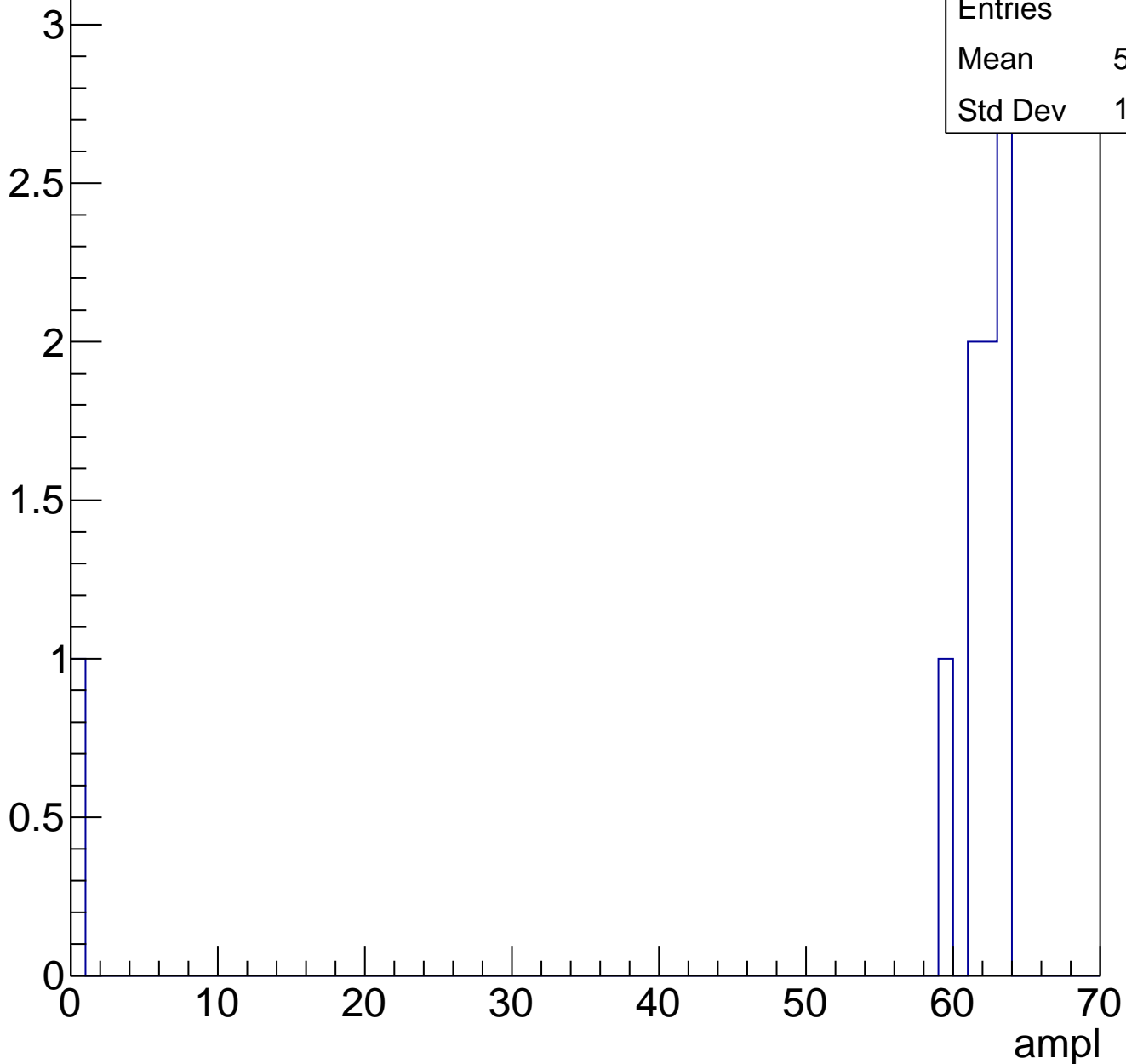
Entries	74
Mean	58.54
Std Dev	2.937



B1L103S, U18-ch110, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



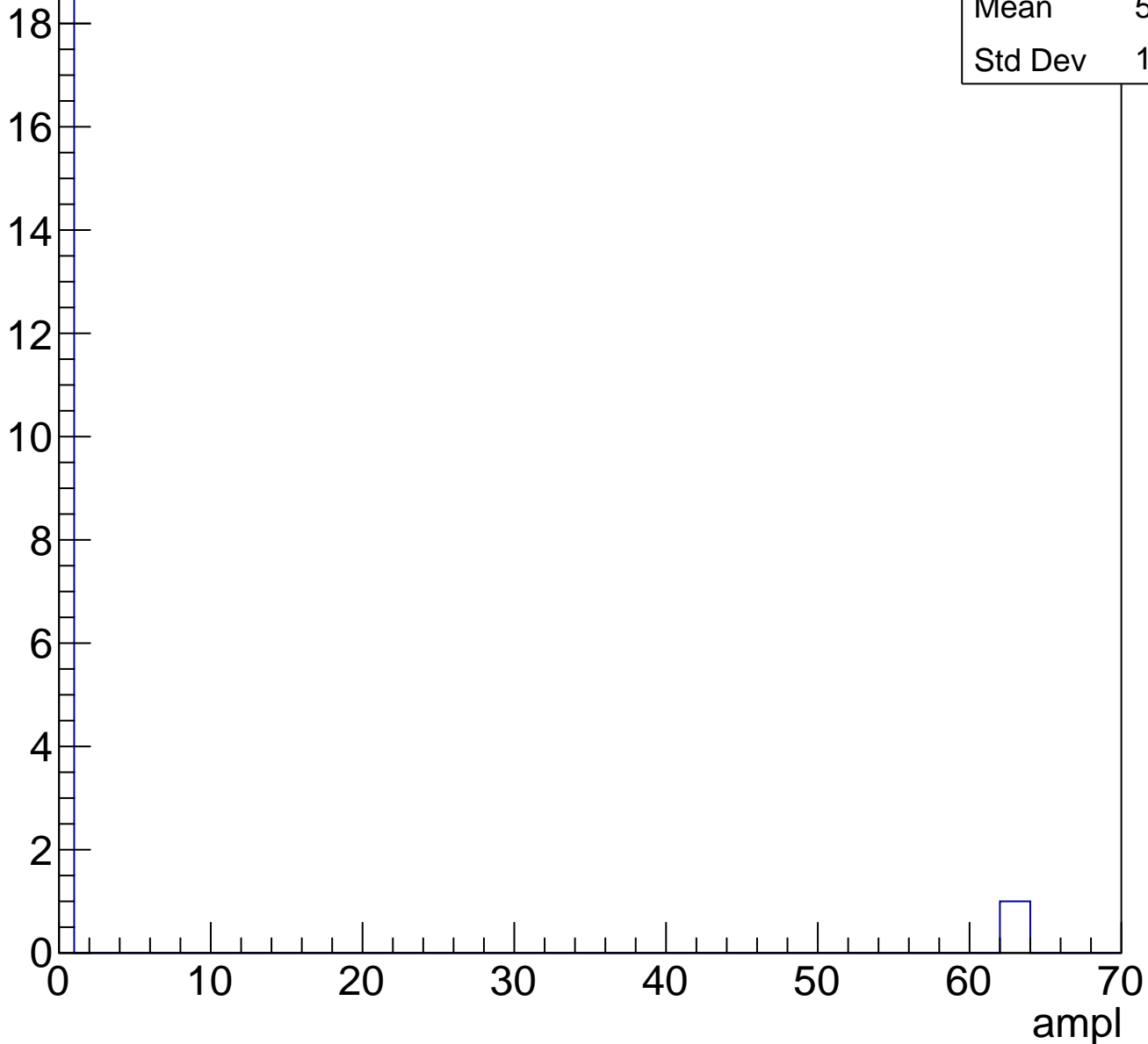
Entries	9
Mean	54.89
Std Dev	19.44

B1L103S, U18-ch110, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



B1L103S, U18-ch111, adc0

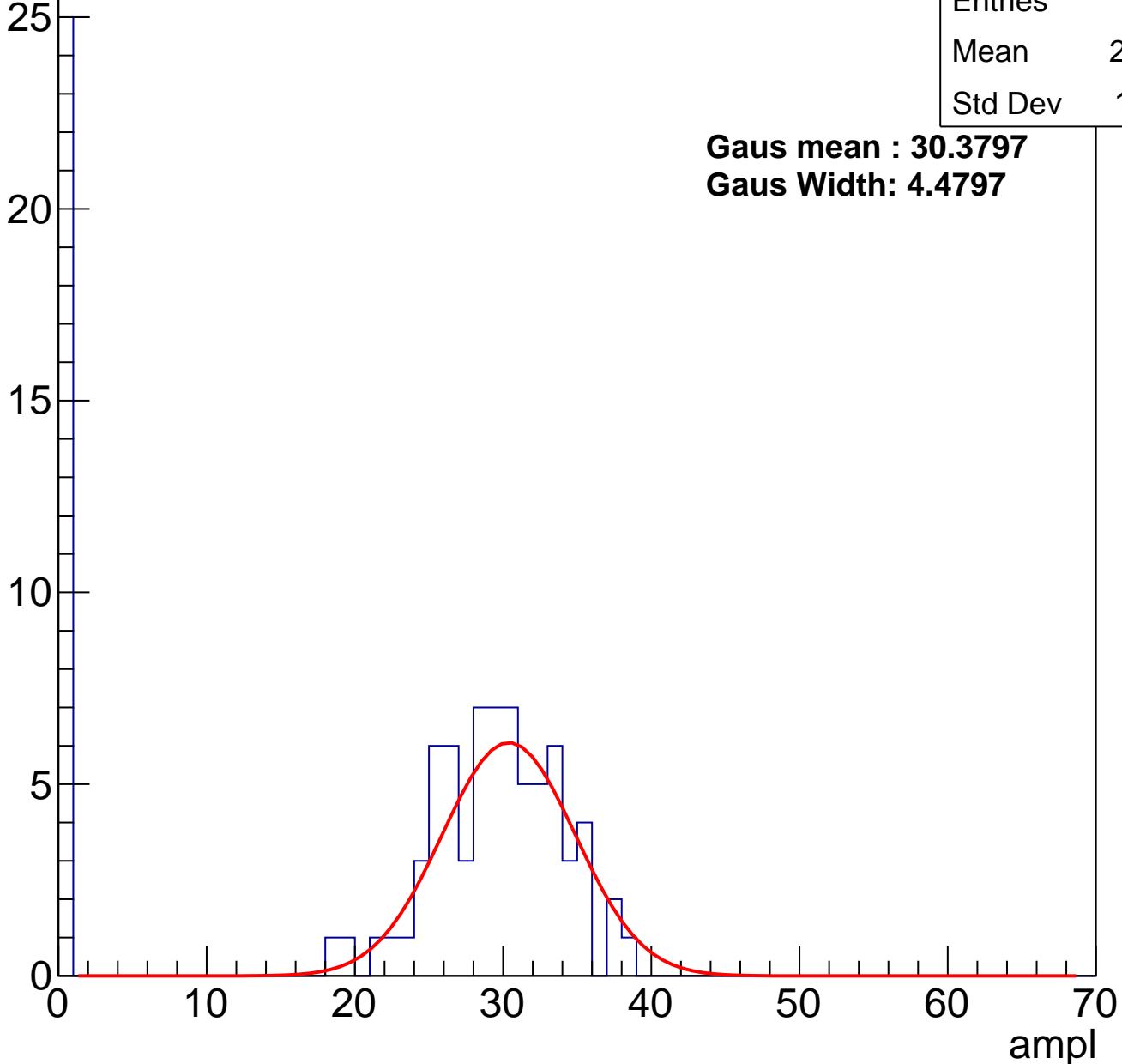
calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	21.45
Std Dev	13.31

Gaus mean : 30.3797

Gaus Width: 4.4797

Entry



B1L103S, U18-ch111, adc1

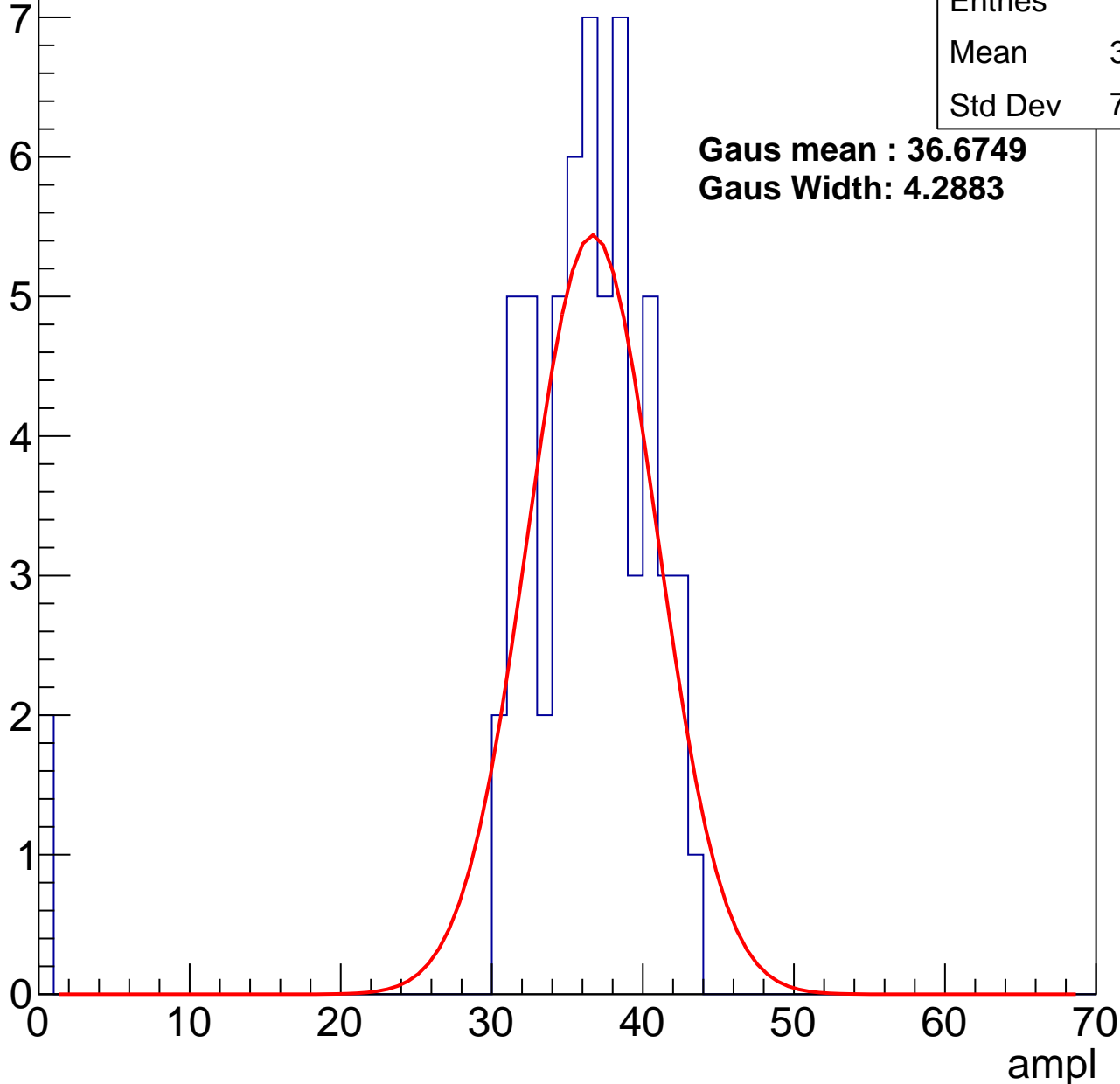
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.97
Std Dev	7.265

Gaus mean : 36.6749

Gaus Width: 4.2883



B1L103S, U18-ch111, adc2

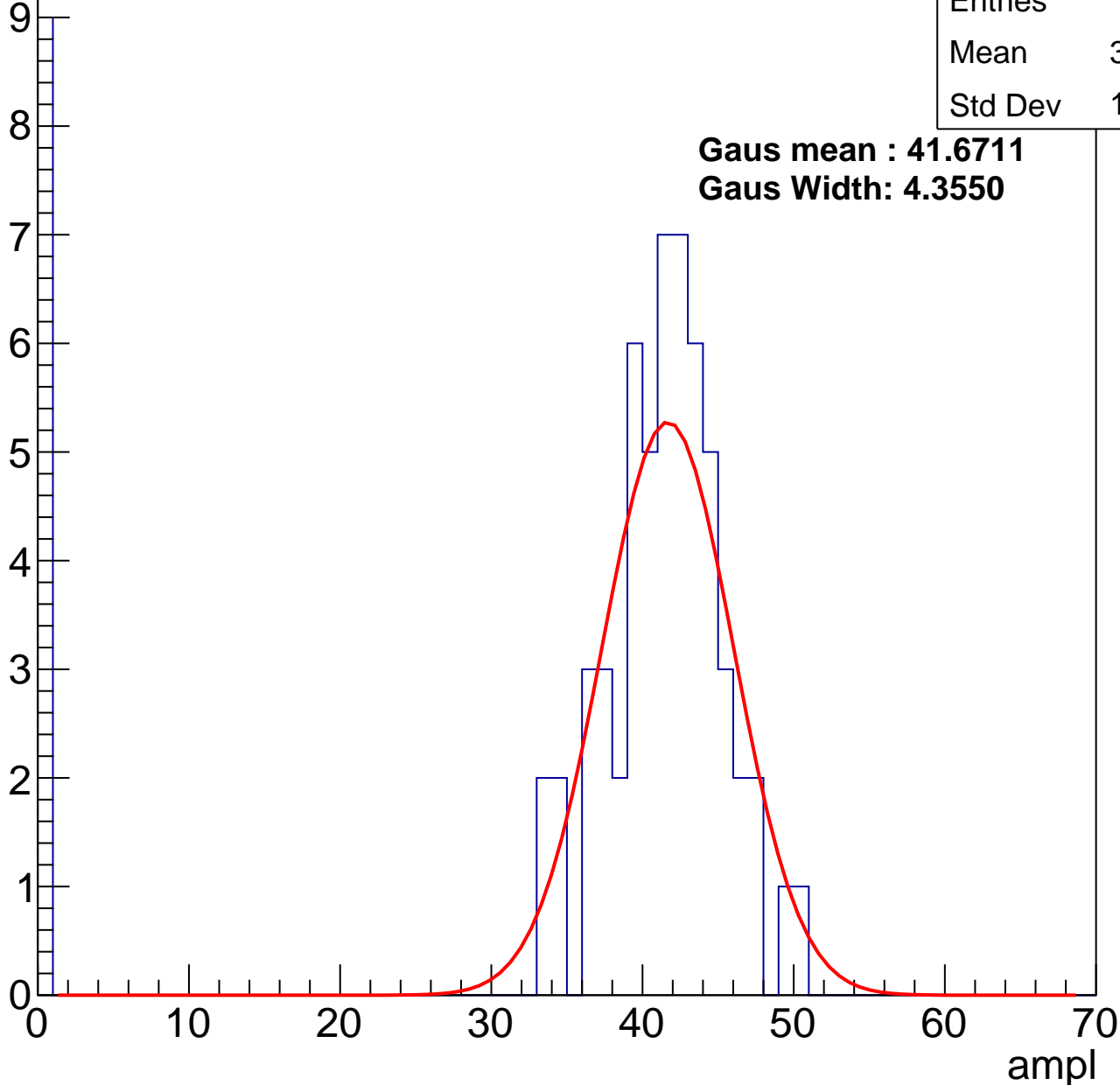
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	35.48
Std Dev	14.52

Gaus mean : 41.6711

Gaus Width: 4.3550



B1L103S, U18-ch111, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	46.18
Std Dev	10.86

Entry

10

8

6

4

2

0

0

10

20

30

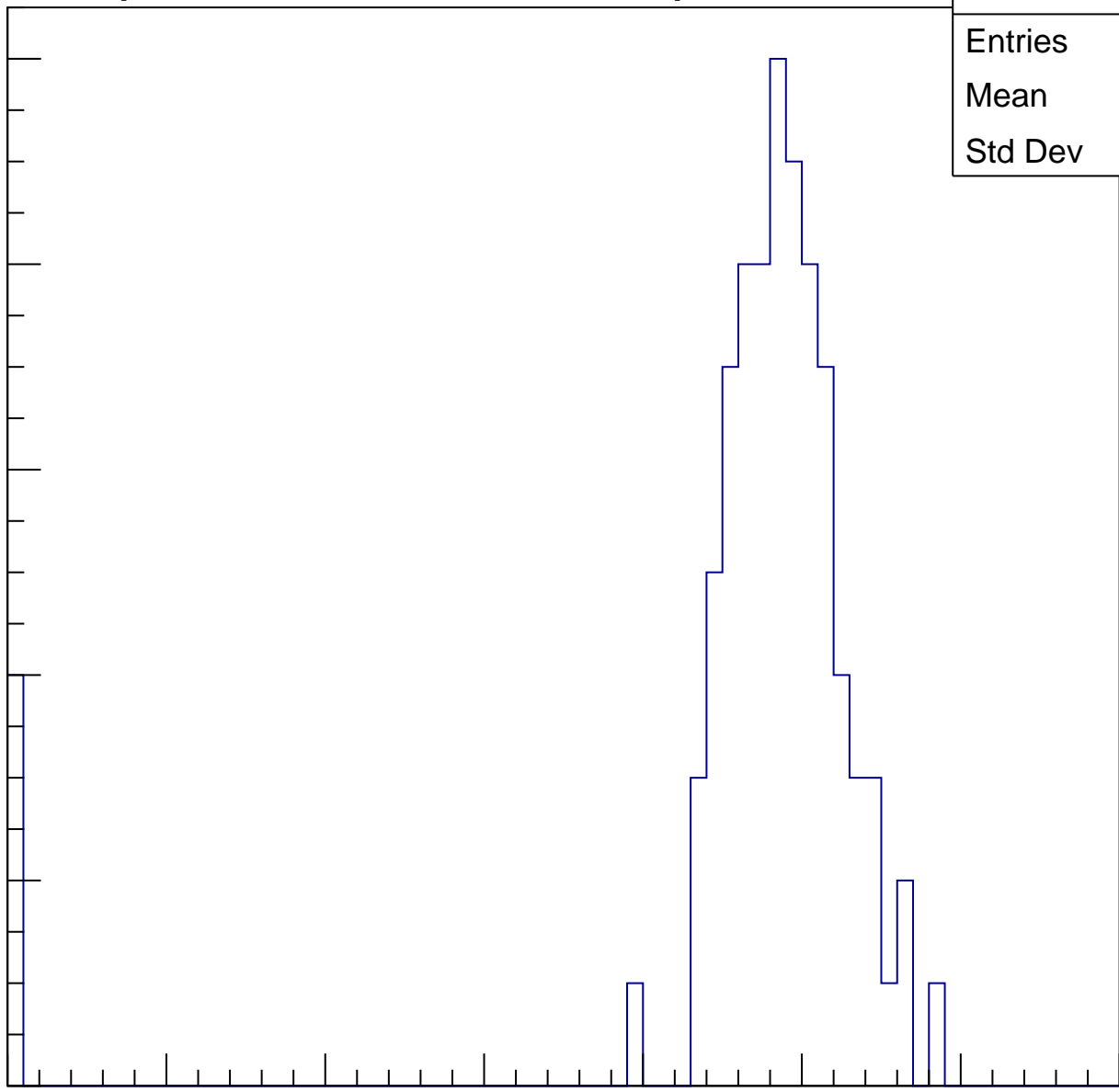
40

50

60

70

ampl

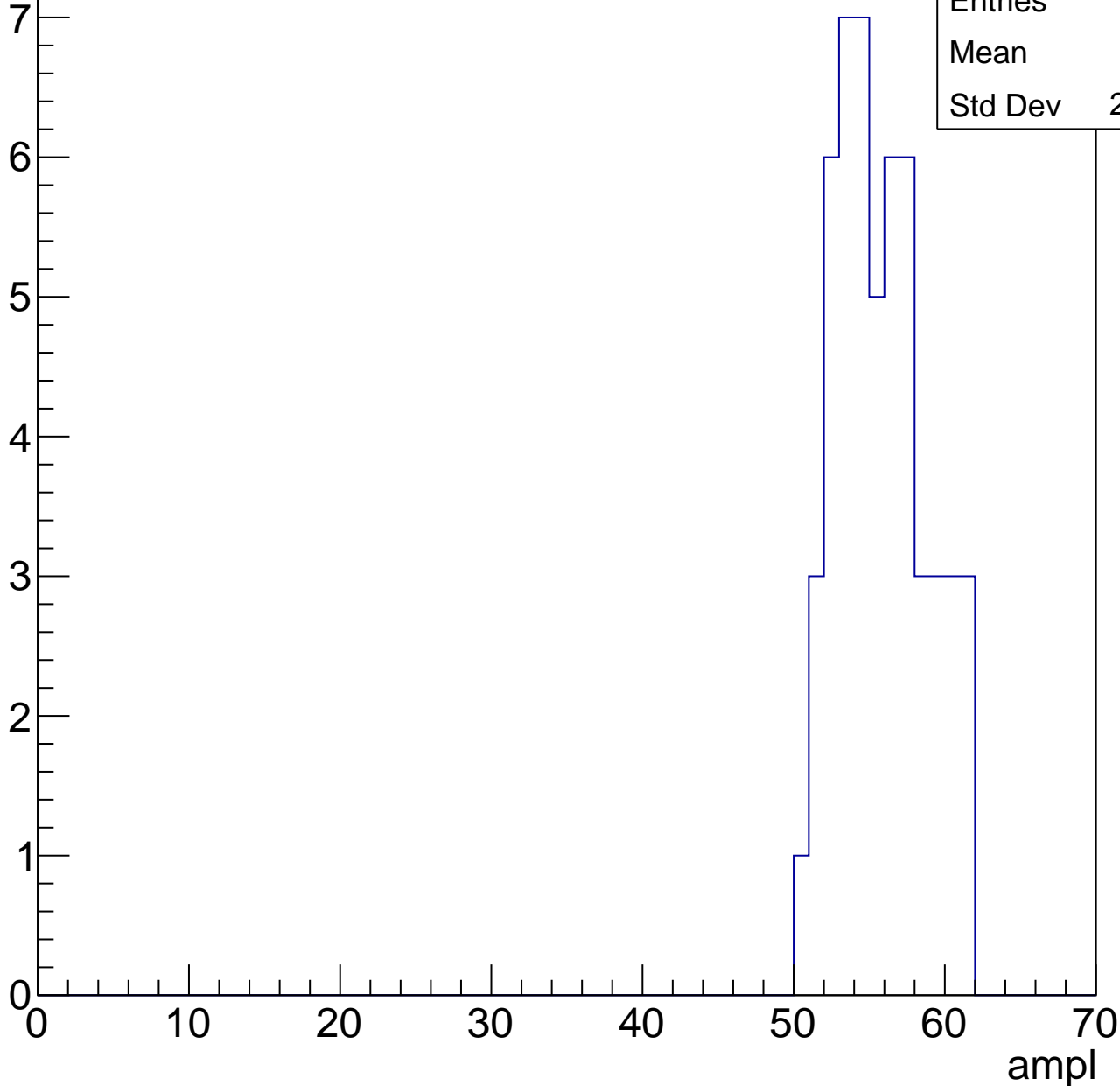


B1L103S, U18-ch111, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	53
Mean	55.3
Std Dev	2.898

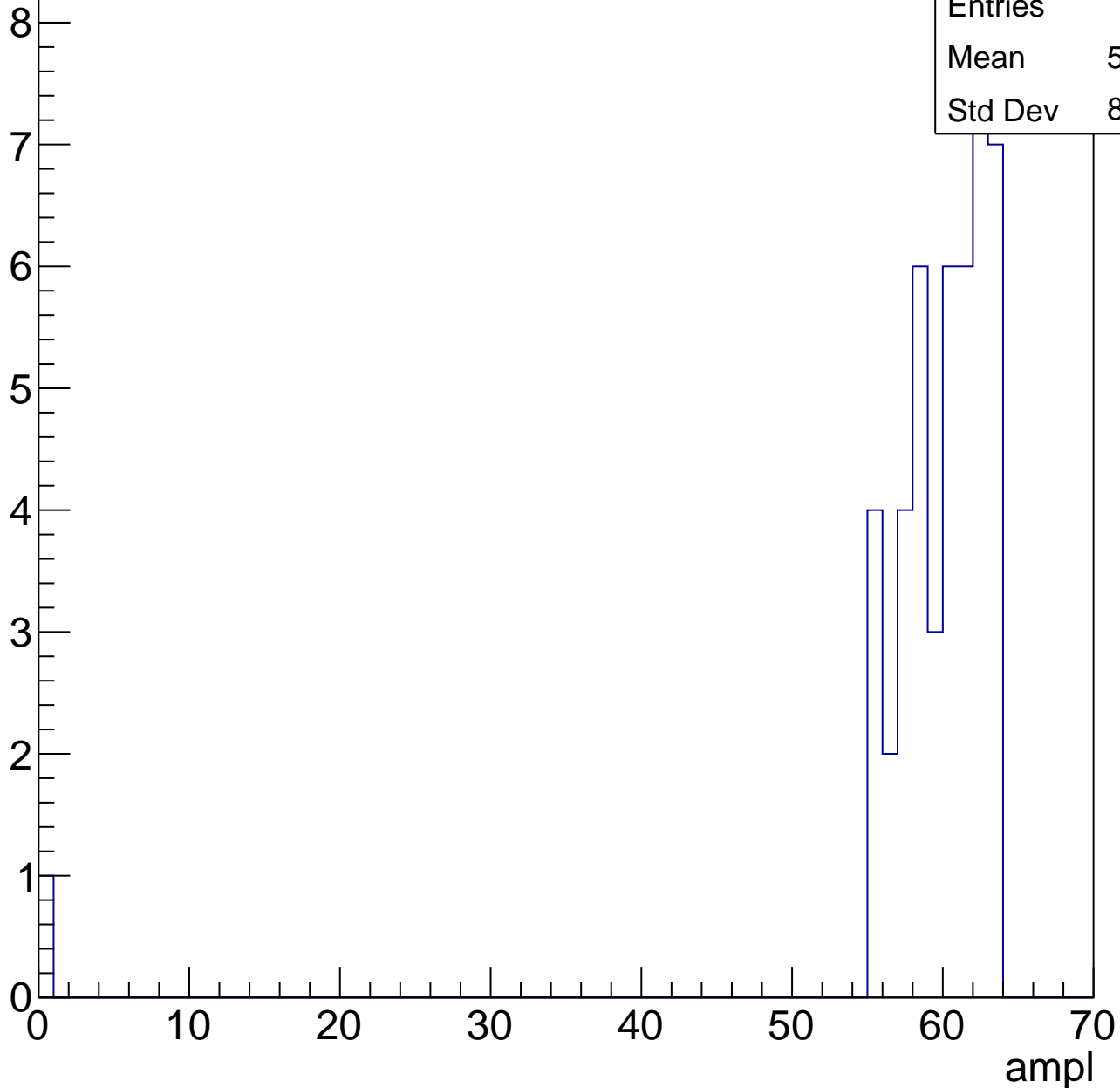


B1L103S, U18-ch111, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	47
Mean	58.47
Std Dev	8.975



B1L103S, U18-ch111, adc6

calib_packv5_041523_1651.root, FC#0, port C2

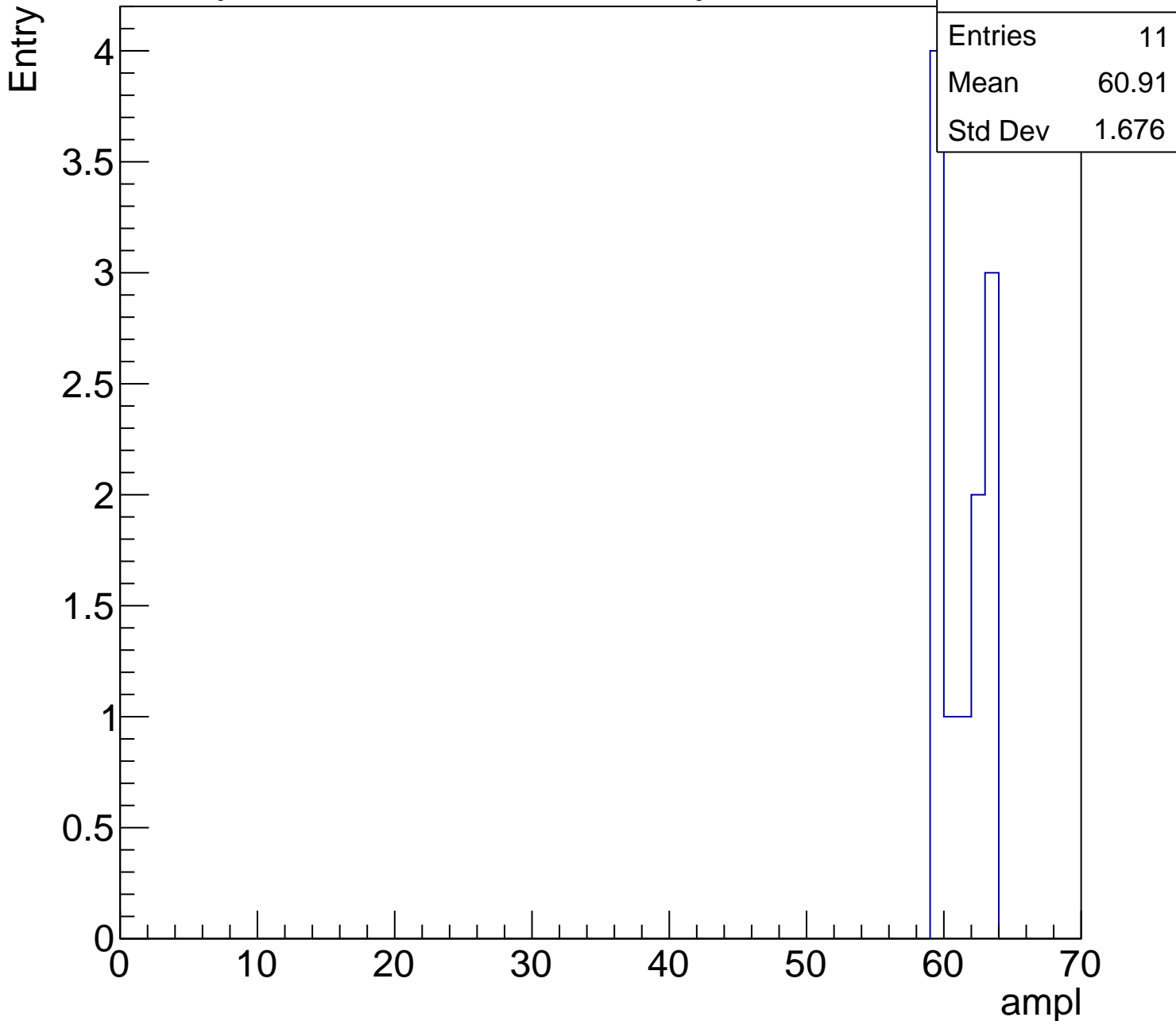
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	60.91
Std Dev	1.676

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch111, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

B1L103S, U18-ch112, adc0

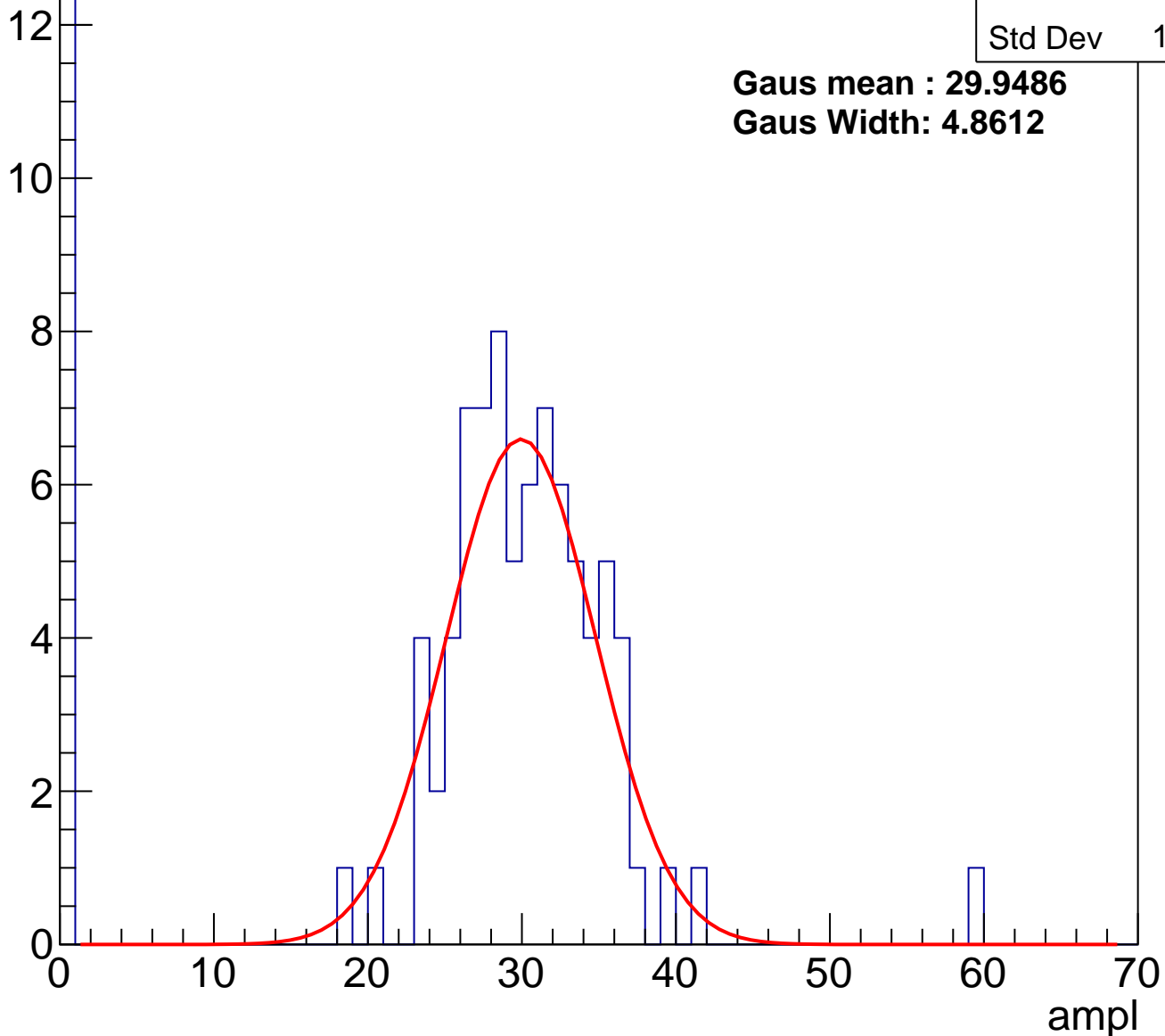
calib_packv5_041523_1651.root, FC#0, port C2

Entries	93
Mean	25.84
Std Dev	11.55

Gaus mean : 29.9486

Gaus Width: 4.8612

Entry



B1L103S, U18-ch112, adc1

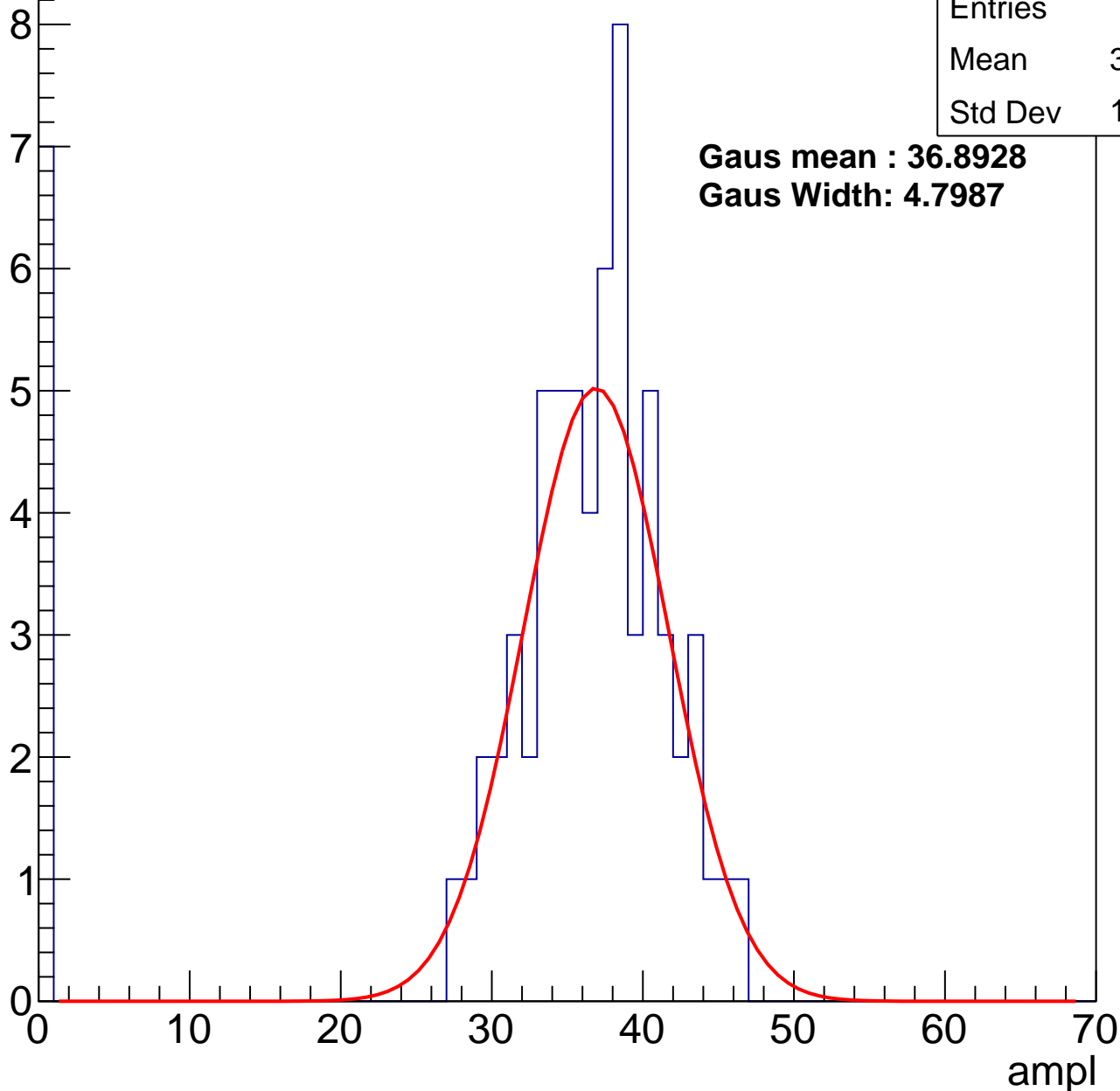
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	32.83
Std Dev	11.68

Gaus mean : 36.8928

Gaus Width: 4.7987



B1L103S, U18-ch112, adc2

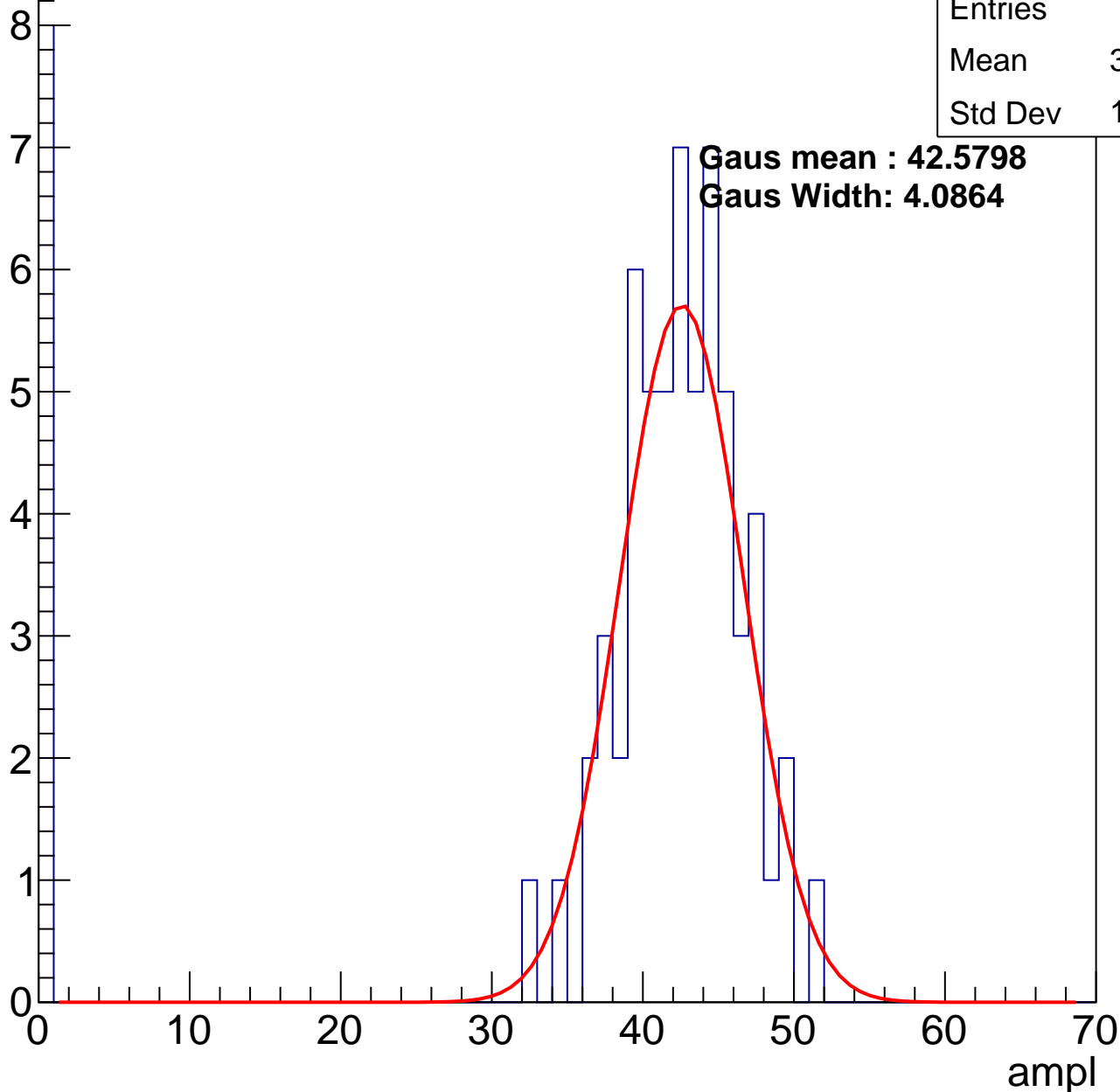
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	37.19
Std Dev	14.04

Gaus mean : 42.5798

Gaus Width: 4.0864

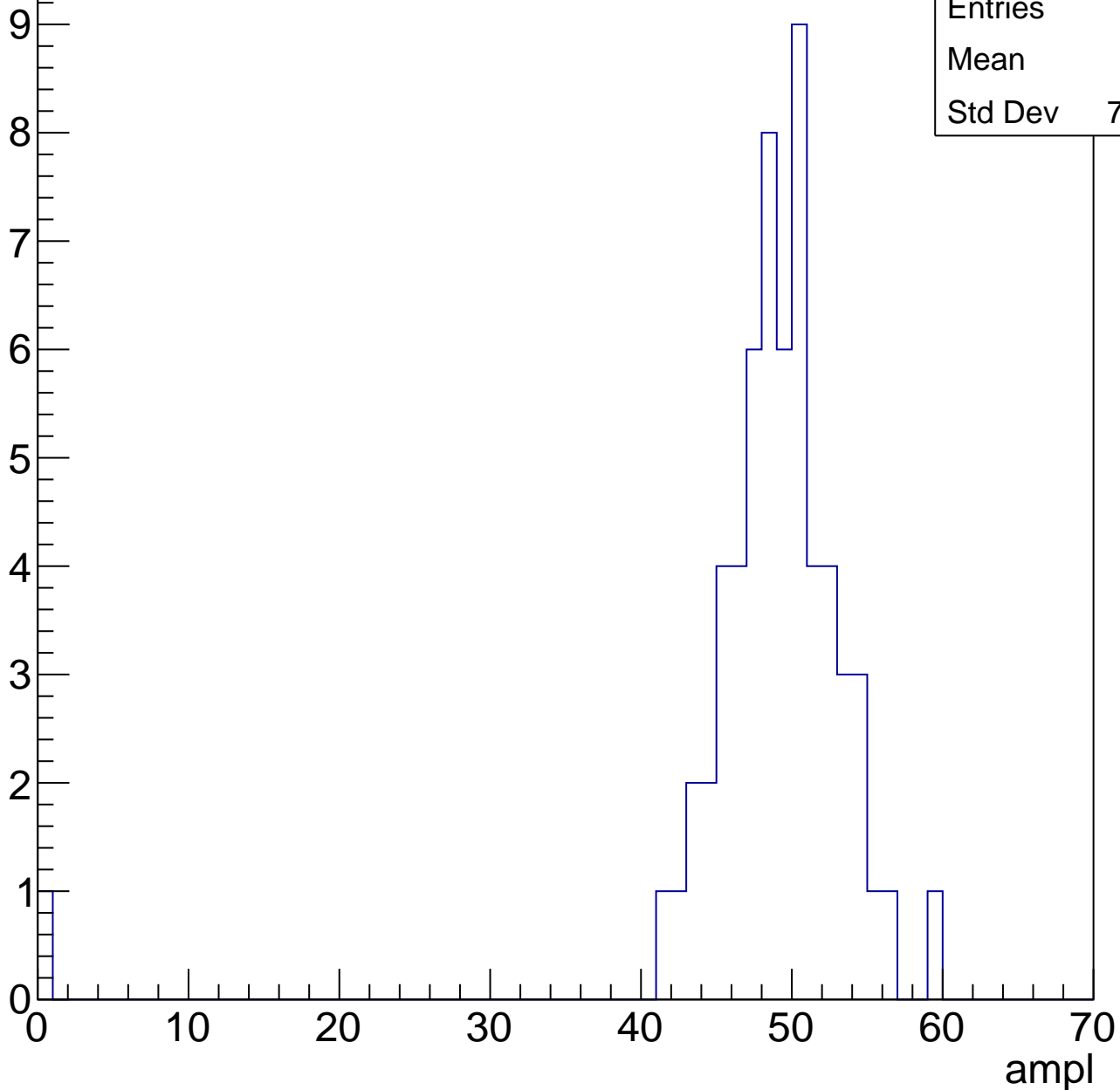


B1L103S, U18-ch112, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

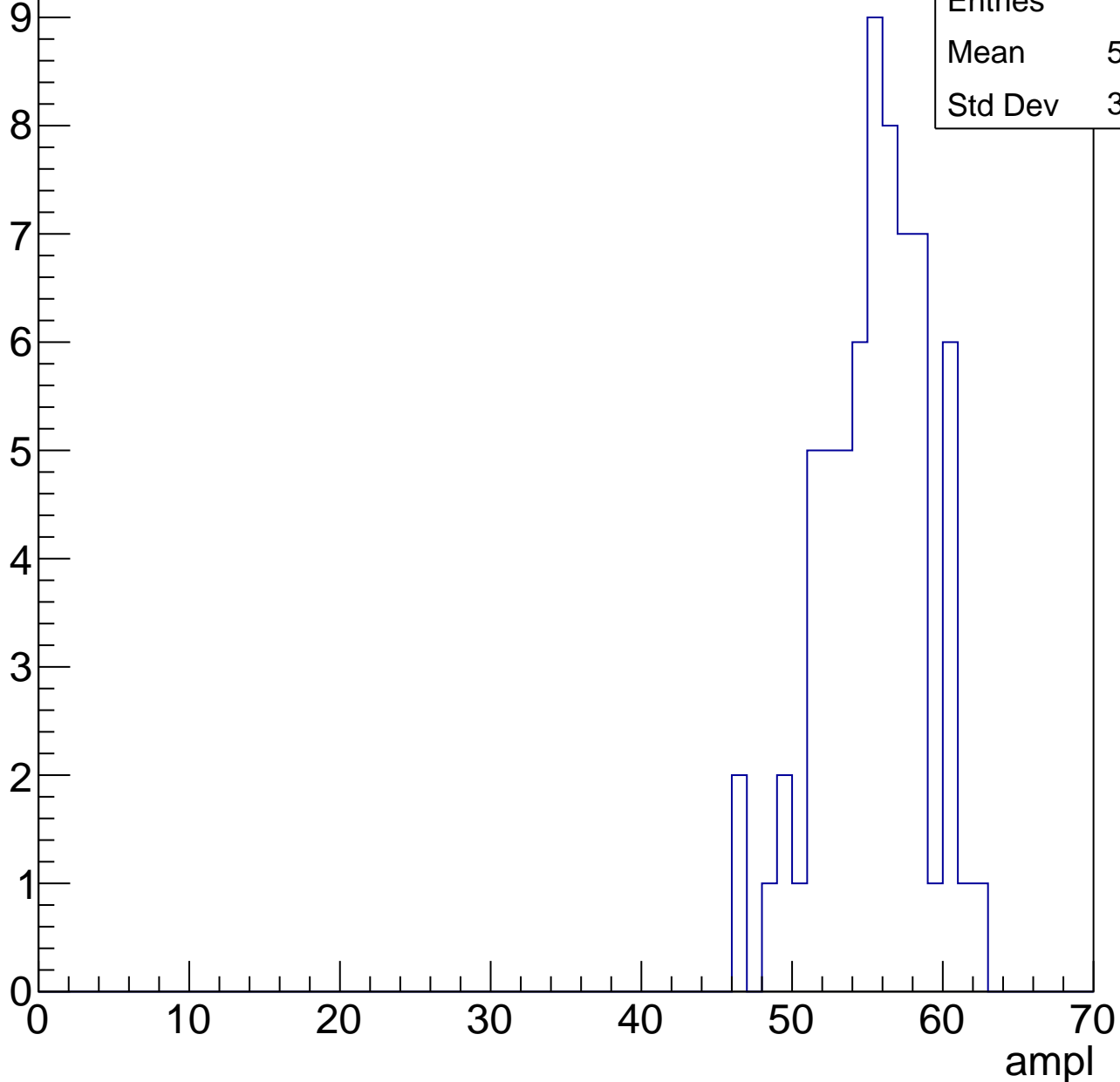
Entries	61
Mean	48.1
Std Dev	7.114



B1L103S, U18-ch112, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

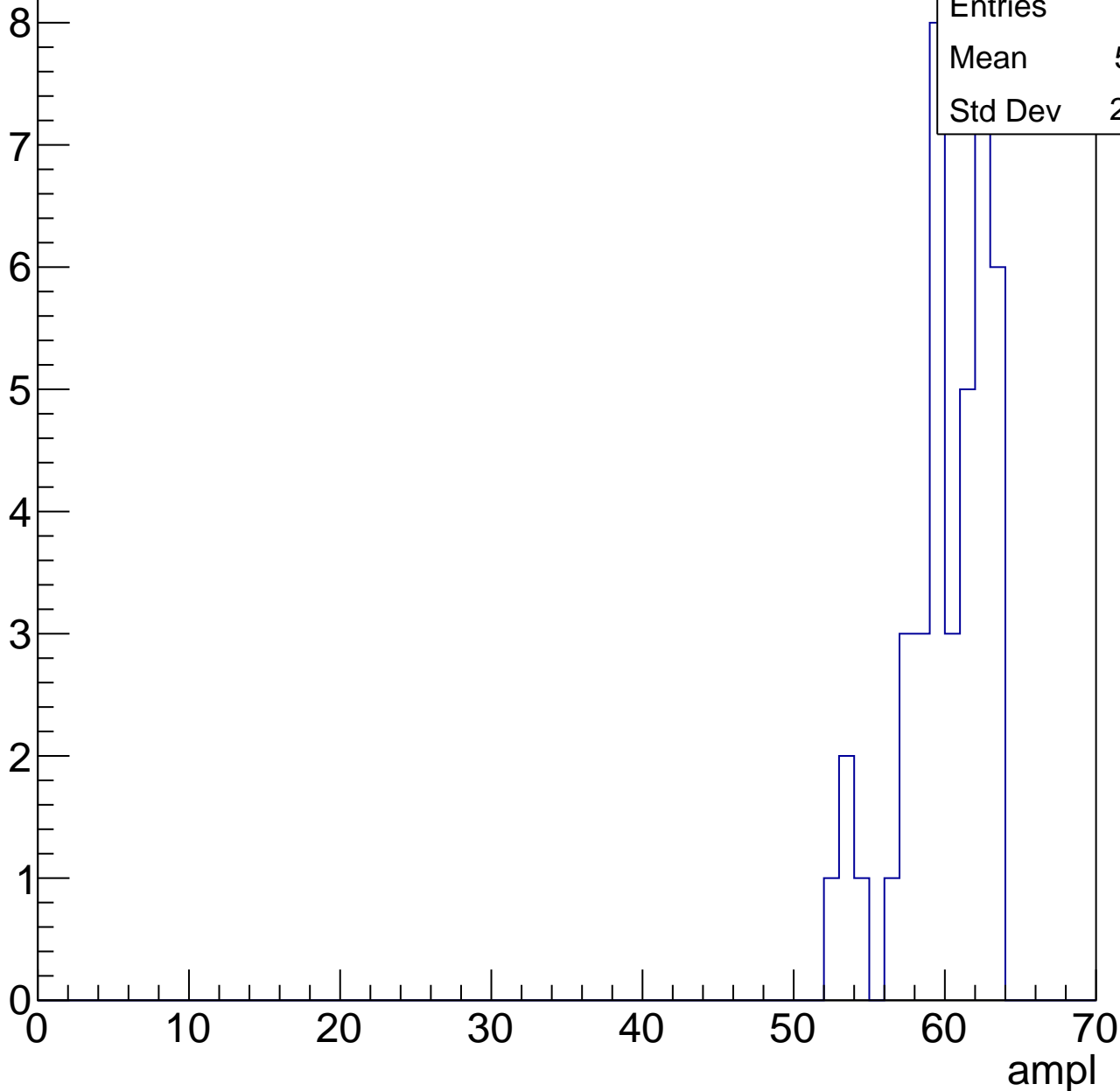


B1L103S, U18-ch112, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

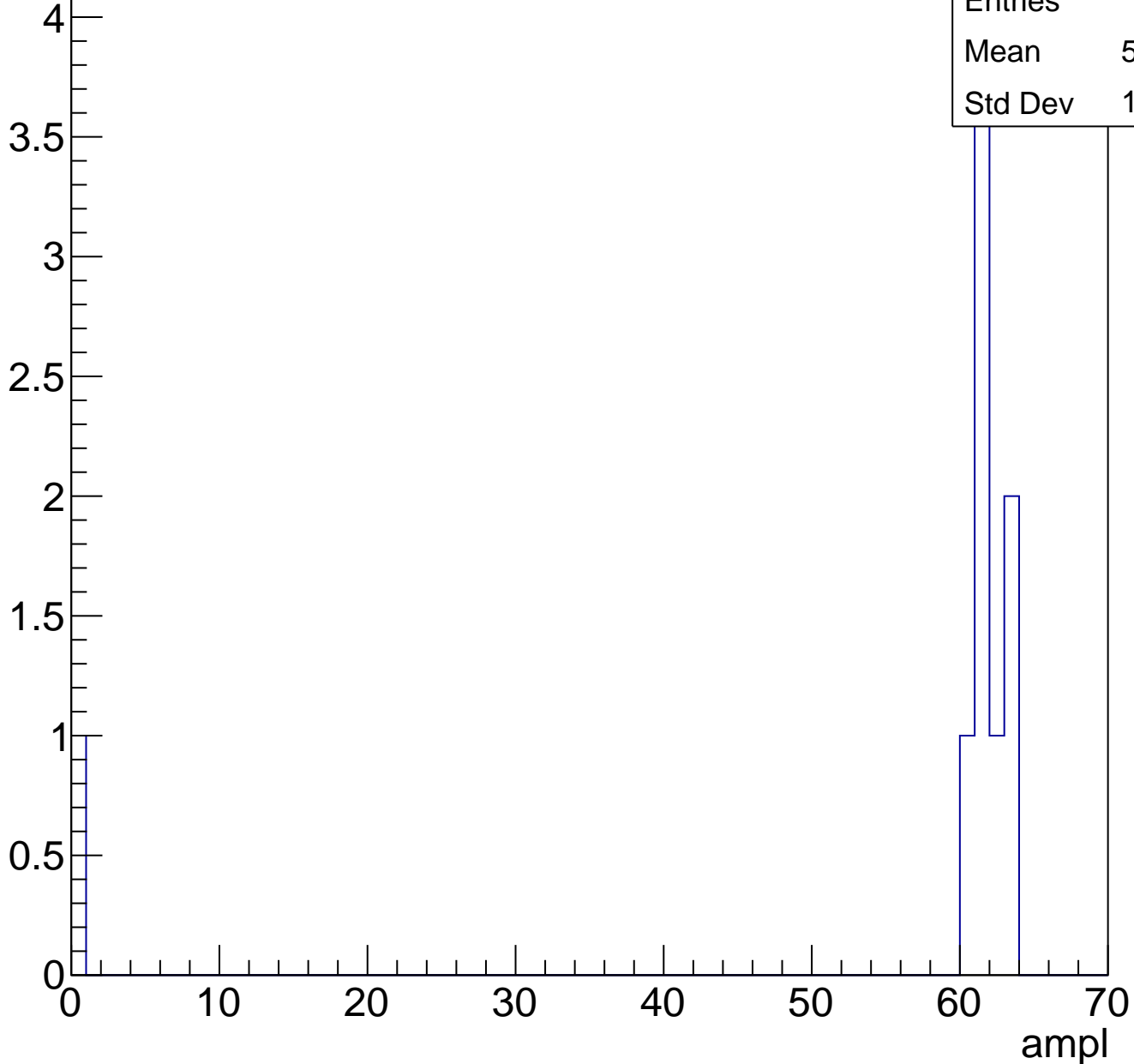
Entries	41
Mean	59.61
Std Dev	2.904



B1L103S, U18-ch112, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch112, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U18-ch113, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	20.26
Std Dev	13.66

Gaus mean : 29.5725

Gaus Width: 5.1237

Entry

25

20

15

10

5

0

0

10

20

30

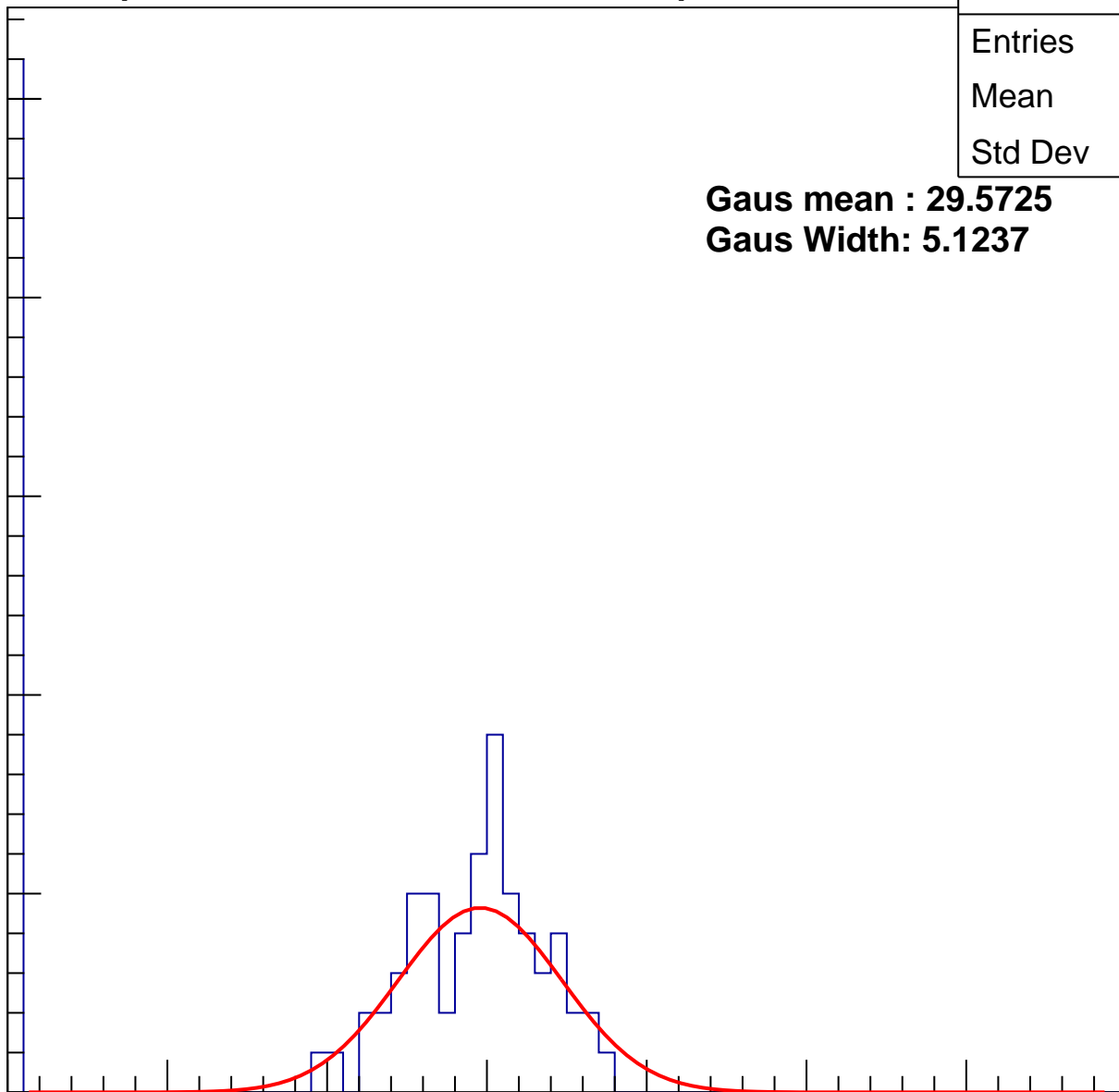
40

50

60

70

ampl



B1L103S, U18-ch113, adc1

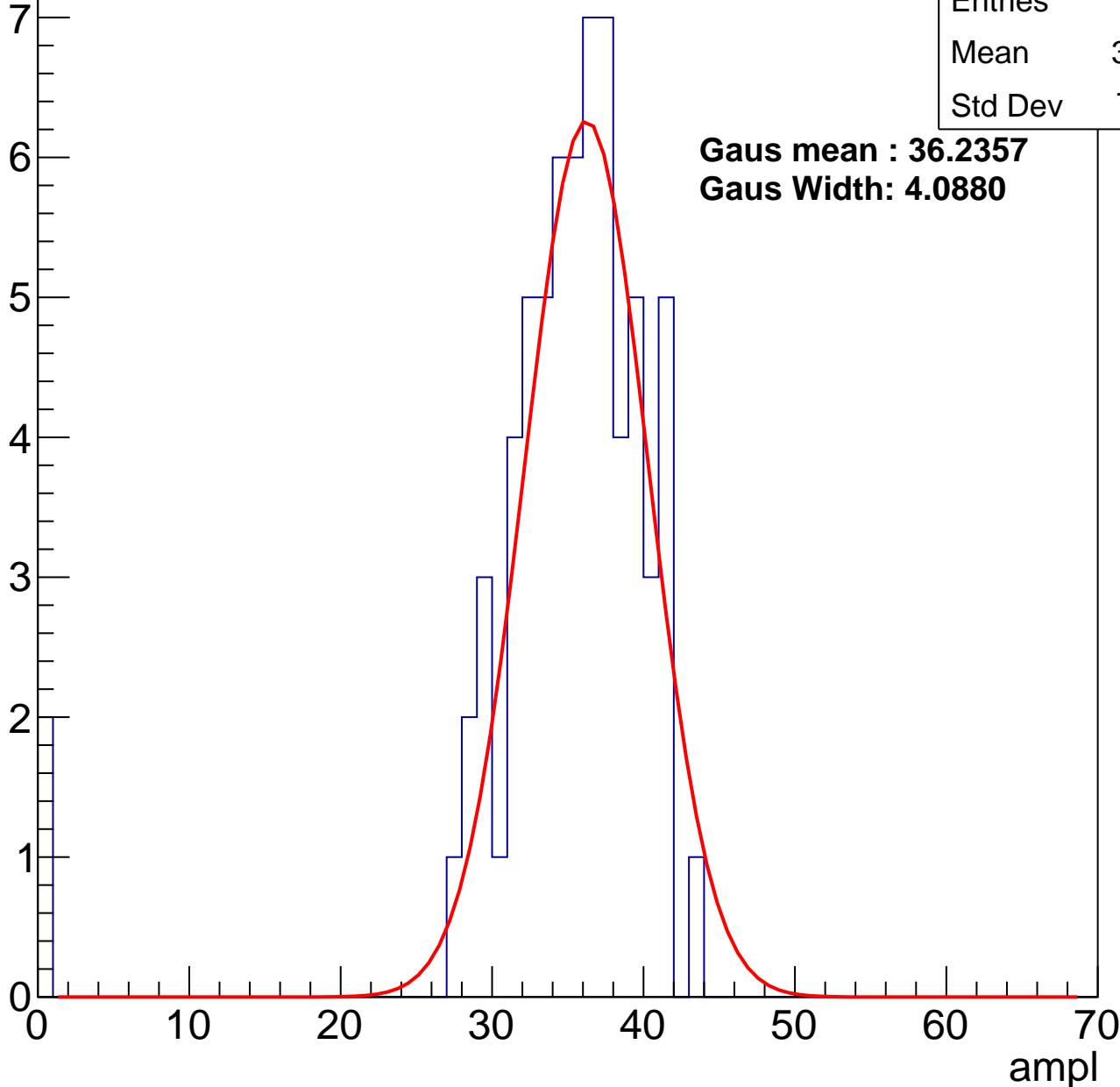
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	34.16
Std Dev	7.021

Gaus mean : 36.2357

Gaus Width: 4.0880



B1L103S, U18-ch113, adc2

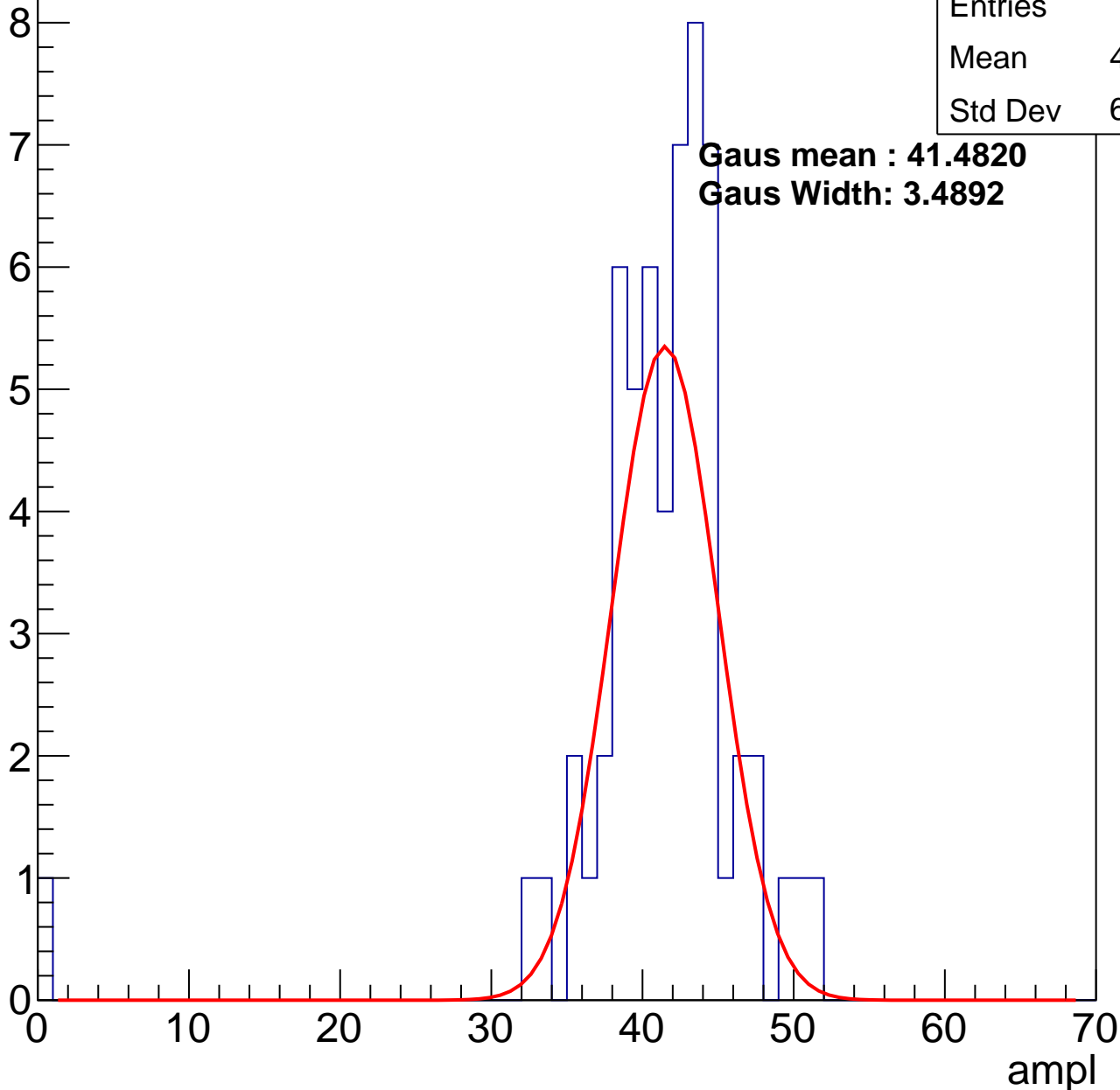
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	40.66
Std Dev	6.527

Gaus mean : 41.4820

Gaus Width: 3.4892



B1L103S, U18-ch113, adc3

calib_packv5_041523_1651.root, FC#0, port C2

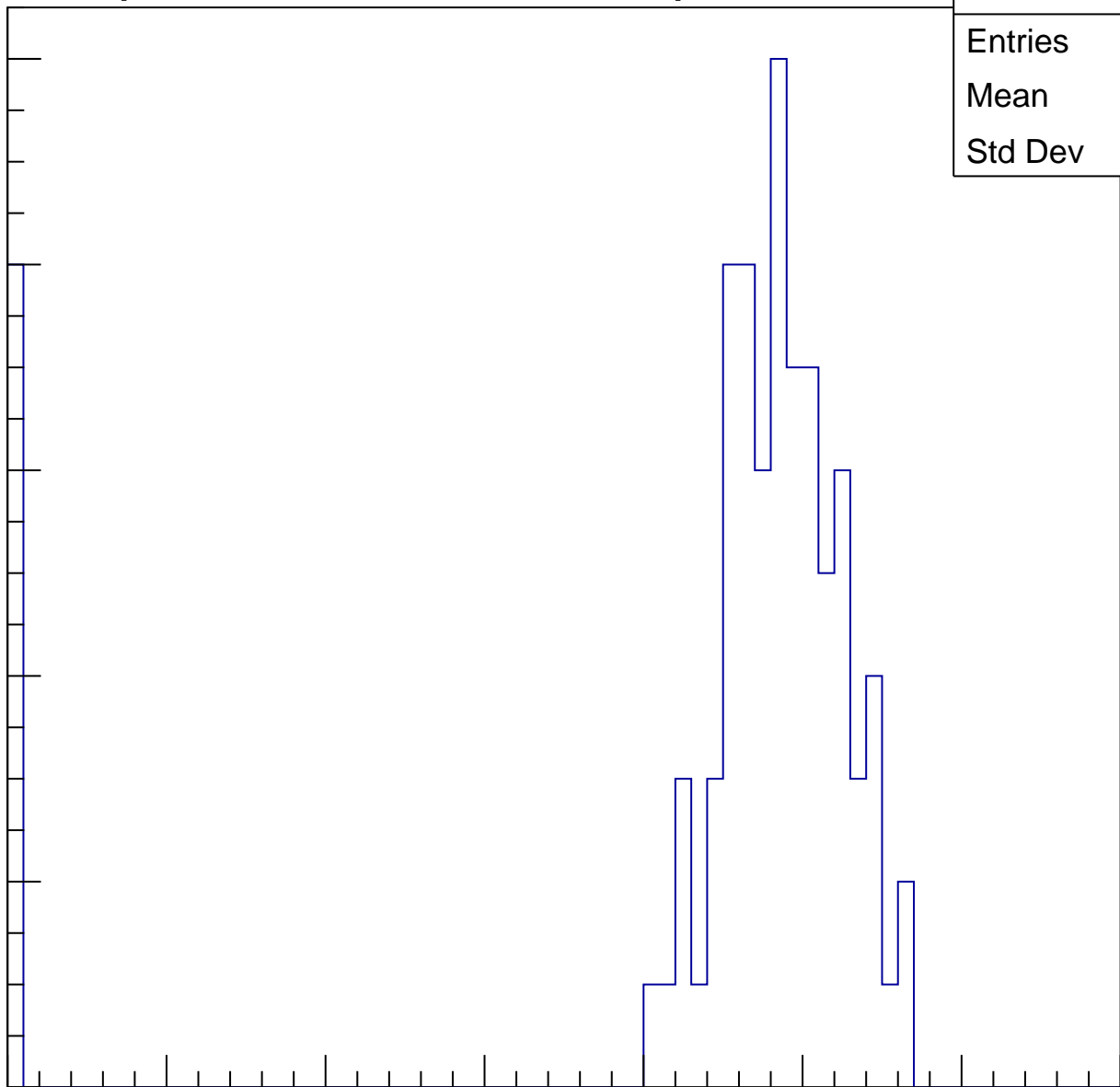
Entries	84
Mean	43.74
Std Dev	14.59

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

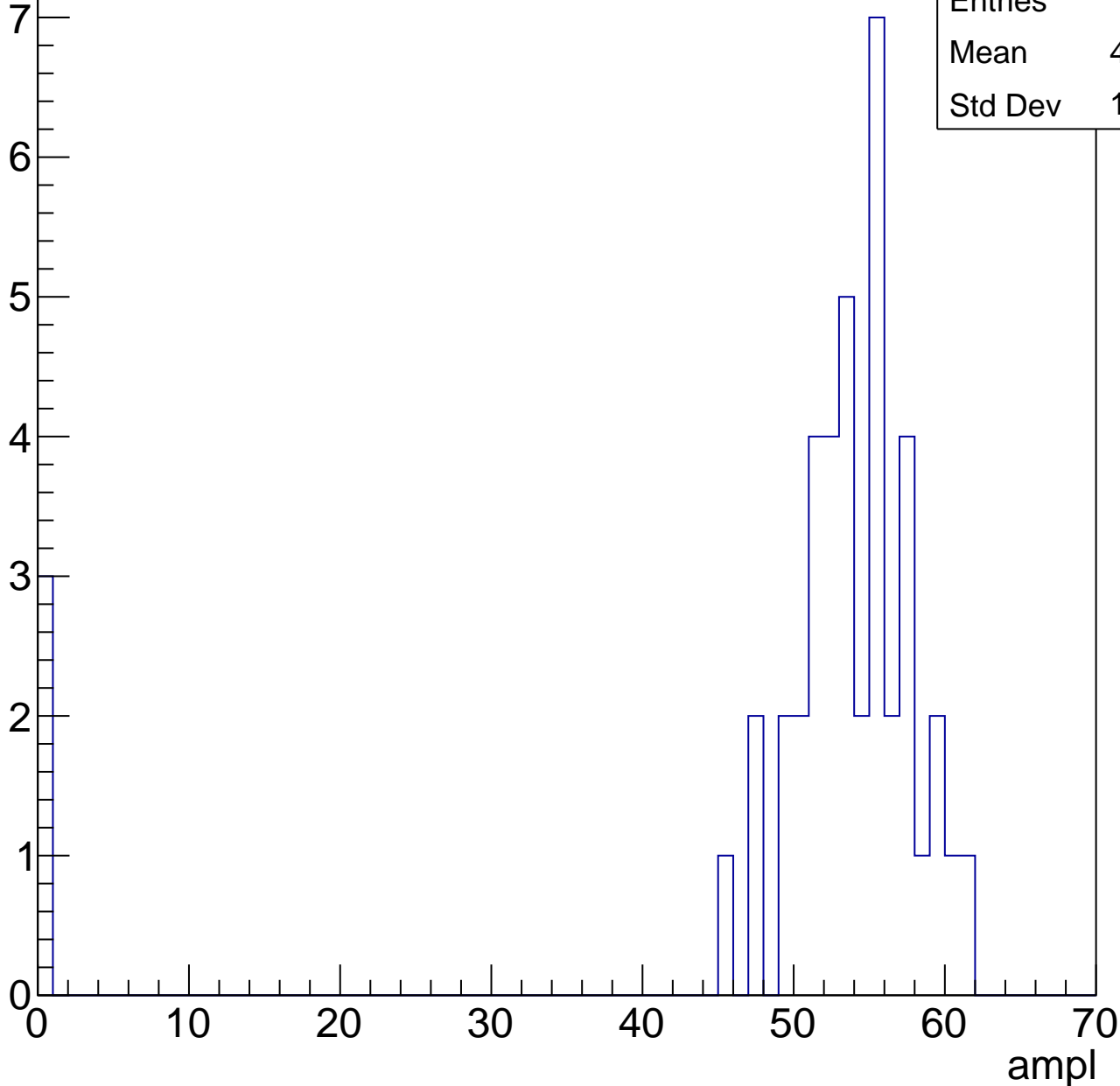


B1L103S, U18-ch113, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

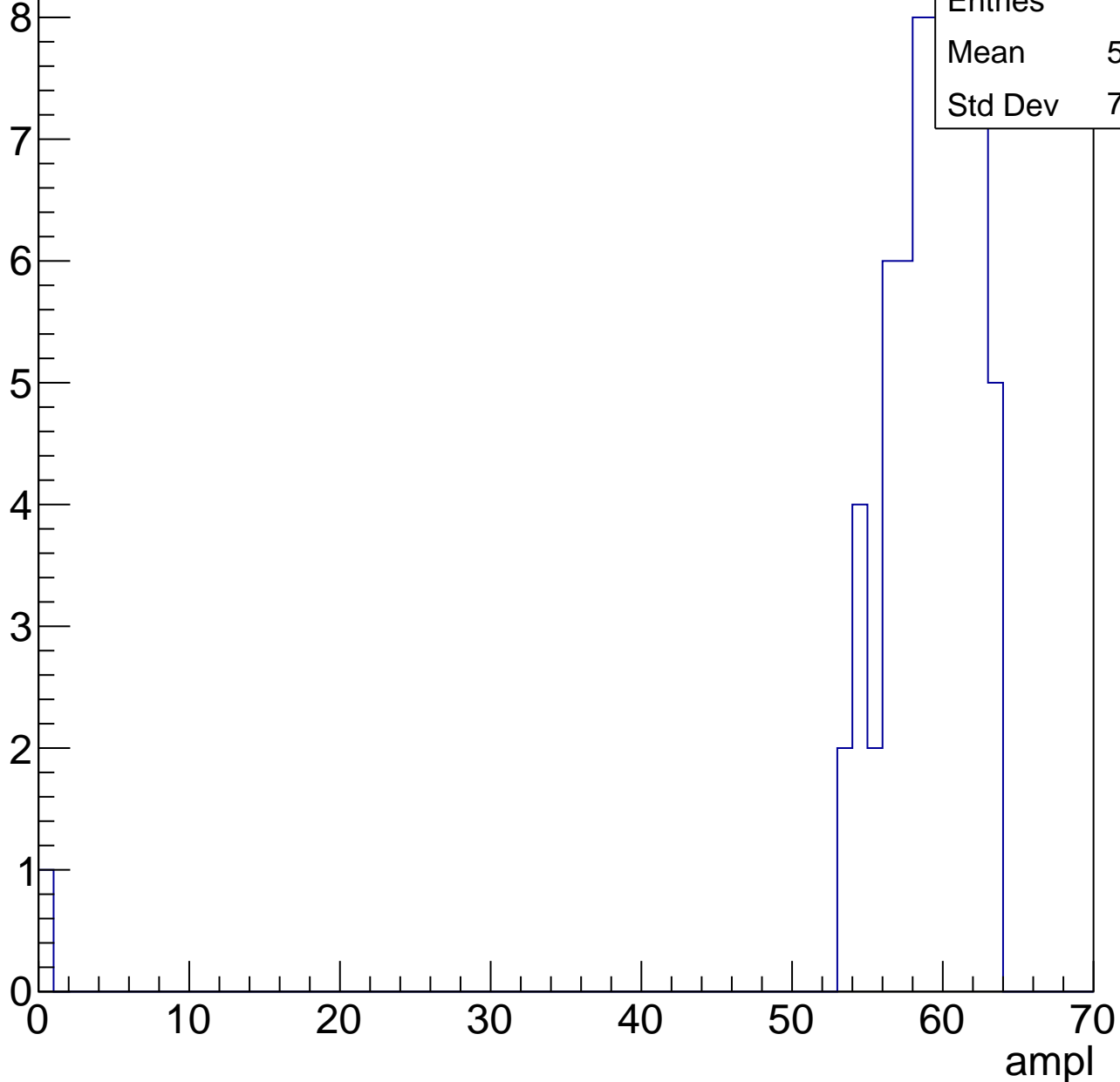
Entries	43
Mean	49.86
Std Dev	14.09



B1L103S, U18-ch113, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

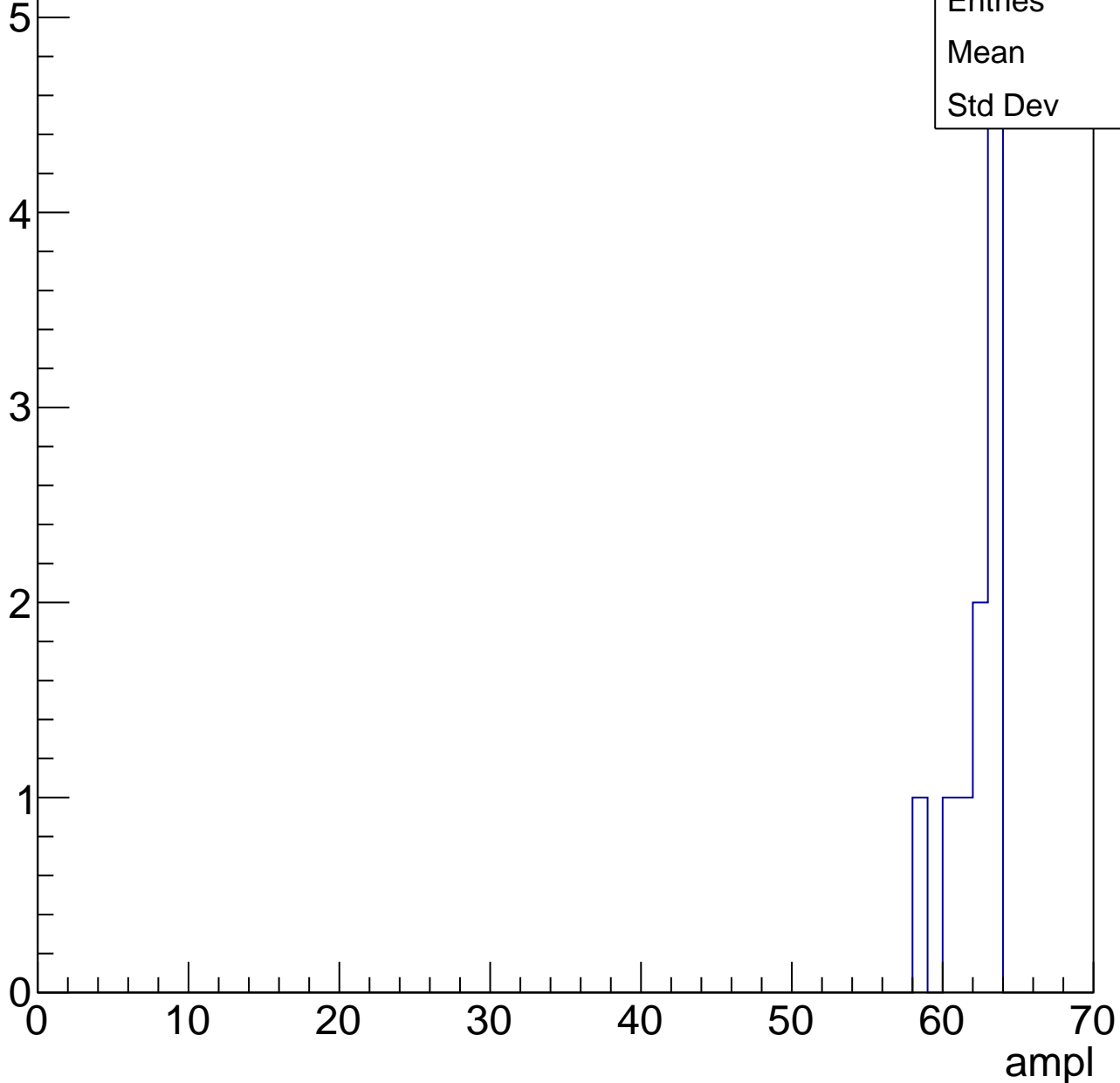


B1L103S, U18-ch113, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	10
Mean	61.8
Std Dev	1.6



B1L103S, U18-ch113, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch114, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	87
Mean	22.98
Std Dev	11.37

Gaus mean : 29.2708

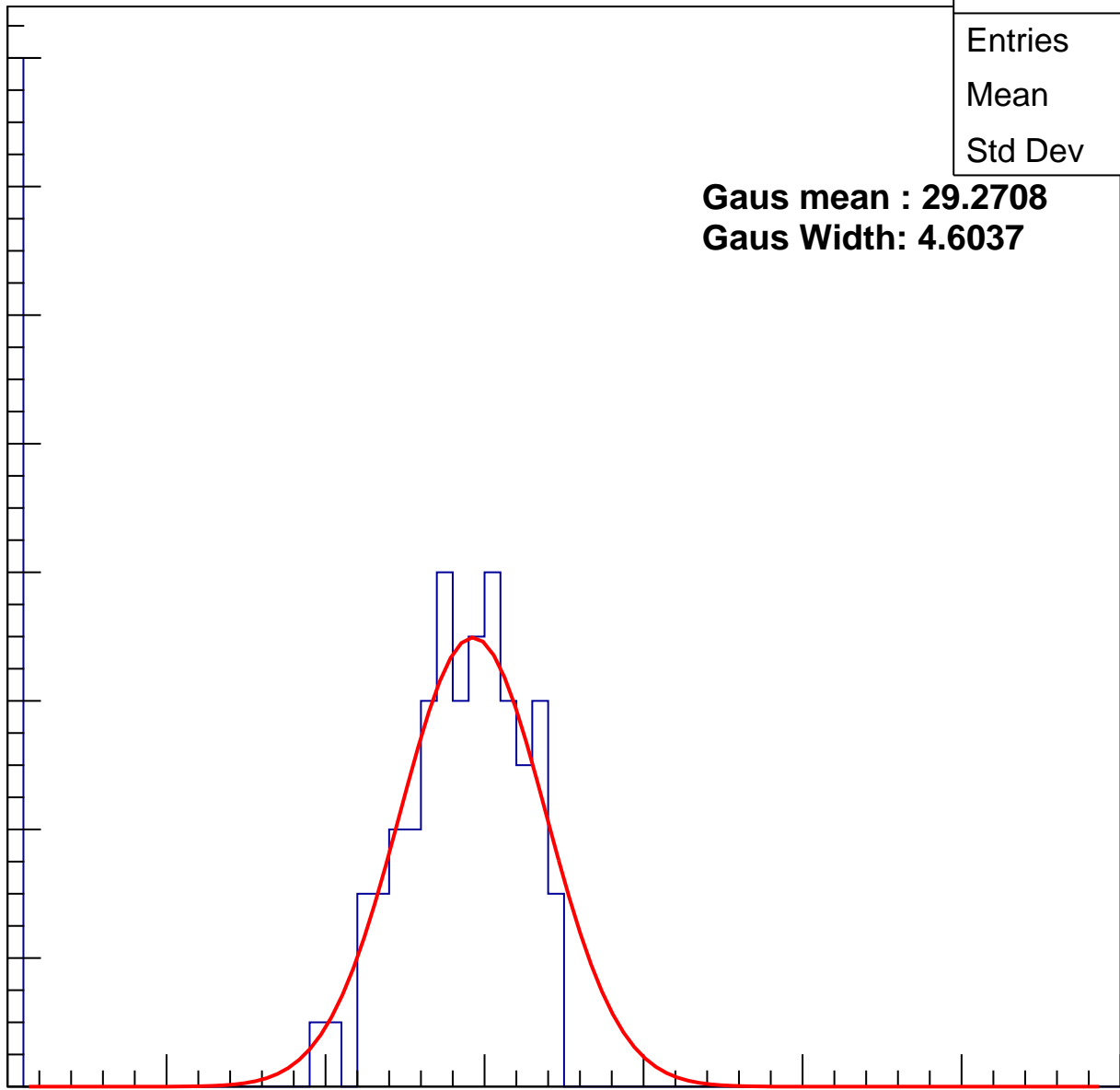
Gaus Width: 4.6037

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch114, adc1

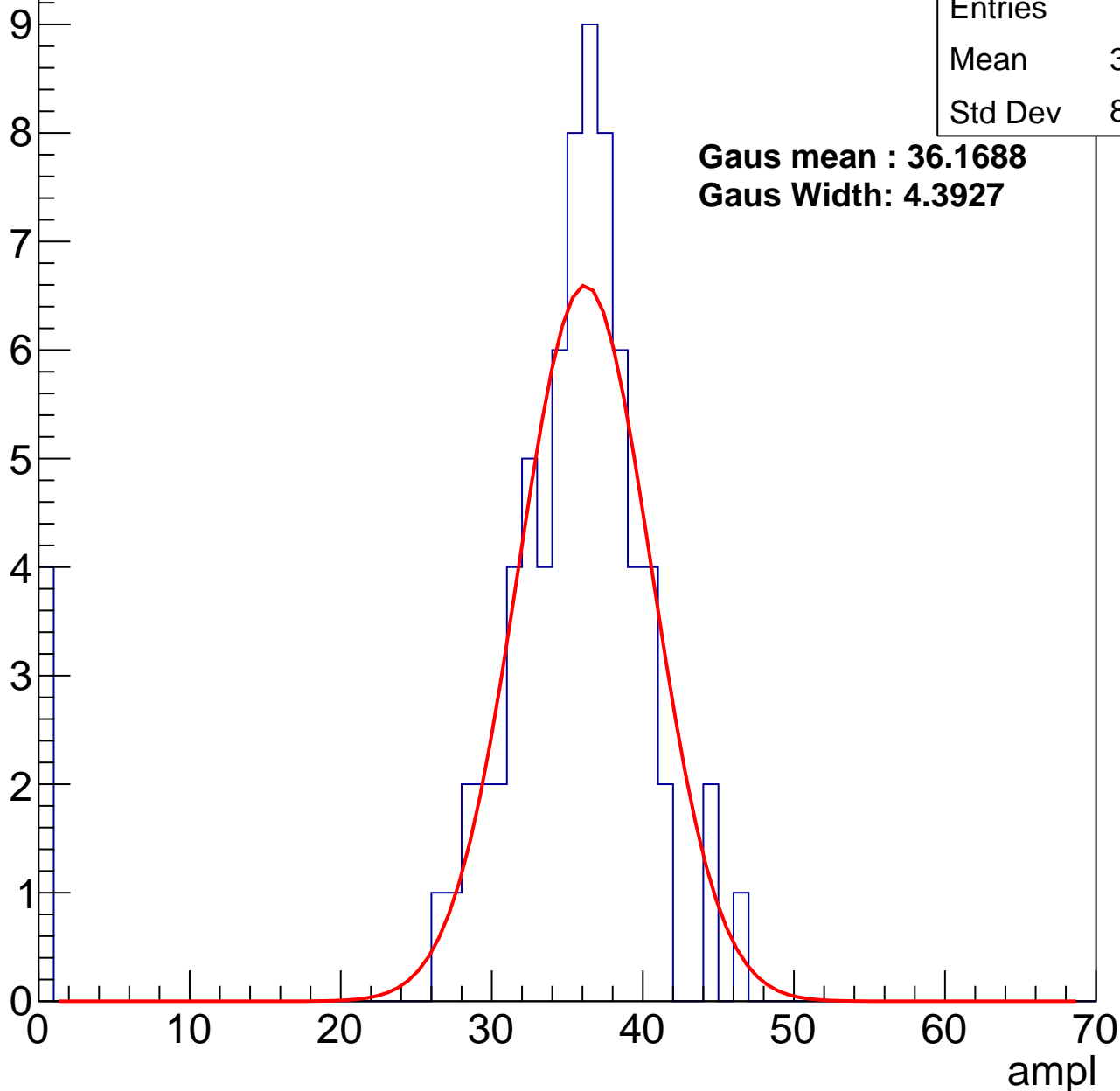
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	33.43
Std Dev	8.812

Gaus mean : 36.1688

Gaus Width: 4.3927



B1L103S, U18-ch114, adc2

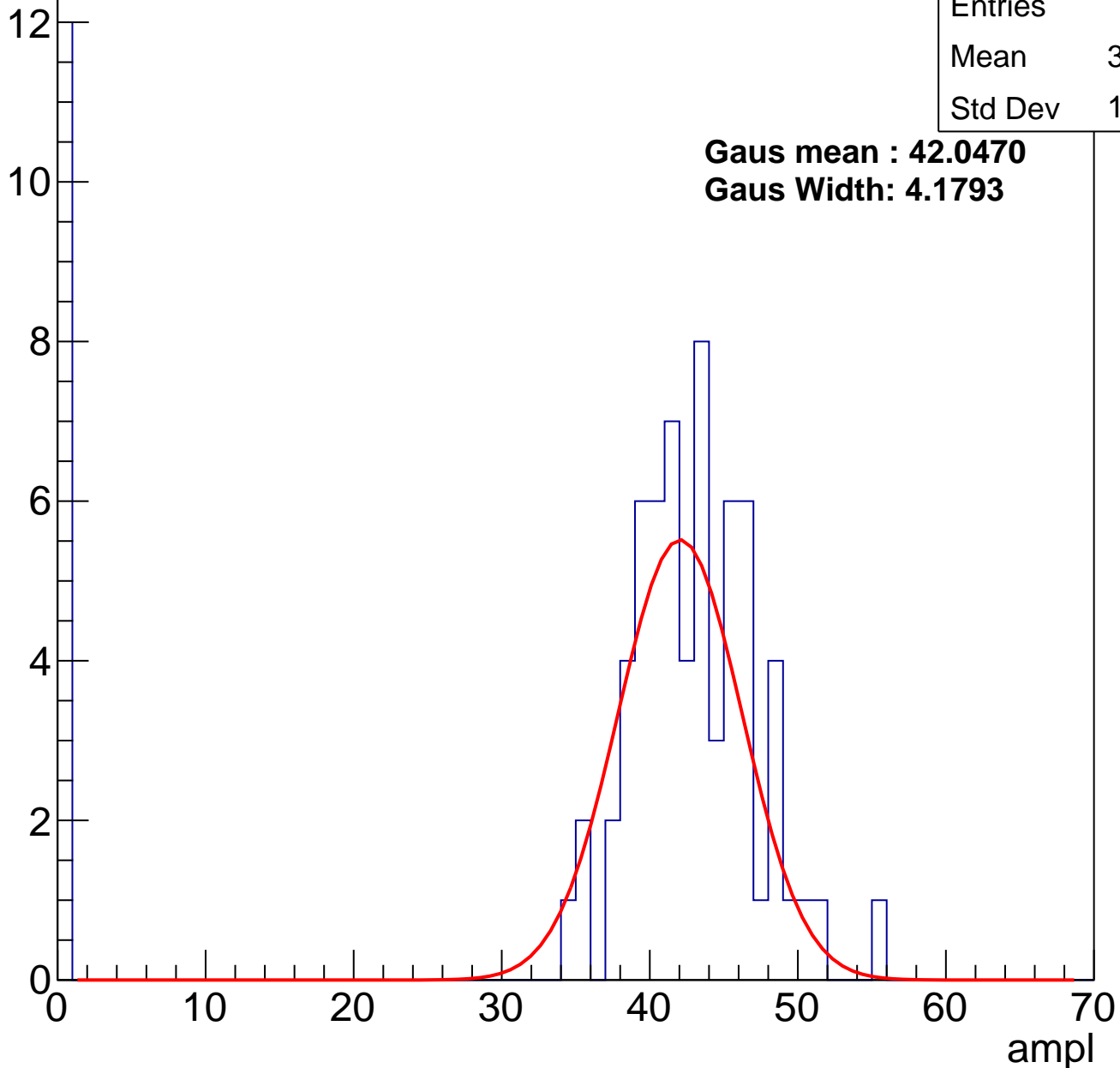
calib_packv5_041523_1651.root, FC#0, port C2

Entries	76
Mean	35.86
Std Dev	15.96

Gaus mean : 42.0470

Gaus Width: 4.1793

Entry

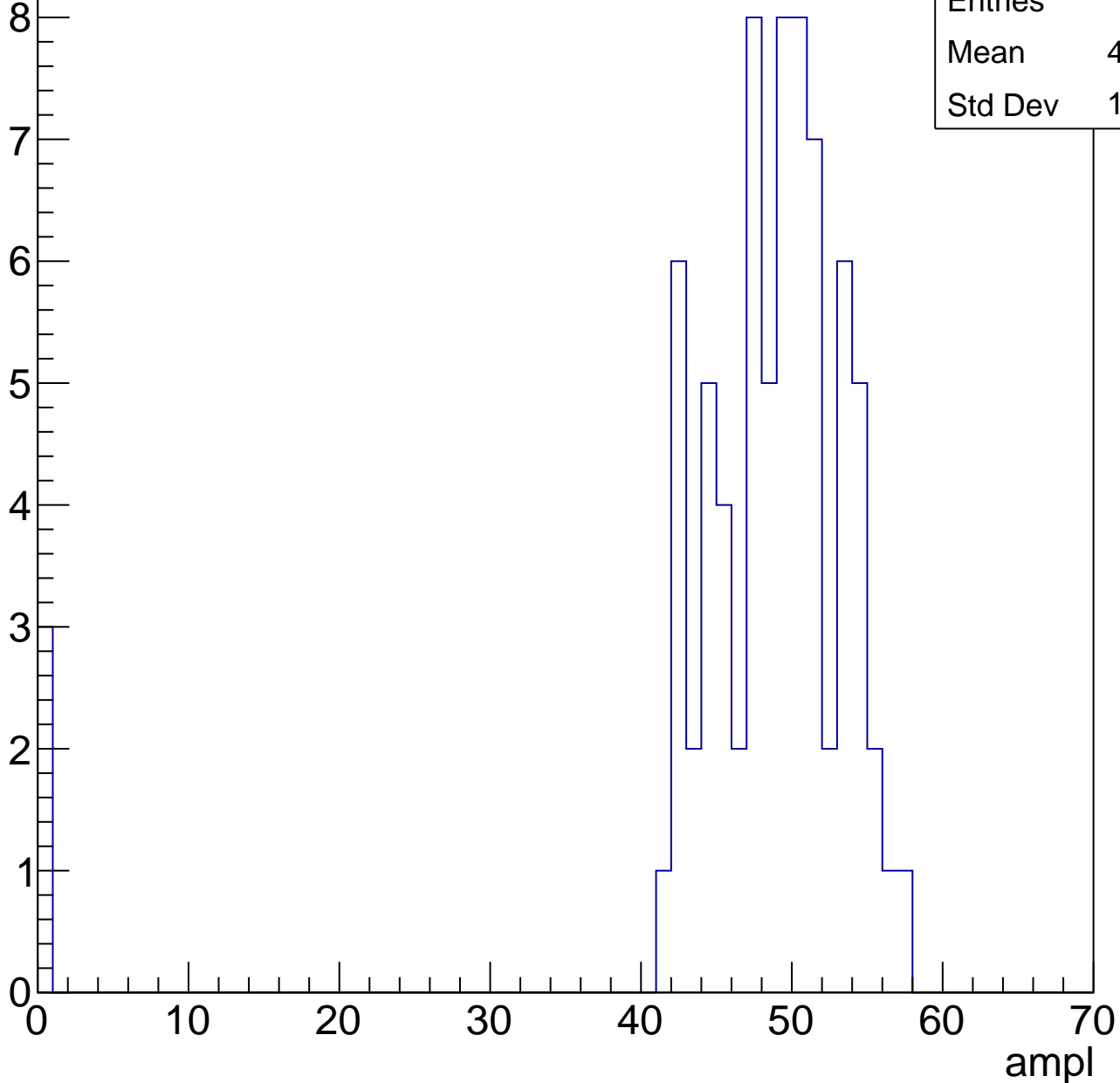


B1L103S, U18-ch114, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	76
Mean	46.72
Std Dev	10.22

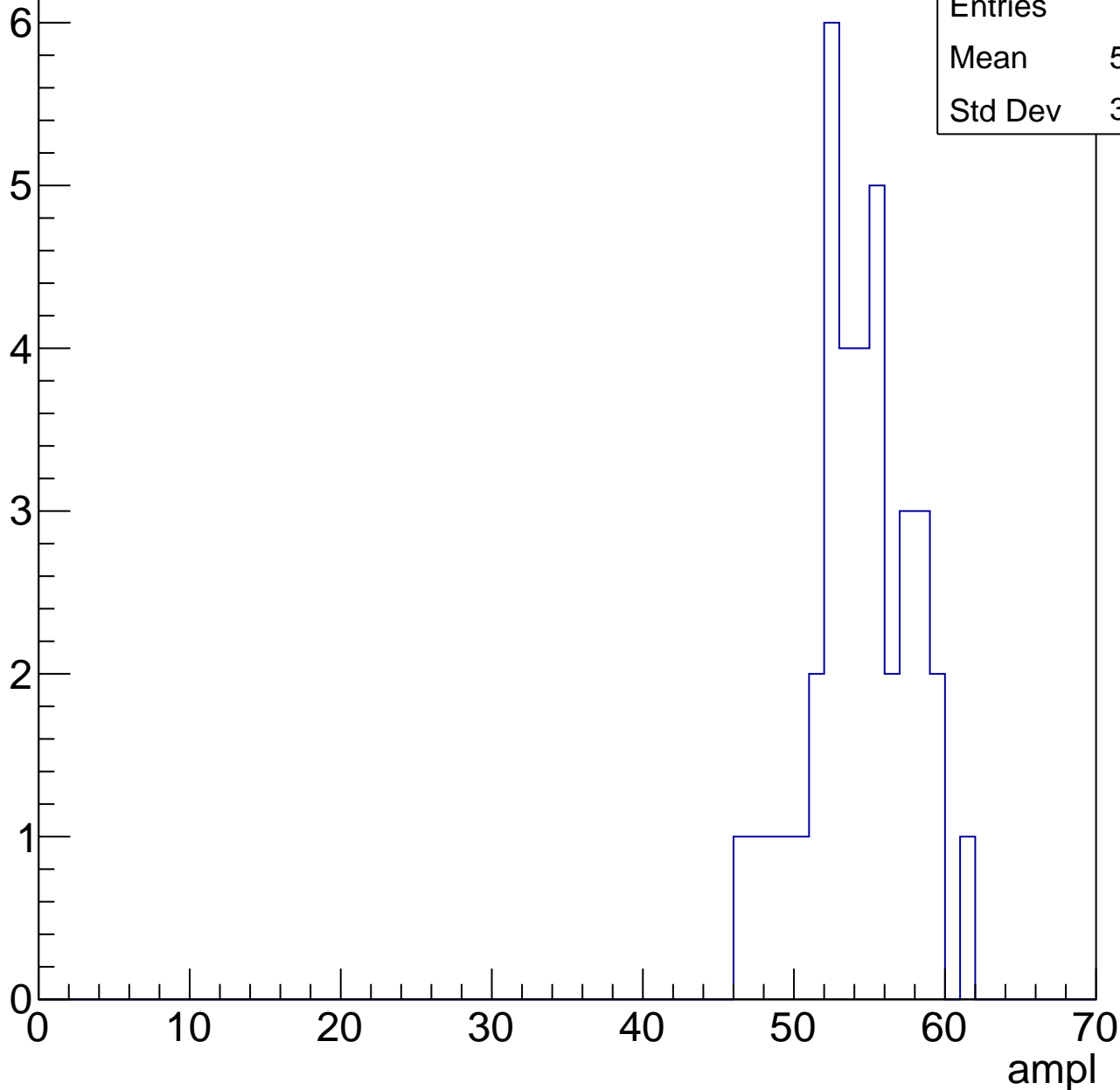


B1L103S, U18-ch114, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	37
Mean	53.86
Std Dev	3.378

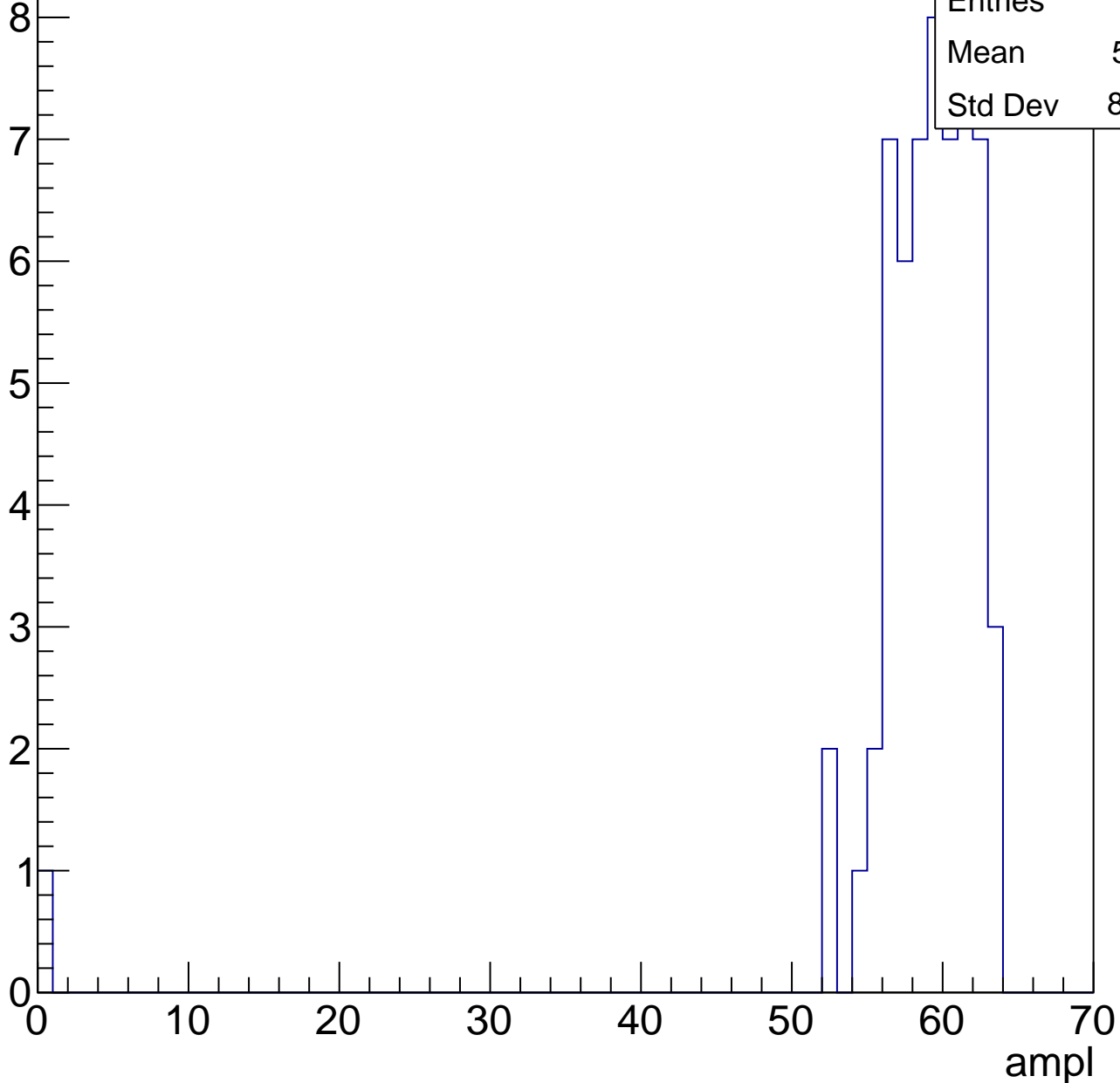


B1L103S, U18-ch114, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	57.81
Std Dev	8.022

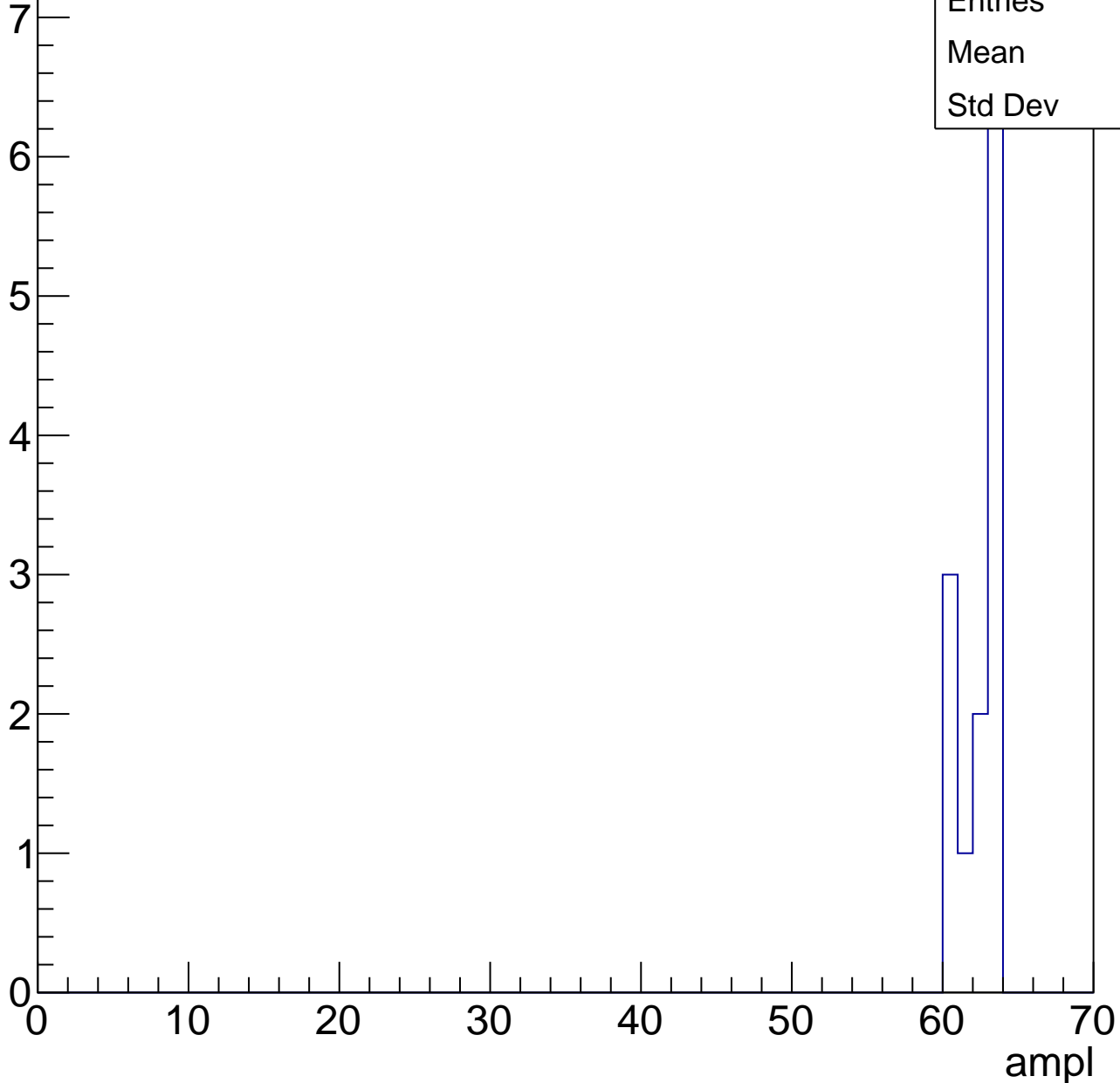


B1L103S, U18-ch114, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

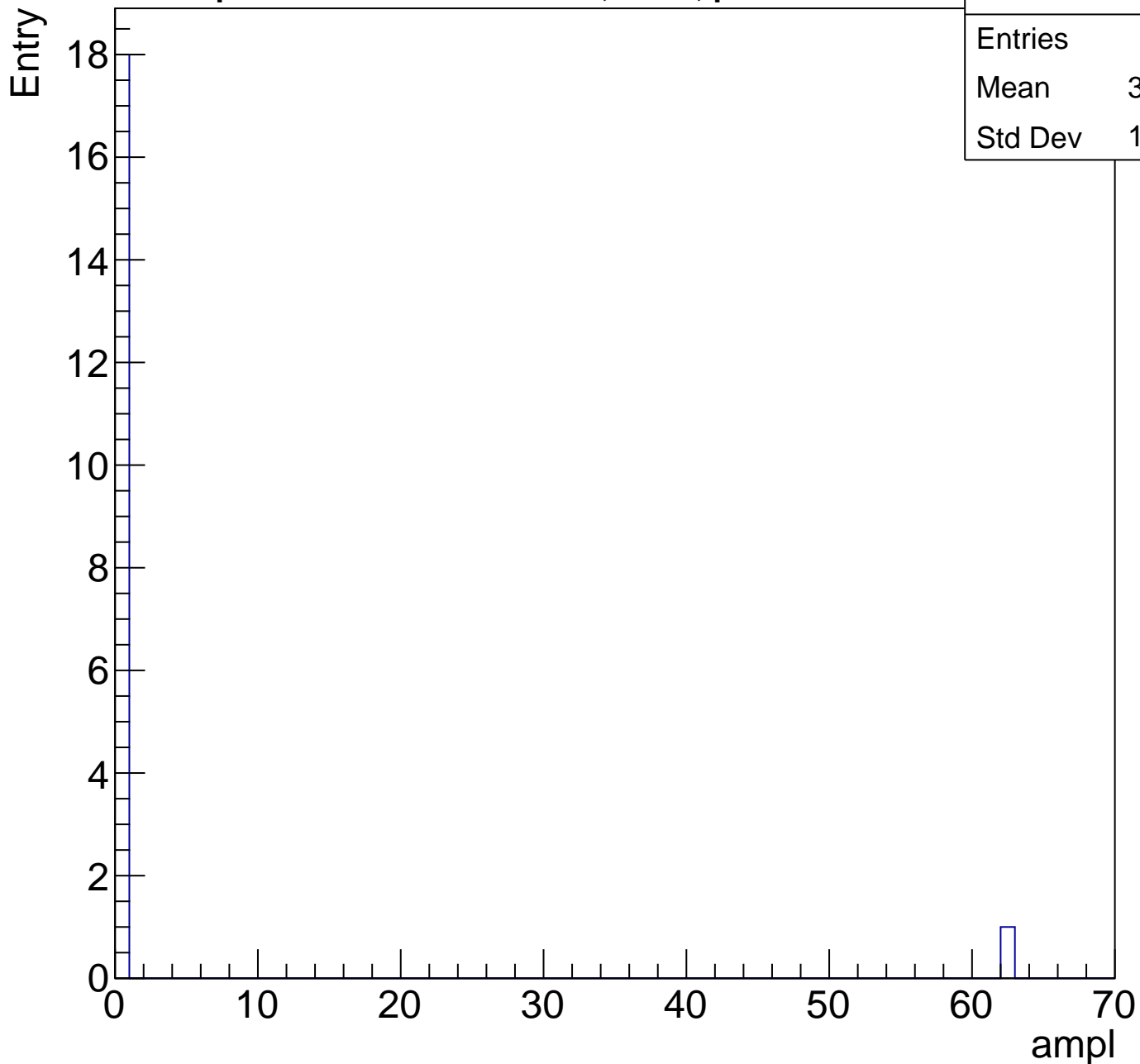
Entries	13
Mean	62
Std Dev	1.24



B1L103S, U18-ch114, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84



B1L103S, U18-ch115, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	18.62
Std Dev	13.43

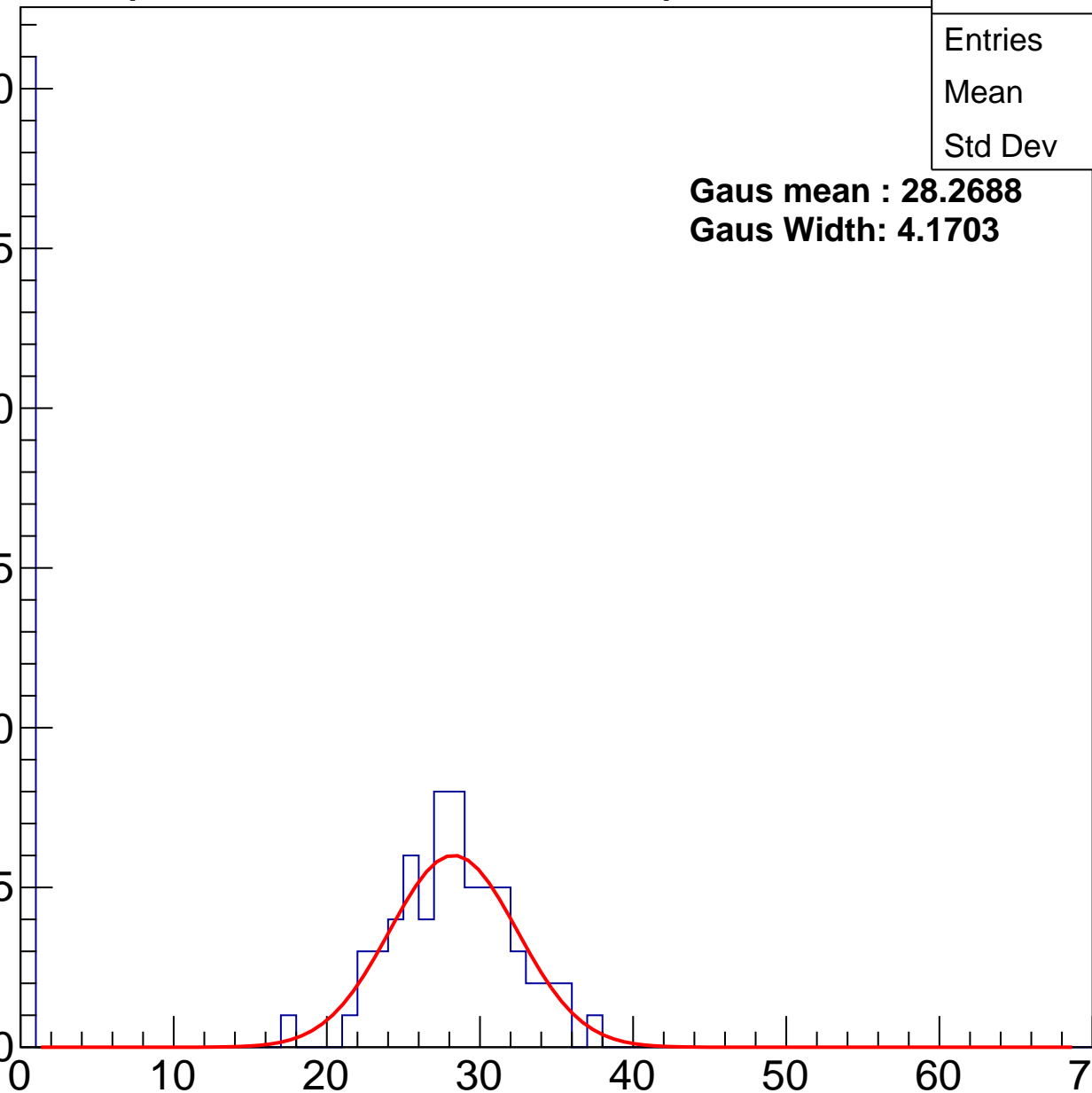
Gaus mean : 28.2688

Gaus Width: 4.1703

Entry

30
25
20
15
10
5
0

ampl



B1L103S, U18-ch115, adc1

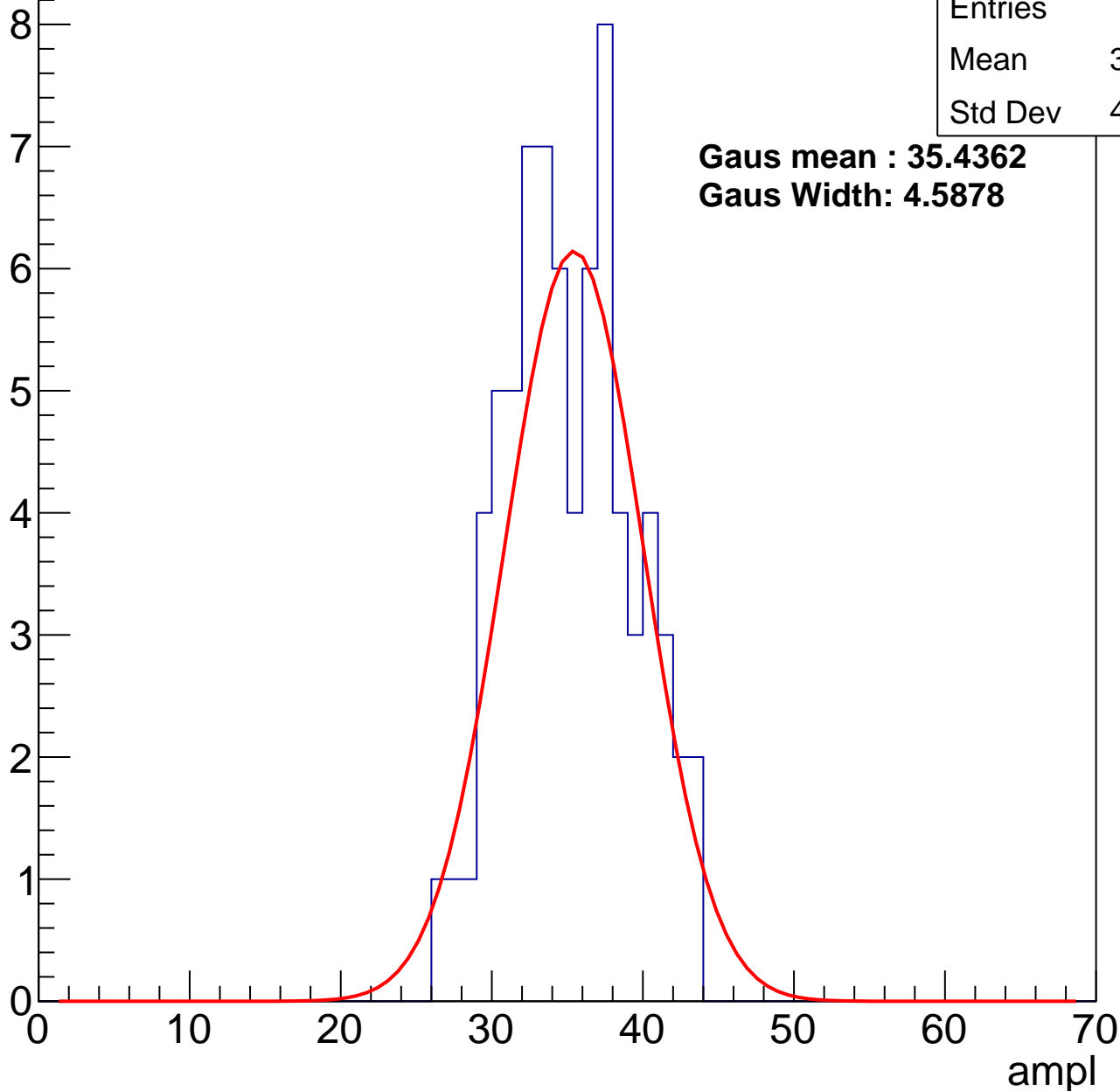
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	34.73
Std Dev	4.038

Gaus mean : 35.4362

Gaus Width: 4.5878



B1L103S, U18-ch115, adc2

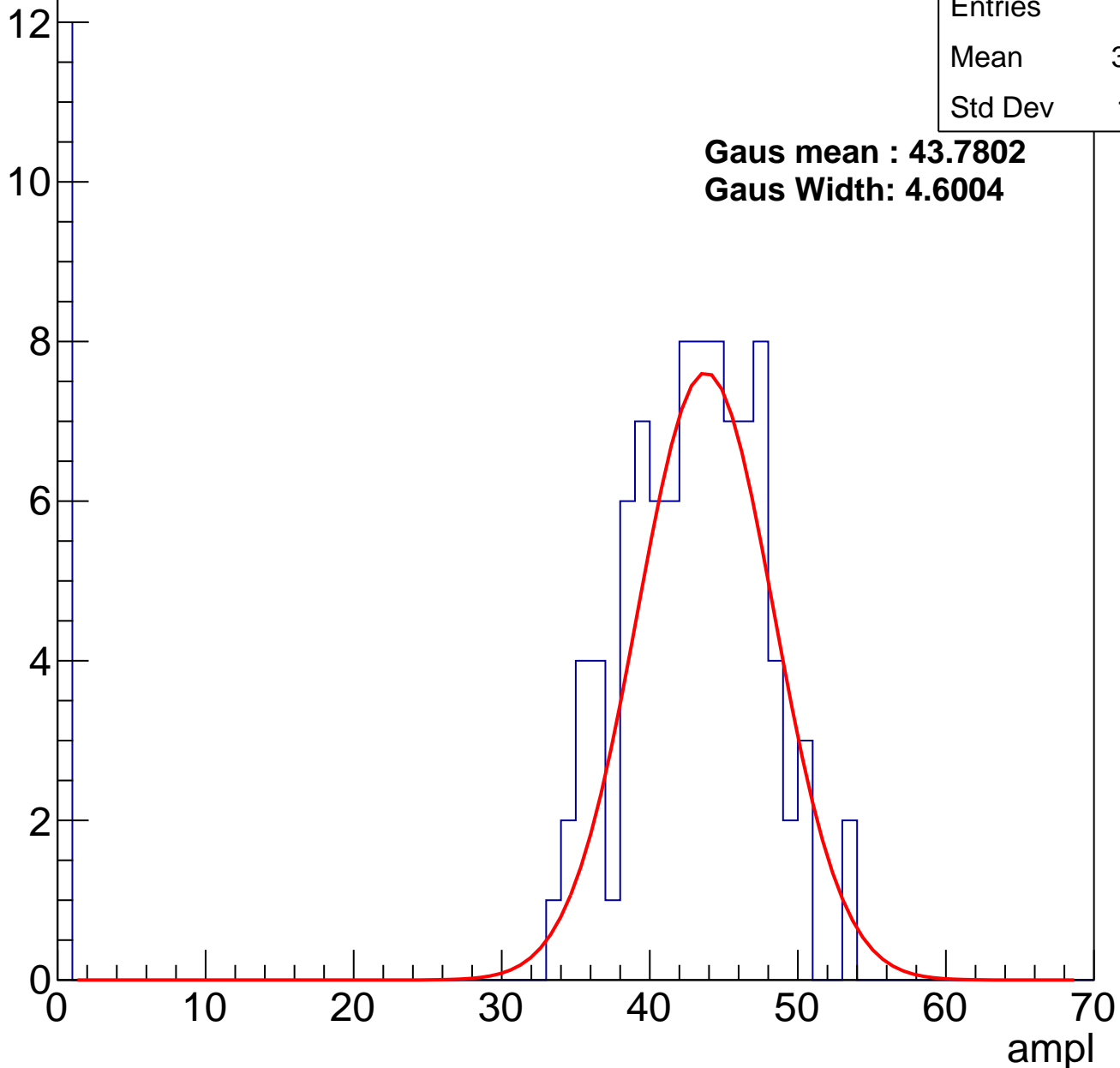
calib_packv5_041523_1651.root, FC#0, port C2

Entries	106
Mean	37.74
Std Dev	14.11

Gaus mean : 43.7802

Gaus Width: 4.6004

Entry

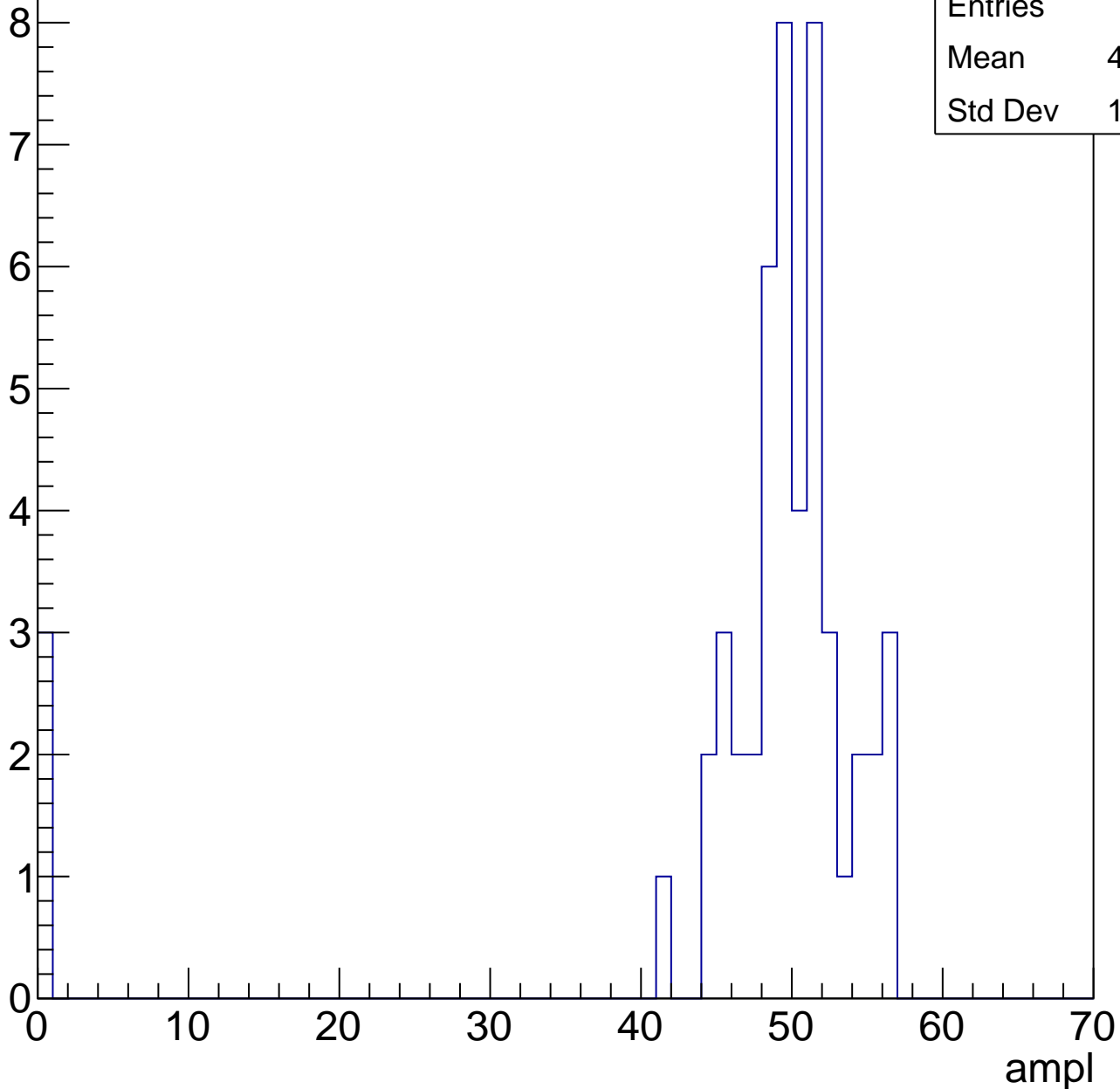


B1L103S, U18-ch115, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	50
Mean	46.66
Std Dev	12.22

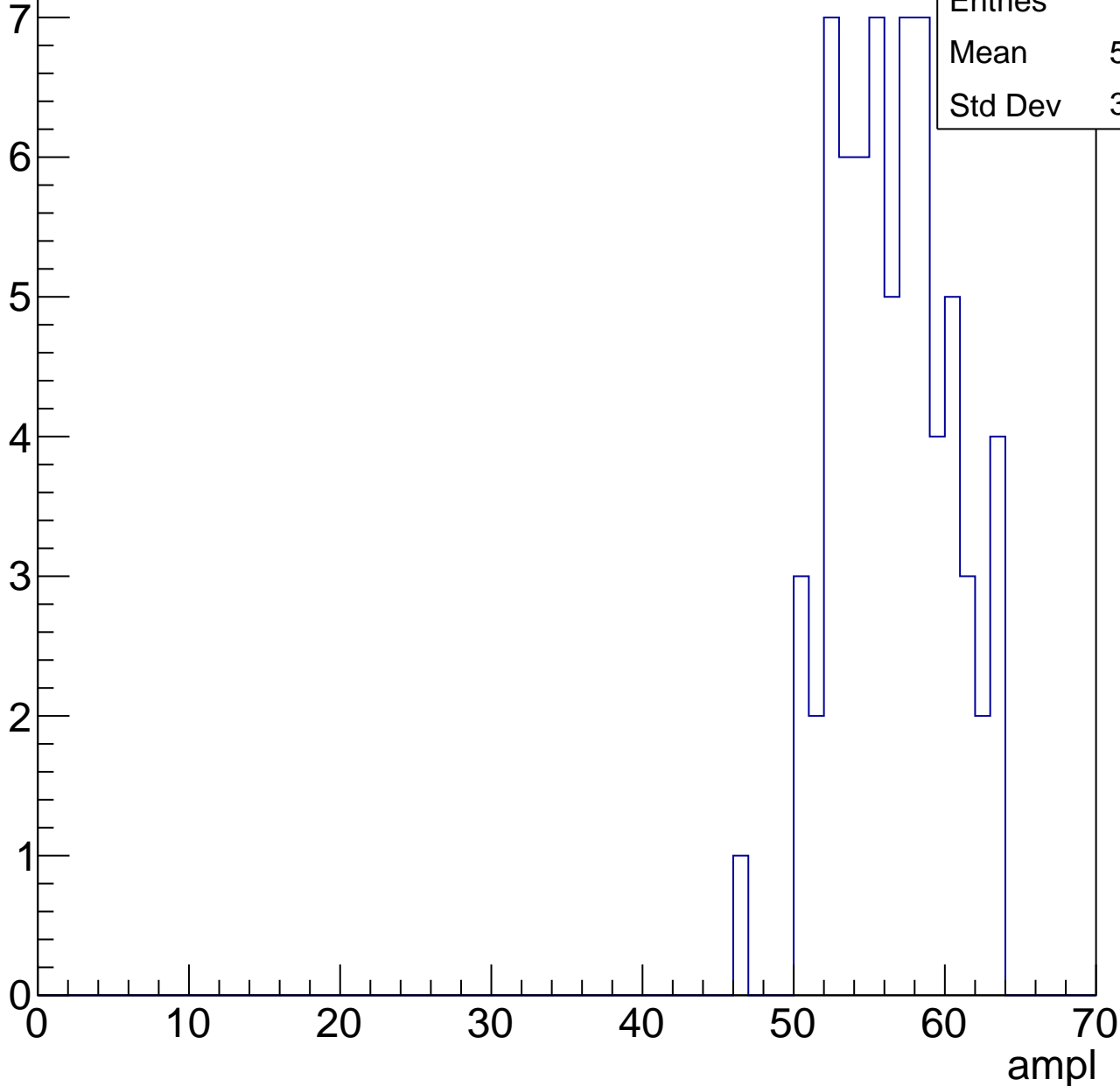


B1L103S, U18-ch115, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

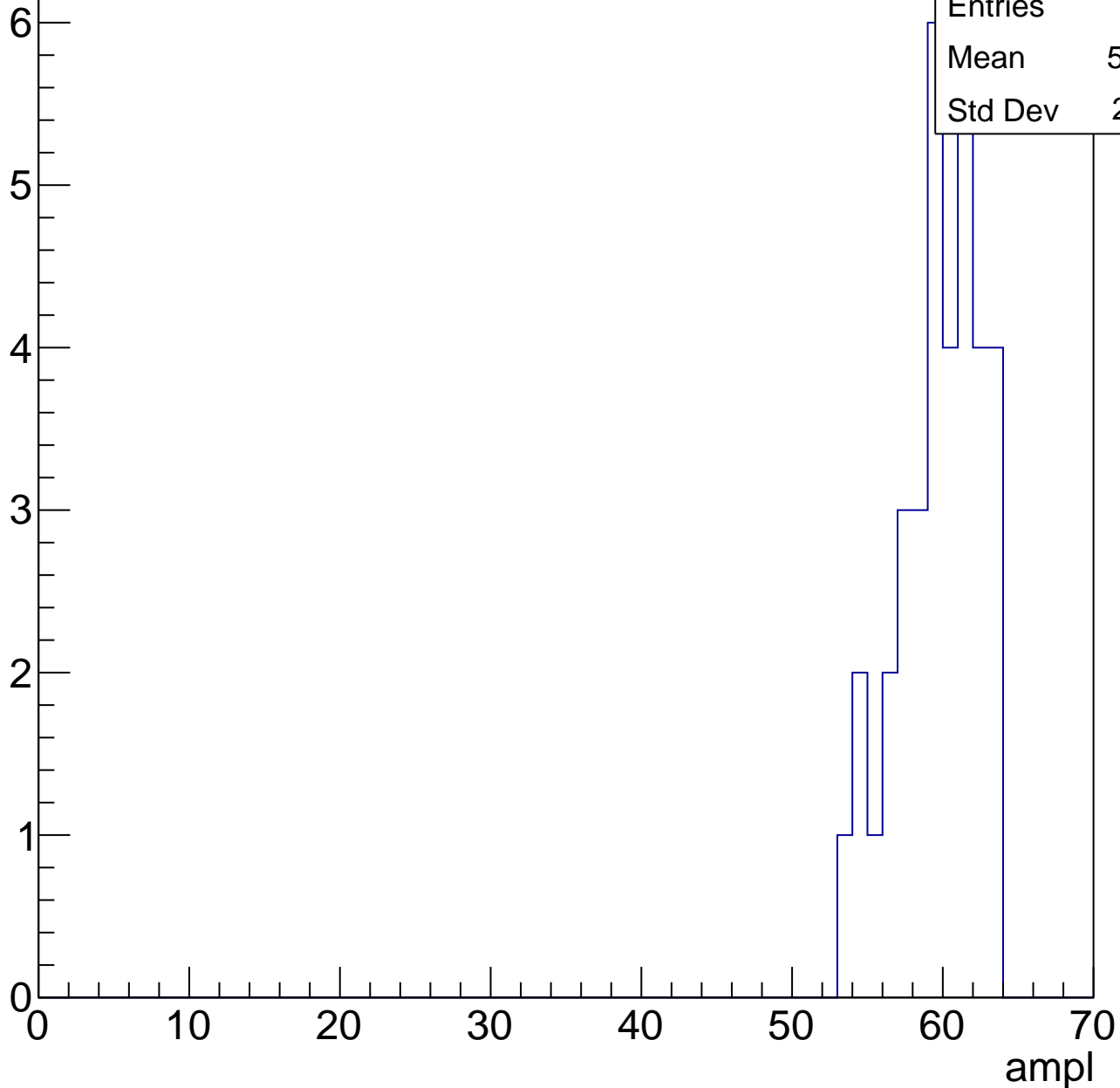
Entries	69
Mean	56.07
Std Dev	3.708



B1L103S, U18-ch115, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

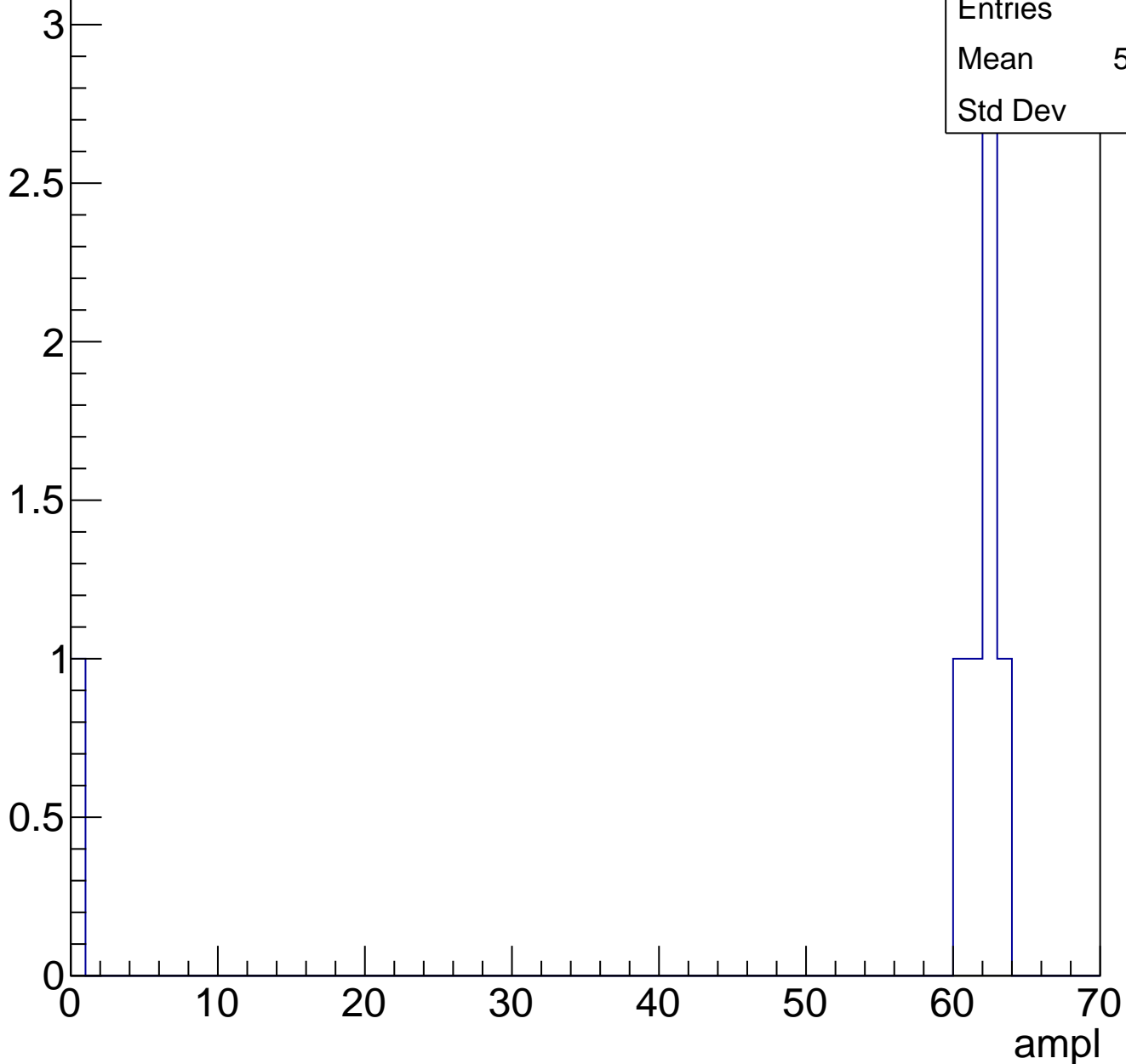


Entries	36
Mean	59.25
Std Dev	2.691

B1L103S, U18-ch115, adc6

calib_packv5_041523_1651.root, FC#0, port C2

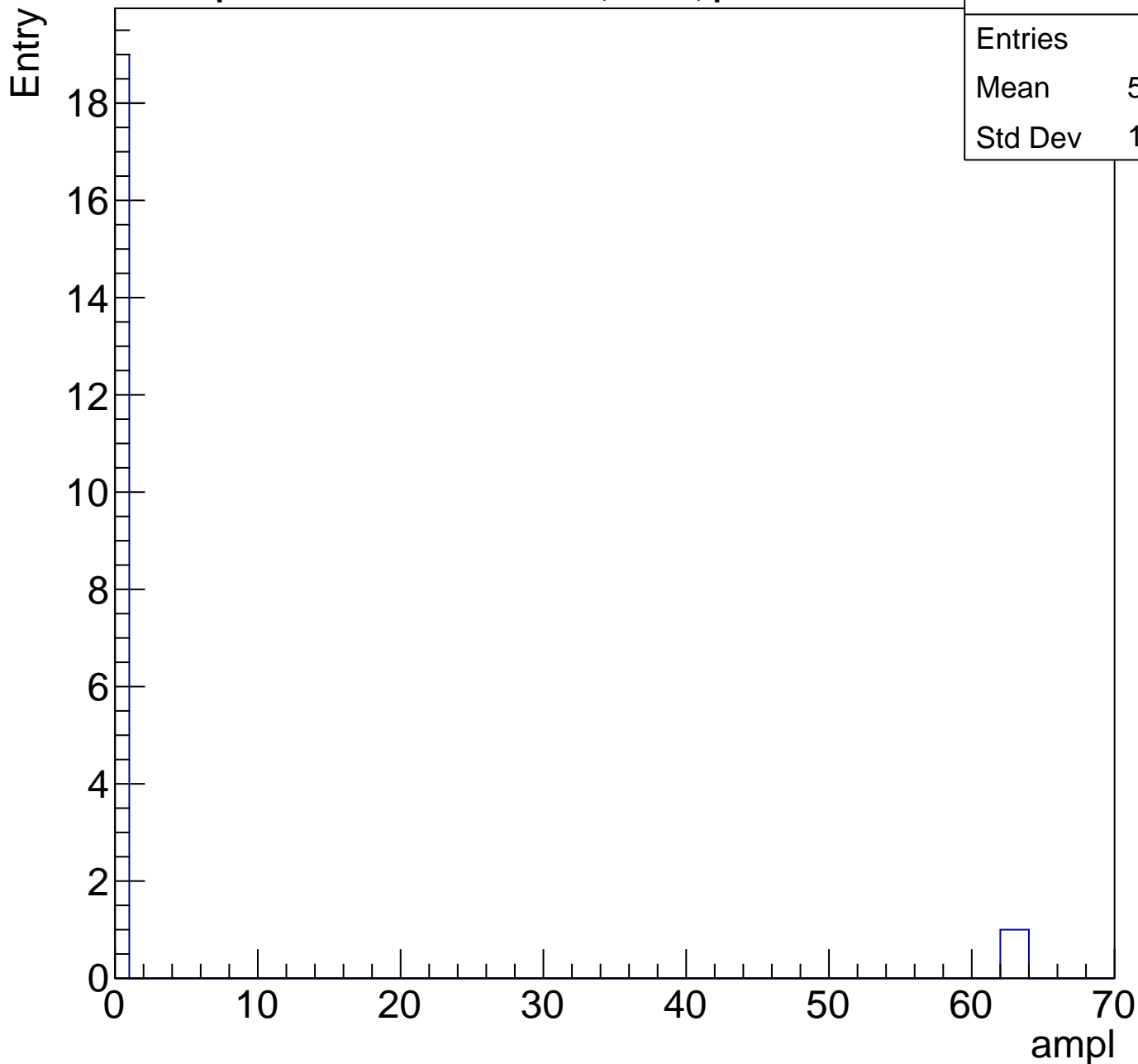
Entry



B1L103S, U18-ch115, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



B1L103S, U18-ch116, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	115
Mean	19.35
Std Dev	12.73

Gaus mean : 27.9425

Gaus Width: 3.8245

Entry

30

25

20

15

10

5

0

0

10

20

30

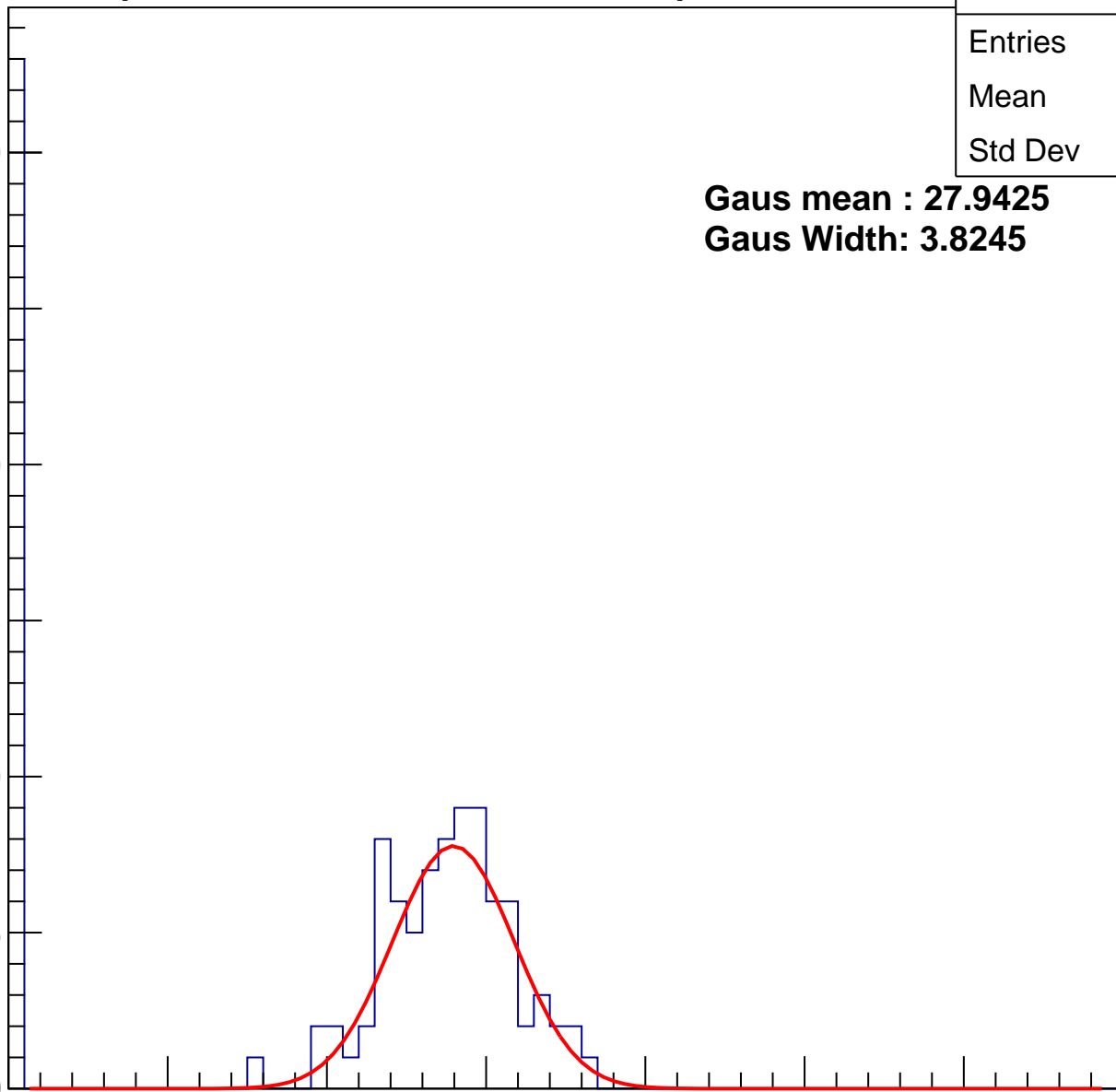
40

50

60

70

ampl



B1L103S, U18-ch116, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entries	73
Mean	29.4
Std Dev	12.88

Gaus mean : 34.2970

Gaus Width: 4.1624

Entry

10

8

6

4

2

0

0

10

20

30

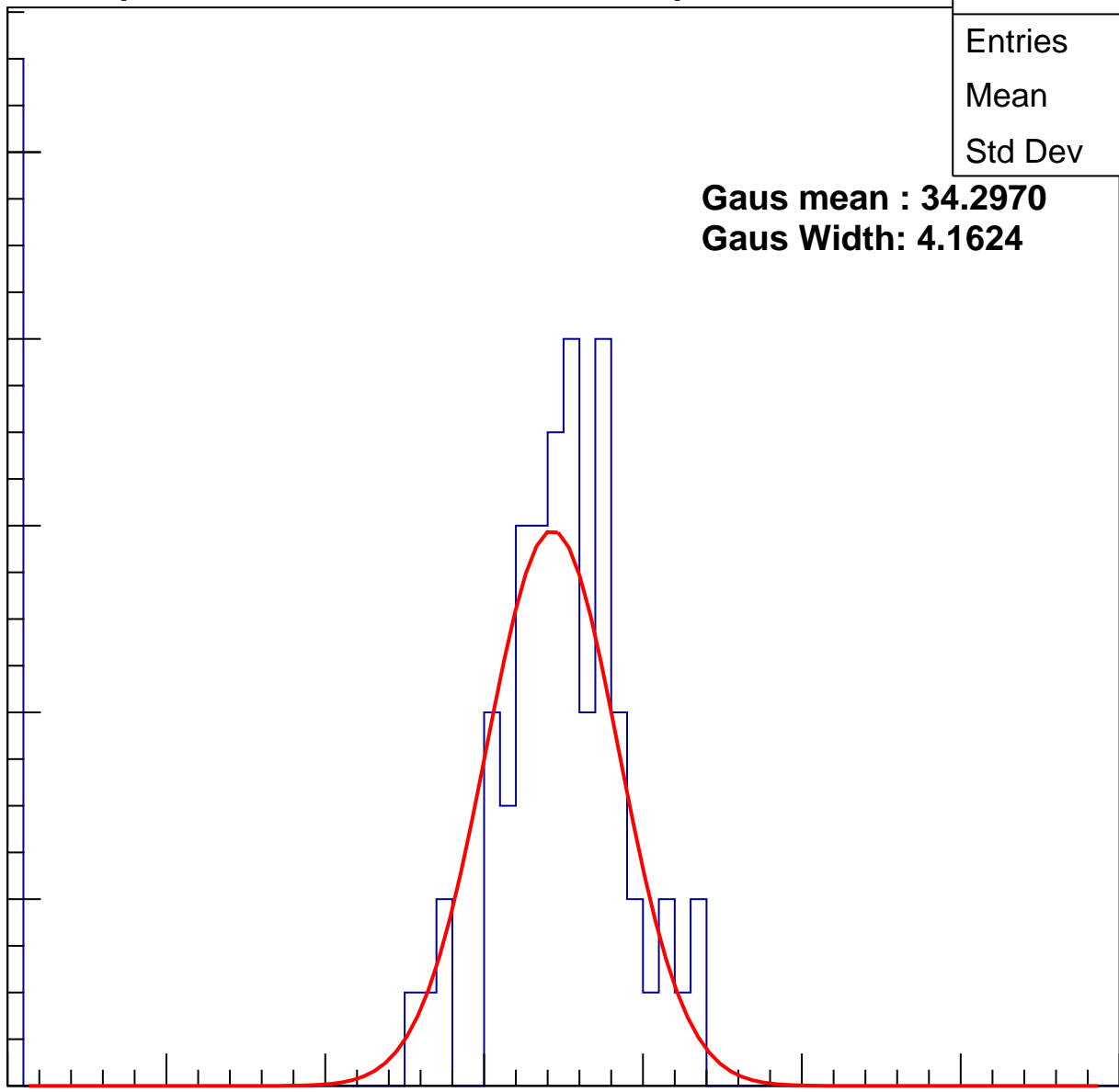
40

50

60

70

ampl



B1L103S, U18-ch116, adc2

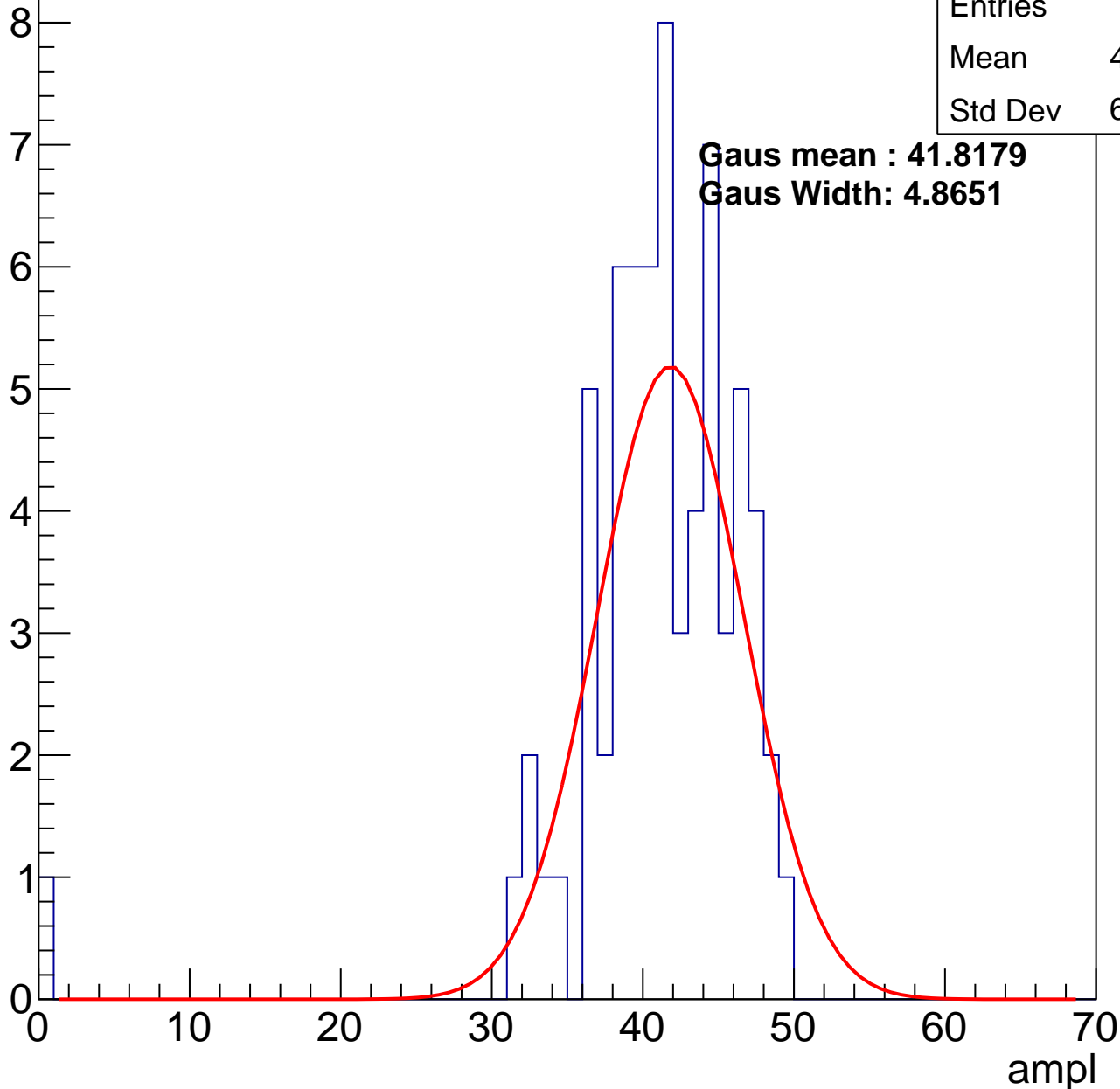
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	40.44
Std Dev	6.459

Gaus mean : 41.8179

Gaus Width: 4.8651

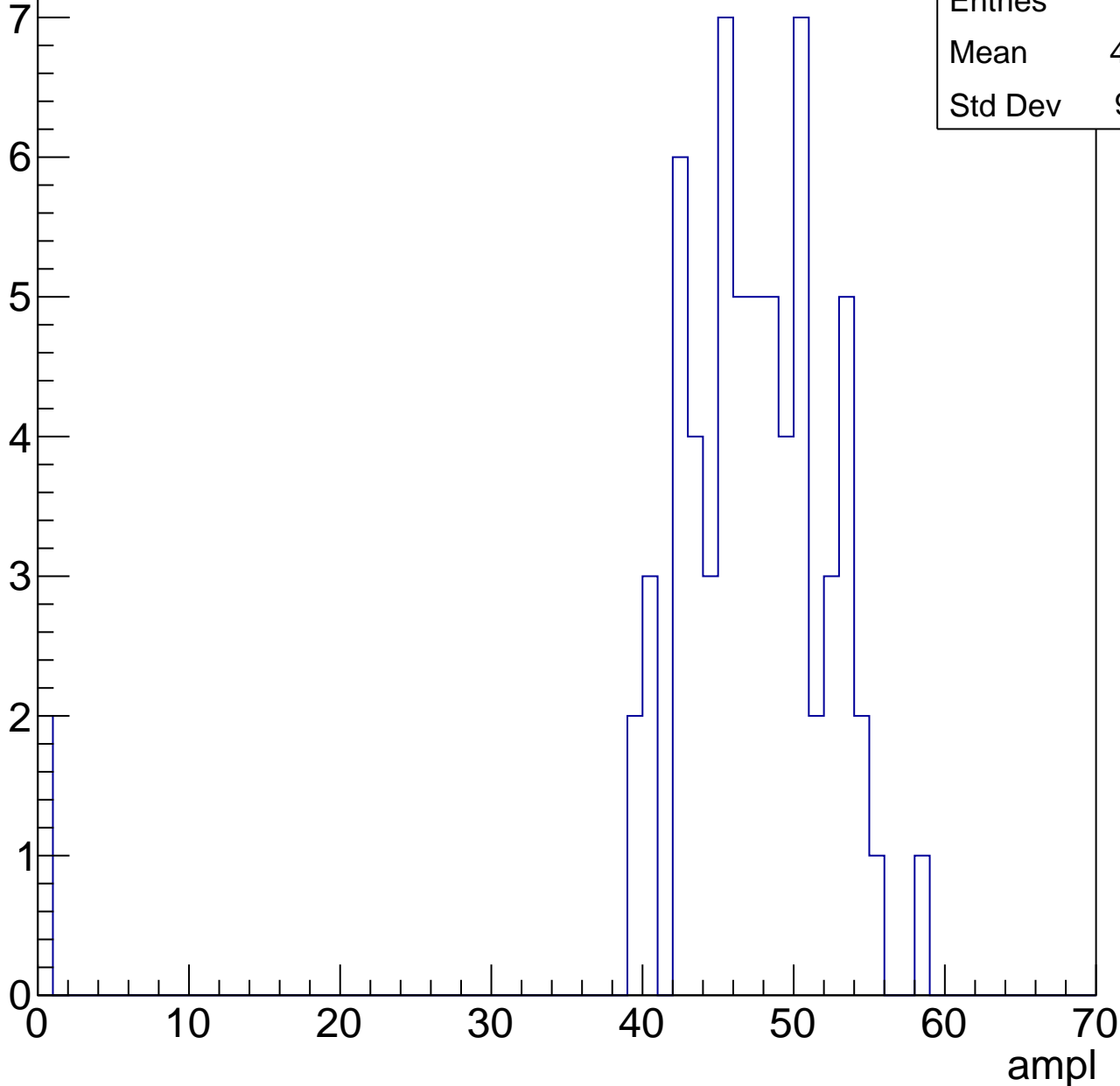


B1L103S, U18-ch116, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	45.73
Std Dev	9.071

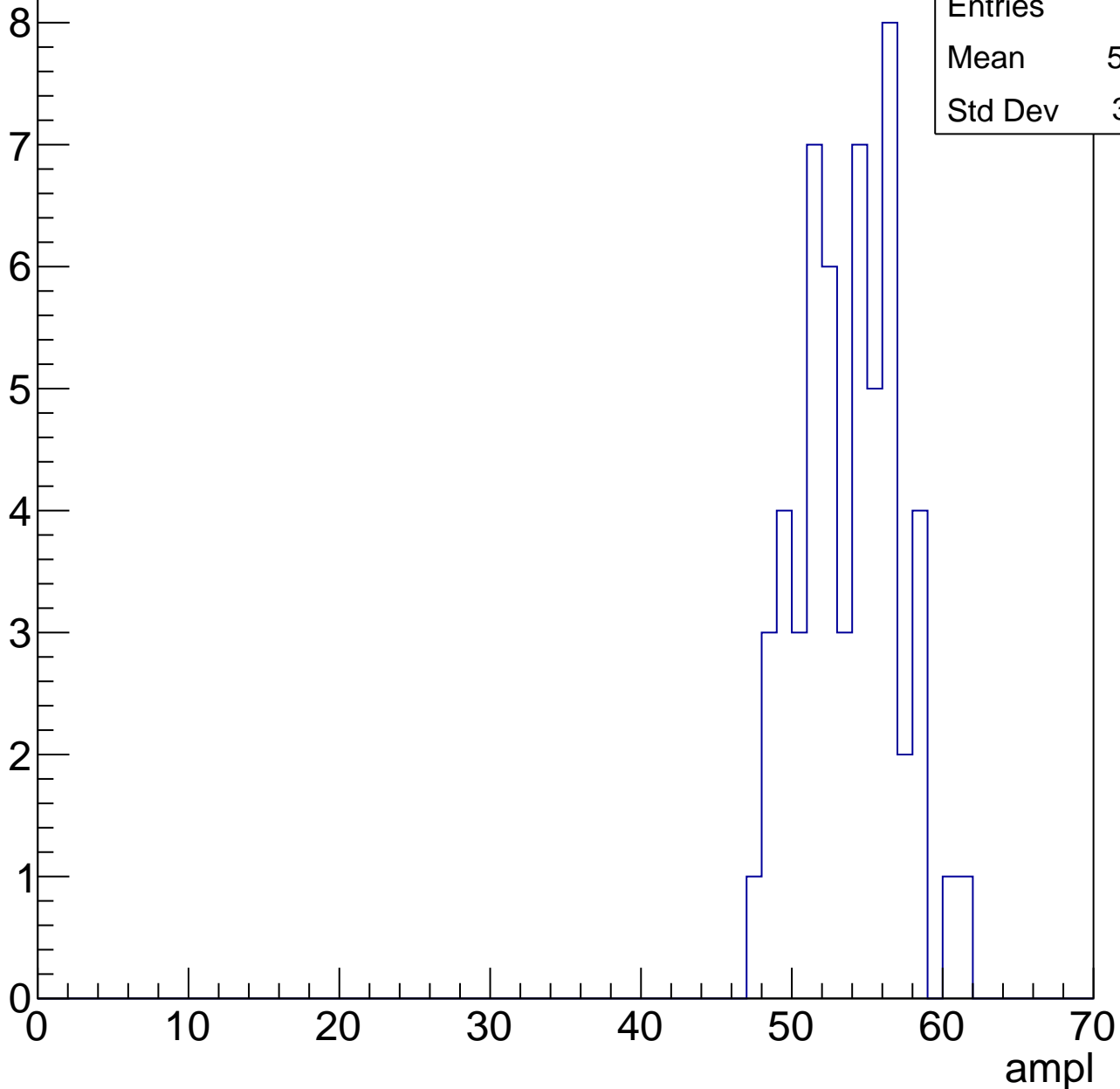


B1L103S, U18-ch116, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	55
Mean	53.33
Std Dev	3.231

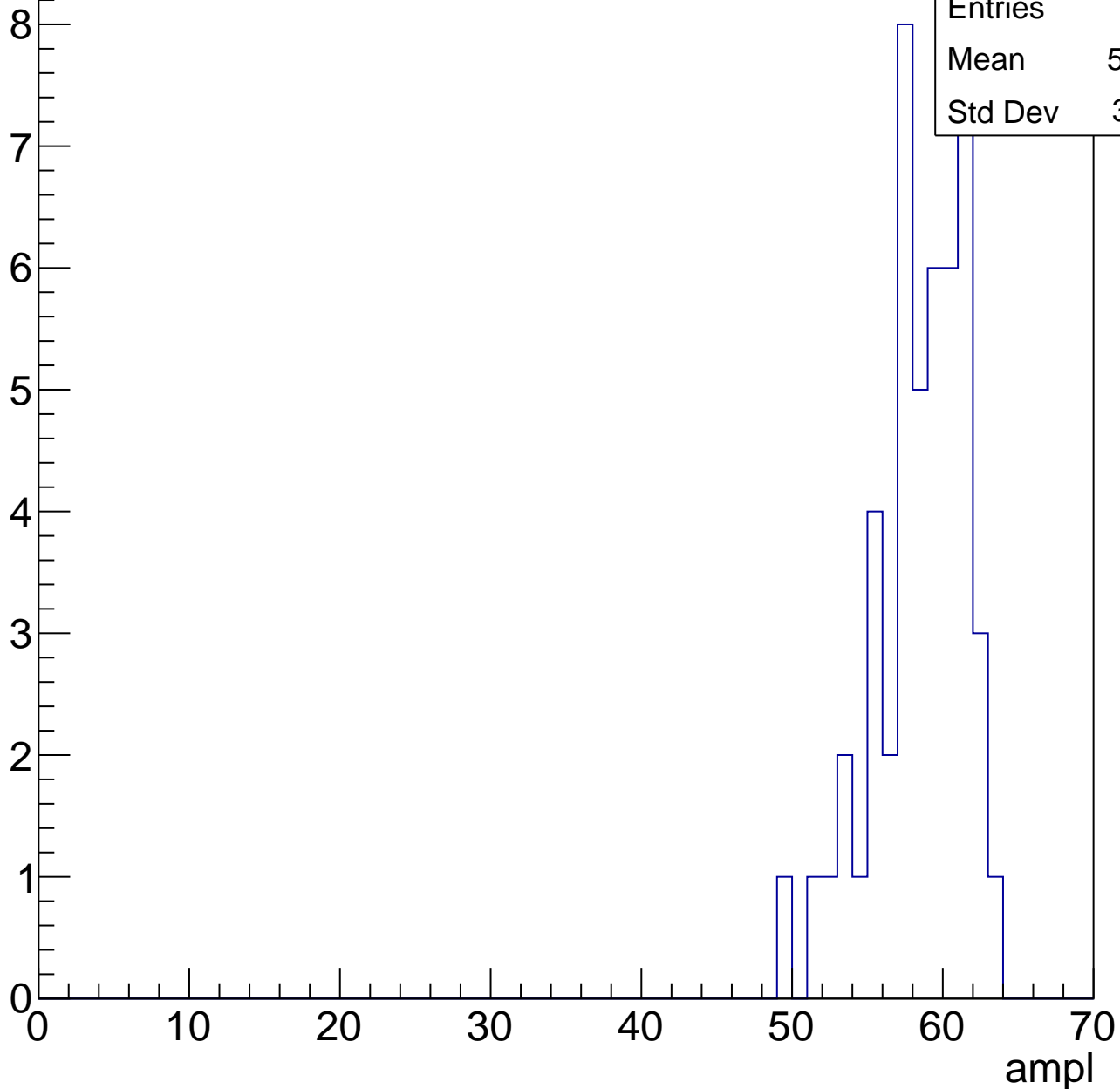


B1L103S, U18-ch116, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	57.98
Std Dev	3.061

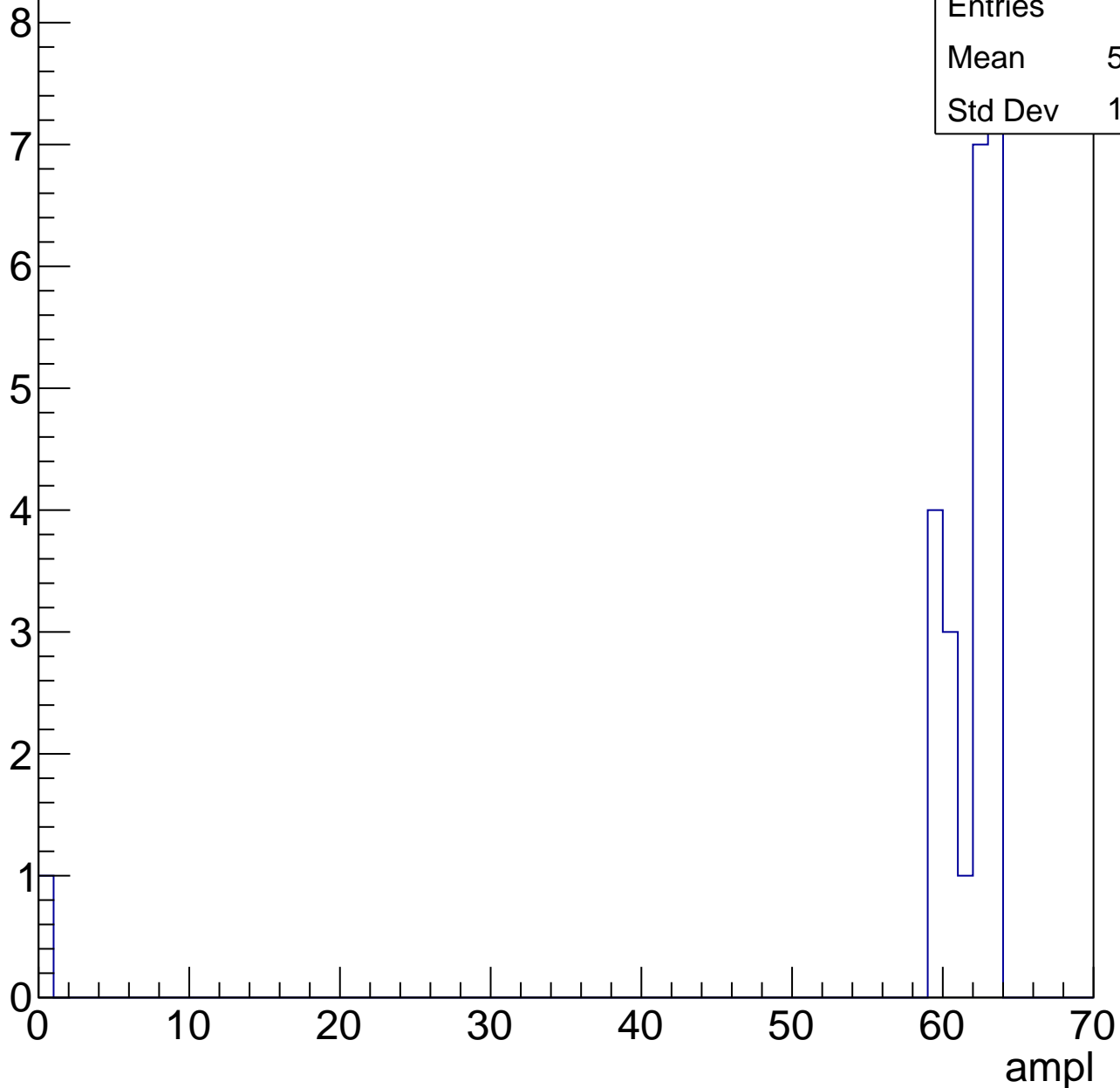


B1L103S, U18-ch116, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.96
Std Dev	12.38



B1L103S, U18-ch116, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



B1L103S, U18-ch117, adc0

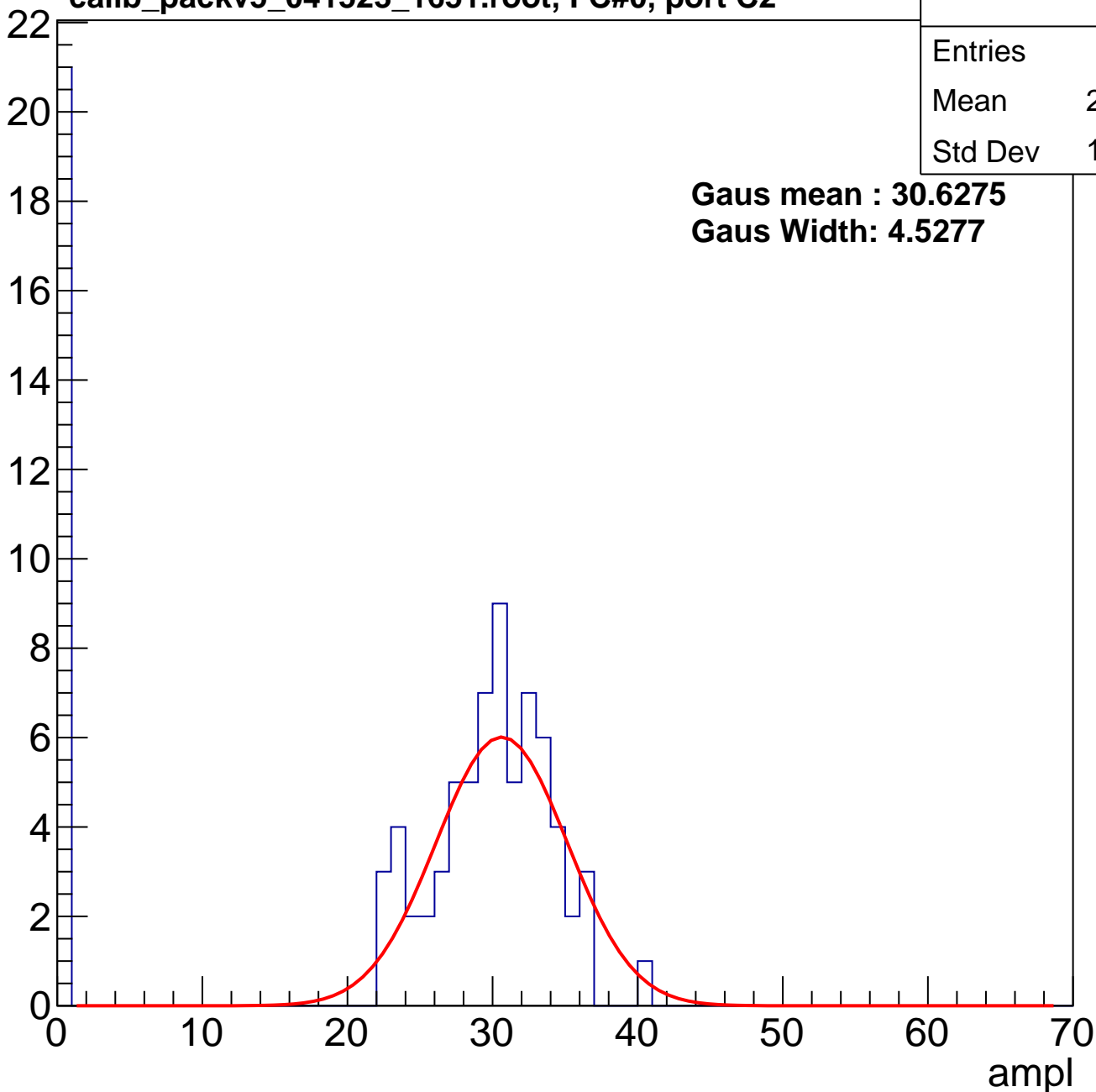
calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	22.62
Std Dev	13.02

Gaus mean : 30.6275

Gaus Width: 4.5277

Entry



B1L103S, U18-ch117, adc1

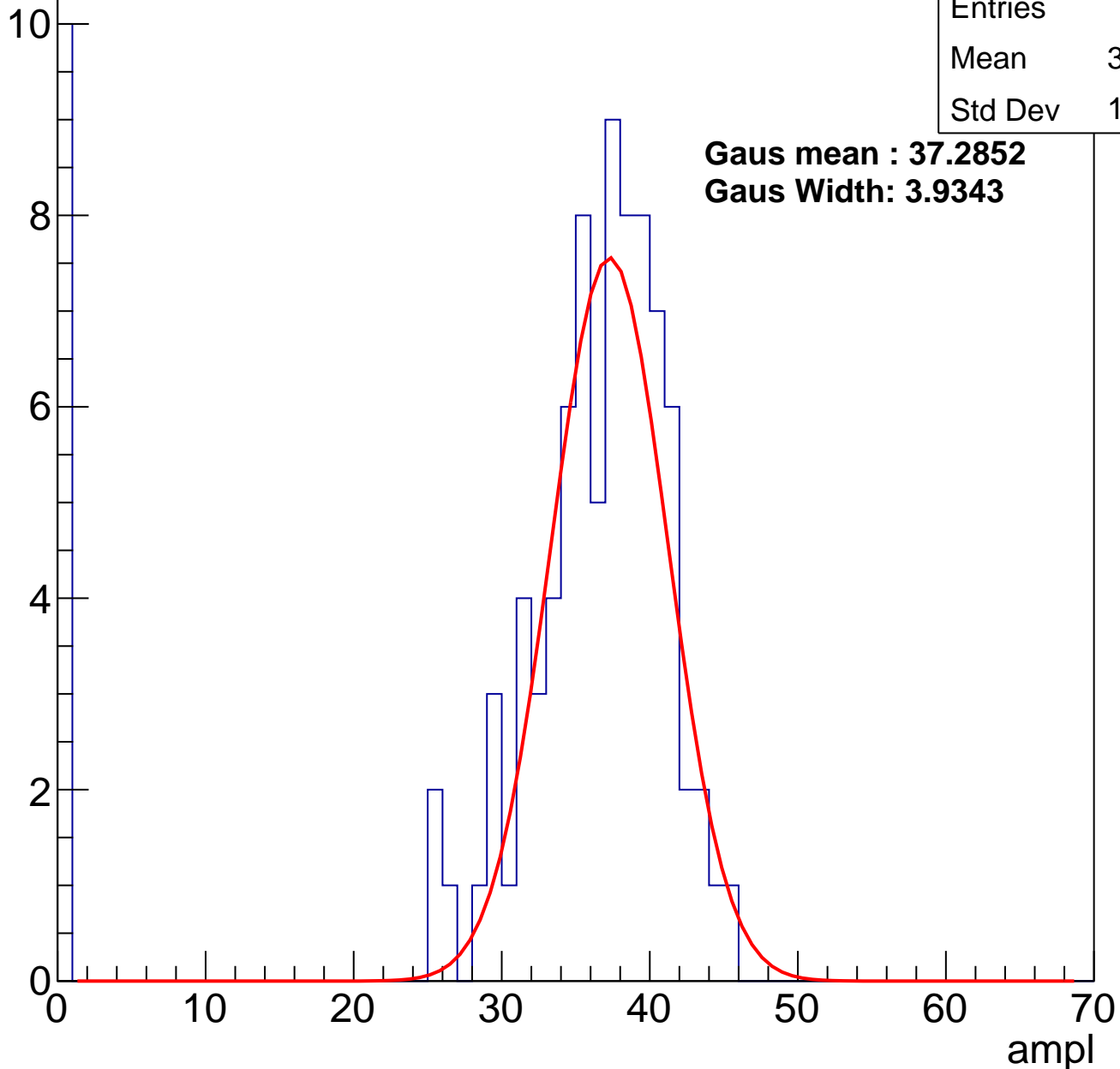
calib_packv5_041523_1651.root, FC#0, port C2

Entries	92
Mean	32.29
Std Dev	11.97

Gaus mean : 37.2852

Gaus Width: 3.9343

Entry



B1L103S, U18-ch117, adc2

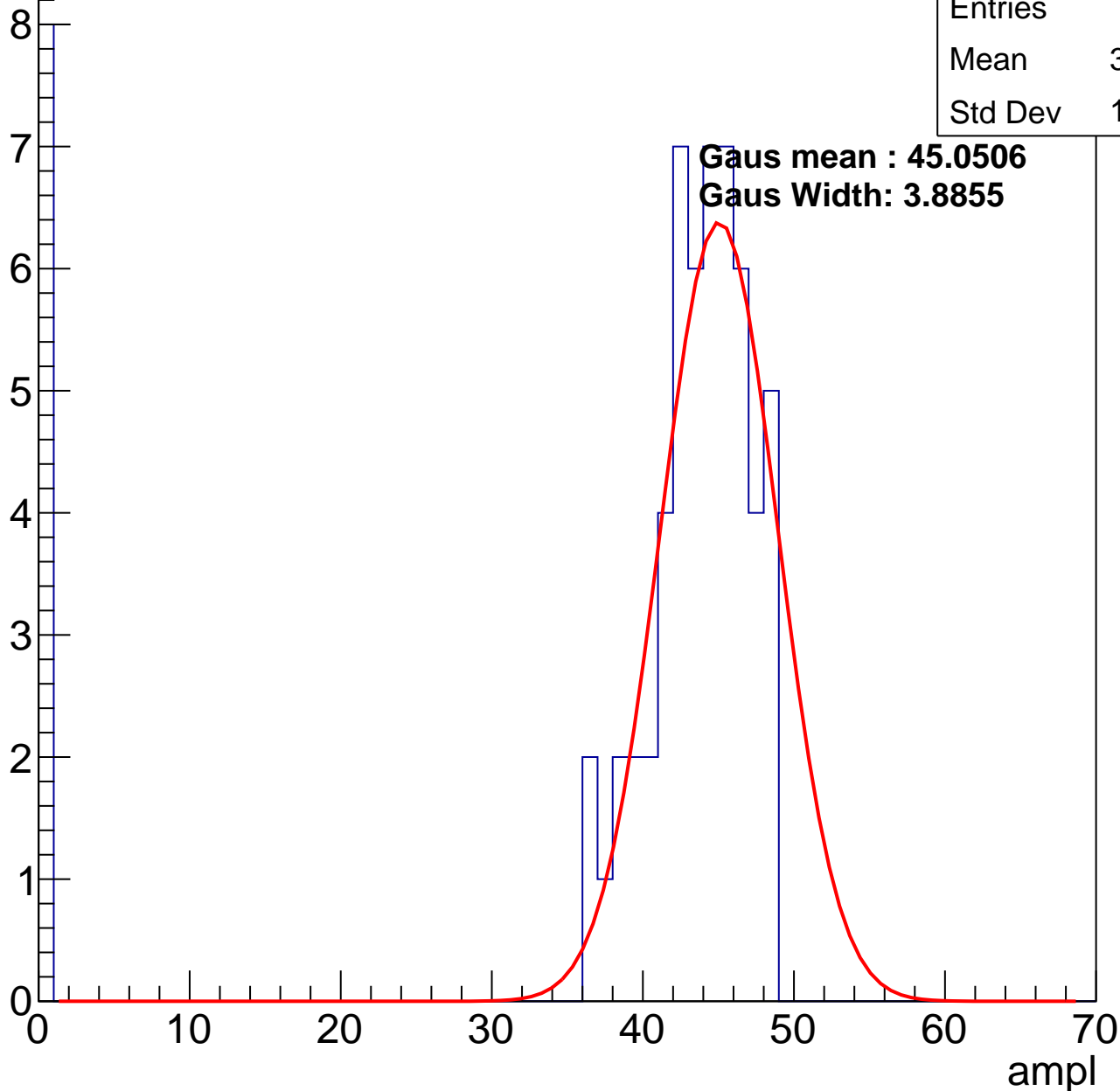
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	37.87
Std Dev	14.73

Gaus mean : 45.0506

Gaus Width: 3.8855

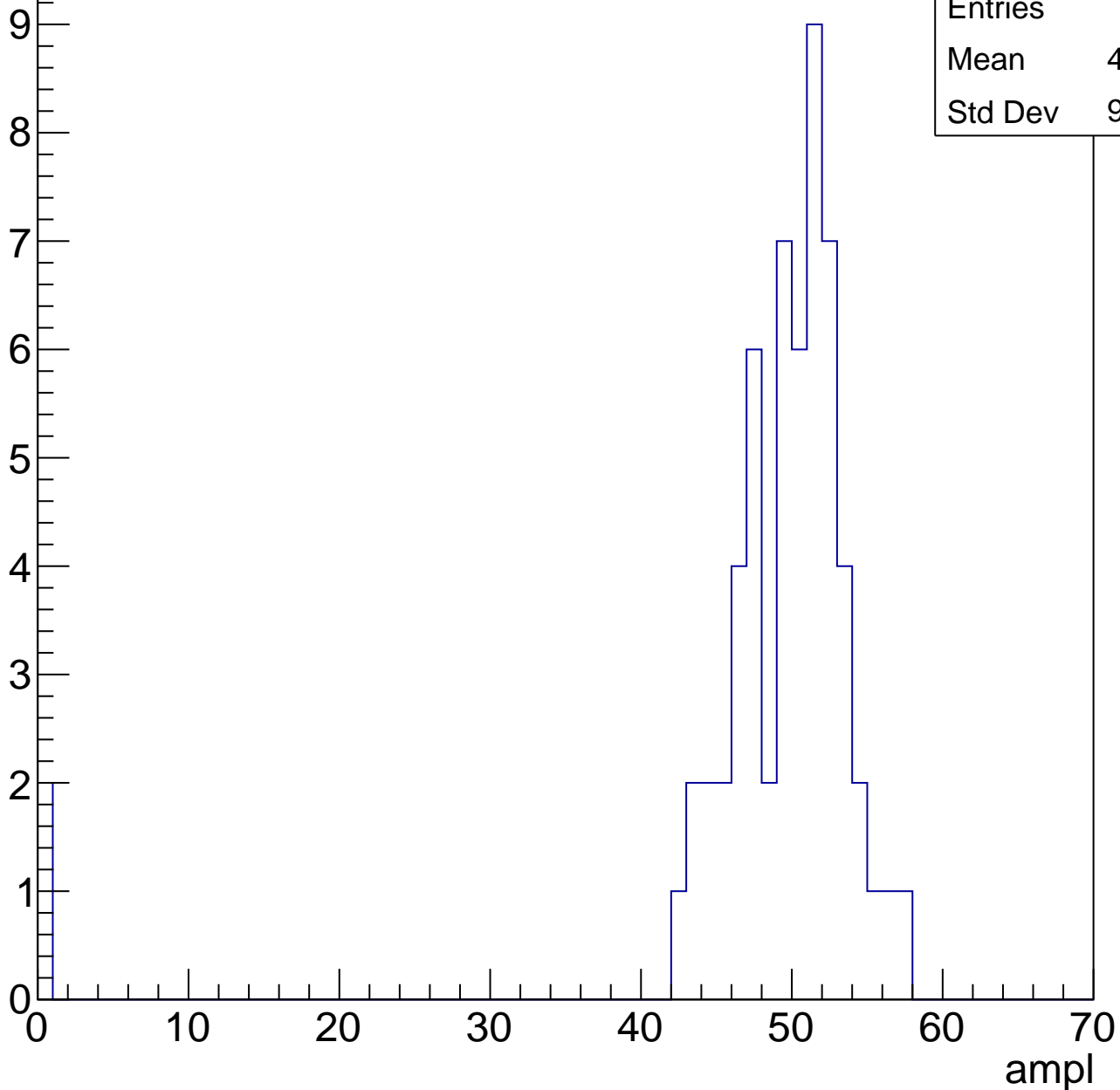


B1L103S, U18-ch117, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	47.83
Std Dev	9.526

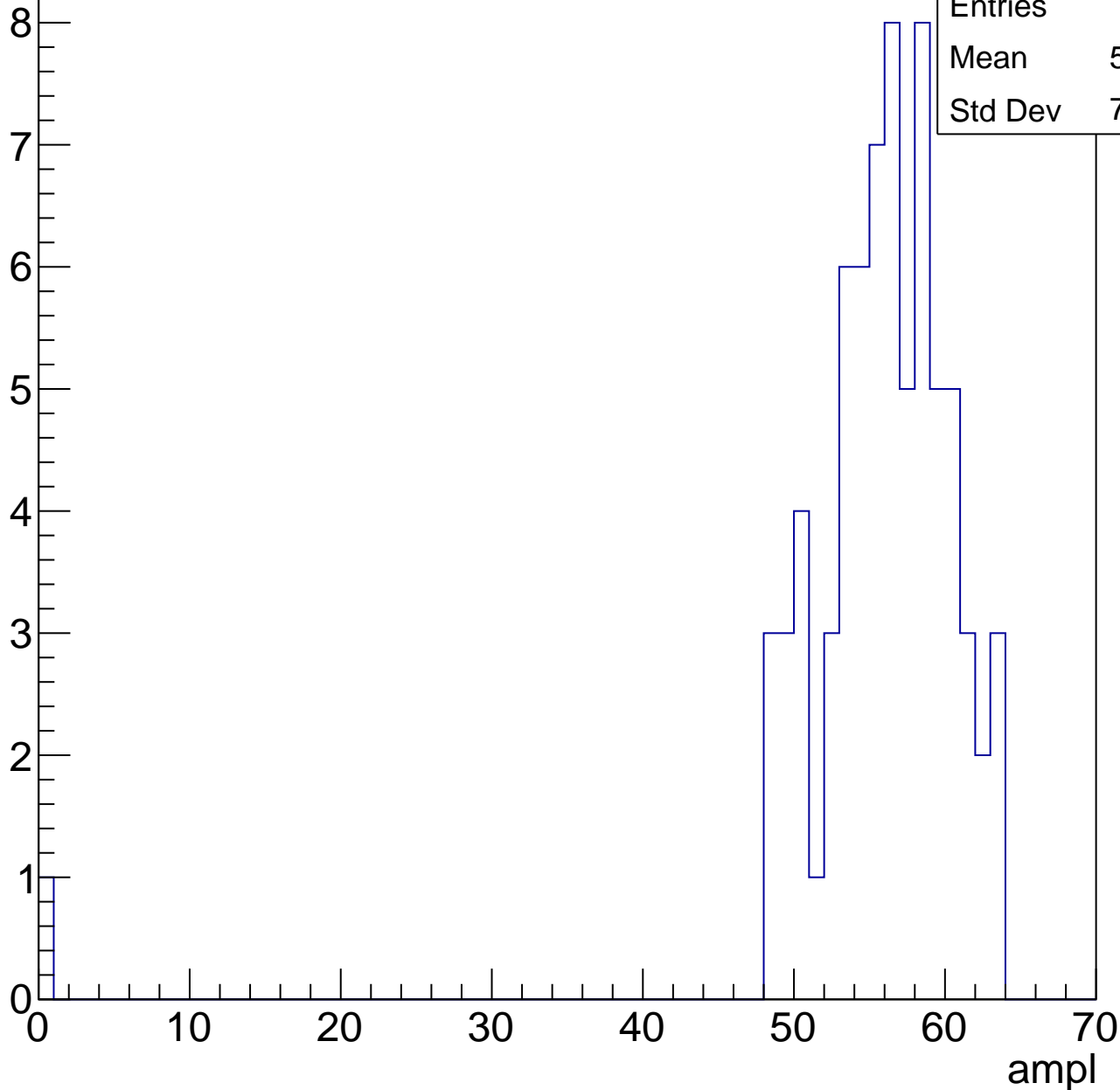


B1L103S, U18-ch117, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	73
Mean	54.97
Std Dev	7.545

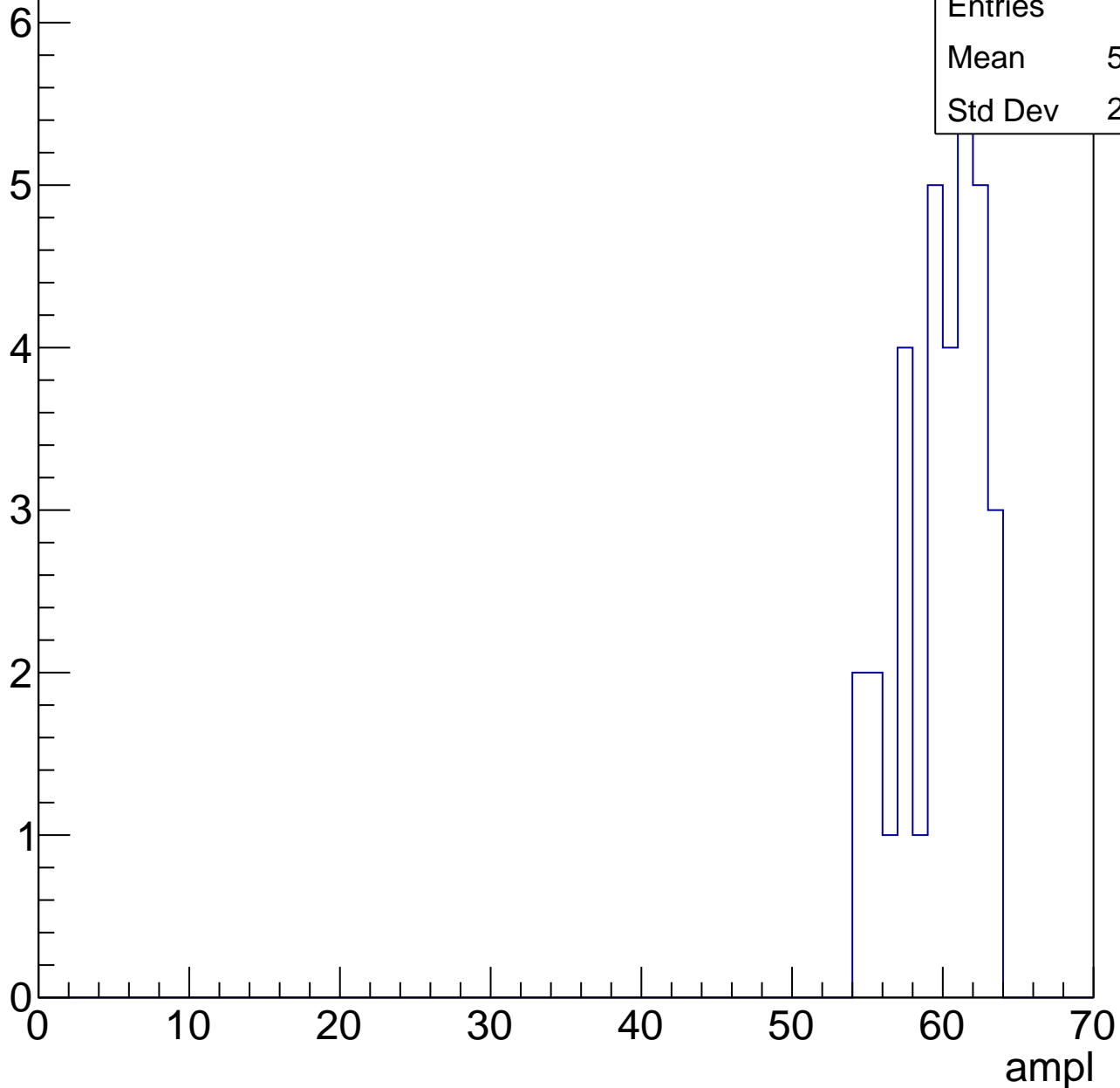


B1L103S, U18-ch117, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

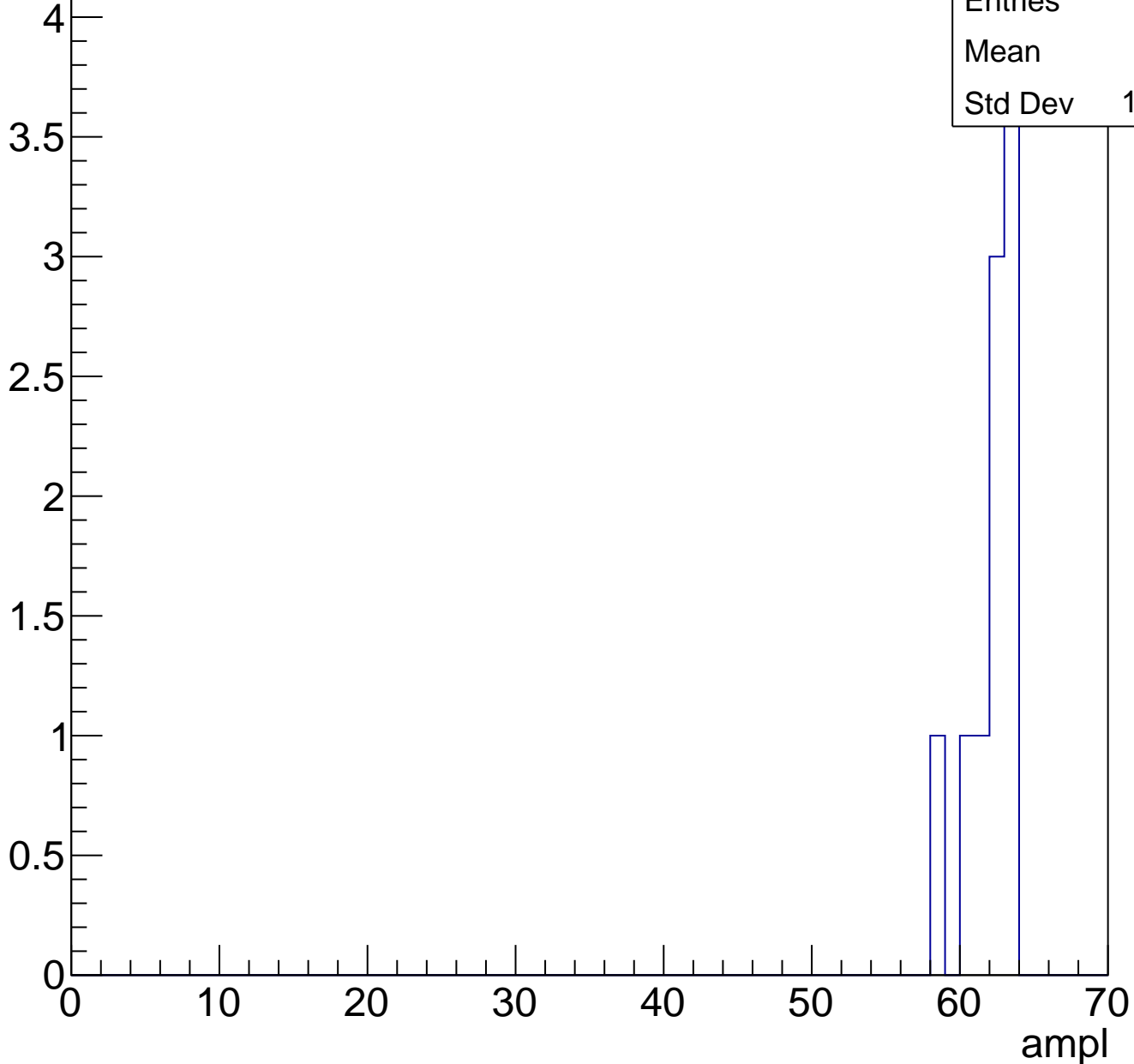
Entries	33
Mean	59.39
Std Dev	2.605



B1L103S, U18-ch117, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



Entries	10
Mean	61.7
Std Dev	1.552

B1L103S, U18-ch117, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U18-ch118, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	69
Mean	23.58
Std Dev	11.85

Gaus mean : 29.0989

Gaus Width: 4.0926

Entry

12

10

8

6

4

2

0

0

10

20

30

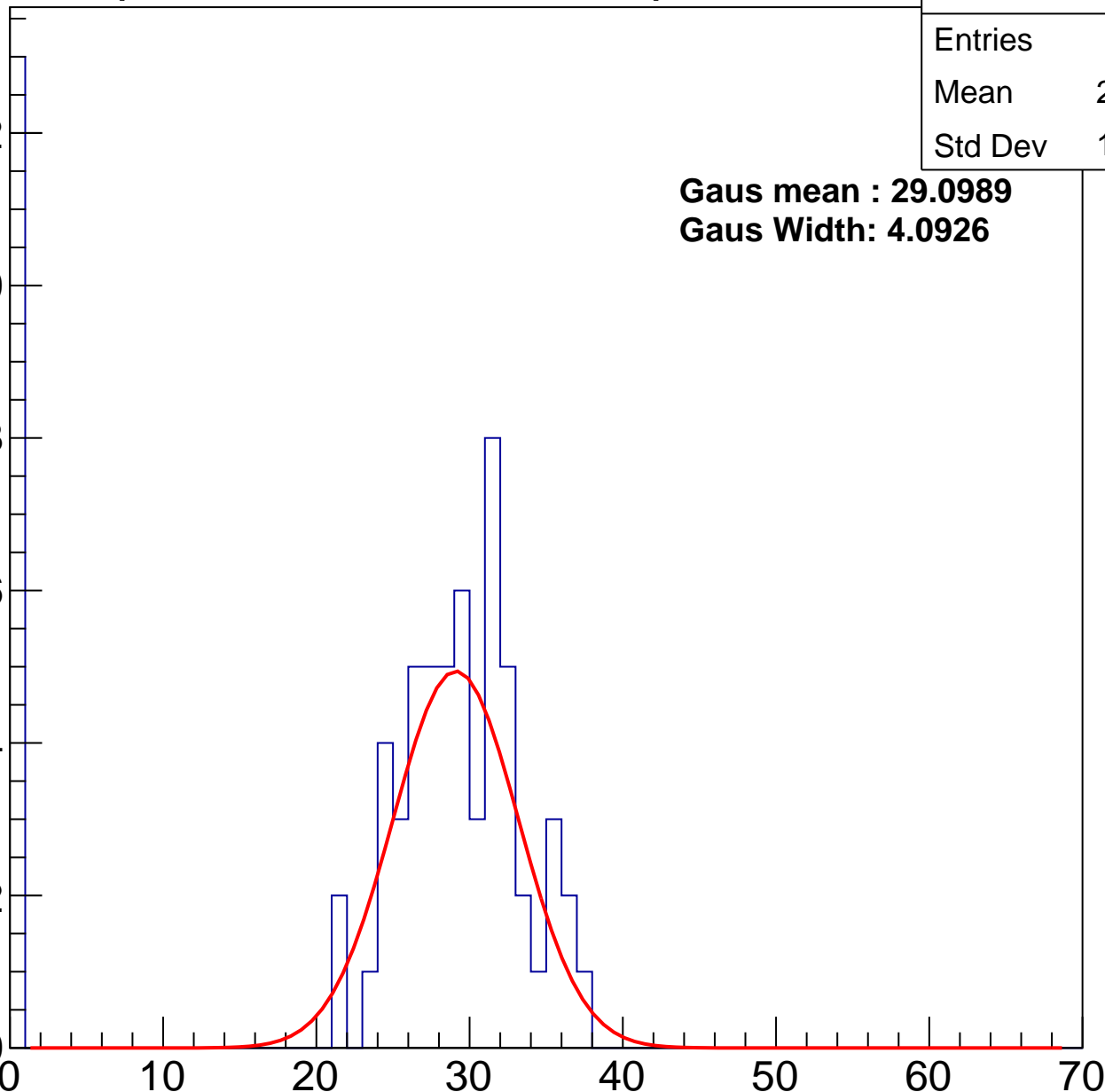
40

50

60

70

ampl



B1L103S, U18-ch118, adc1

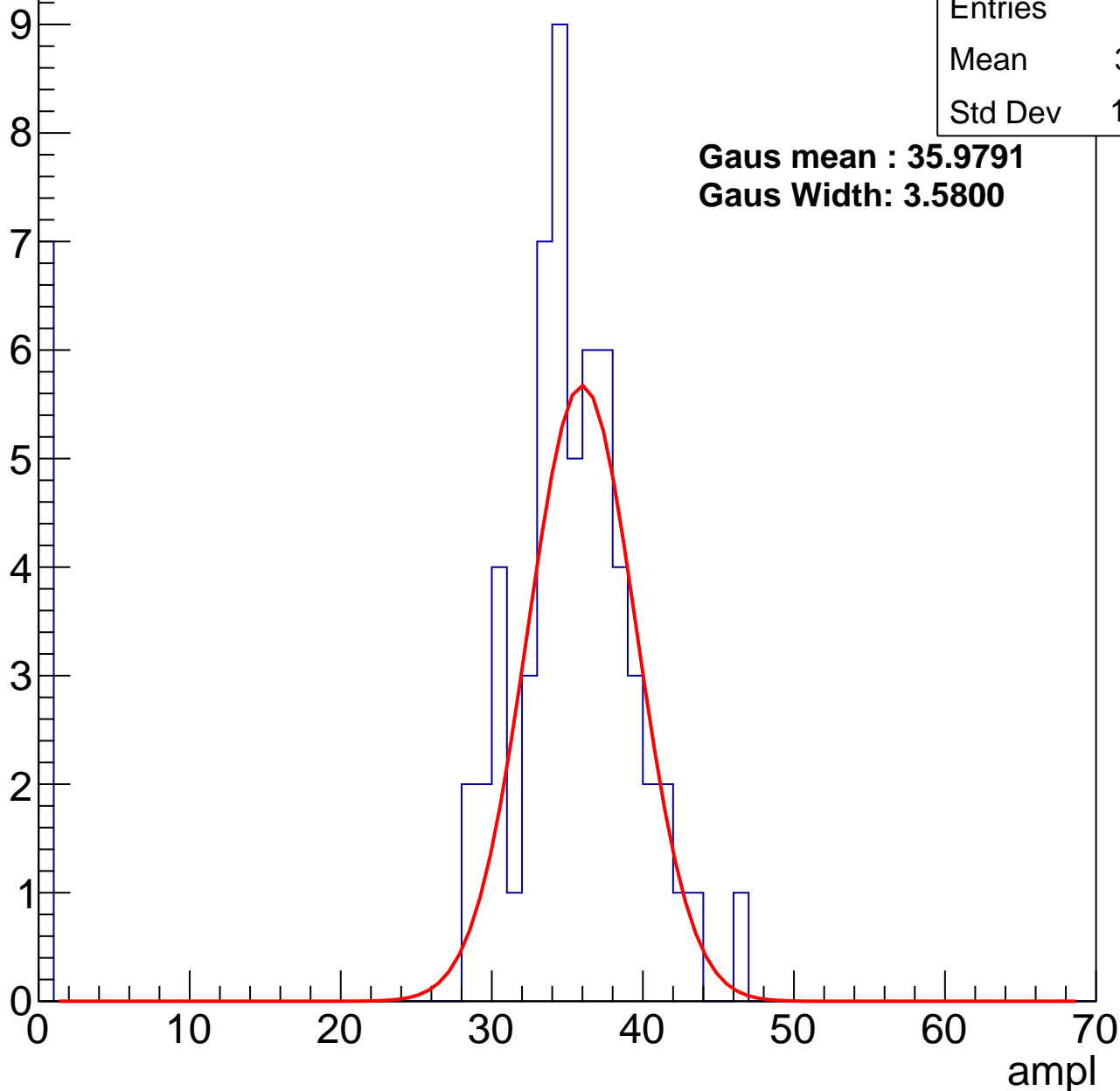
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	31.41
Std Dev	11.38

Gaus mean : 35.9791

Gaus Width: 3.5800



B1L103S, U18-ch118, adc2

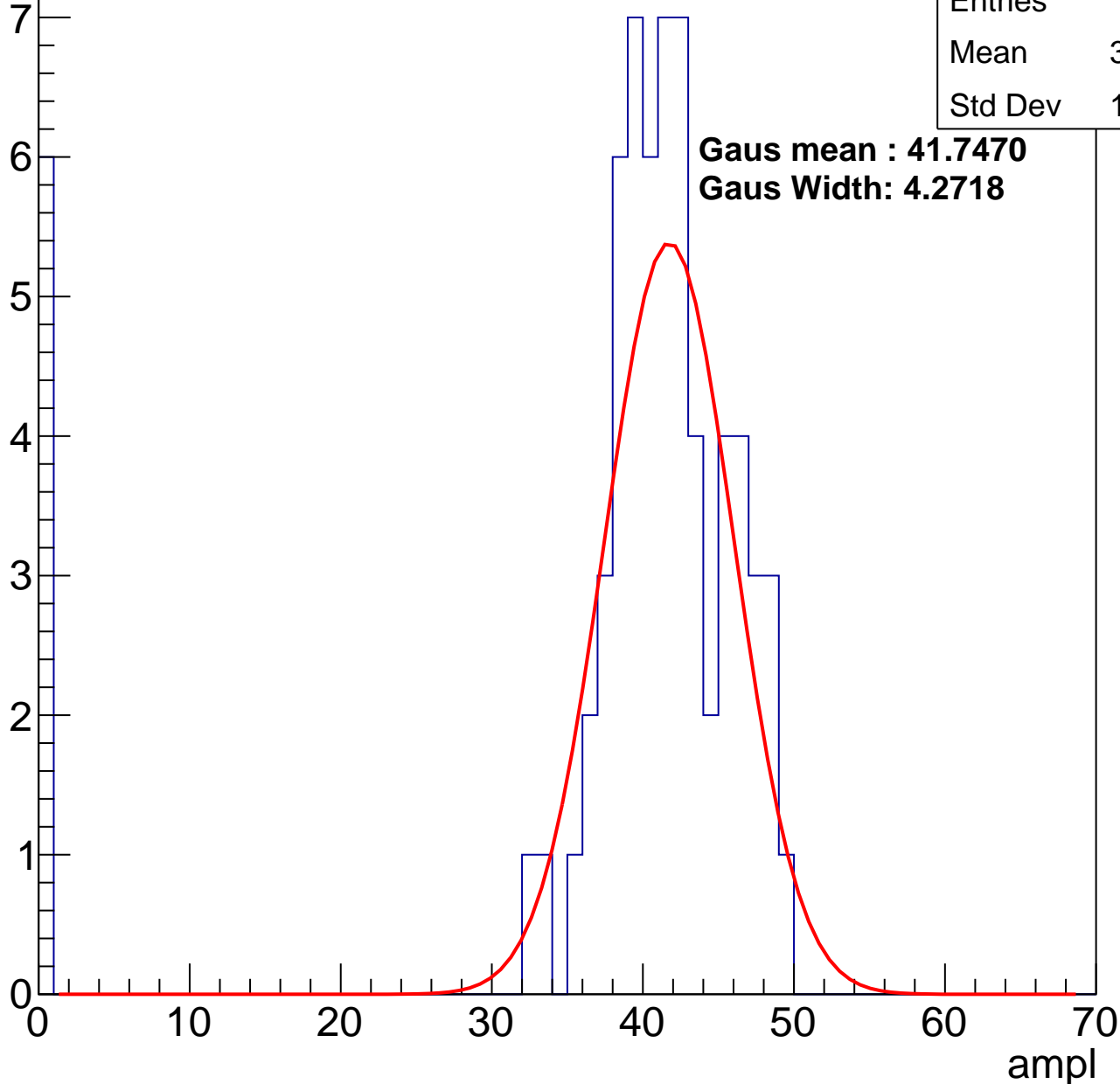
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	37.69
Std Dev	12.27

Gaus mean : 41.7470

Gaus Width: 4.2718

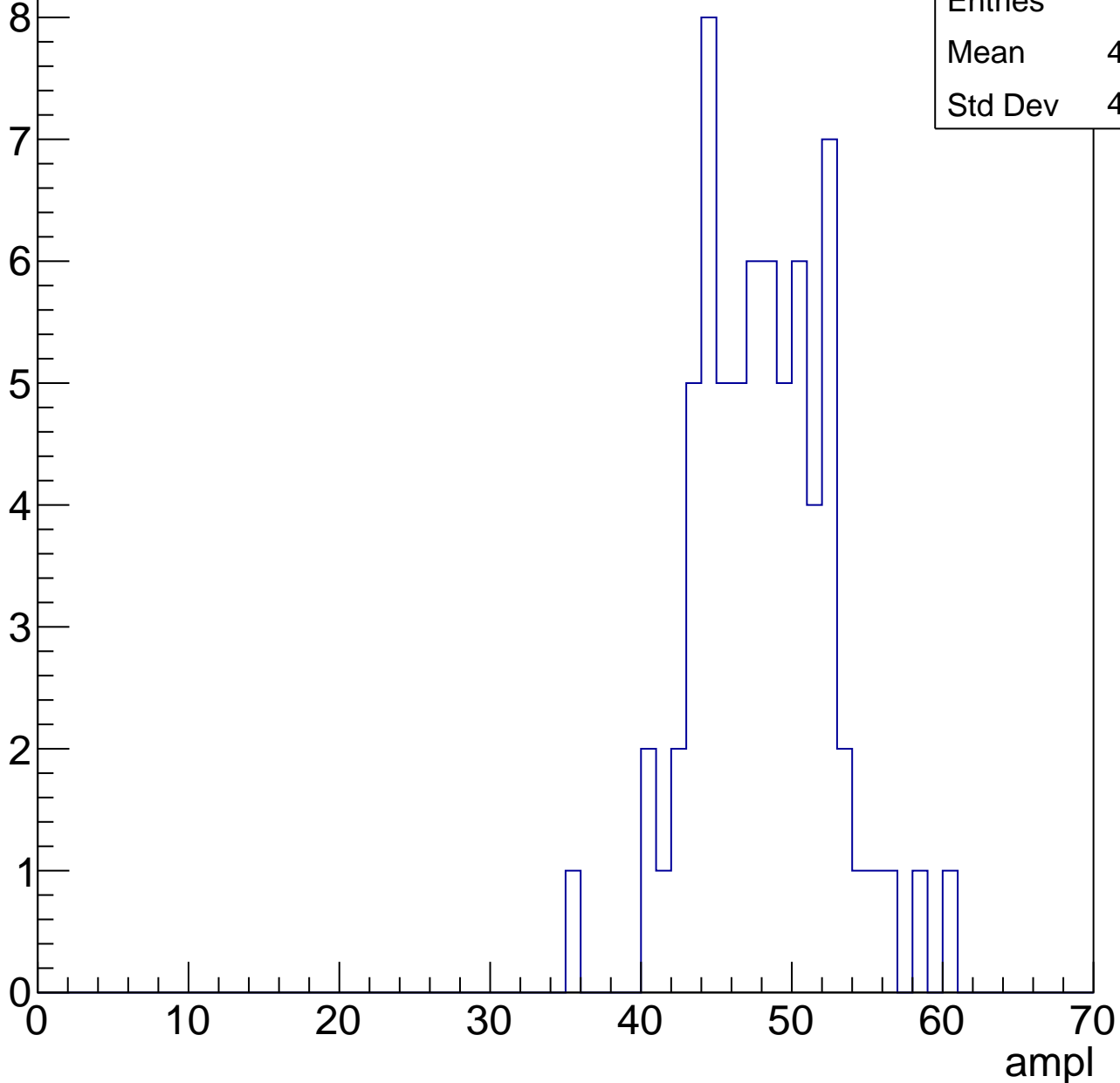


B1L103S, U18-ch118, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	47.63
Std Dev	4.395

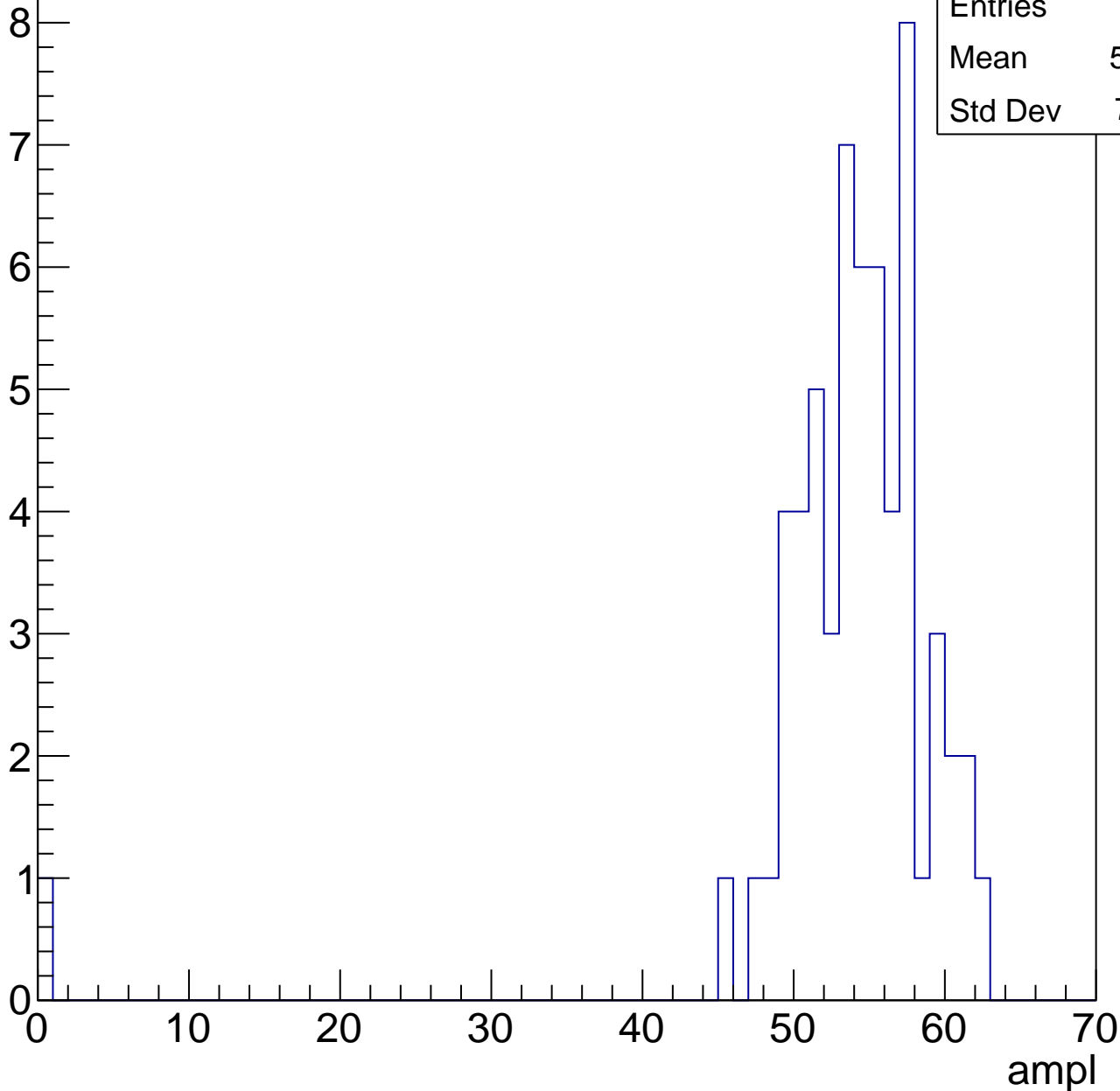


B1L103S, U18-ch118, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	53.18
Std Dev	7.841

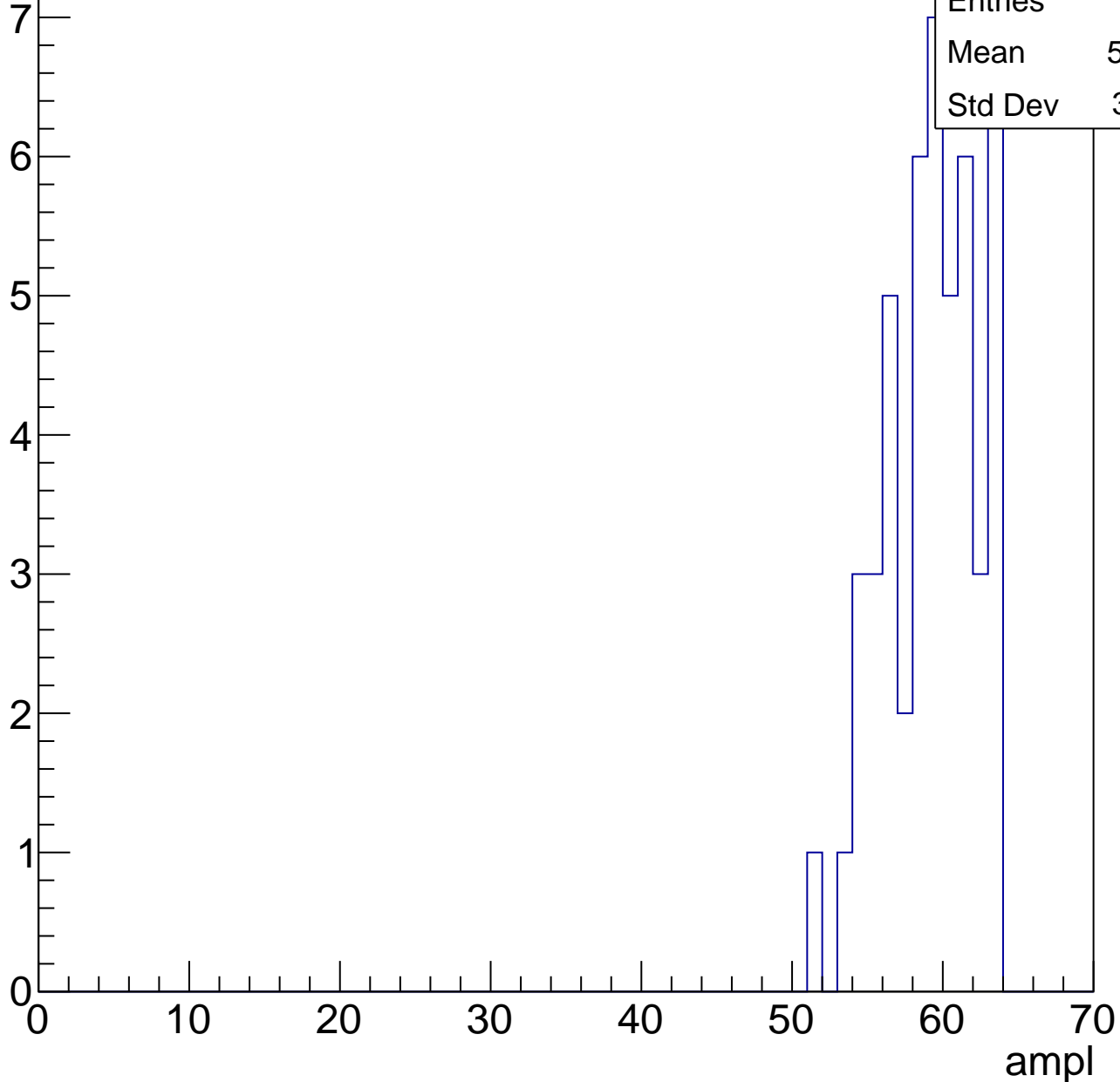


B1L103S, U18-ch118, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.76
Std Dev	3.021

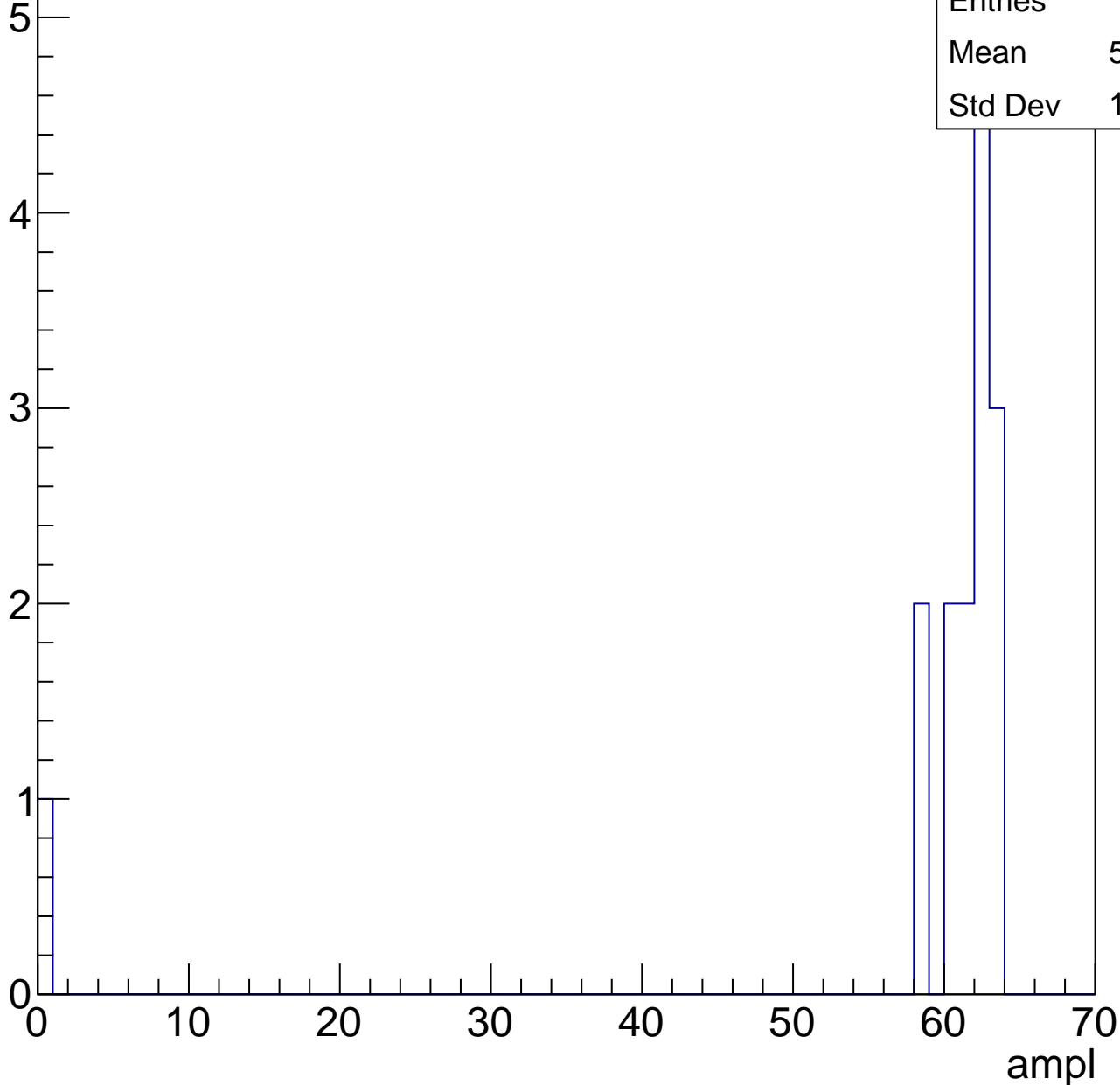


B1L103S, U18-ch118, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.13
Std Dev	15.35



B1L103S, U18-ch118, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U18-ch119, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	84
Mean	24.39
Std Dev	11

Gaus mean : 29.2808

Gaus Width: 2.7033

Entry

12

10

8

6

4

2

0

0

10

20

30

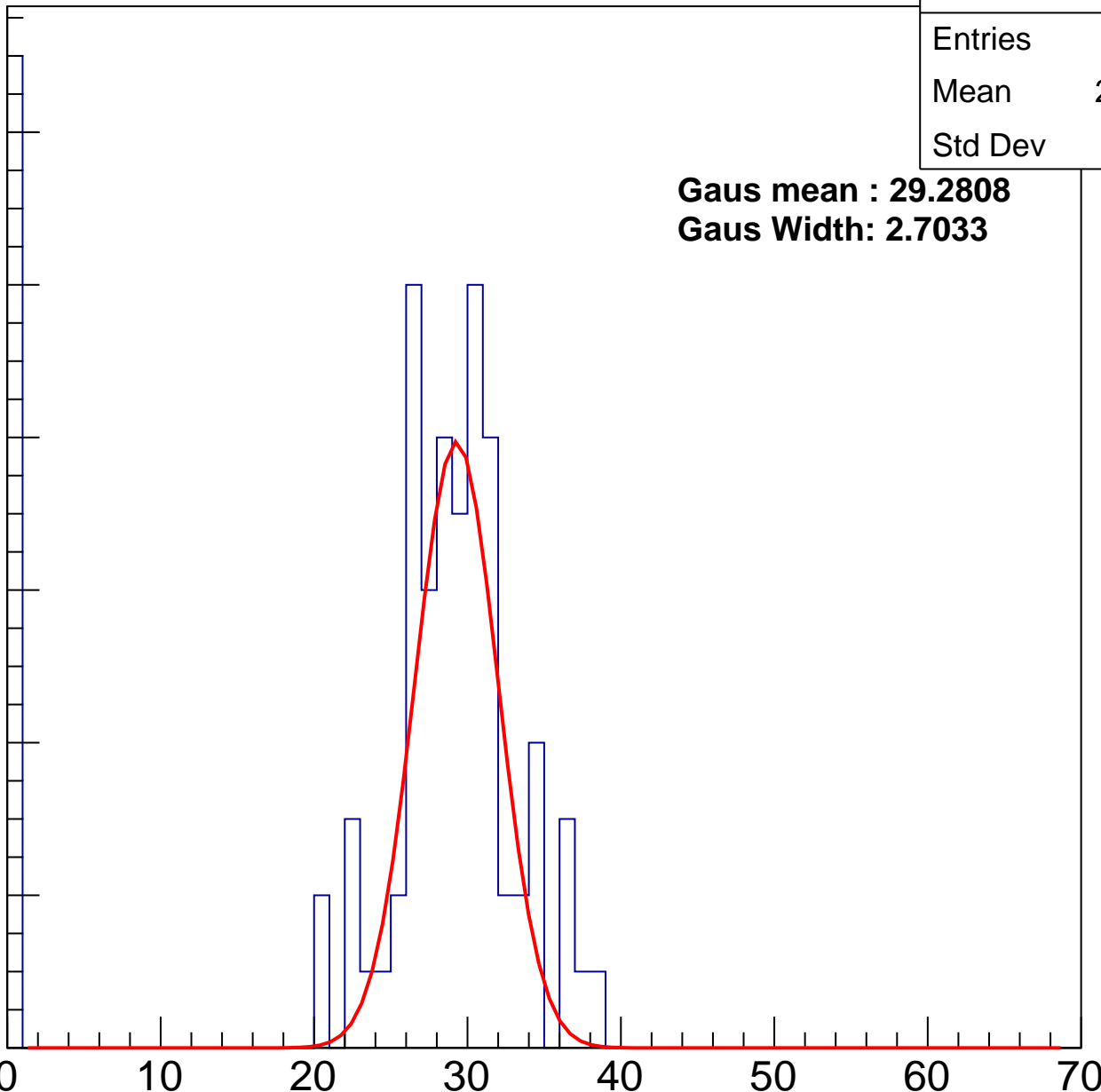
40

50

60

70

ampl



B1L103S, U18-ch119, adc1

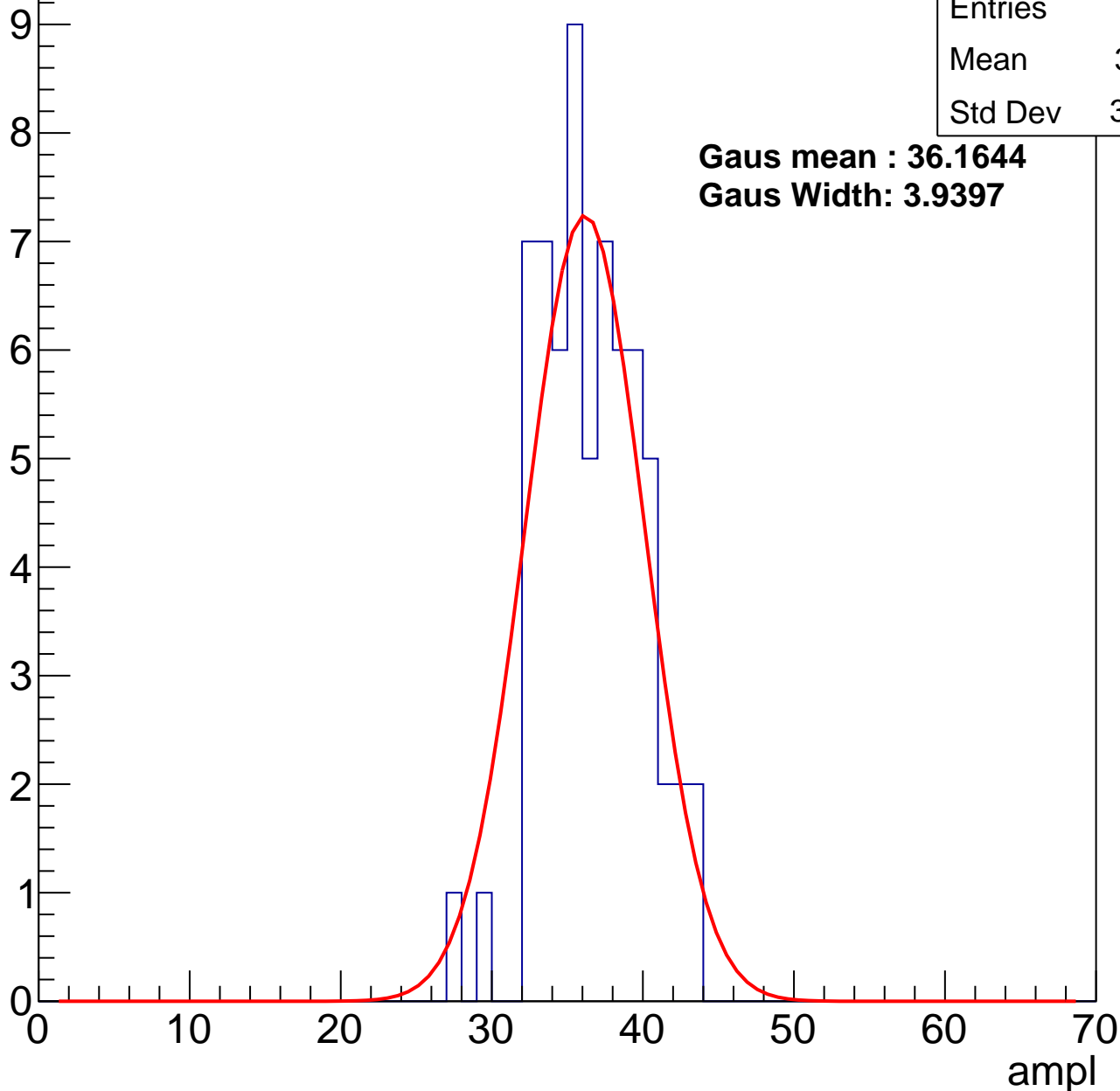
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	36.11
Std Dev	3.308

Gaus mean : 36.1644

Gaus Width: 3.9397



B1L103S, U18-ch119, adc2

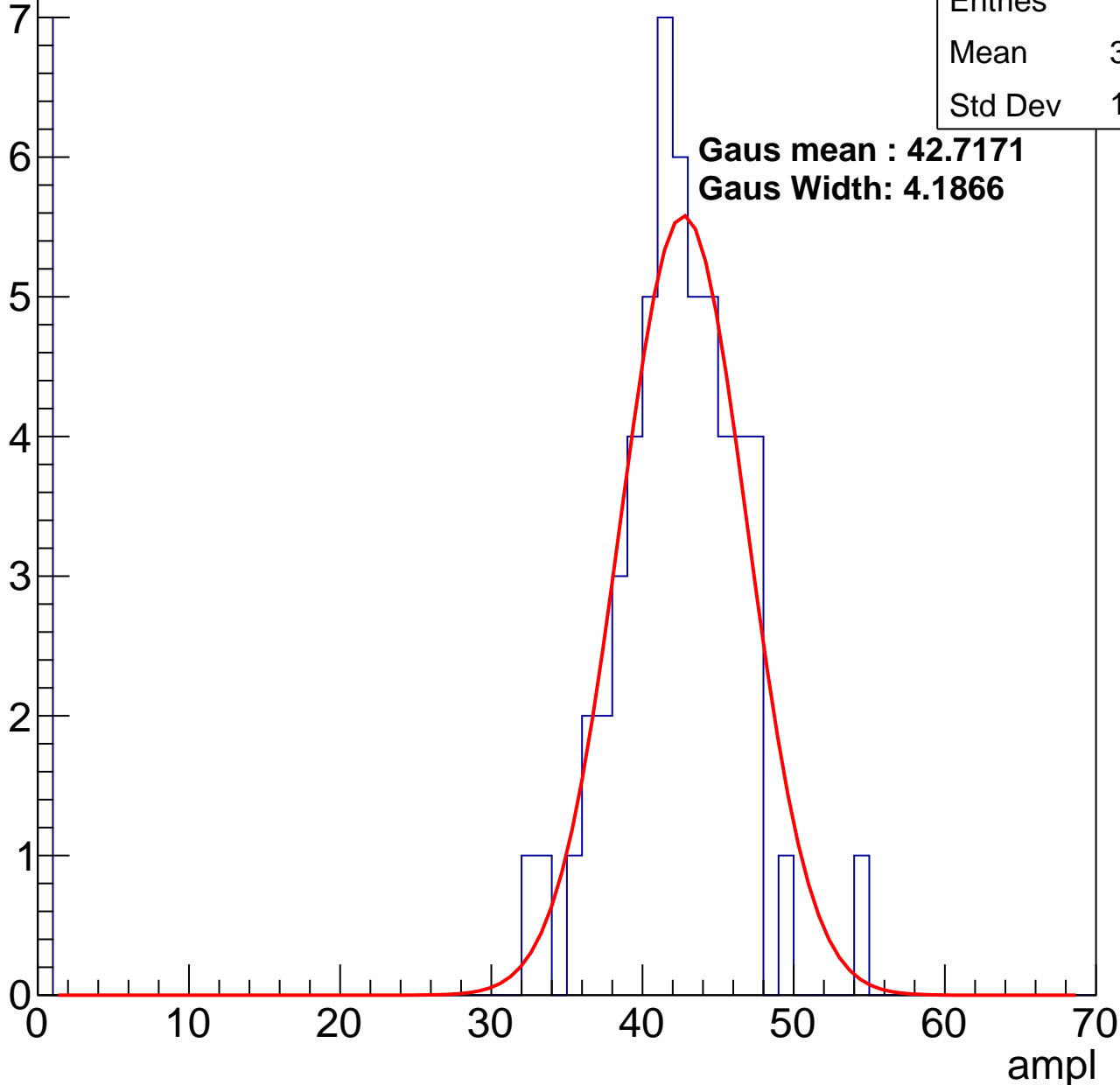
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	37.22
Std Dev	13.68

Gaus mean : 42.7171

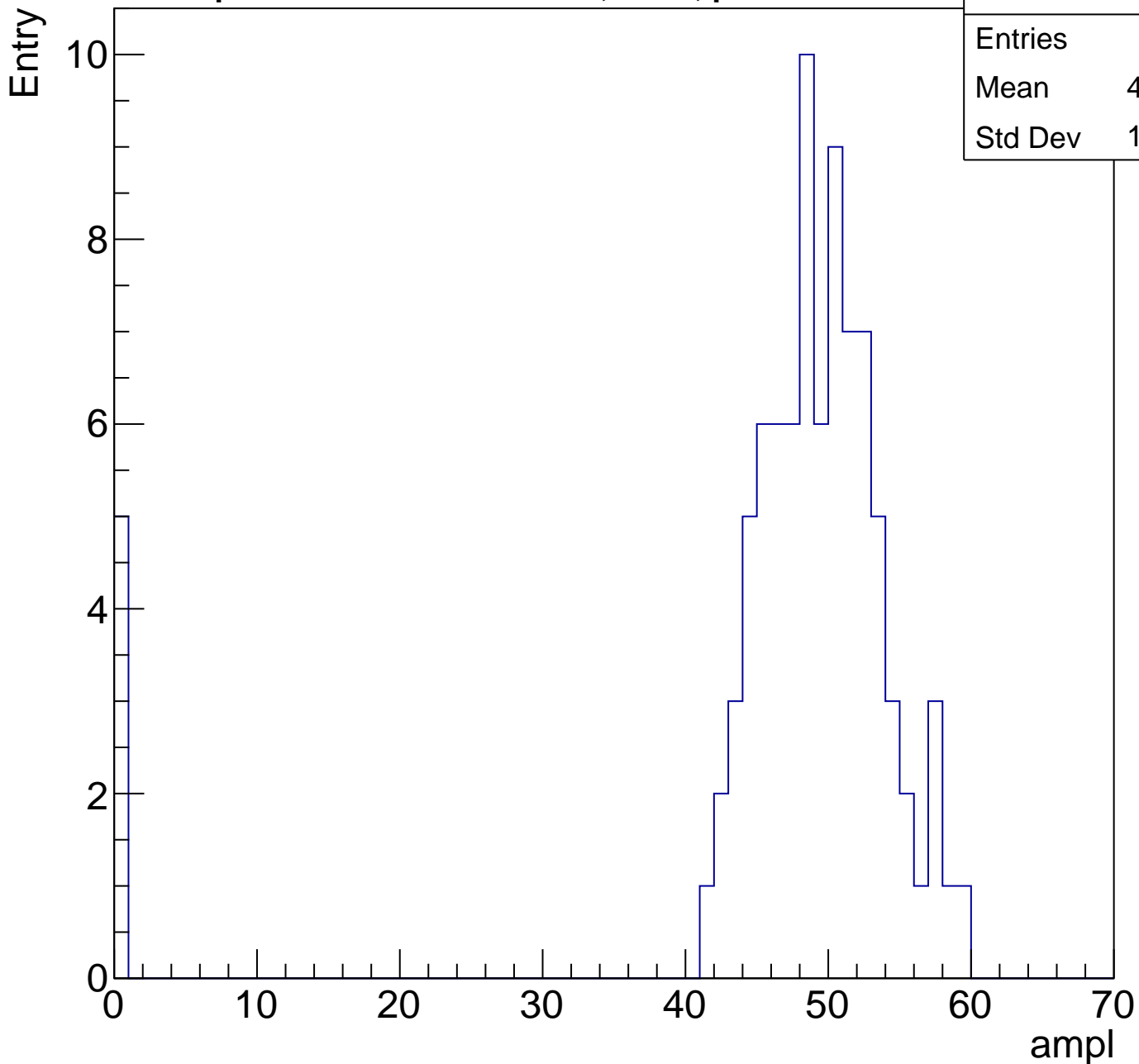
Gaus Width: 4.1866



B1L103S, U18-ch119, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	89
Mean	46.38
Std Dev	11.96

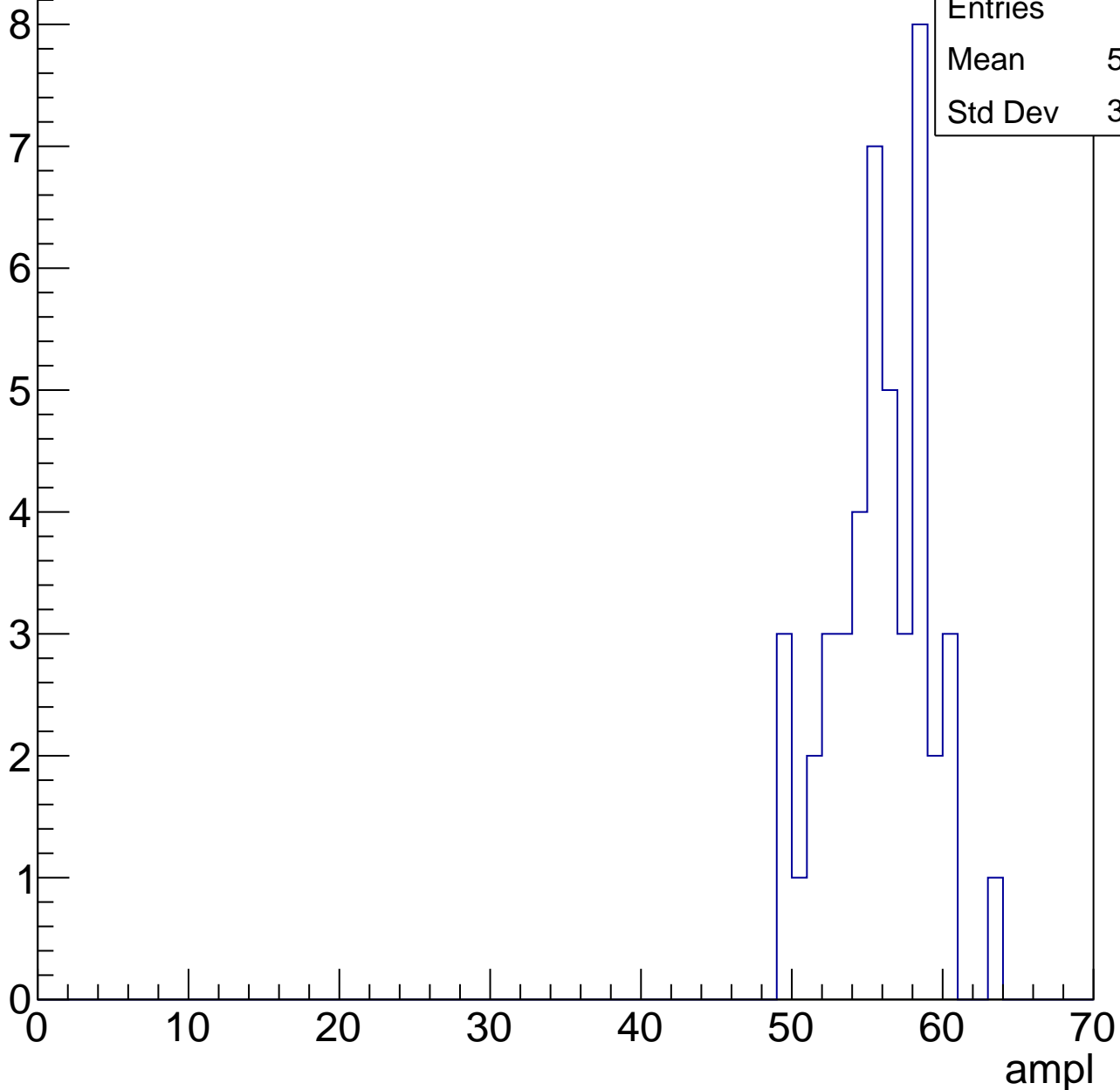


B1L103S, U18-ch119, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	45
Mean	55.36
Std Dev	3.205

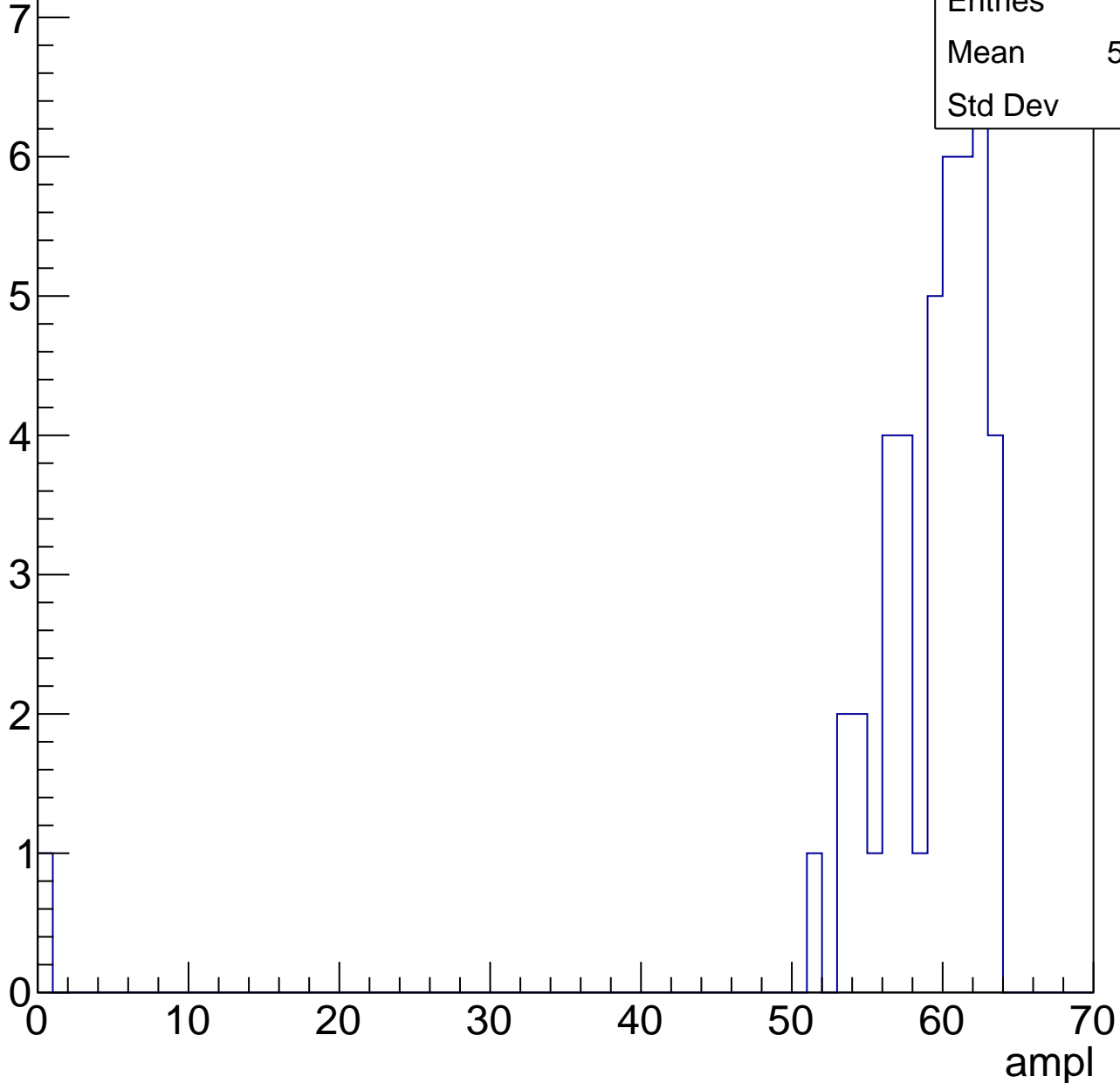


B1L103S, U18-ch119, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	44
Mean	57.66
Std Dev	9.31

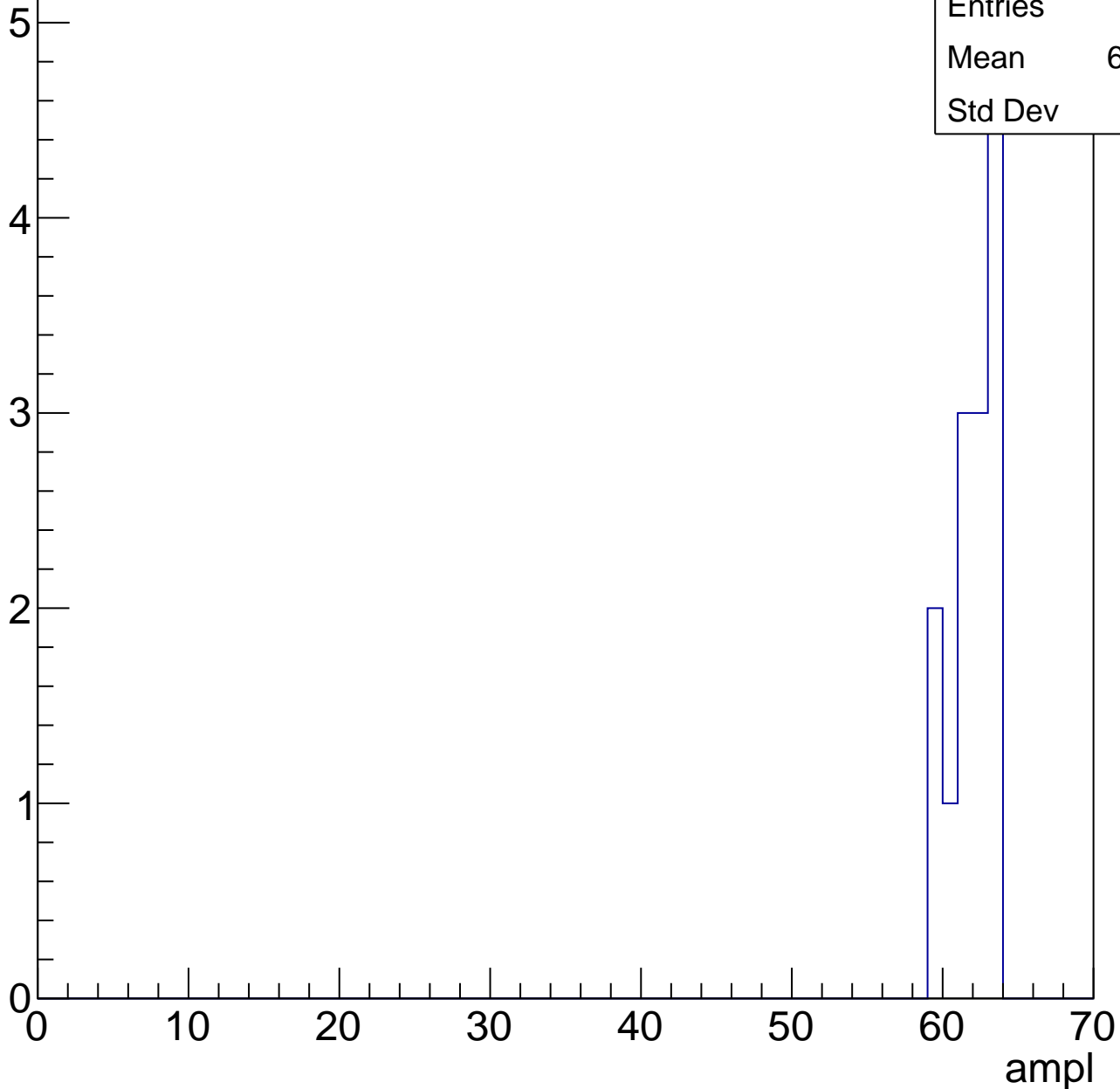


B1L103S, U18-ch119, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

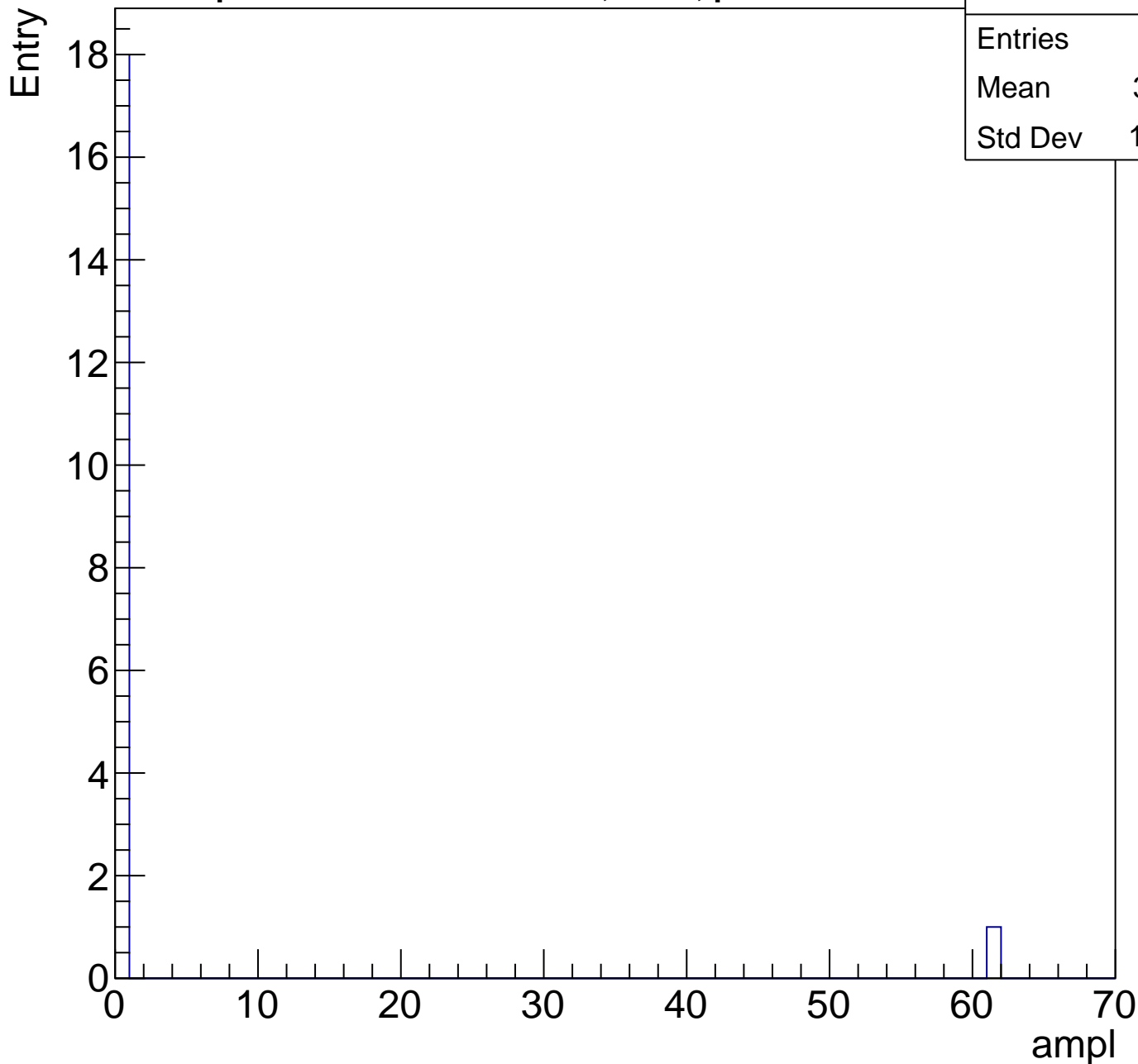
Entries	14
Mean	61.57
Std Dev	1.4



B1L103S, U18-ch119, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.211
Std Dev	13.62



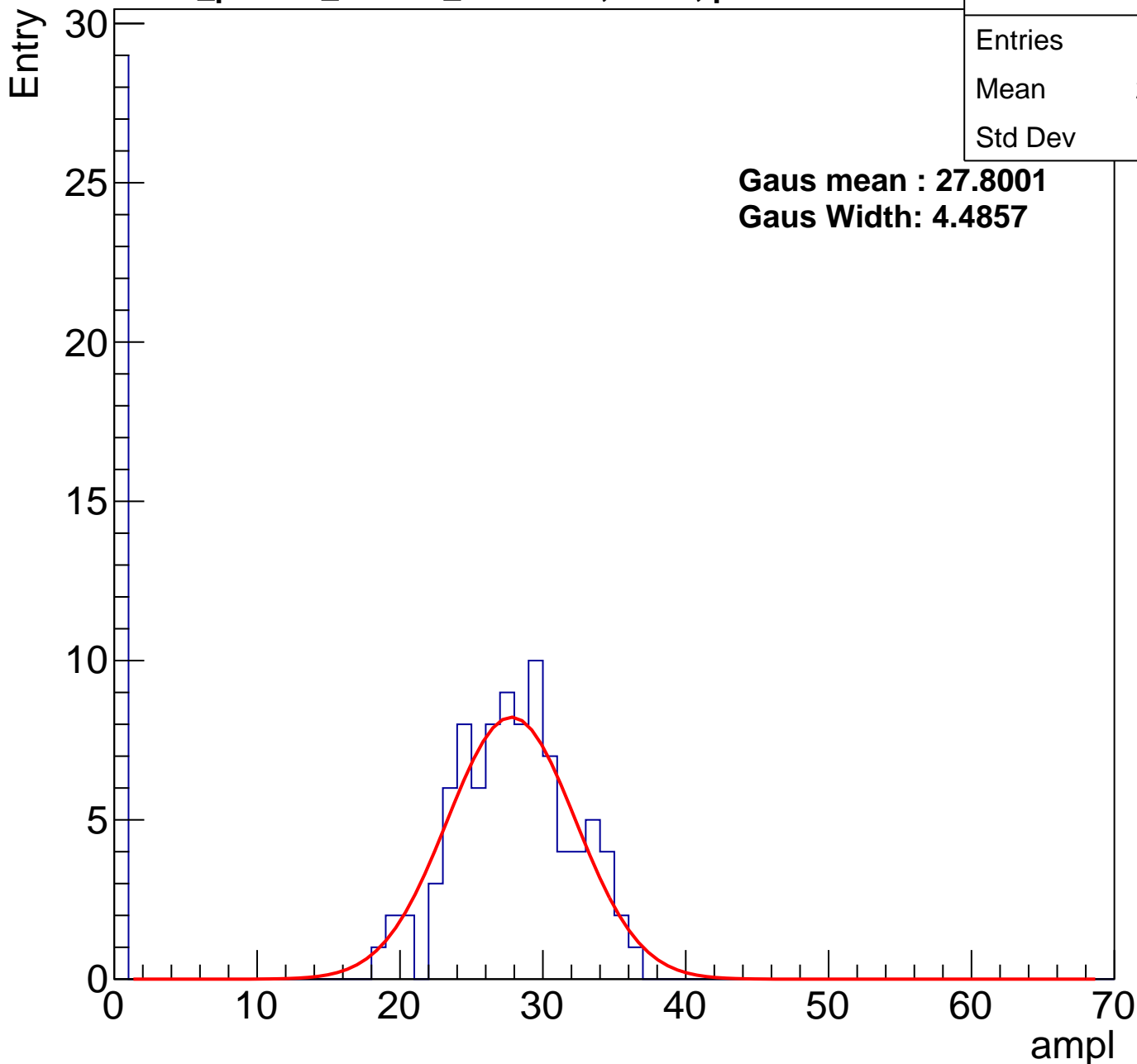
B1L103S, U18-ch120, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	119
Mean	20.81
Std Dev	12.31

Gaus mean : 27.8001

Gaus Width: 4.4857



B1L103S, U18-ch120, adc1

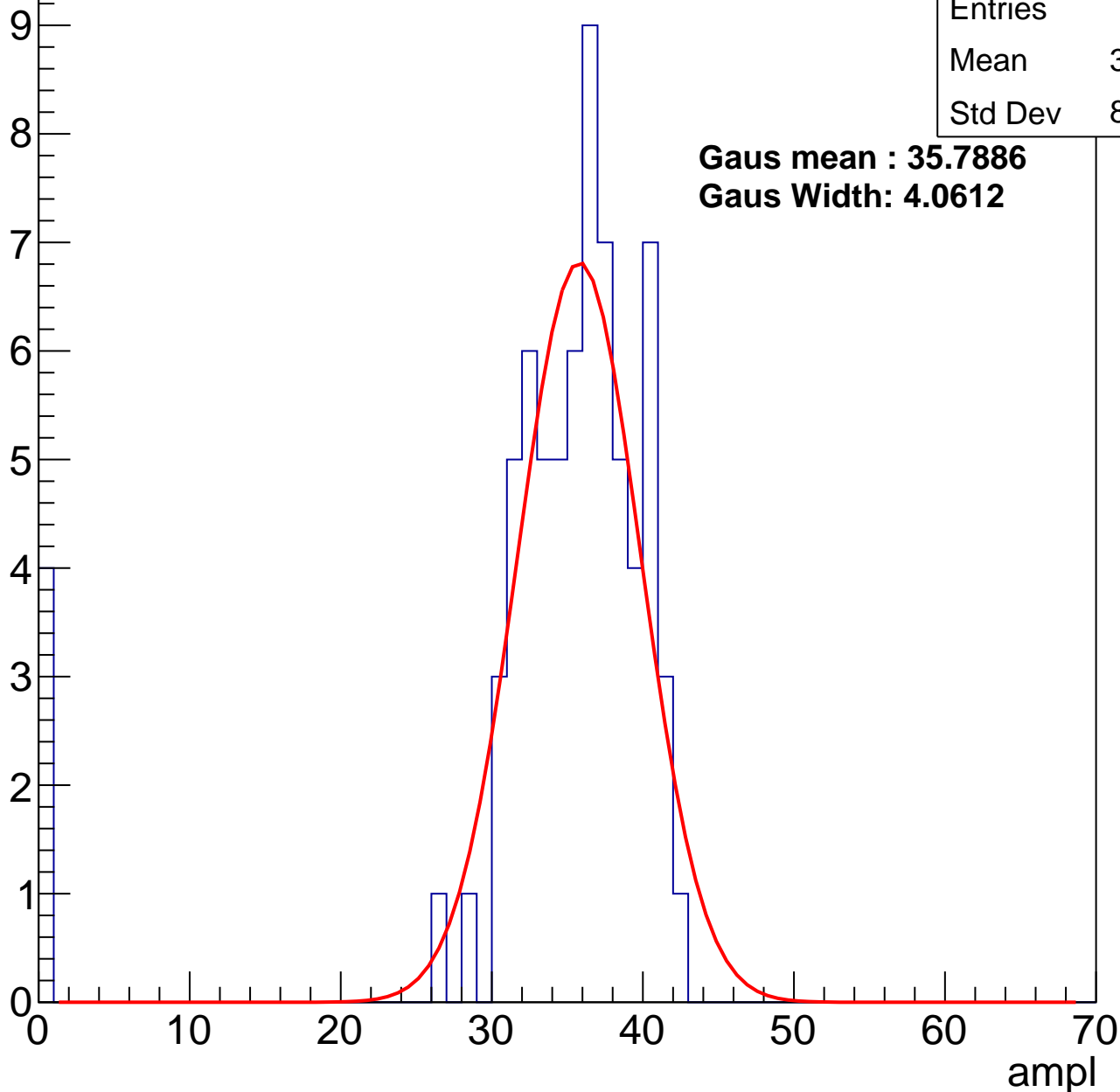
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	33.47
Std Dev	8.799

Gaus mean : 35.7886

Gaus Width: 4.0612



B1L103S, U18-ch120, adc2

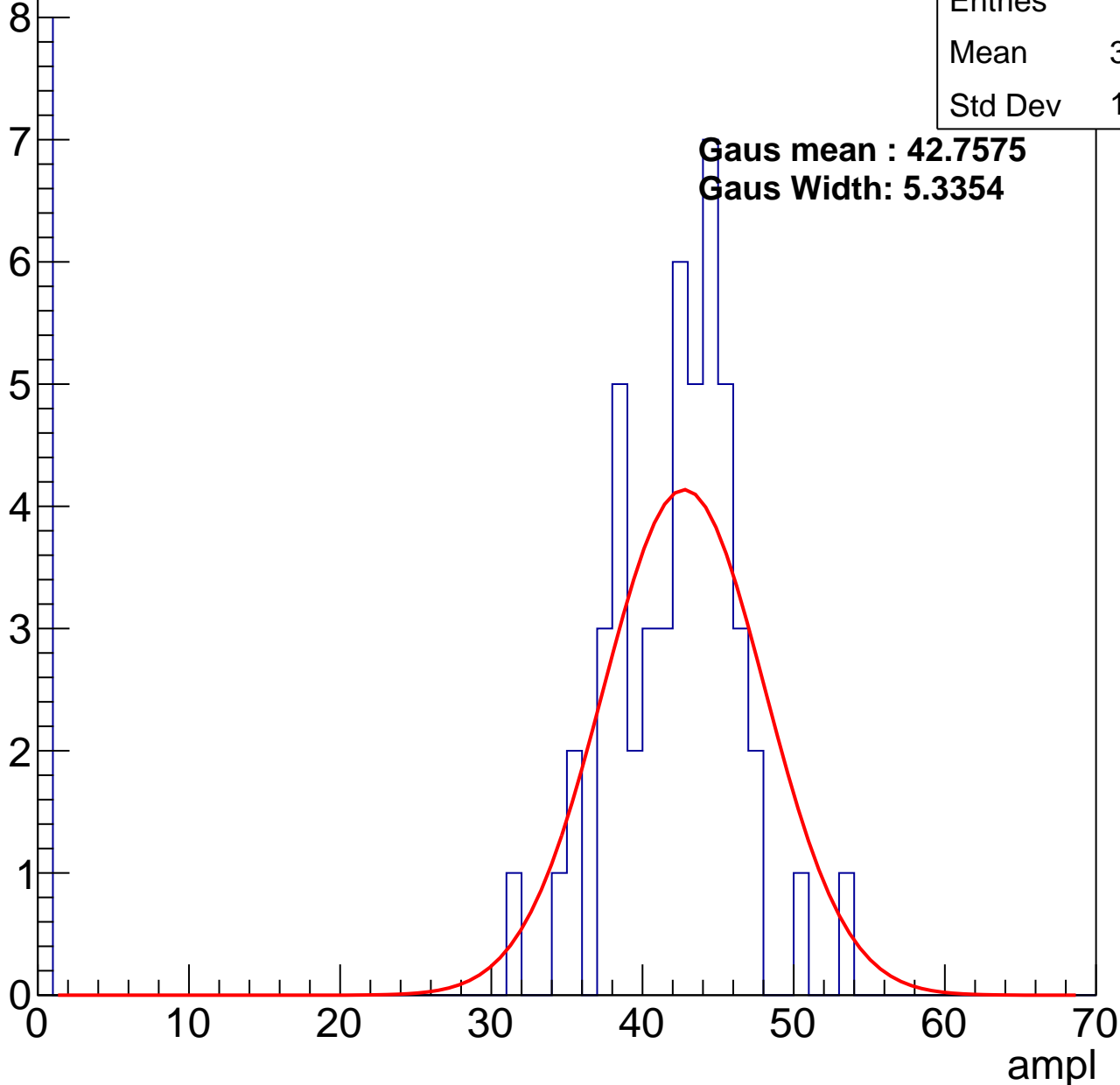
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	36.07
Std Dev	14.92

Gaus mean : 42.7575

Gaus Width: 5.3354

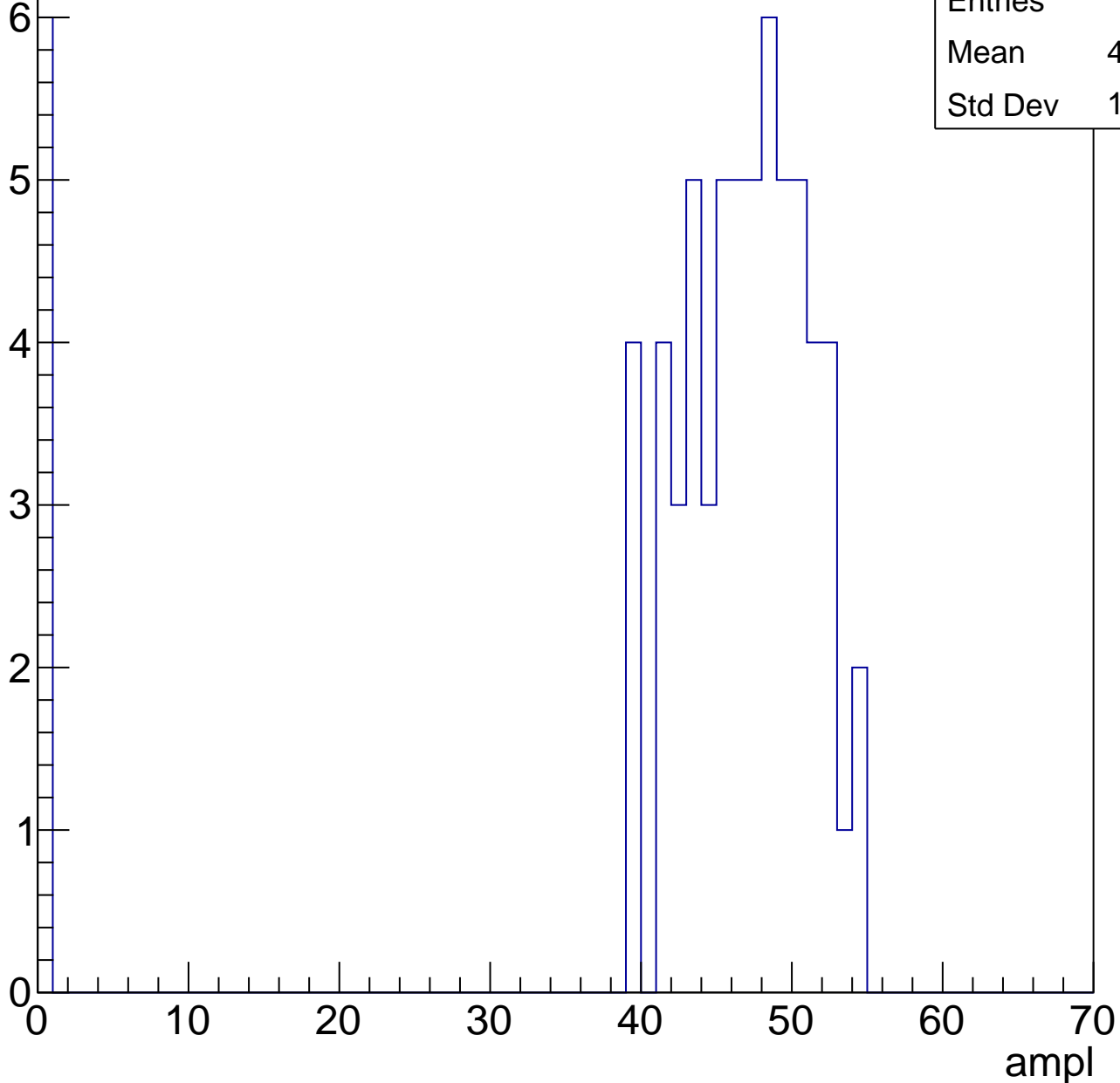


B1L103S, U18-ch120, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	42.37
Std Dev	13.82

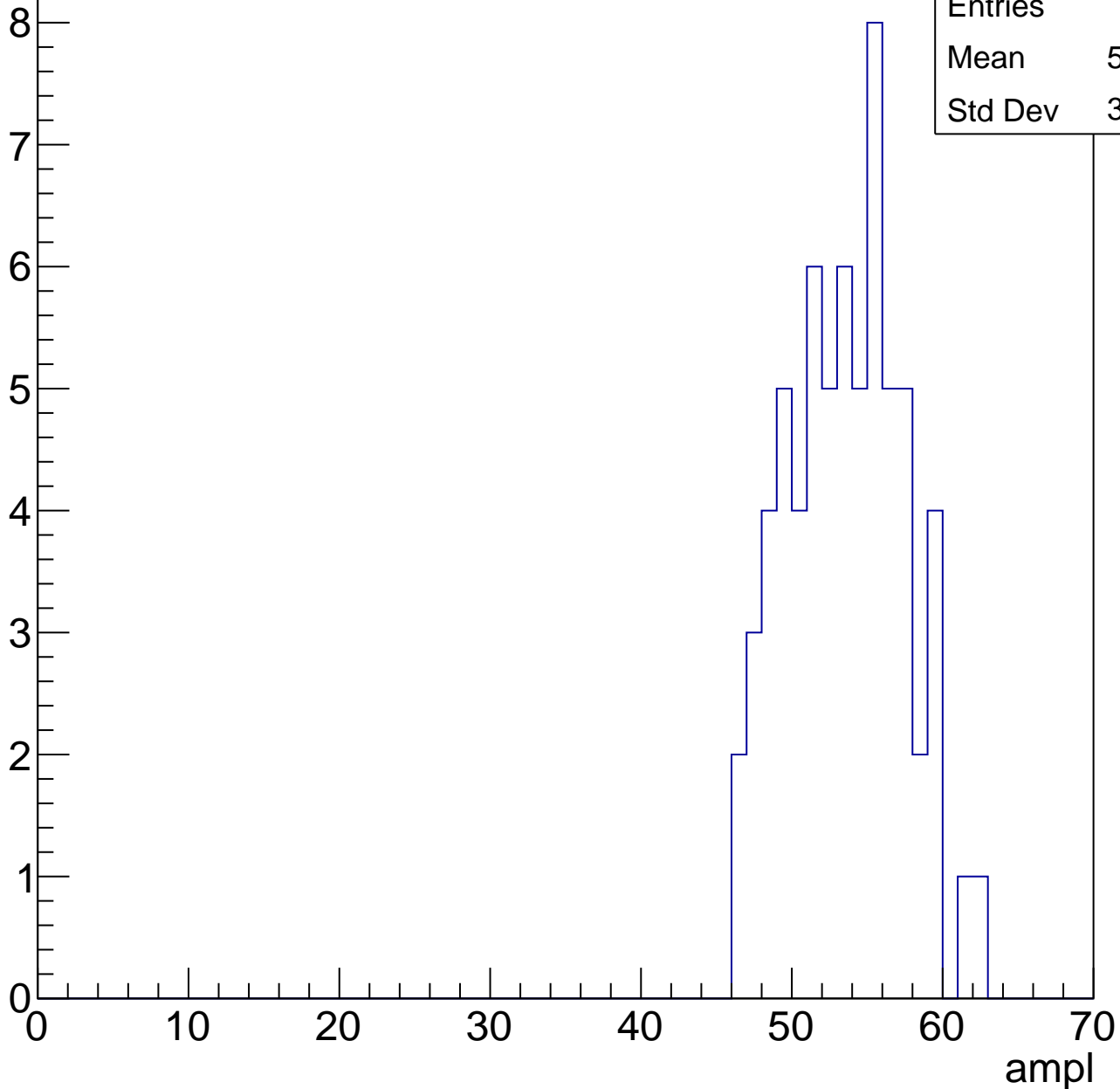


B1L103S, U18-ch120, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

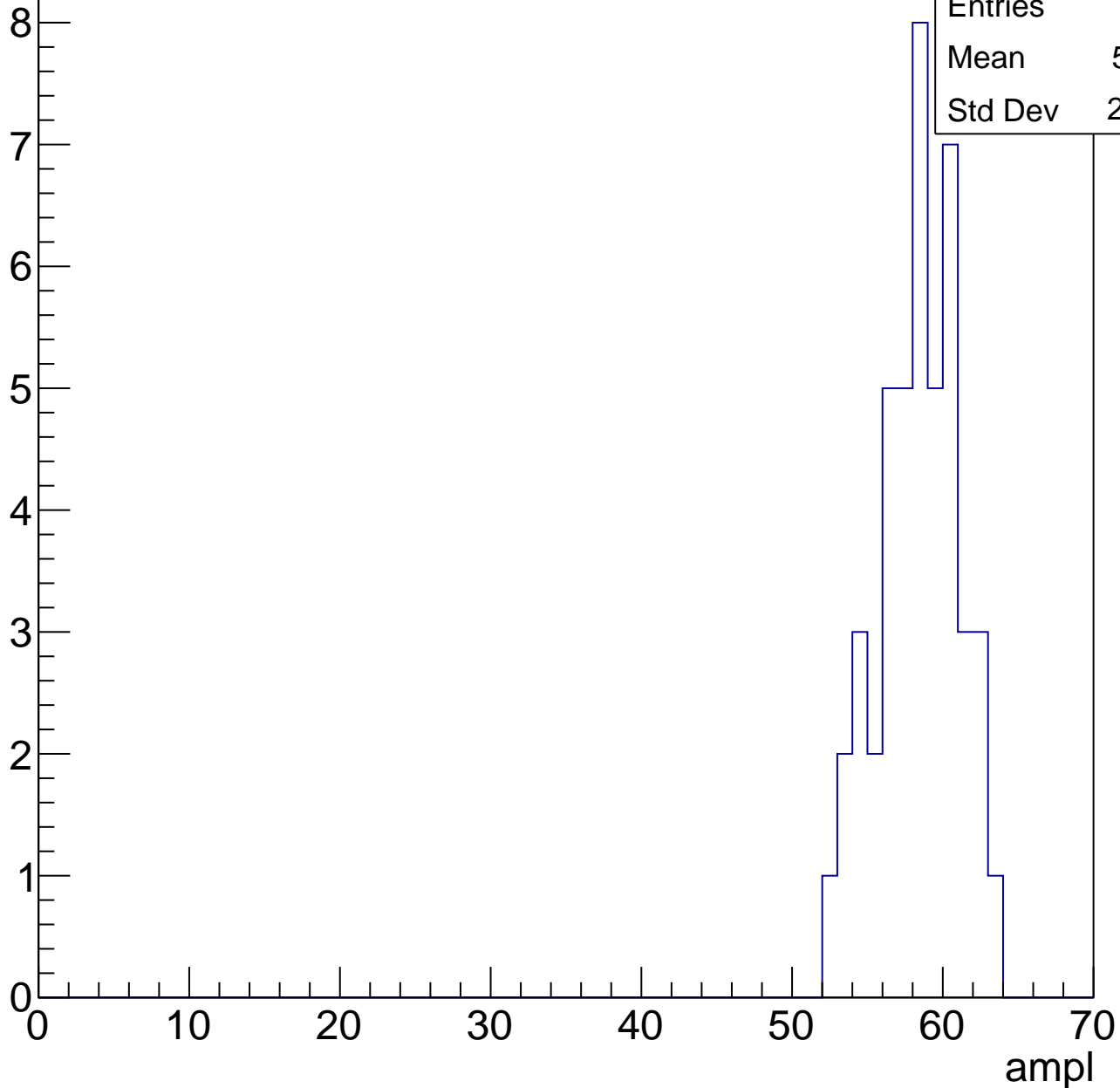
Entries	66
Mean	53.09
Std Dev	3.797



B1L103S, U18-ch120, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

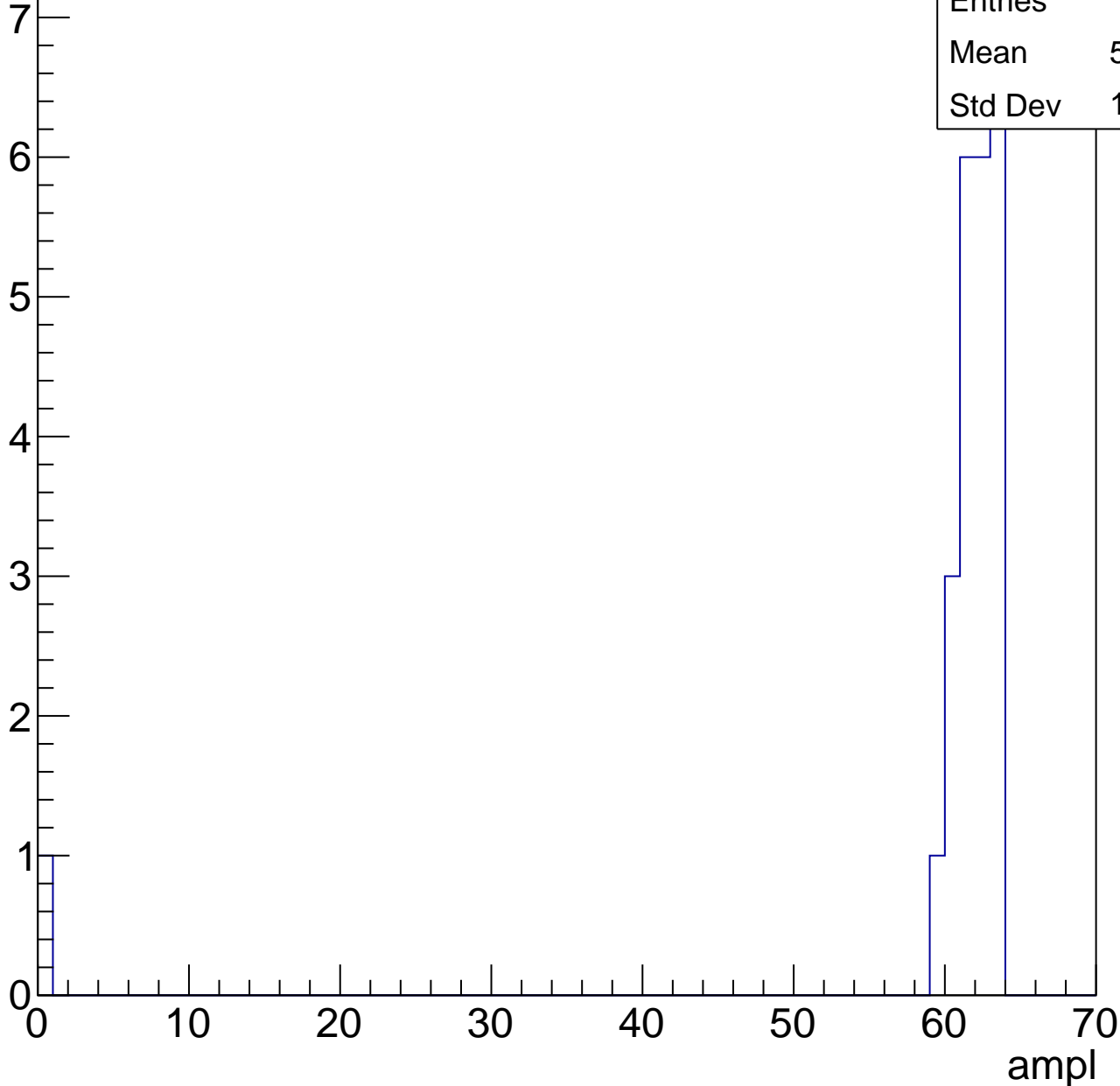


B1L103S, U18-ch120, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	24
Mean	59.08
Std Dev	12.37



B1L103S, U18-ch120, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



B1L103S, U18-ch121, adc0

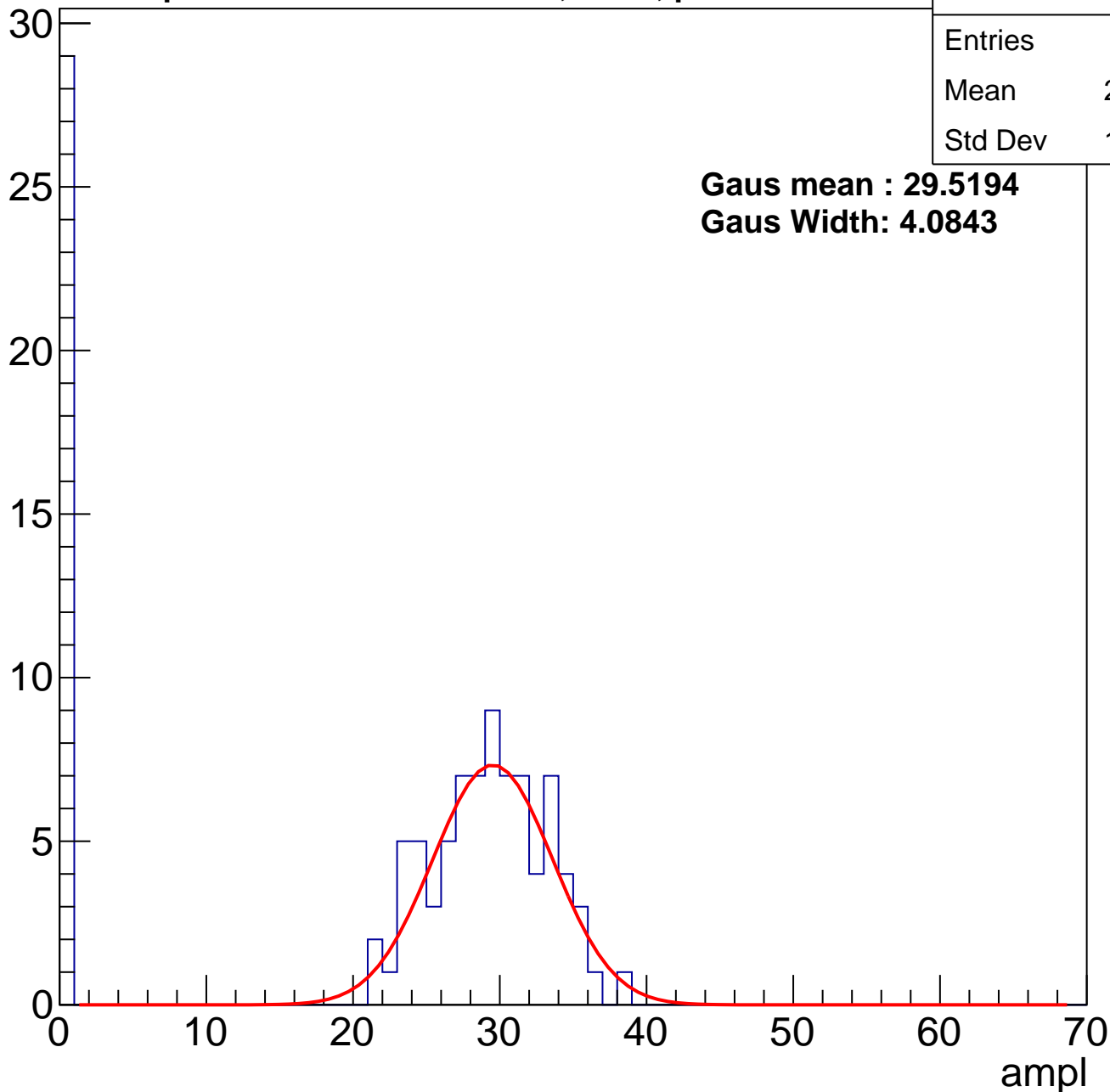
calib_packv5_041523_1651.root, FC#0, port C2

Entries	107
Mean	21.04
Std Dev	13.23

Gaus mean : 29.5194

Gaus Width: 4.0843

Entry



B1L103S, U18-ch121, adc1

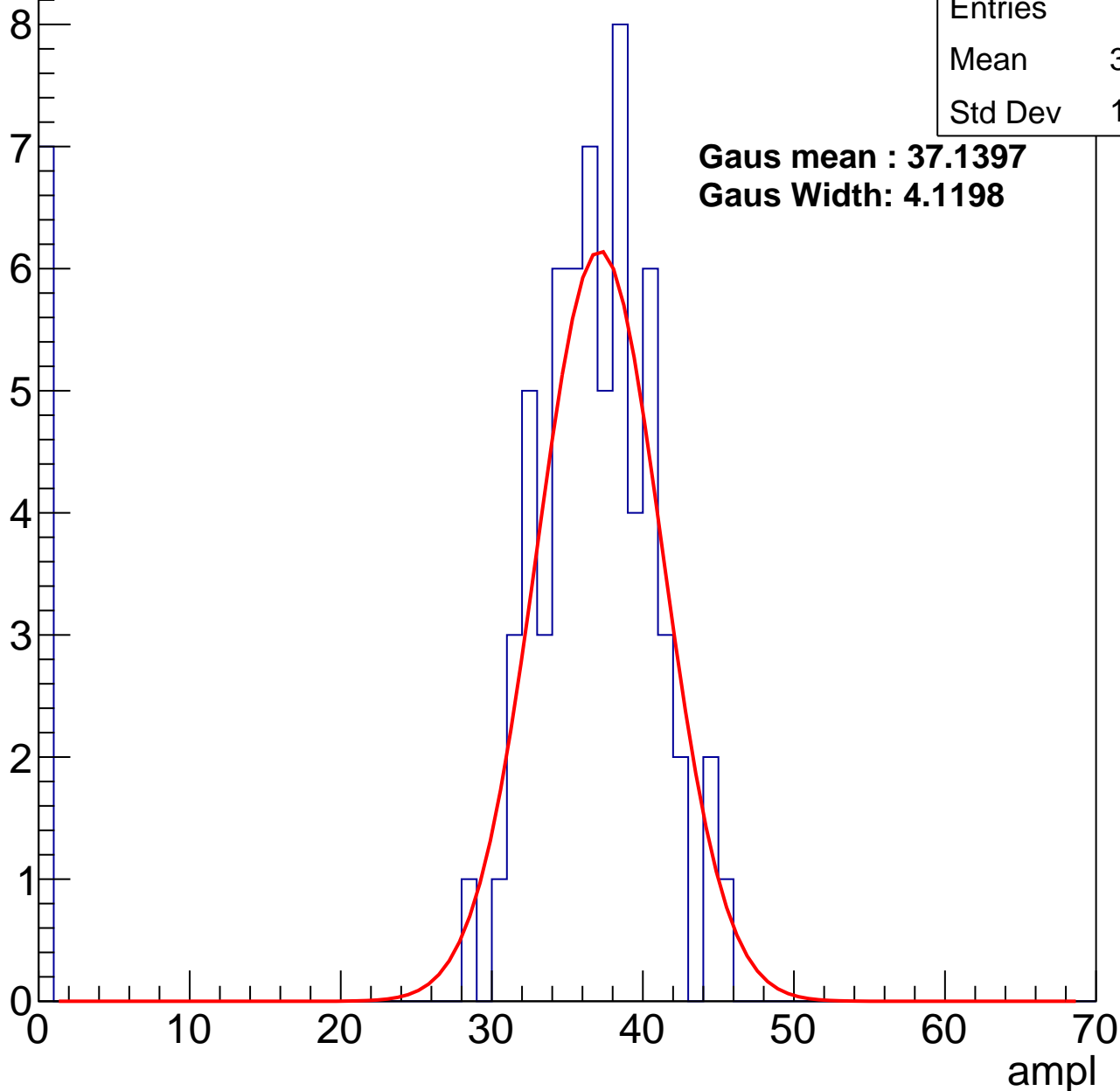
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	32.87
Std Dev	11.48

Gaus mean : 37.1397

Gaus Width: 4.1198



B1L103S, U18-ch121, adc2

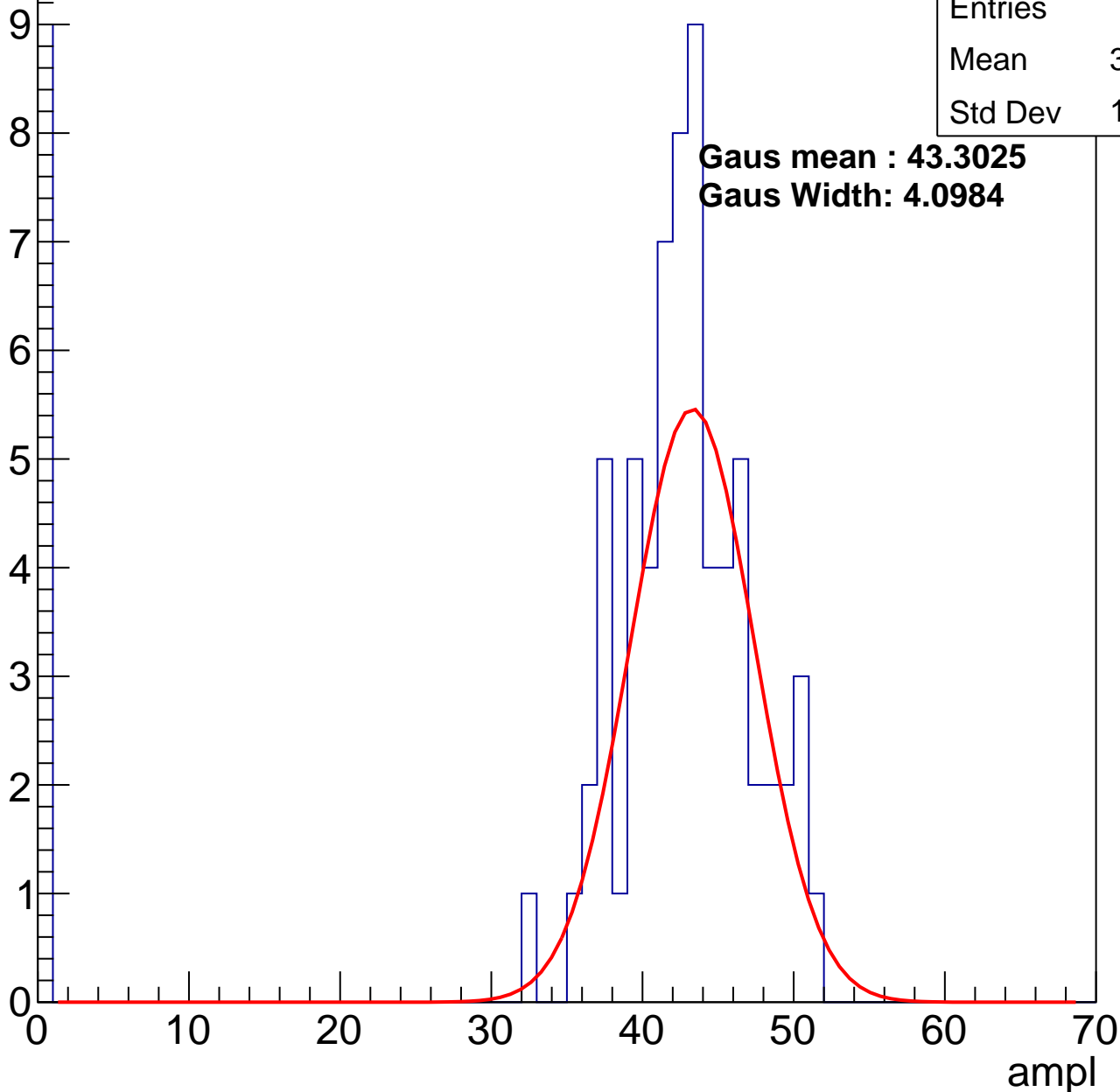
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	75
Mean	37.36
Std Dev	14.29

Gaus mean : 43.3025

Gaus Width: 4.0984

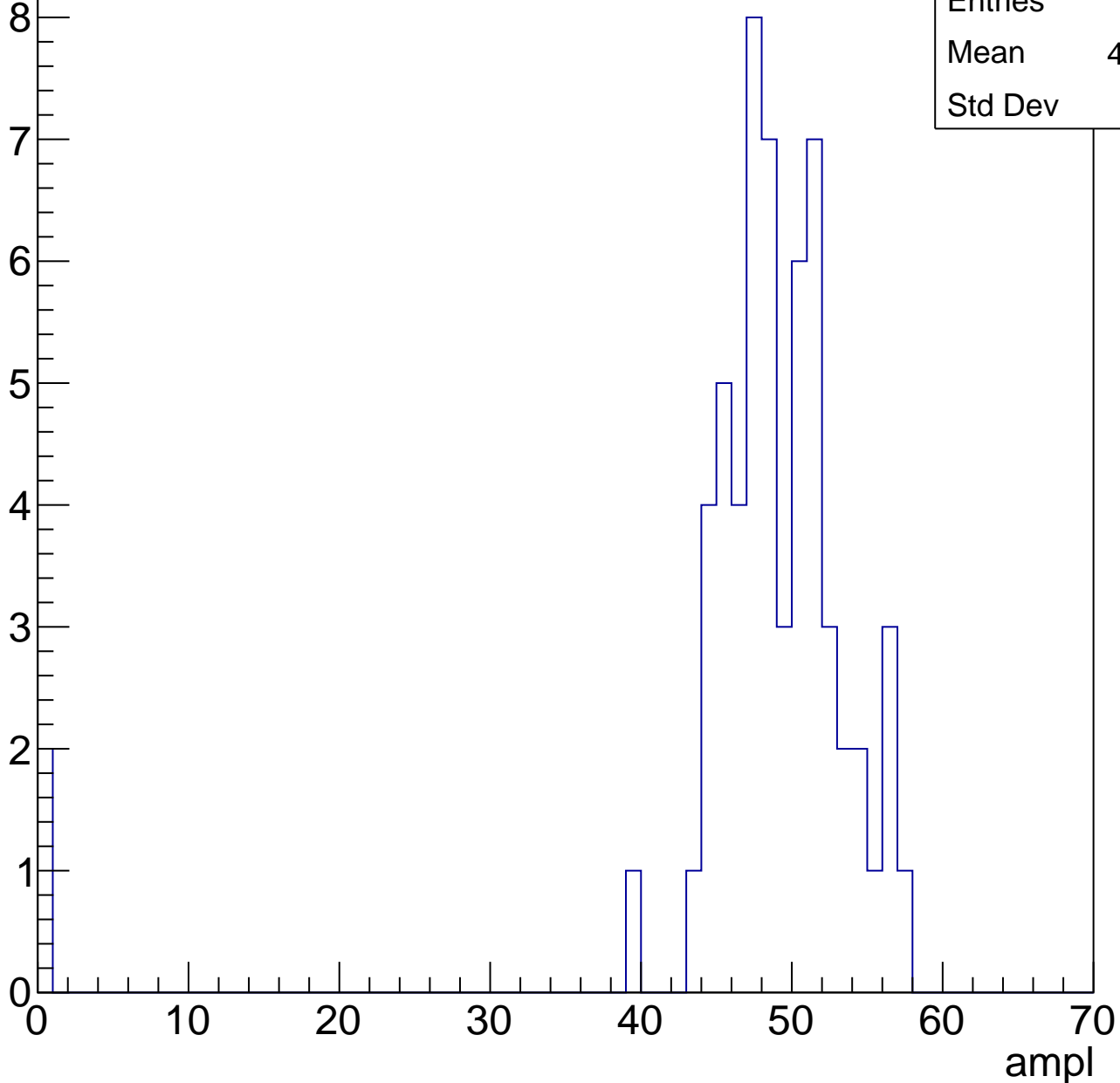


B1L103S, U18-ch121, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	60
Mean	47.22
Std Dev	9.48

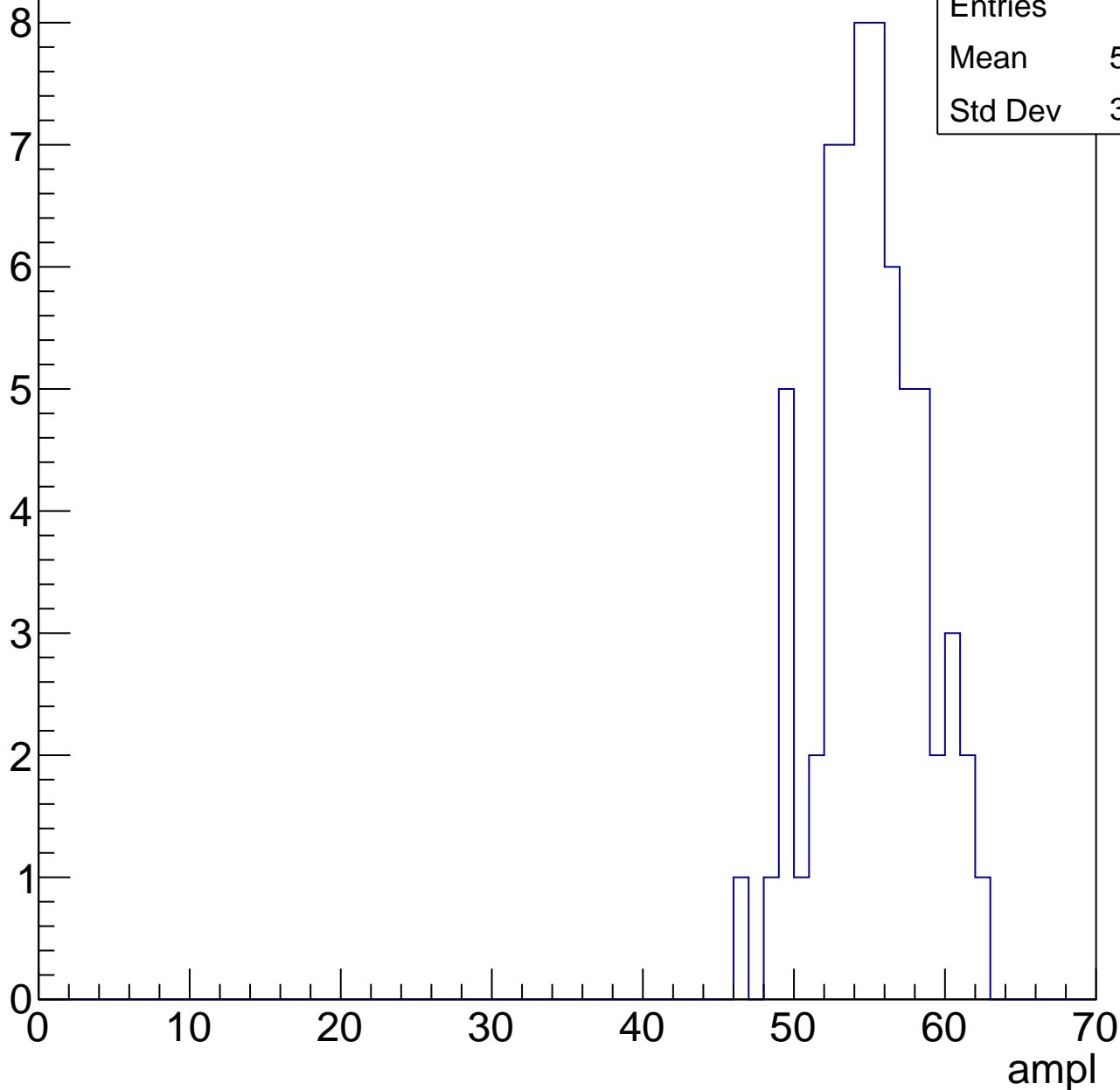


B1L103S, U18-ch121, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	54.55
Std Dev	3.428

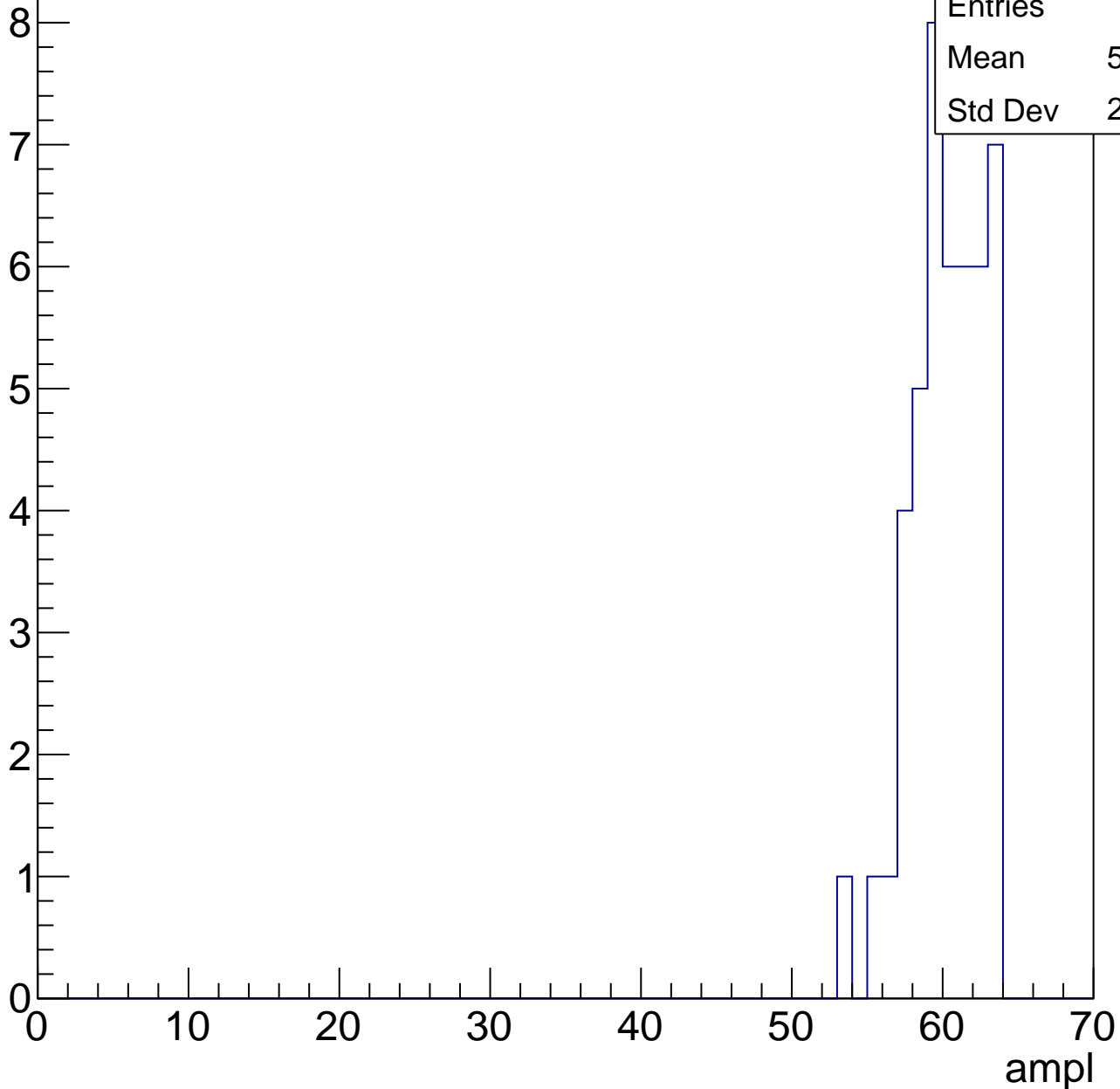


B1L103S, U18-ch121, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

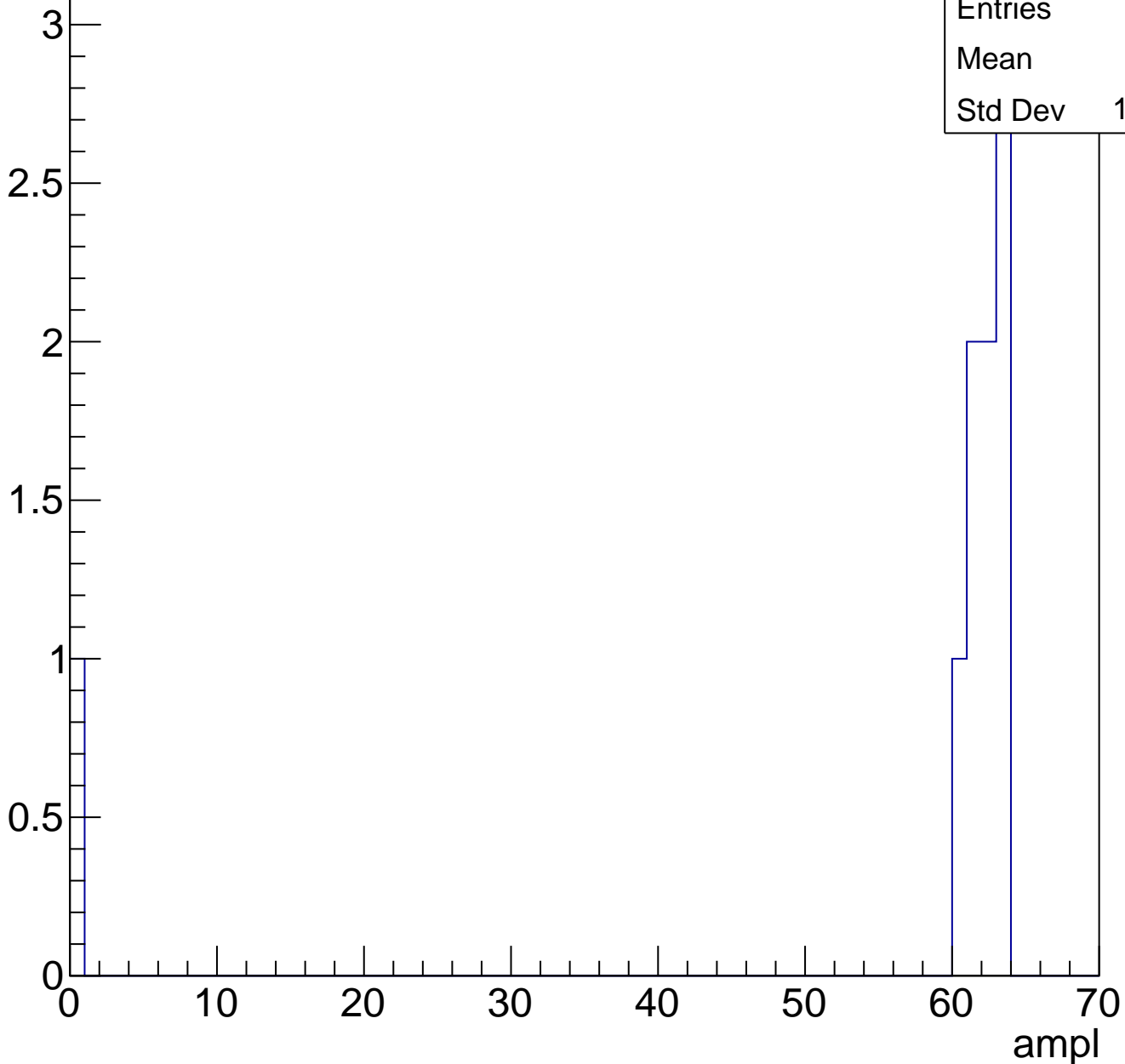
Entries	45
Mean	59.84
Std Dev	2.338



B1L103S, U18-ch121, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



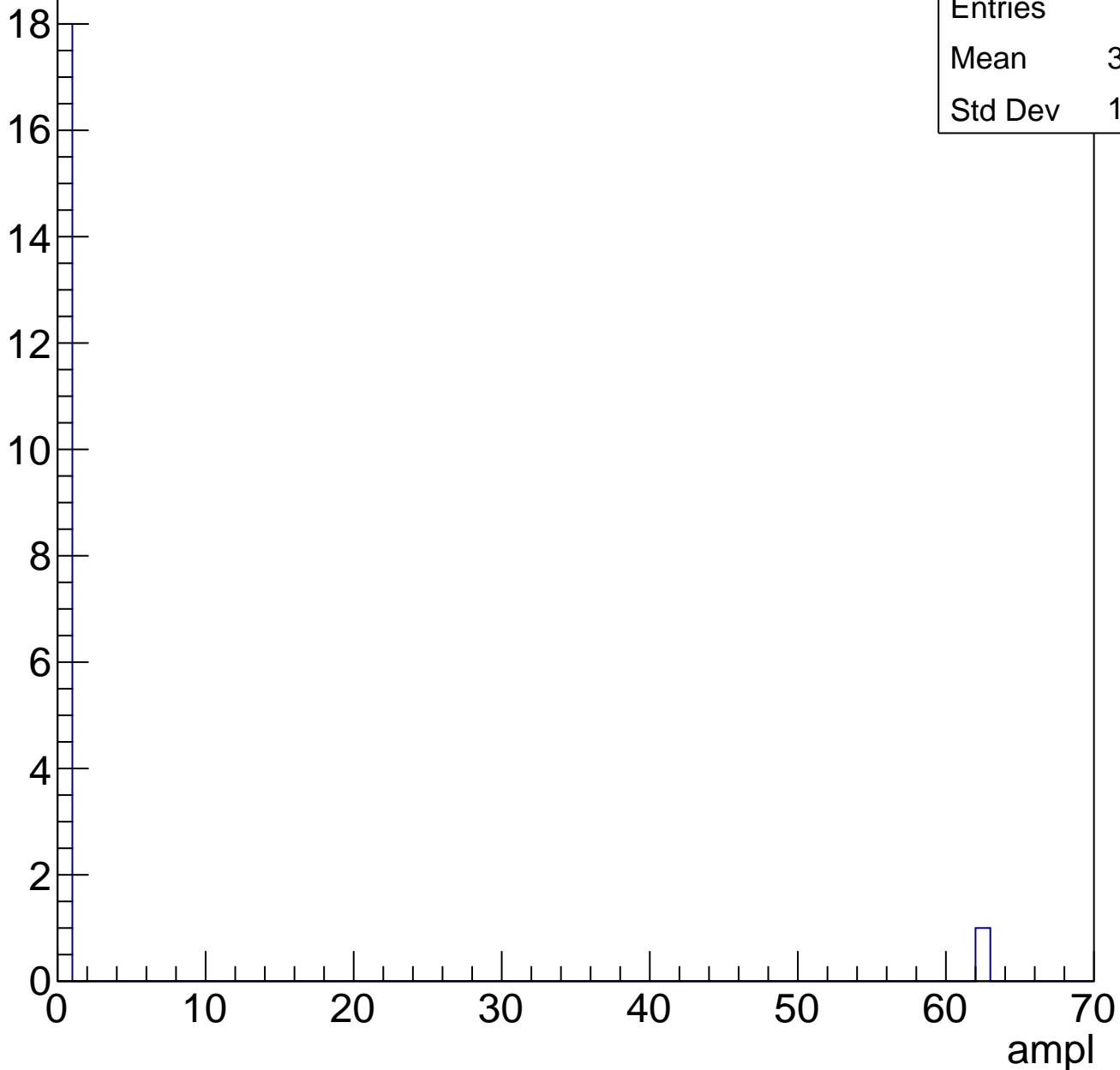
Entries	9
Mean	55
Std Dev	19.47

B1L103S, U18-ch121, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	19
Mean	3.263
Std Dev	13.84

Entry



B1L103S, U18-ch122, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	103
Mean	20.21
Std Dev	12.86

Gaus mean : 28.0444

Gaus Width: 4.0231

Entry

25

20

15

10

5

0

0

10

20

30

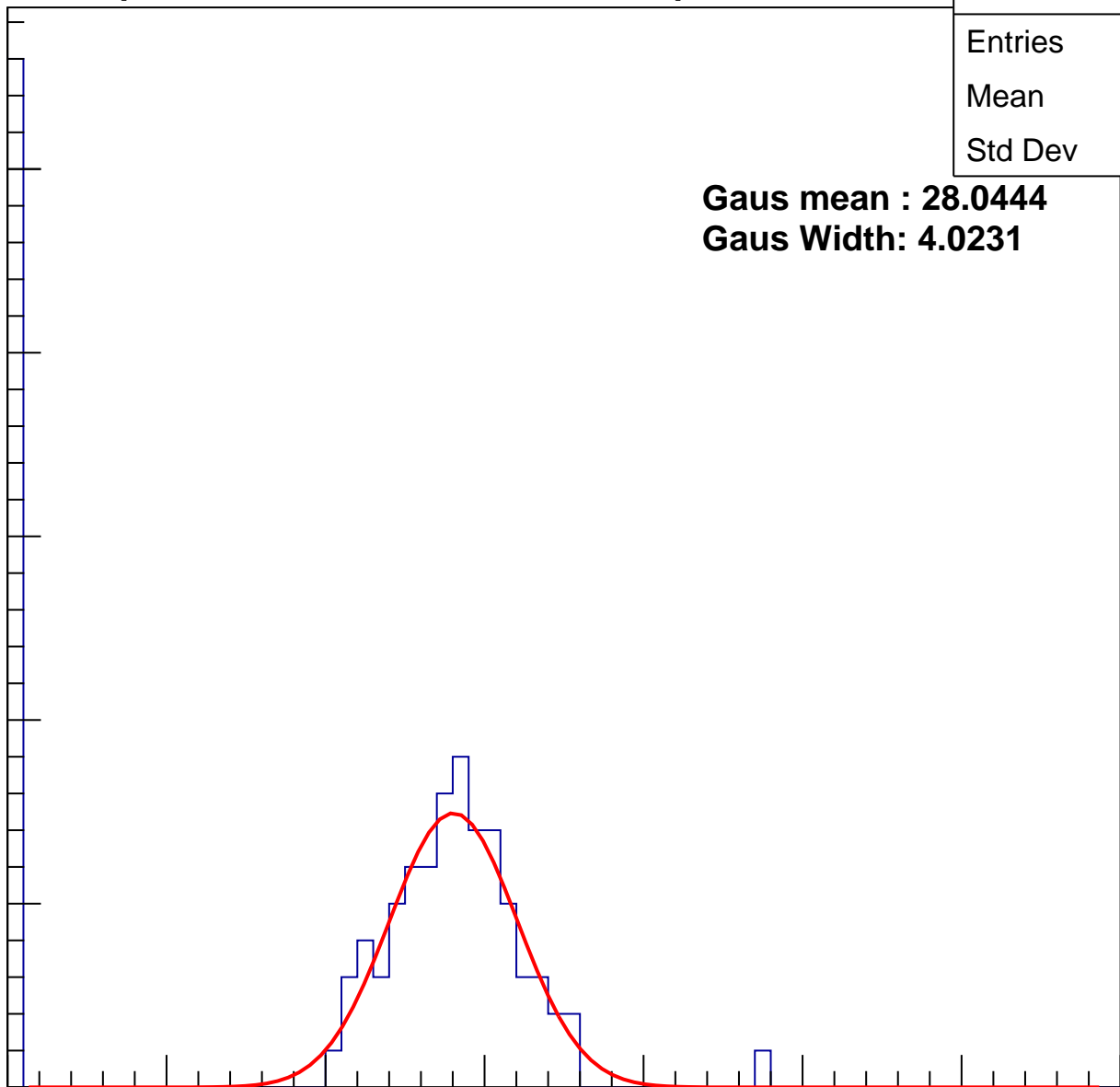
40

50

60

70

ampl



B1L103S, U18-ch122, adc1

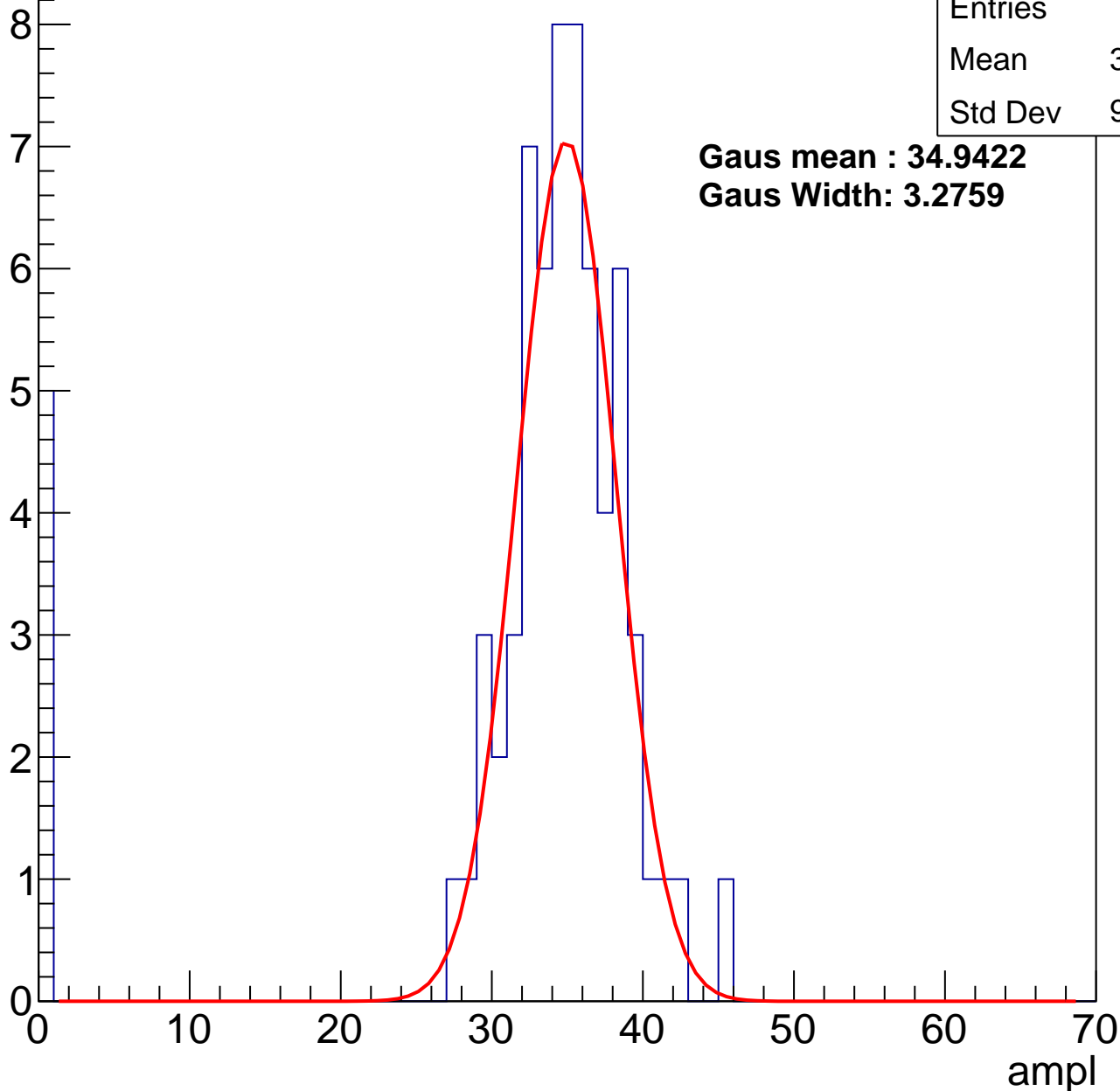
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.03
Std Dev	9.683

Gaus mean : 34.9422

Gaus Width: 3.2759



B1L103S, U18-ch122, adc2

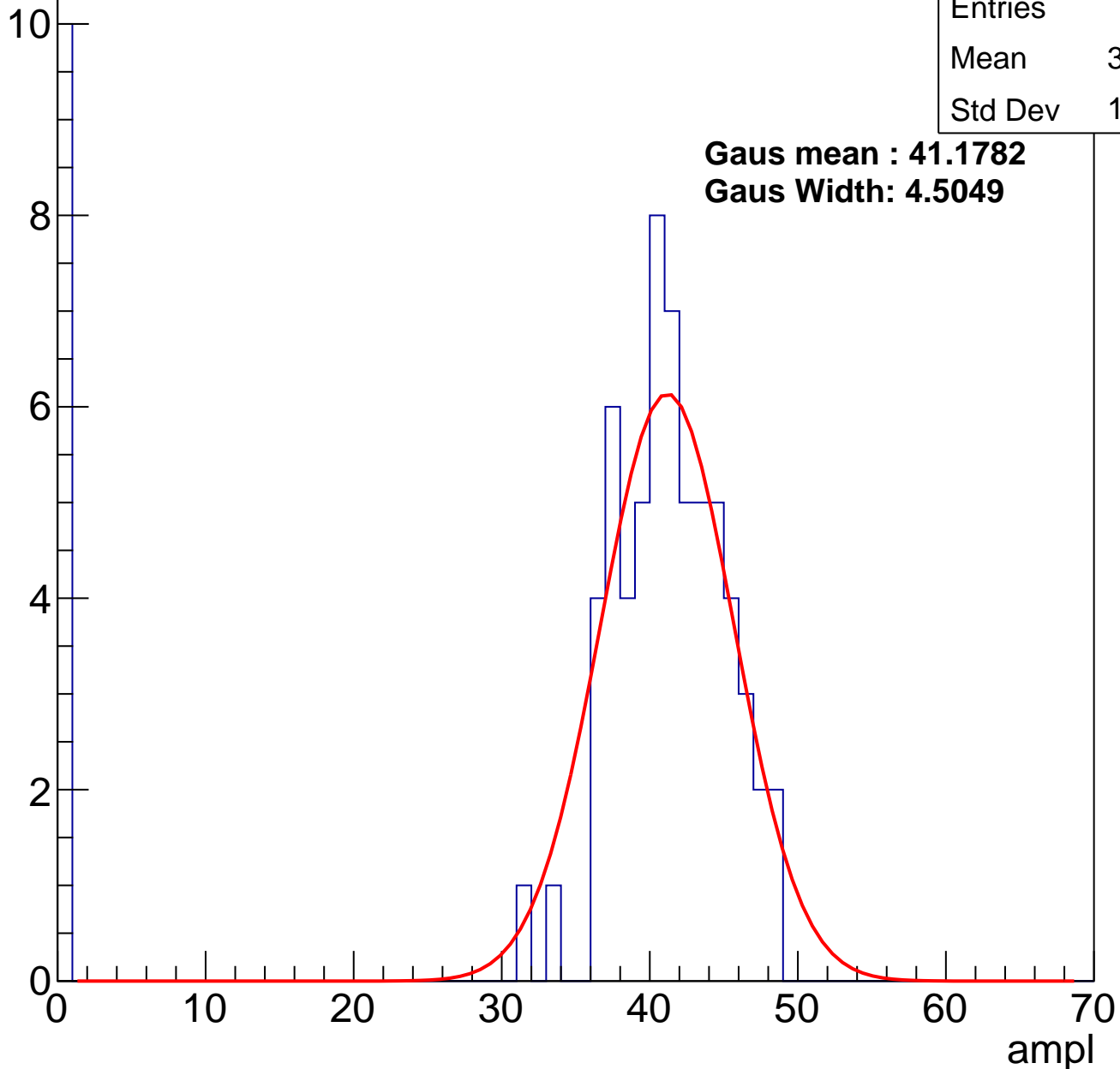
calib_packv5_041523_1651.root, FC#0, port C2

Entries	72
Mean	35.24
Std Dev	14.54

Gaus mean : 41.1782

Gaus Width: 4.5049

Entry

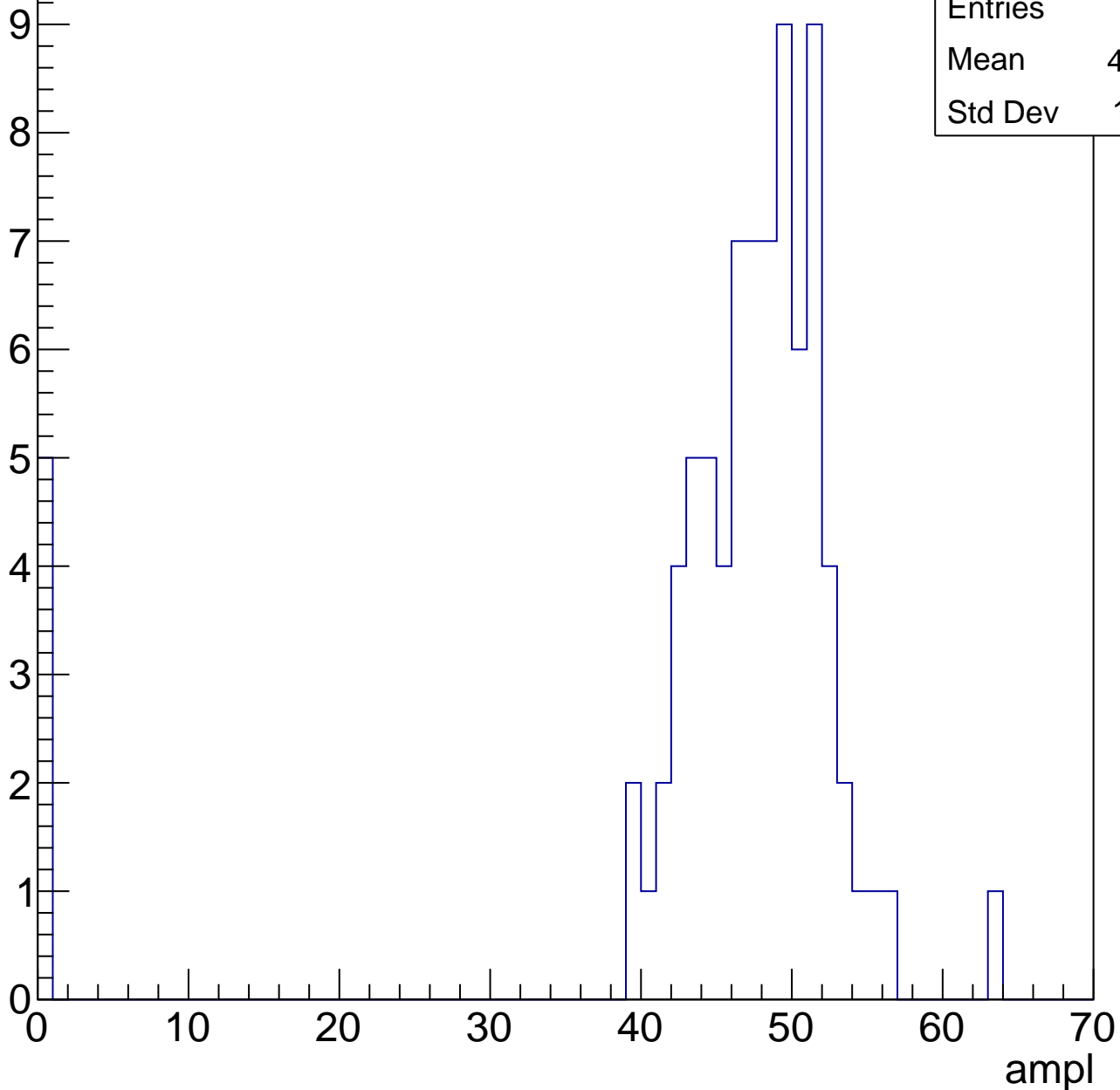


B1L103S, U18-ch122, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	83
Mean	44.72
Std Dev	12.01

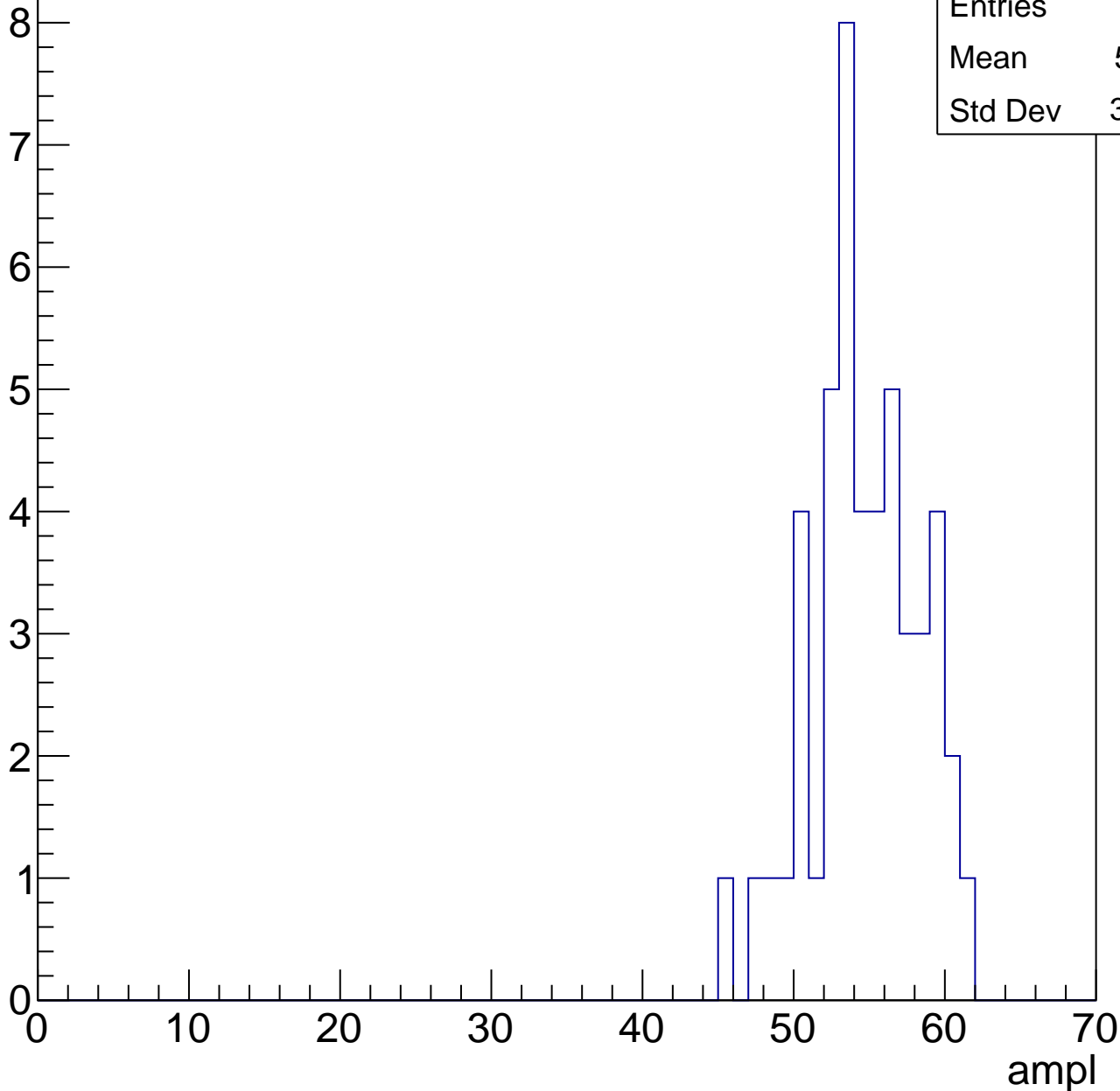


B1L103S, U18-ch122, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	48
Mean	54.21
Std Dev	3.565

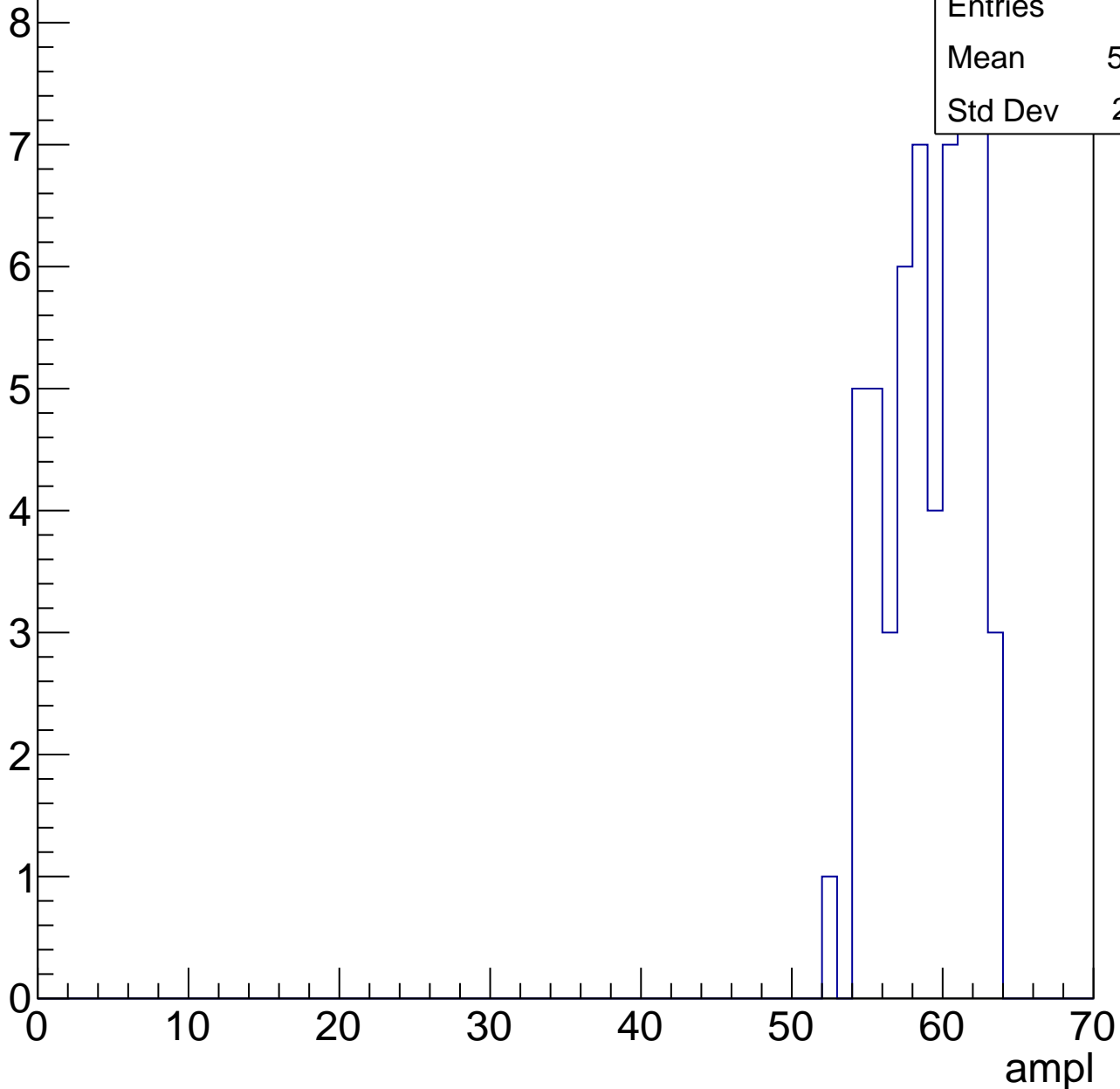


B1L103S, U18-ch122, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.63
Std Dev	2.851

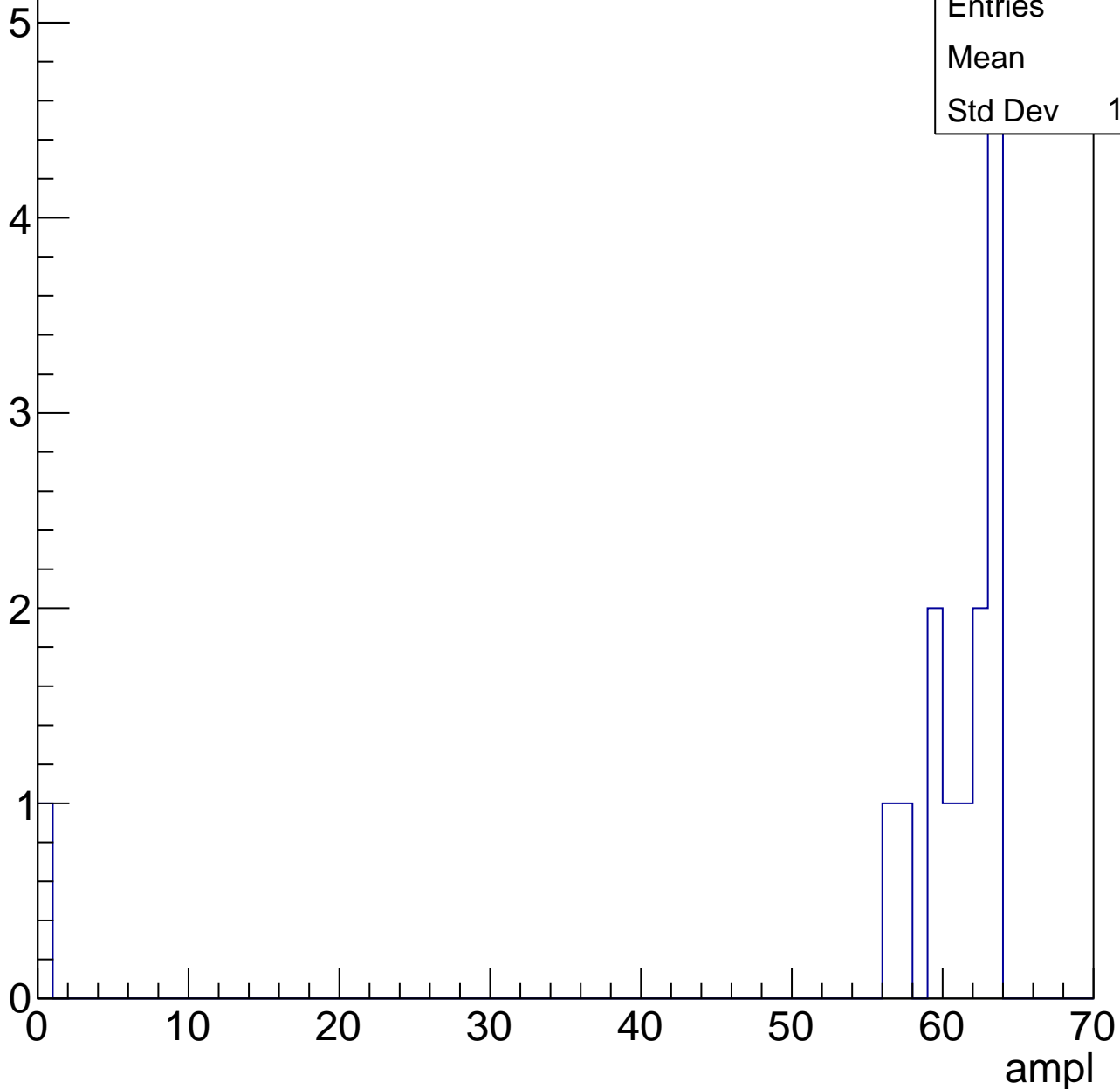


B1L103S, U18-ch122, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	14
Mean	56.5
Std Dev	15.83



B1L103S, U18-ch122, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



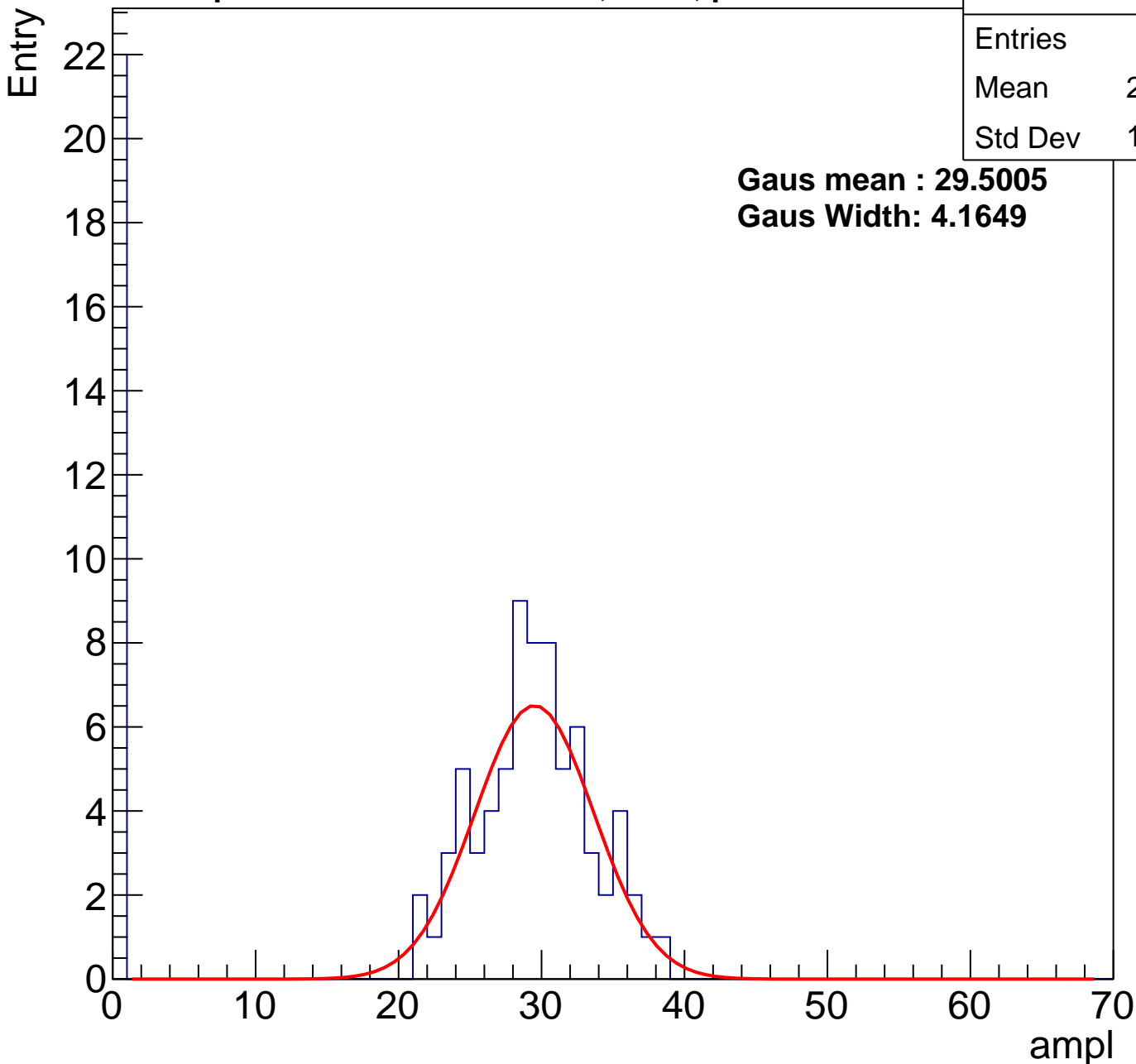
B1L103S, U18-ch123, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	94
Mean	22.26
Std Dev	12.76

Gaus mean : 29.5005

Gaus Width: 4.1649



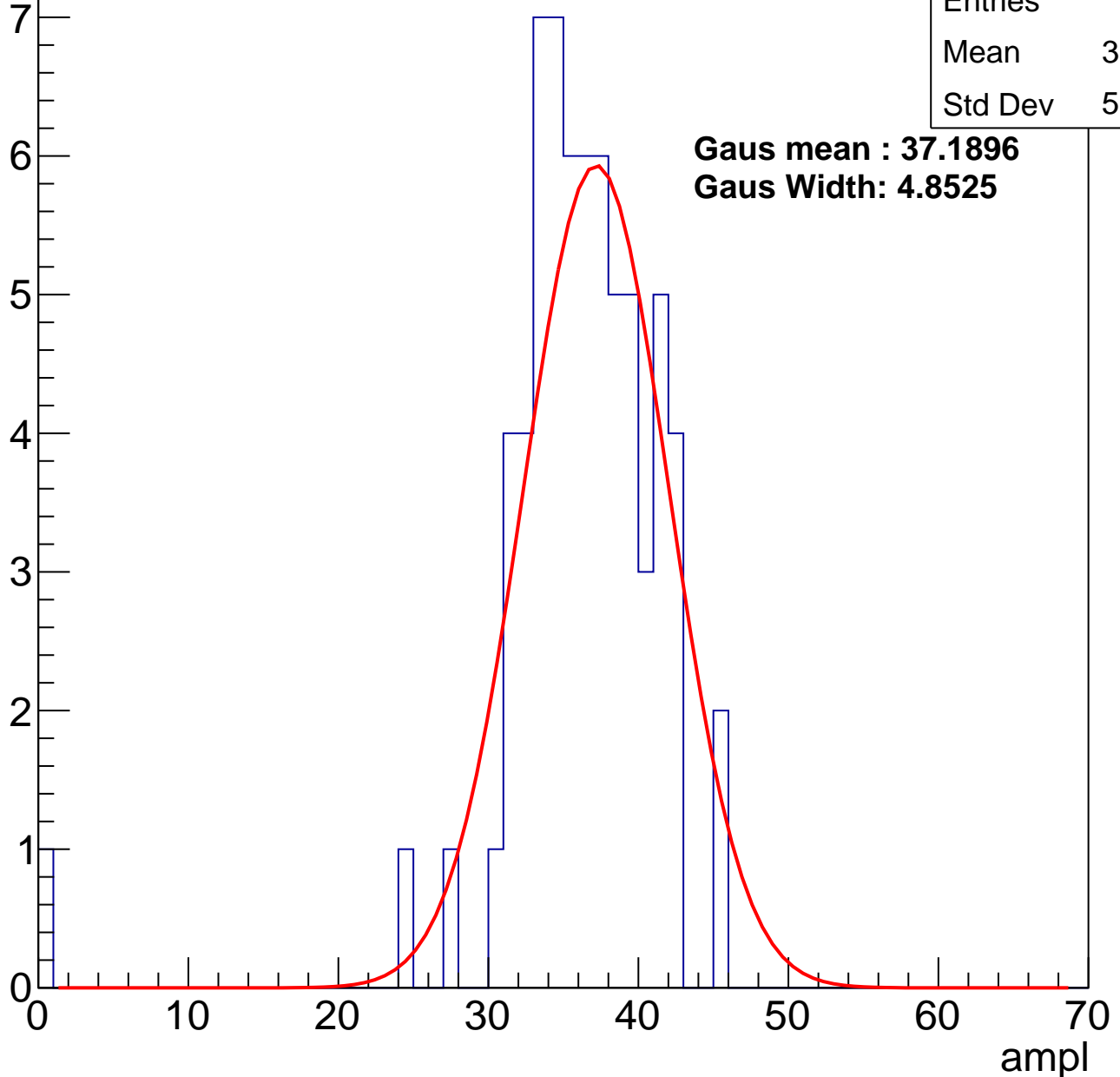
B1L103S, U18-ch123, adc1

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	35.56
Std Dev	5.885

Gaus mean : 37.1896
Gaus Width: 4.8525



B1L103S, U18-ch123, adc2

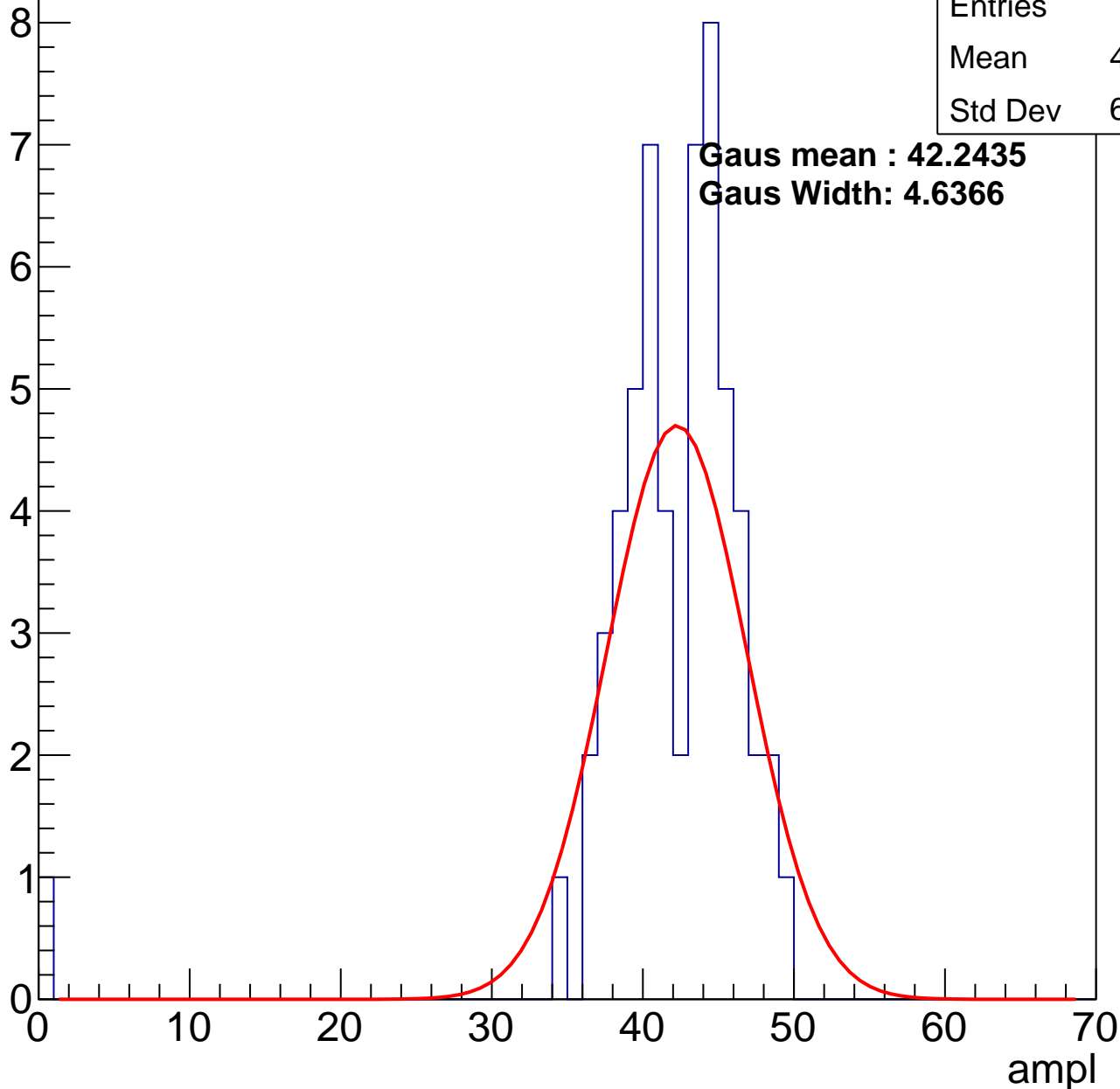
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	58
Mean	41.26
Std Dev	6.437

Gaus mean : 42.2435

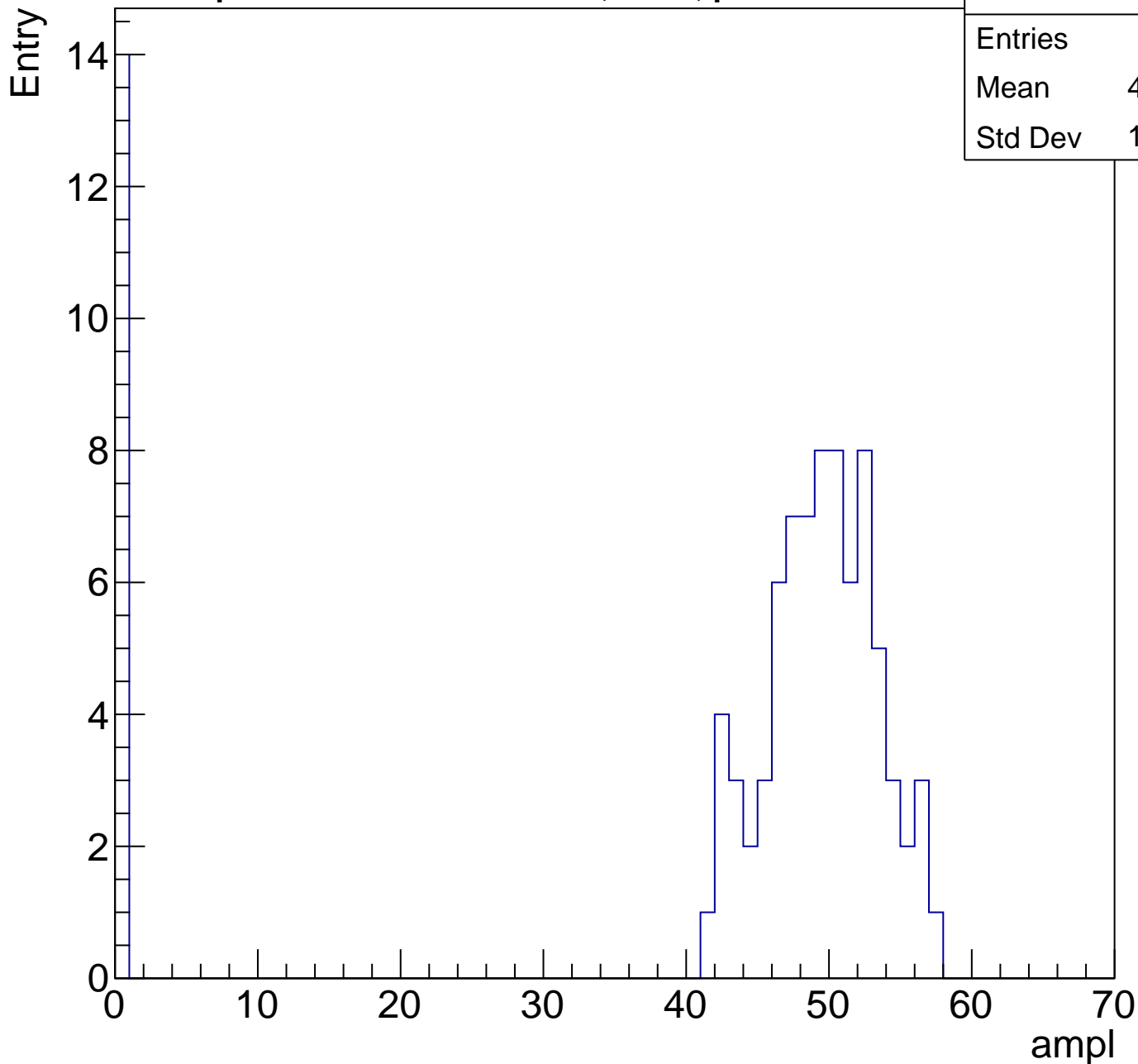
Gaus Width: 4.6366



B1L103S, U18-ch123, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	91
Mean	41.52
Std Dev	18.04

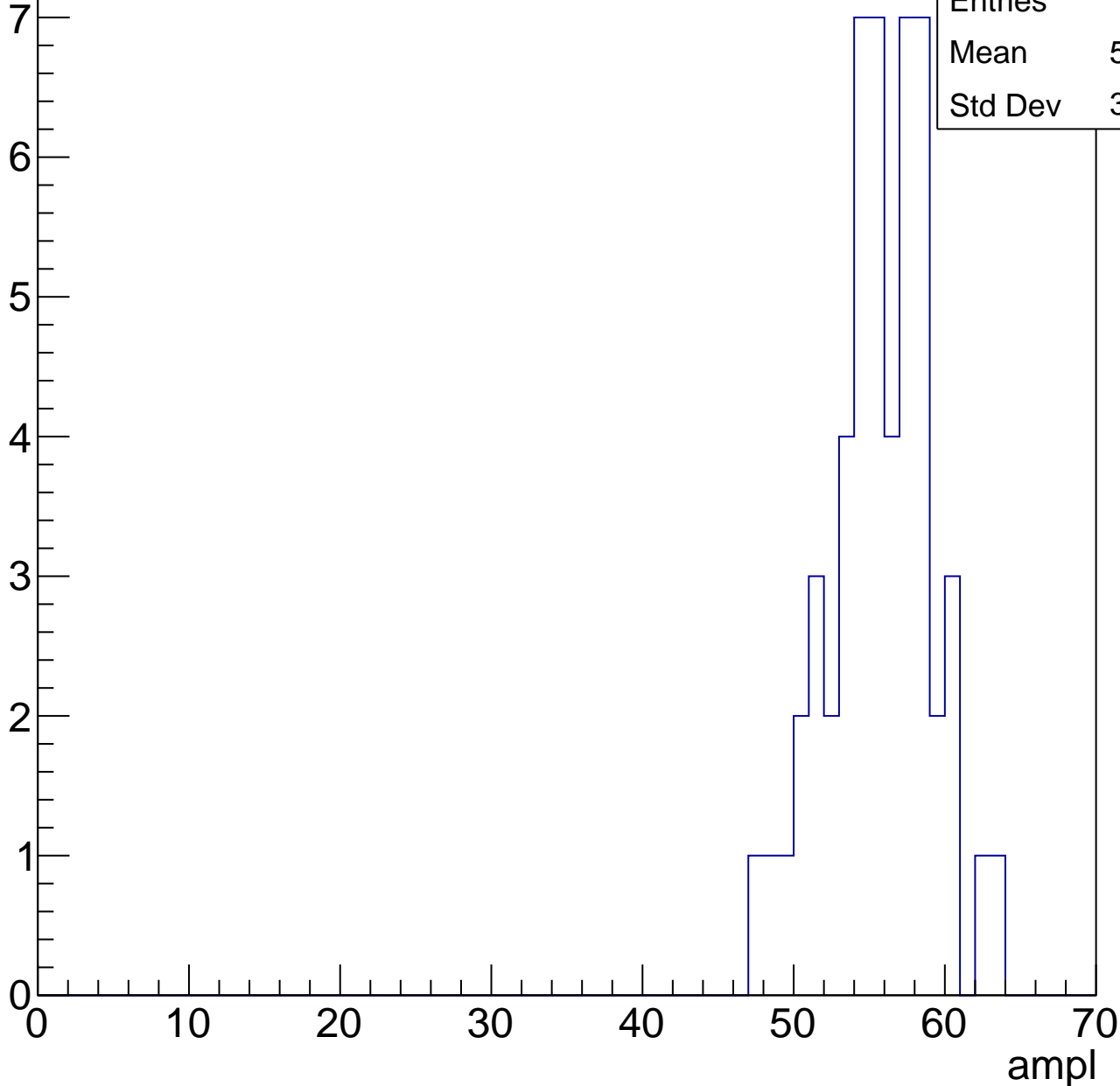


B1L103S, U18-ch123, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

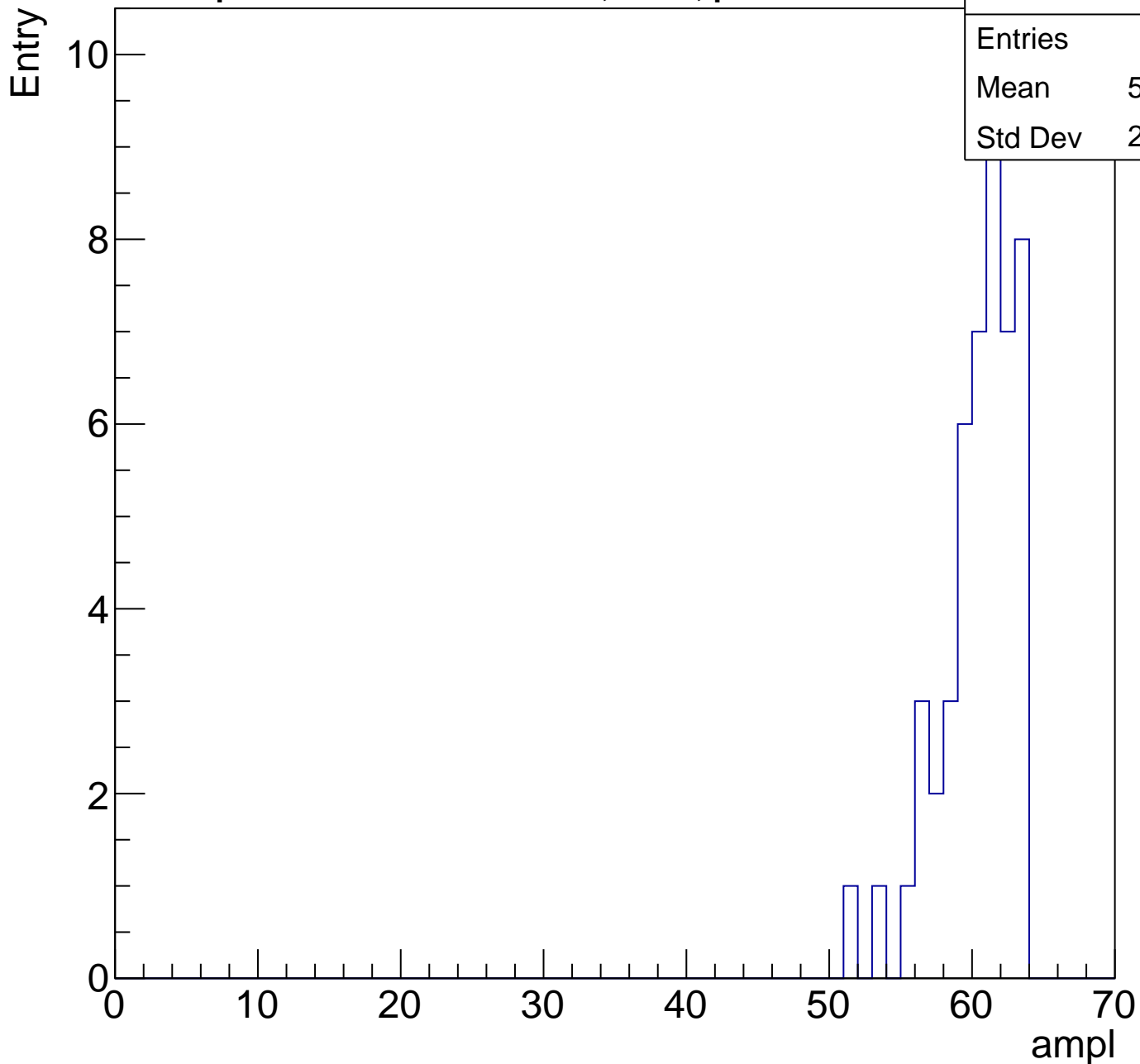
Entries	53
Mean	55.25
Std Dev	3.364



B1L103S, U18-ch123, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entries	49
Mean	59.94
Std Dev	2.668



B1L103S, U18-ch123, adc6

calib_packv5_041523_1651.root, FC#0, port C2

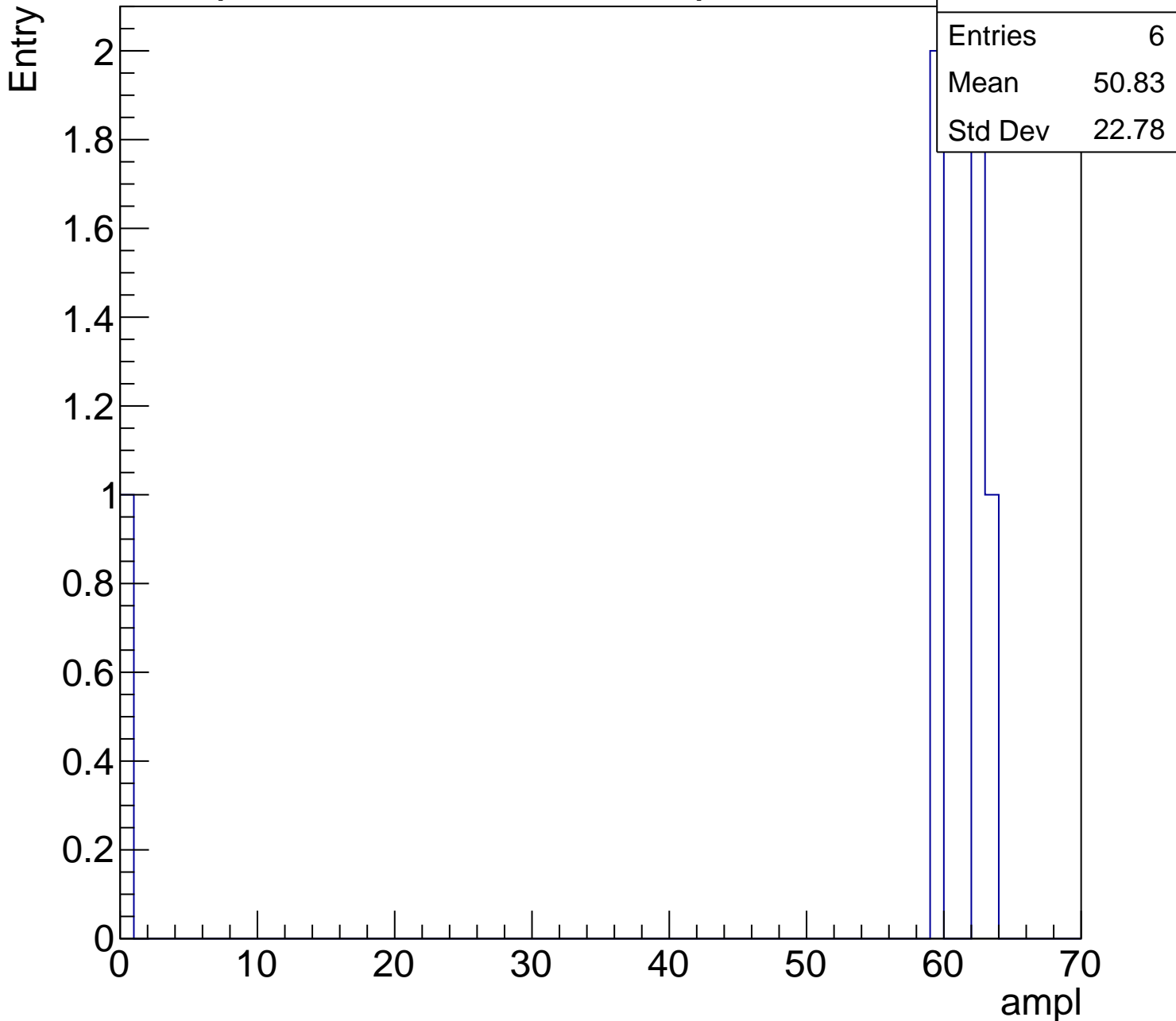
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50.83
Std Dev	22.78

0 10 20 30 40 50 60 70

ampl



B1L103S, U18-ch123, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	19
Mean	0
Std Dev	0

ampl

B1L103S, U18-ch124, adc0

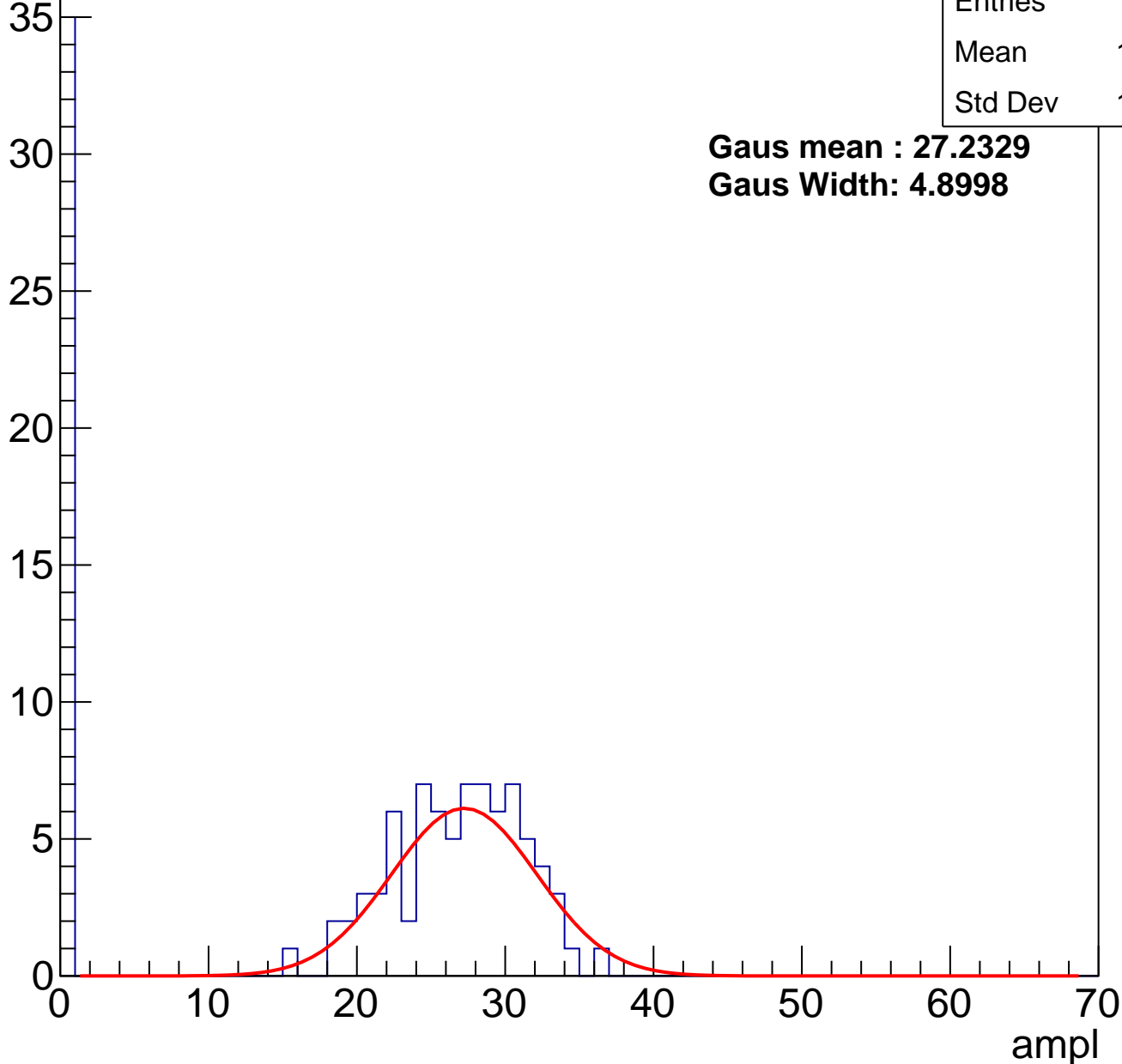
calib_packv5_041523_1651.root, FC#0, port C2

Entries	113
Mean	18.22
Std Dev	12.72

Gaus mean : 27.2329

Gaus Width: 4.8998

Entry



B1L103S, U18-ch124, adc1

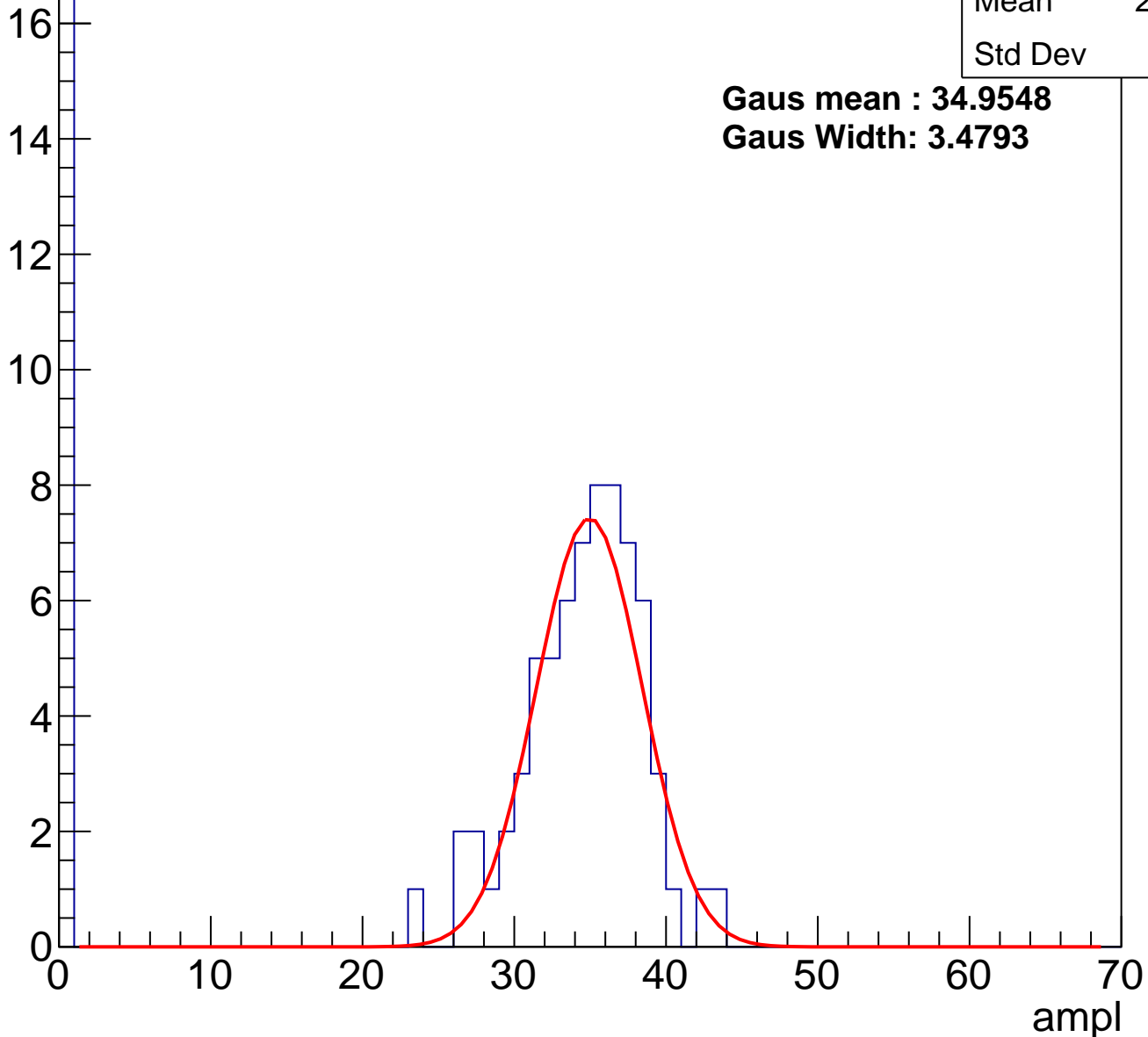
calib_packv5_041523_1651.root, FC#0, port C2

Entries	86
Mean	27.36
Std Dev	14

Gaus mean : 34.9548

Gaus Width: 3.4793

Entry



B1L103S, U18-ch124, adc2

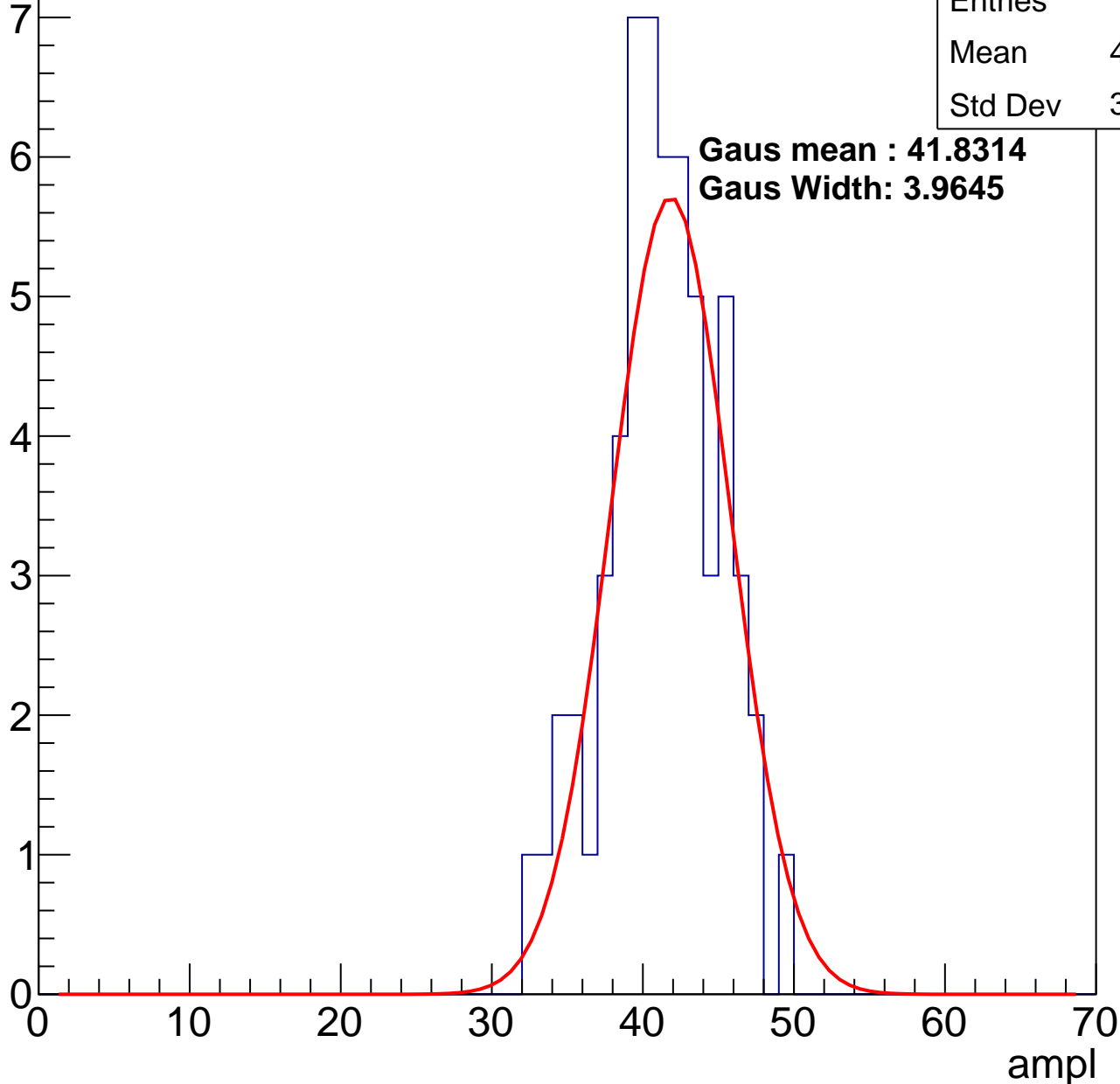
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	59
Mean	40.78
Std Dev	3.683

Gaus mean : 41.8314

Gaus Width: 3.9645

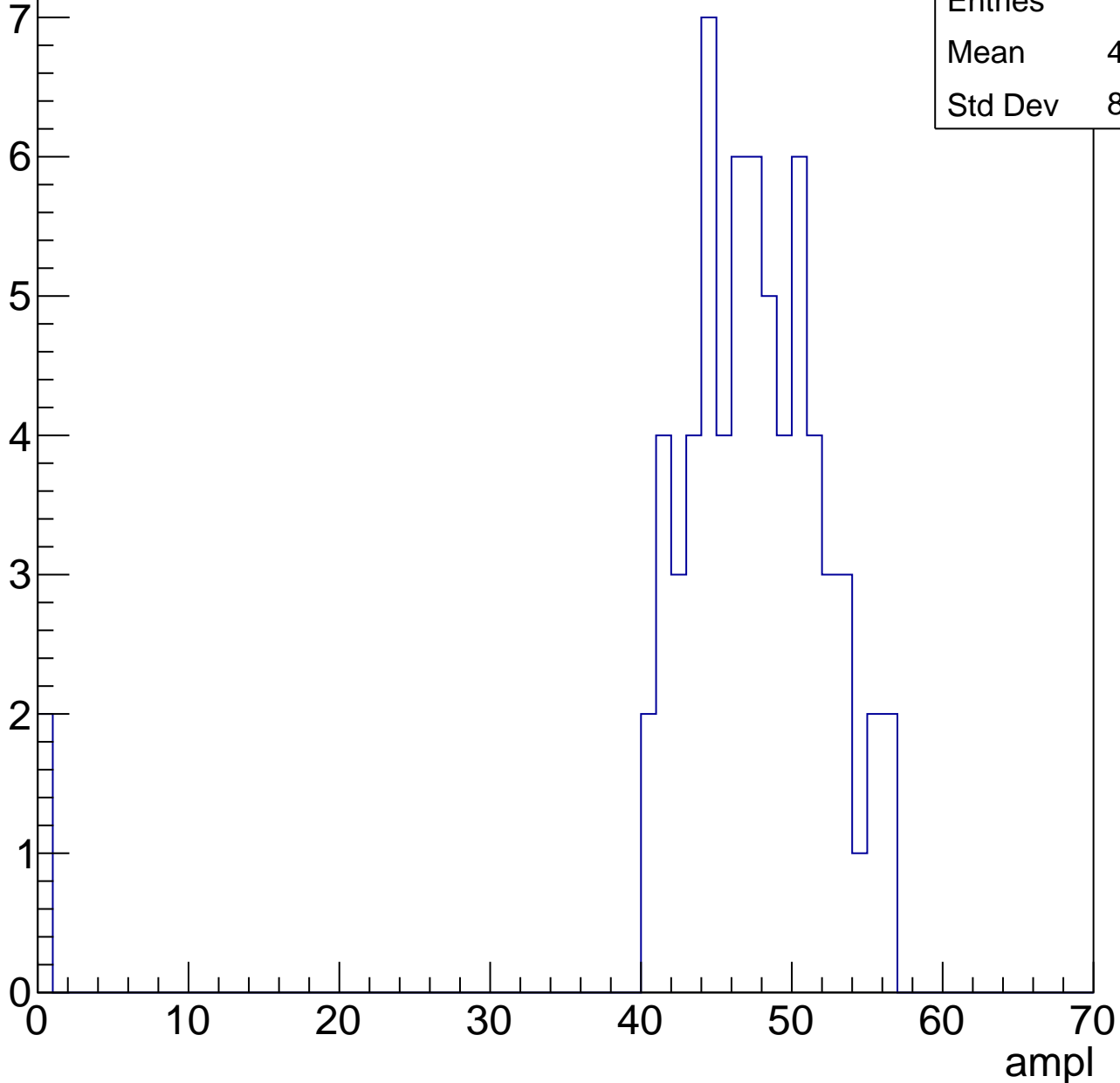


B1L103S, U18-ch124, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	45.87
Std Dev	8.959

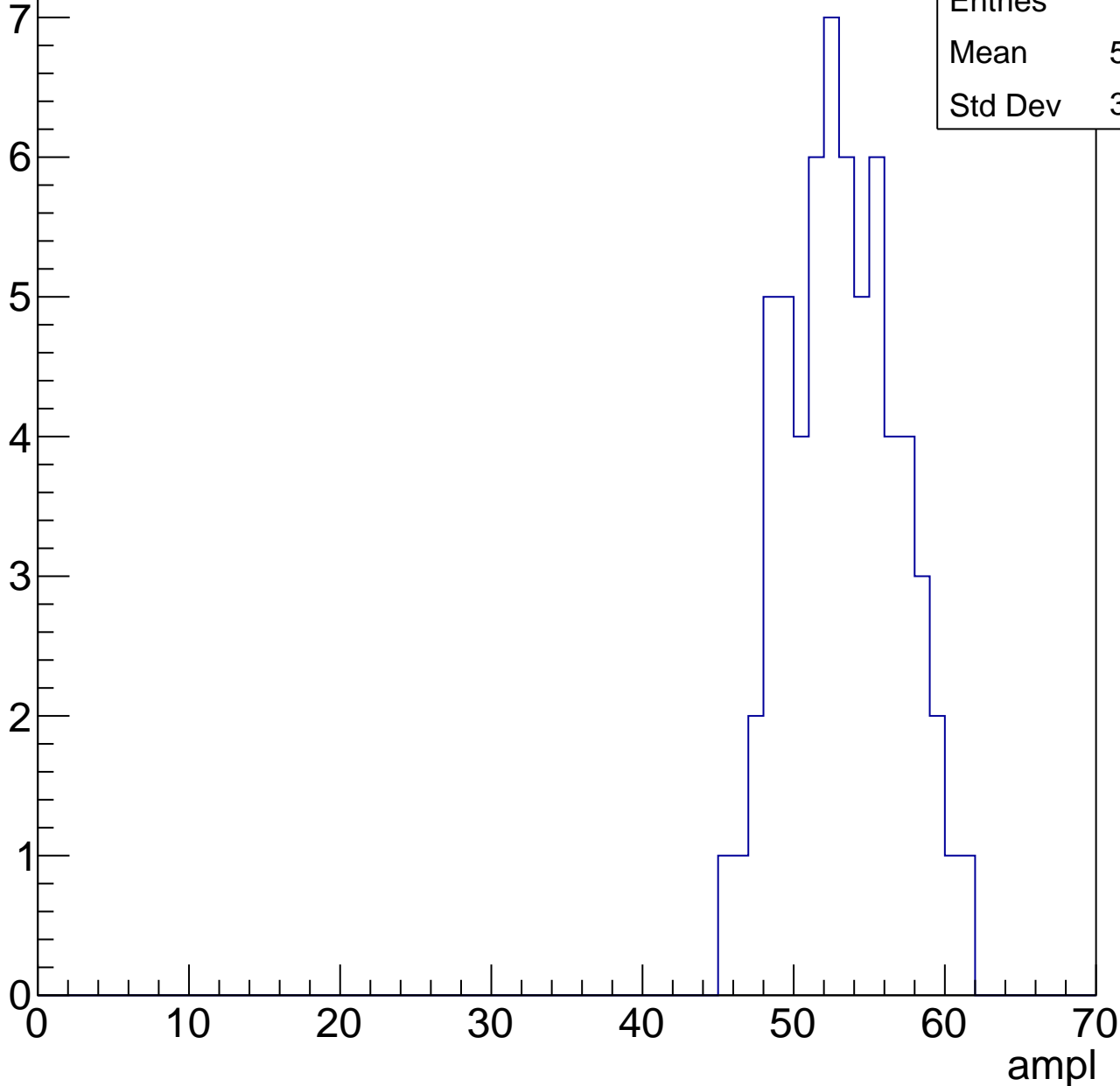


B1L103S, U18-ch124, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	63
Mean	52.75
Std Dev	3.656

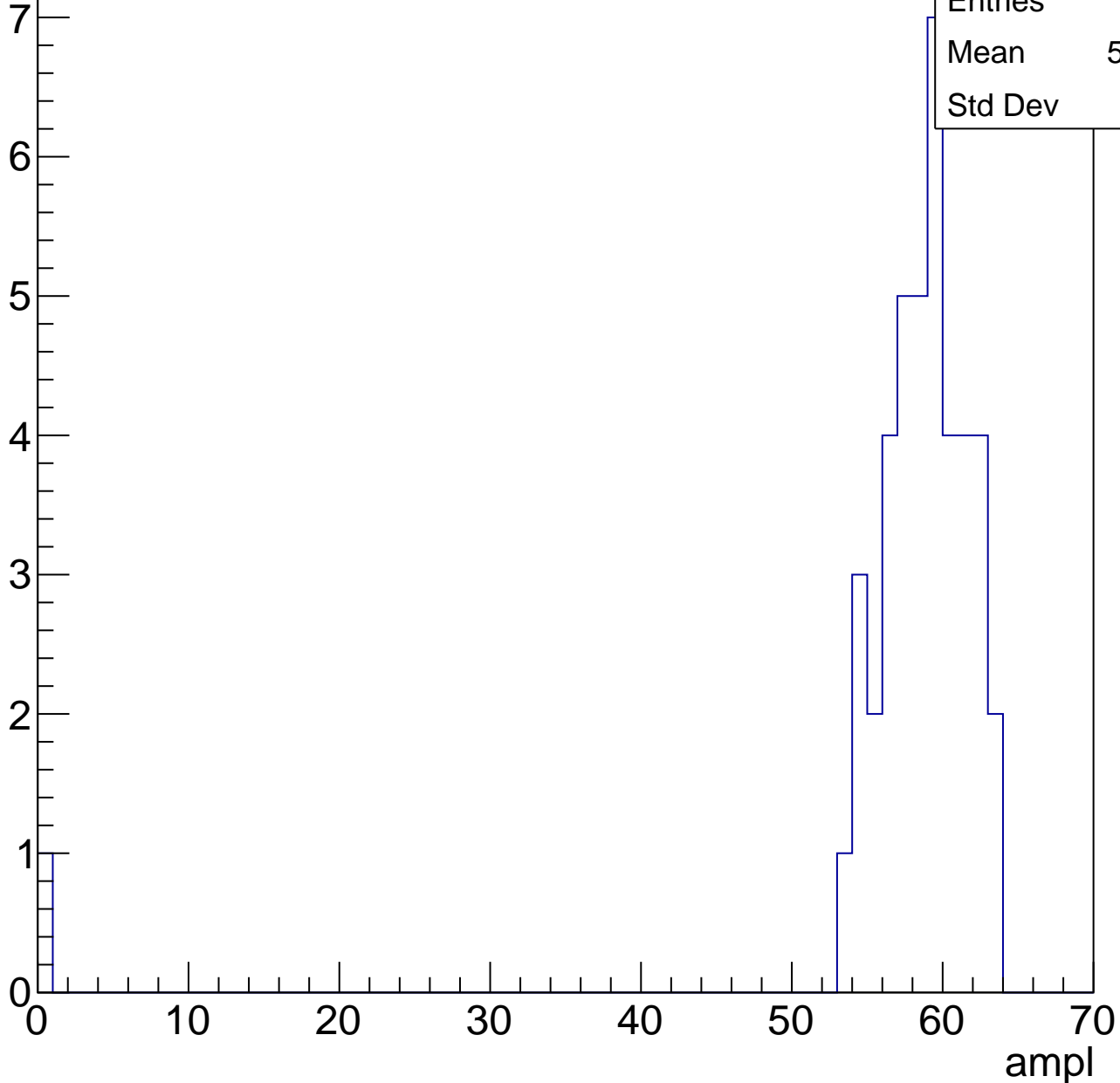


B1L103S, U18-ch124, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	42
Mean	57.02
Std Dev	9.27

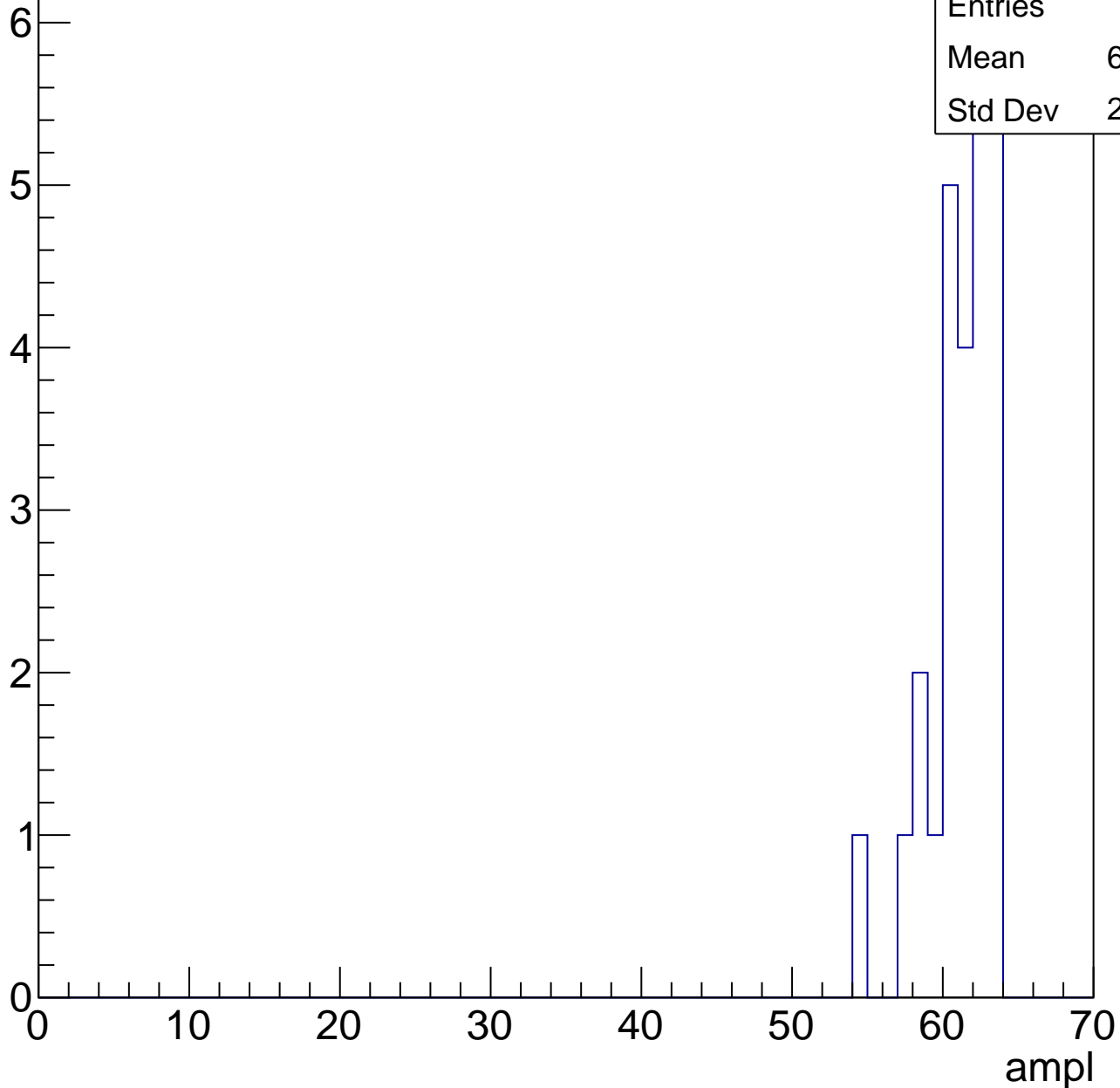


B1L103S, U18-ch124, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

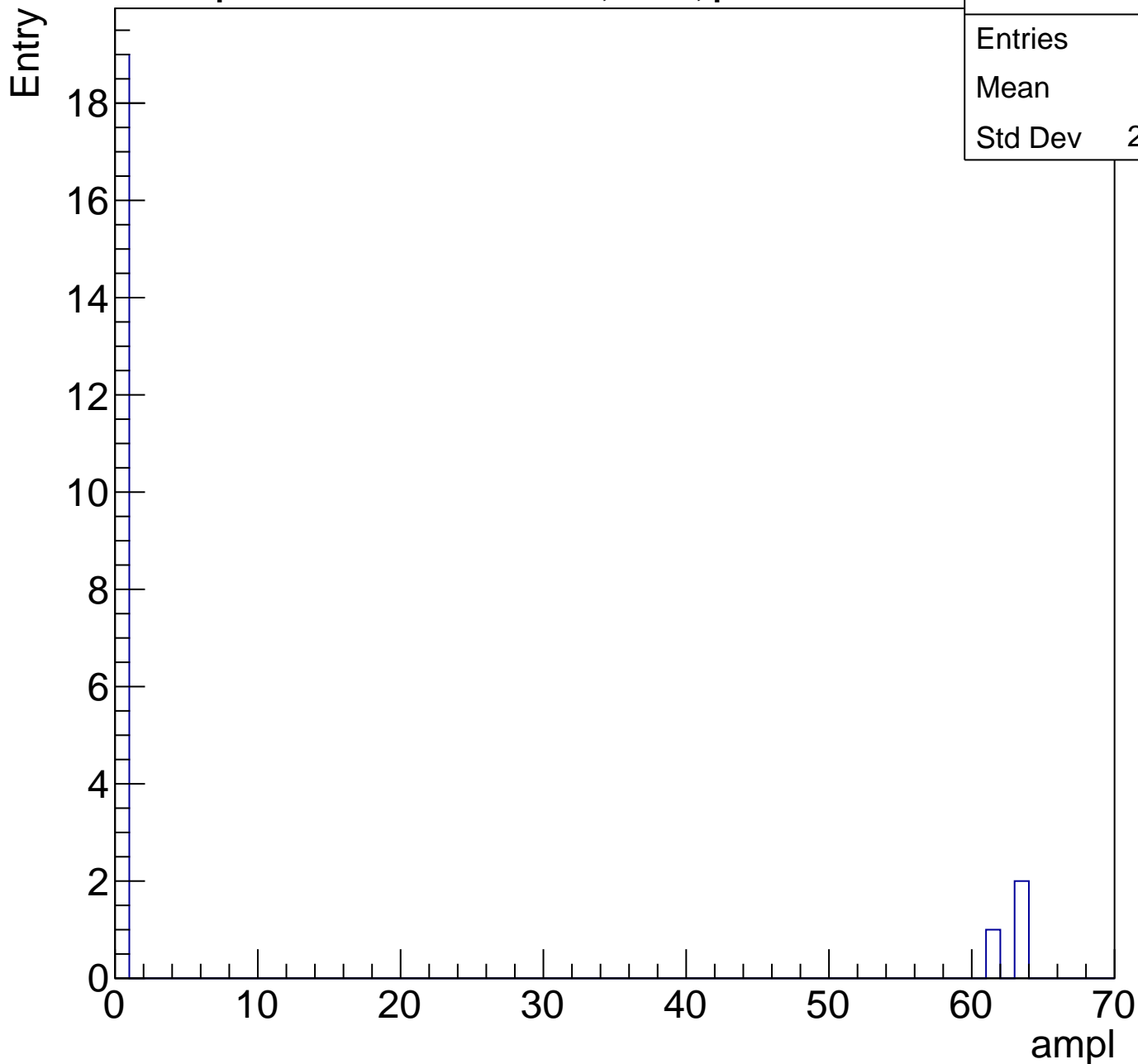
Entries	26
Mean	60.77
Std Dev	2.154



B1L103S, U18-ch124, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	8.5
Std Dev	21.39



B1L103S, U18-ch125, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	95
Mean	21.04
Std Dev	12.9

Gaus mean : 28.7431

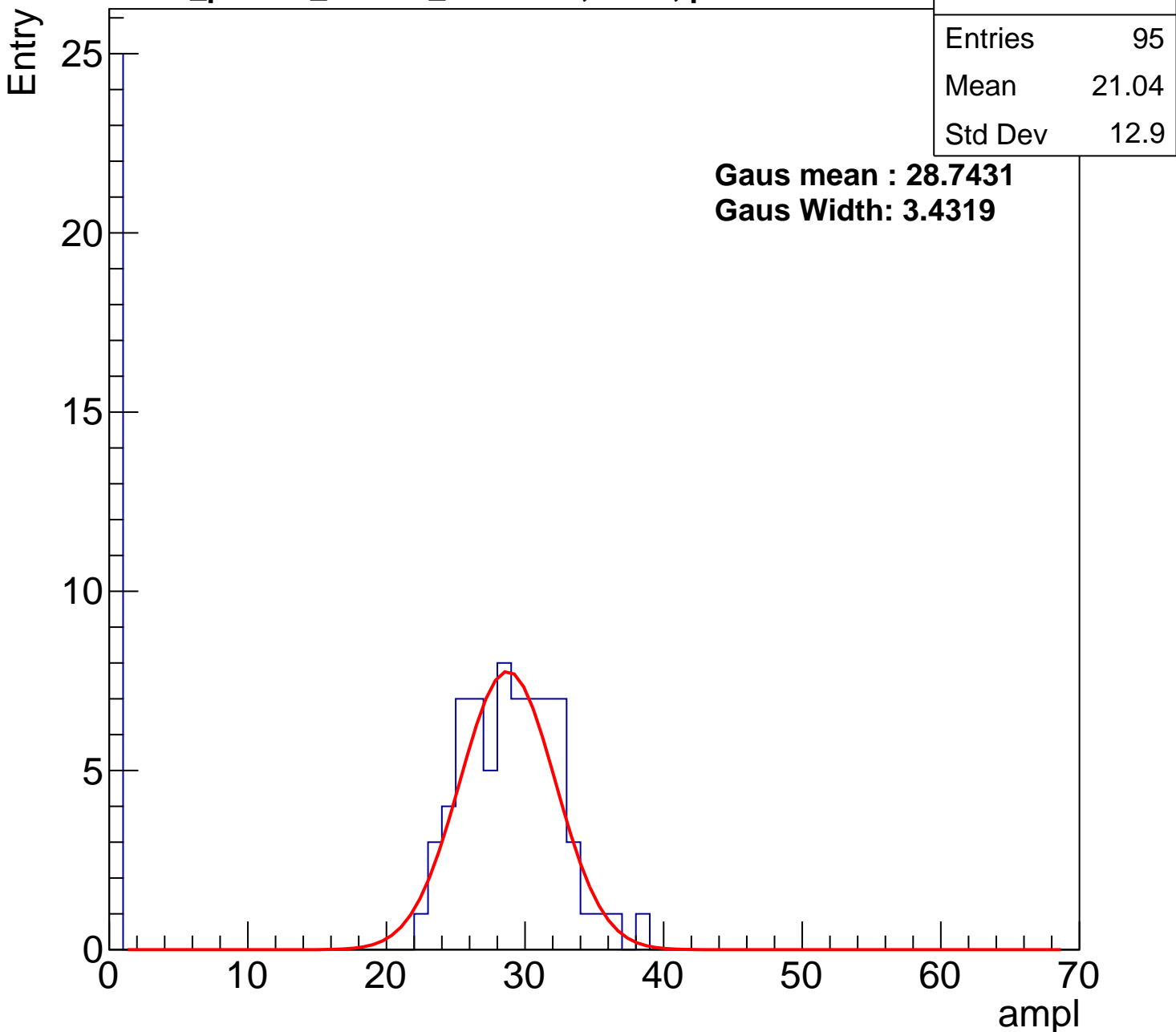
Gaus Width: 3.4319

Entry

25
20
15
10
5
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U18-ch125, adc1

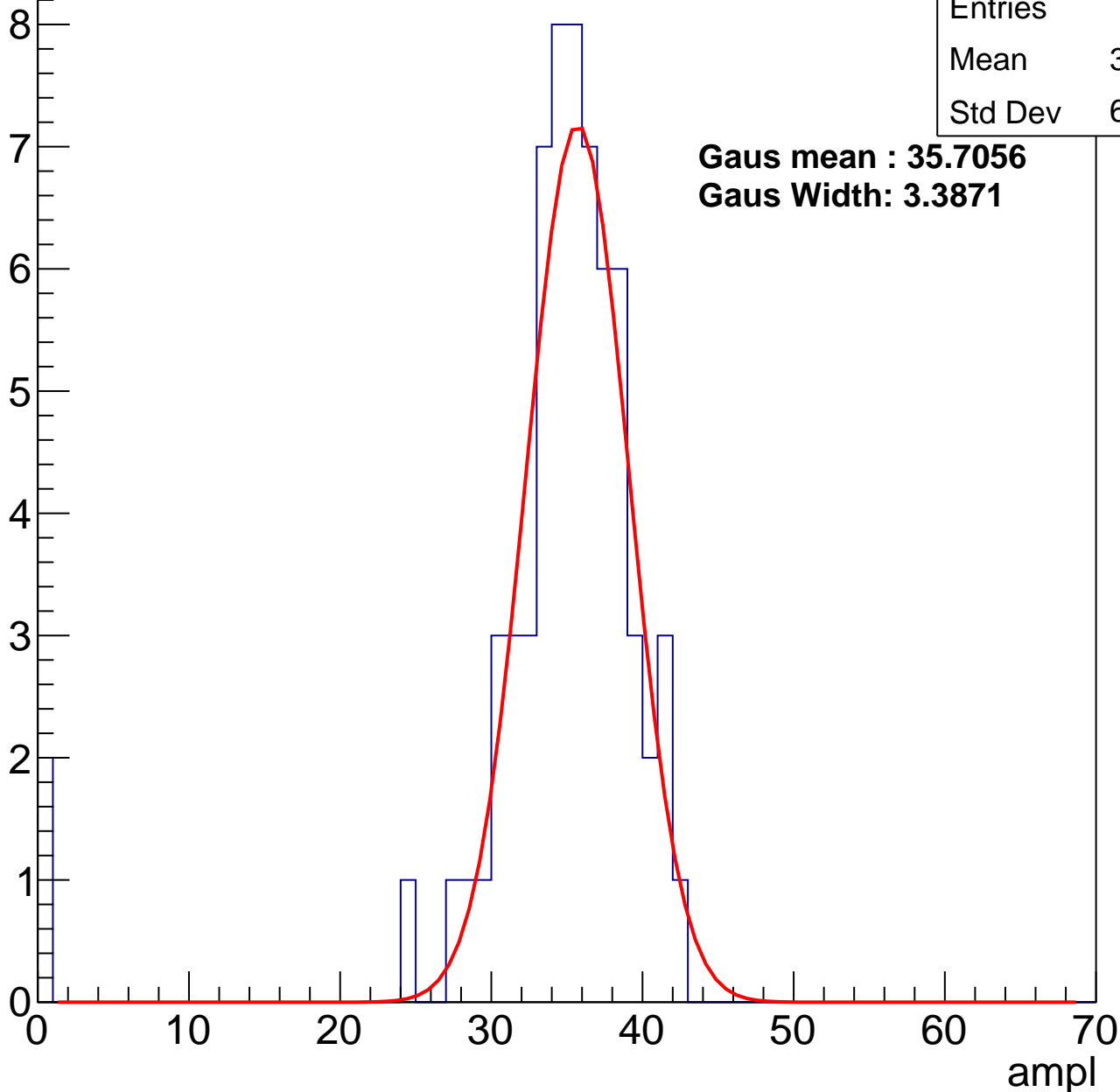
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	66
Mean	33.85
Std Dev	6.918

Gaus mean : 35.7056

Gaus Width: 3.3871



B1L103S, U18-ch125, adc2

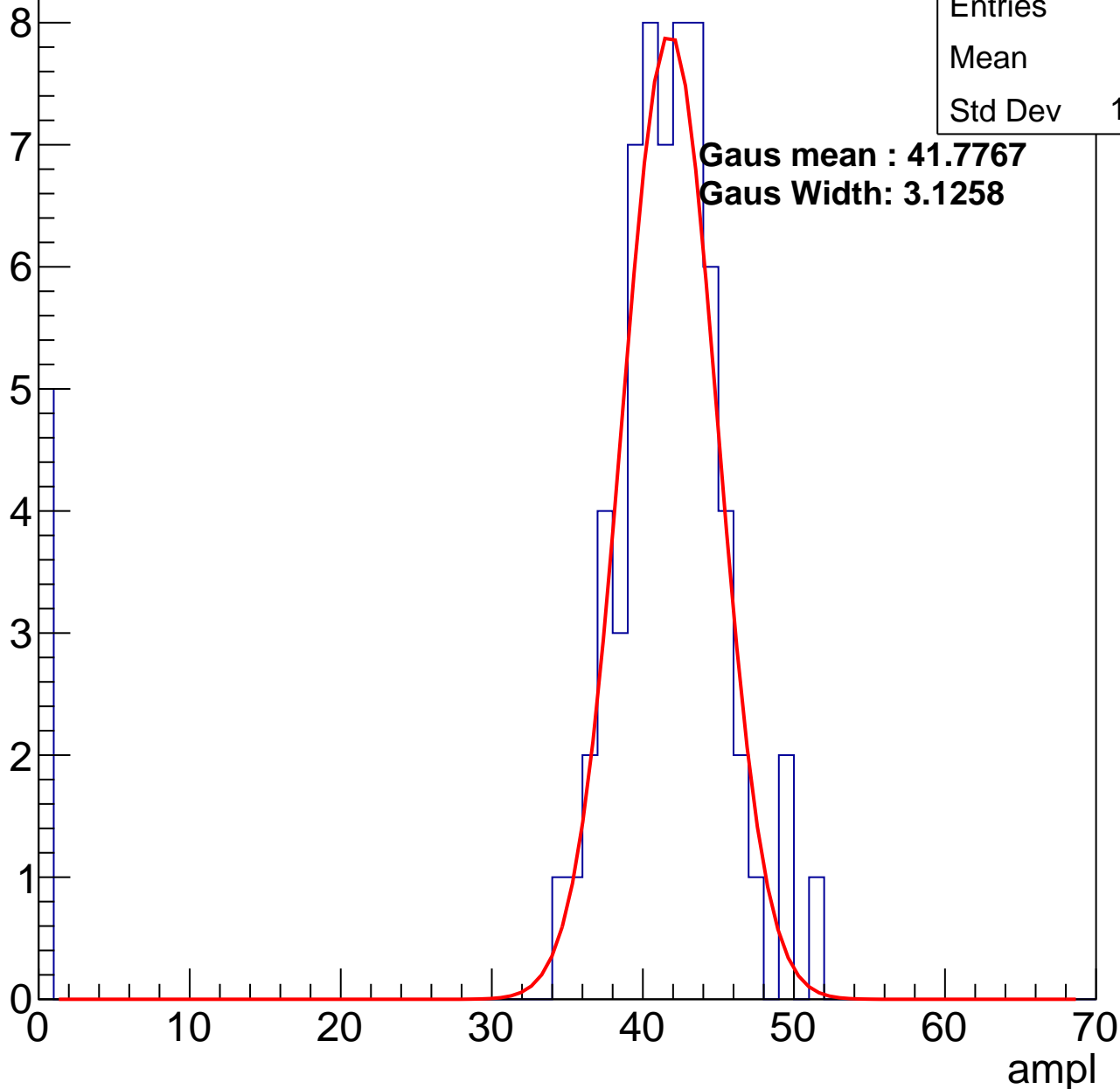
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	70
Mean	38.5
Std Dev	11.15

Gaus mean : 41.7767

Gaus Width: 3.1258



B1L103S, U18-ch125, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entries	75
Mean	41.29
Std Dev	17.38

Entry

10

8

6

4

2

0

0

10

20

30

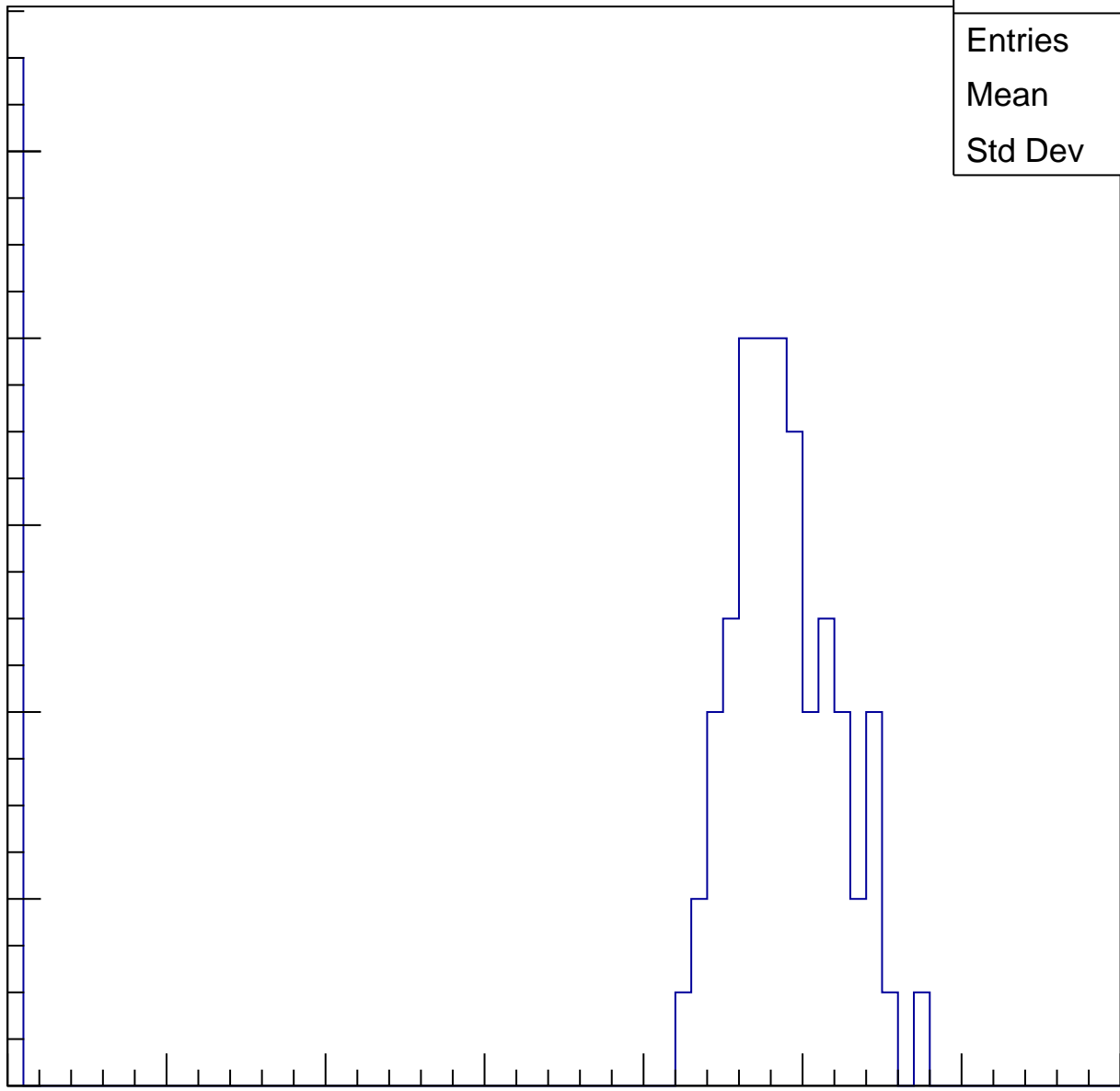
40

50

60

70

ampl

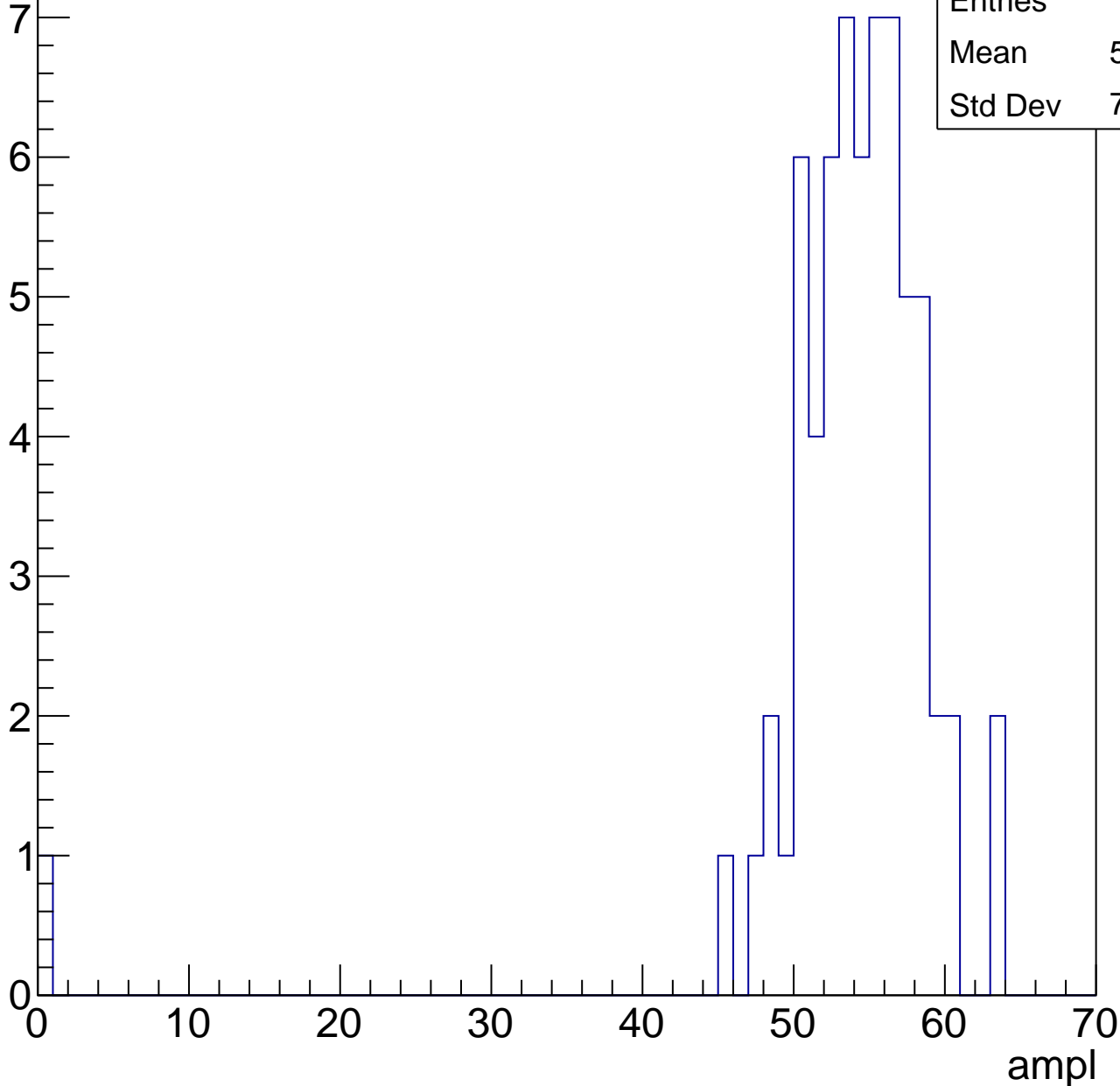


B1L103S, U18-ch125, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	53.29
Std Dev	7.563



B1L103S, U18-ch125, adc5

calib_packv5_041523_1651.root, FC#0, port C2

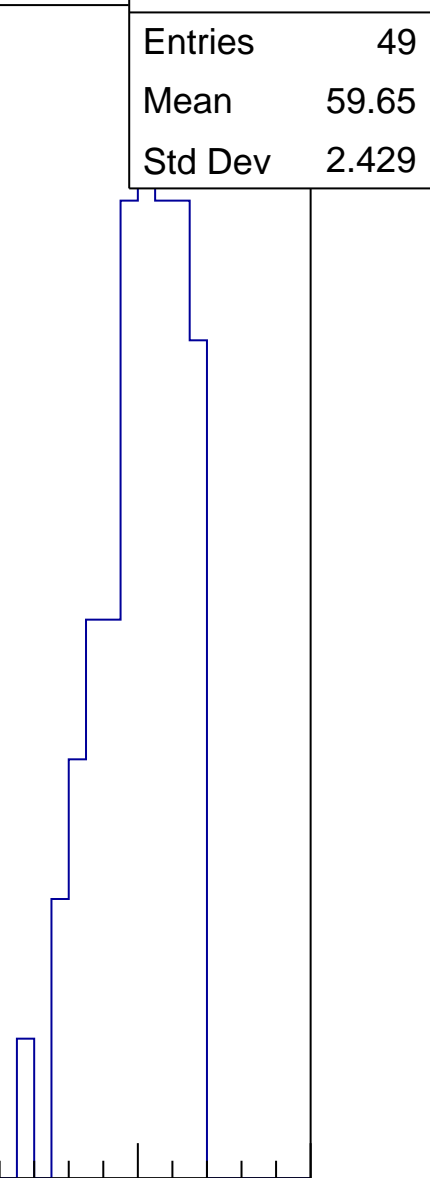
Entry

Entries	49
Mean	59.65
Std Dev	2.429

8
7
6
5
4
3
2
1
0

0 10 20 30 40 50 60 70

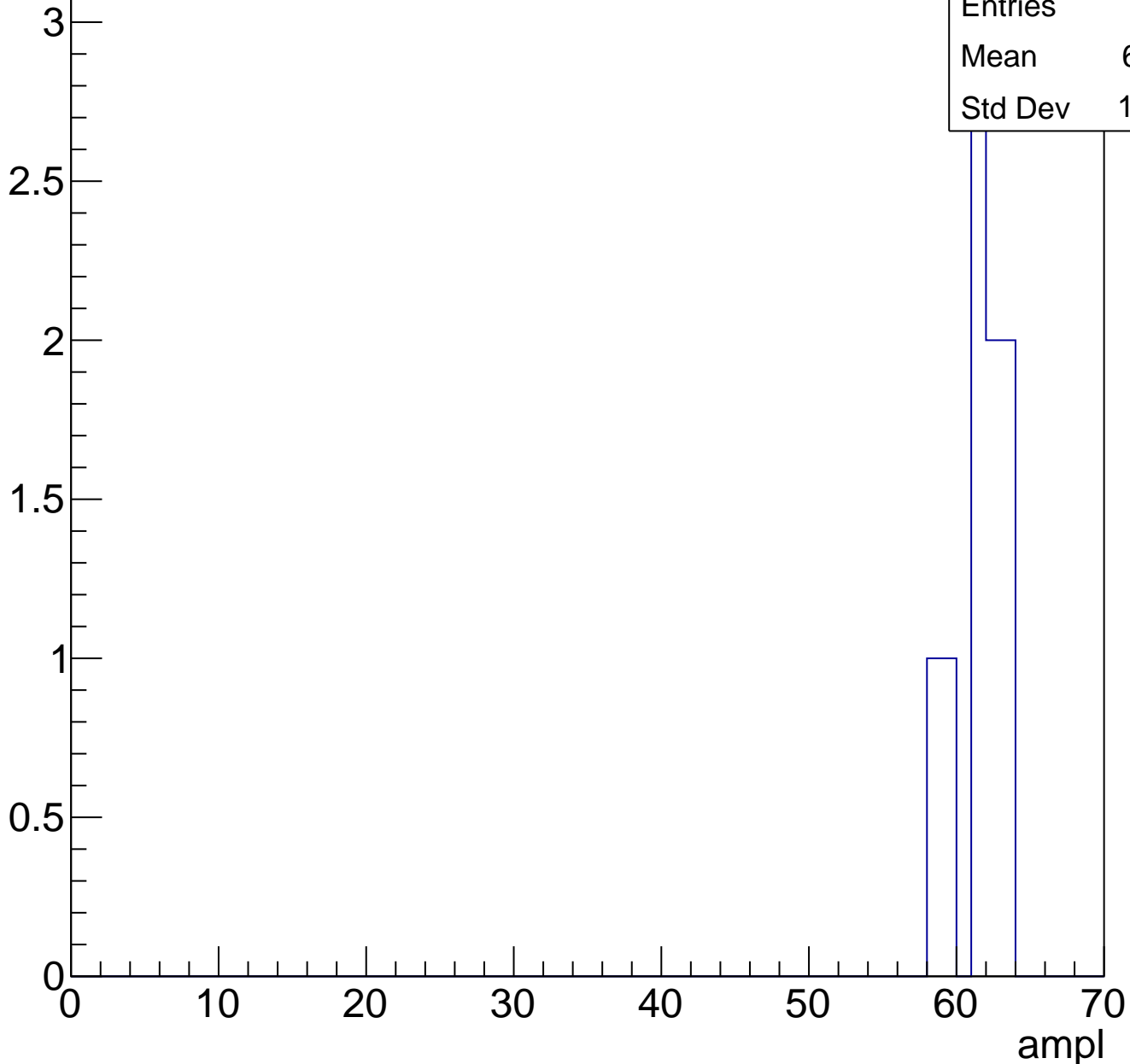
ampl



B1L103S, U18-ch125, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry



B1L103S, U18-ch125, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



B1L103S, U18-ch126, adc0

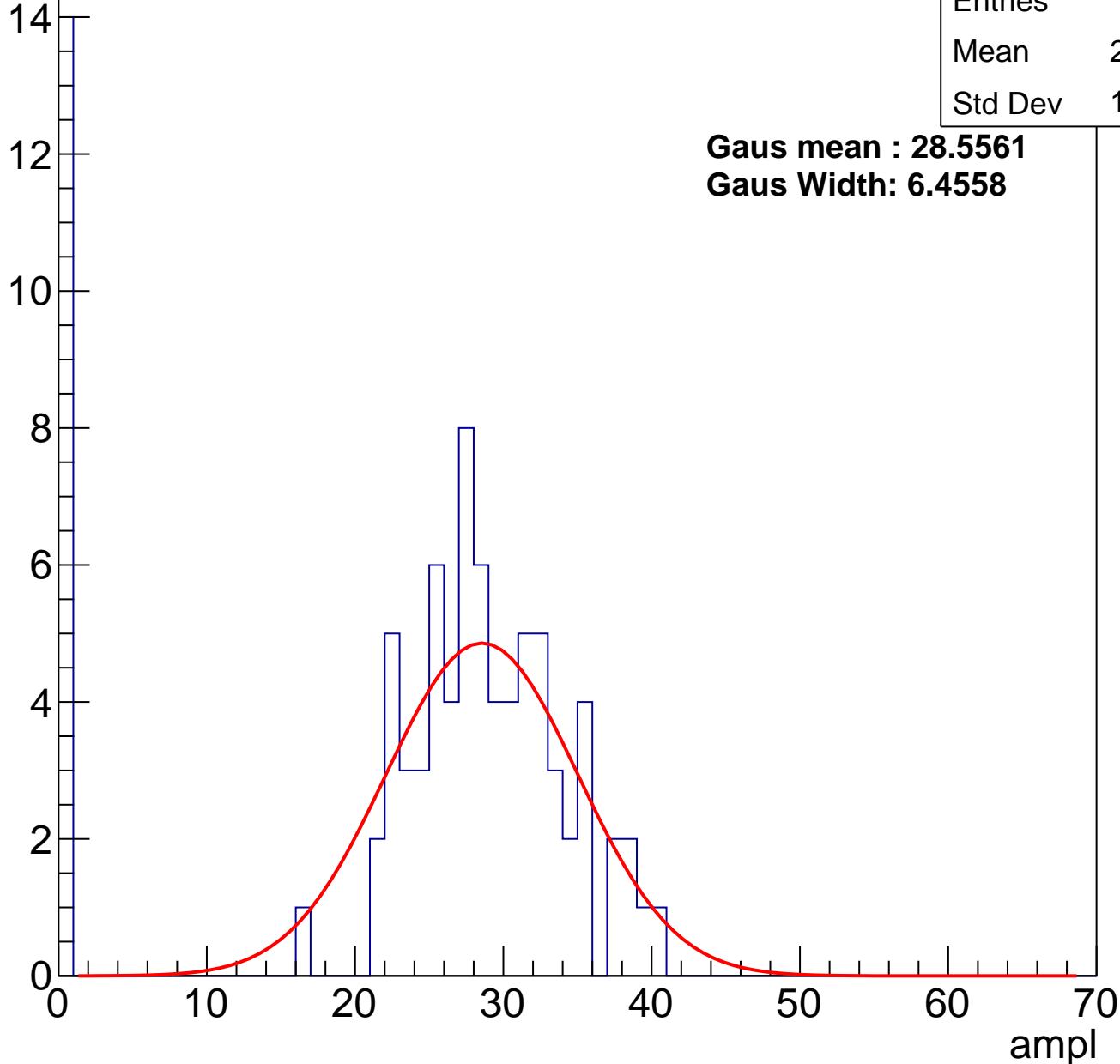
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	23.93
Std Dev	11.53

Gaus mean : 28.5561

Gaus Width: 6.4558

Entry



B1L103S, U18-ch126, adc1

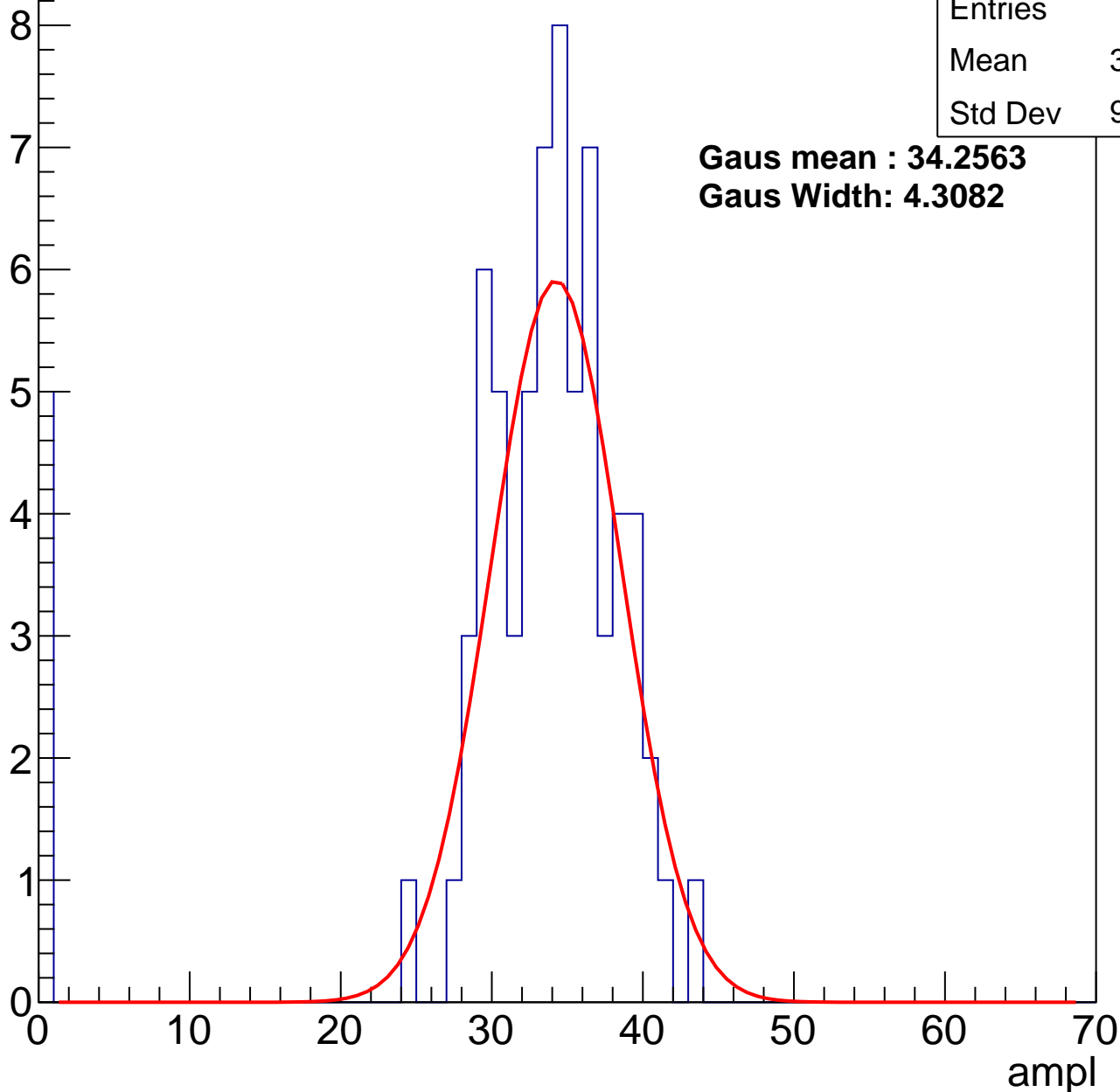
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	71
Mean	31.34
Std Dev	9.378

Gaus mean : 34.2563

Gaus Width: 4.3082



B1L103S, U18-ch126, adc2

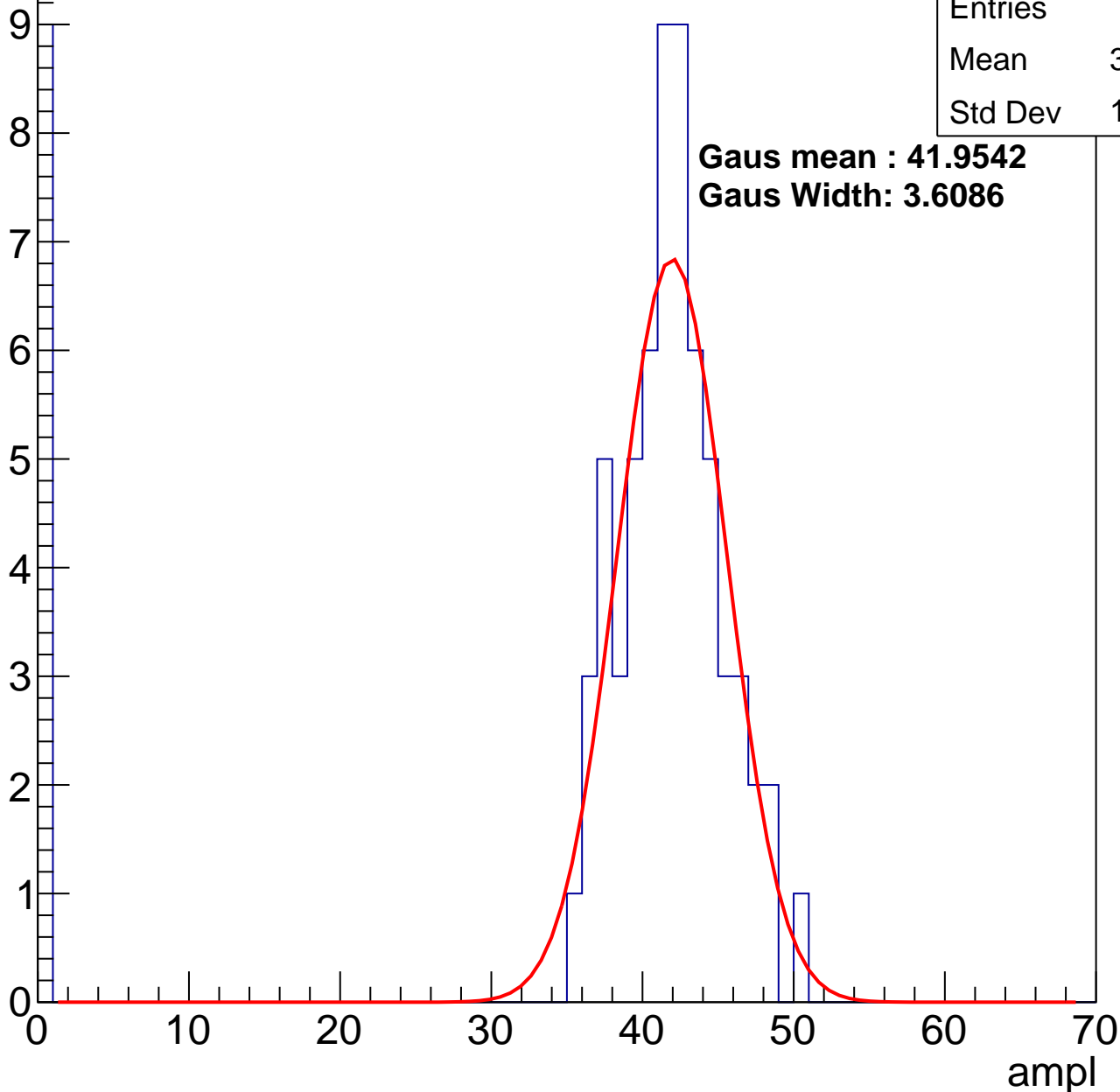
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	72
Mean	36.32
Std Dev	14.07

Gaus mean : 41.9542

Gaus Width: 3.6086

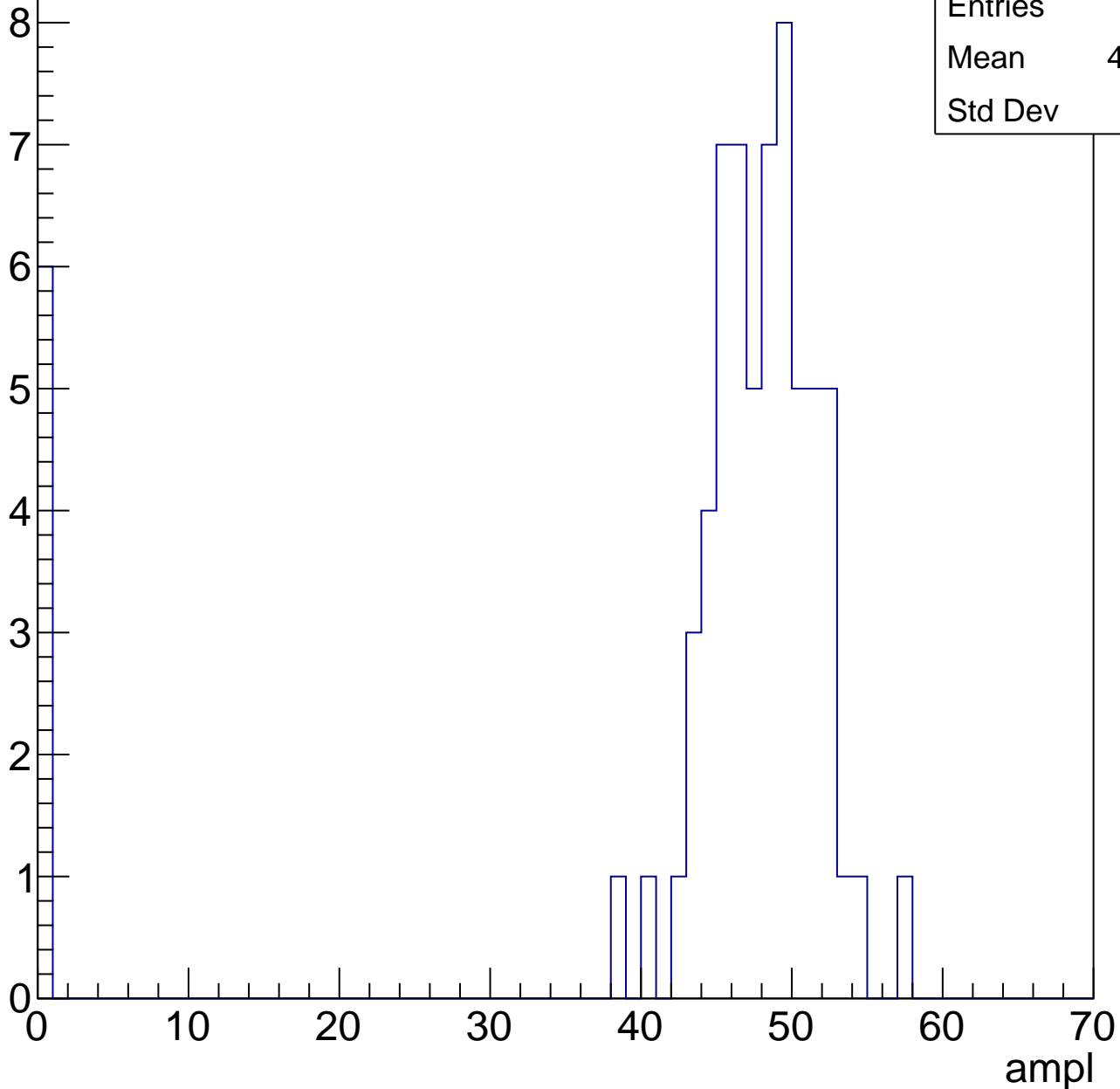


B1L103S, U18-ch126, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	68
Mean	43.44
Std Dev	13.9

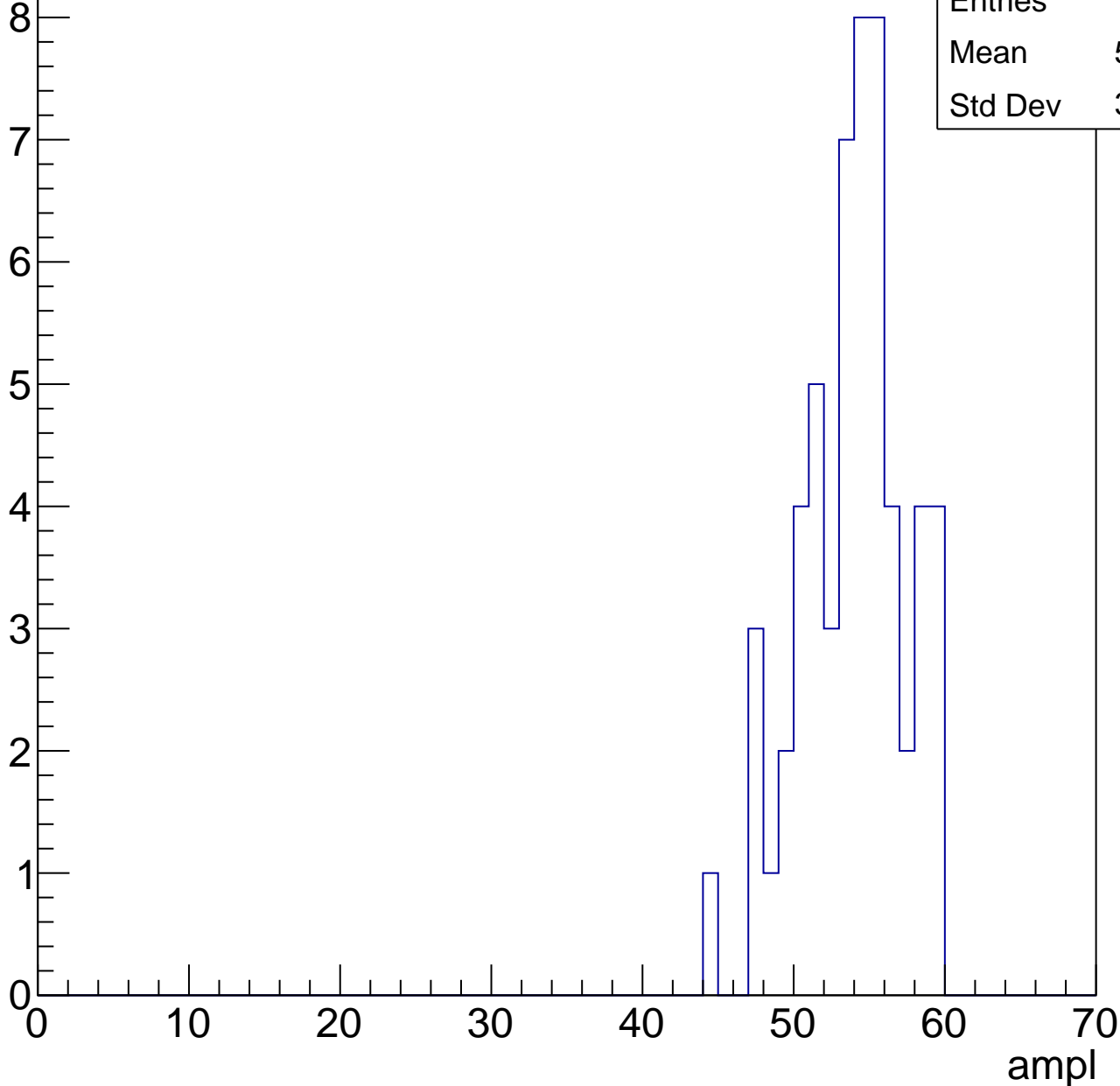


B1L103S, U18-ch126, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	56
Mean	53.41
Std Dev	3.401

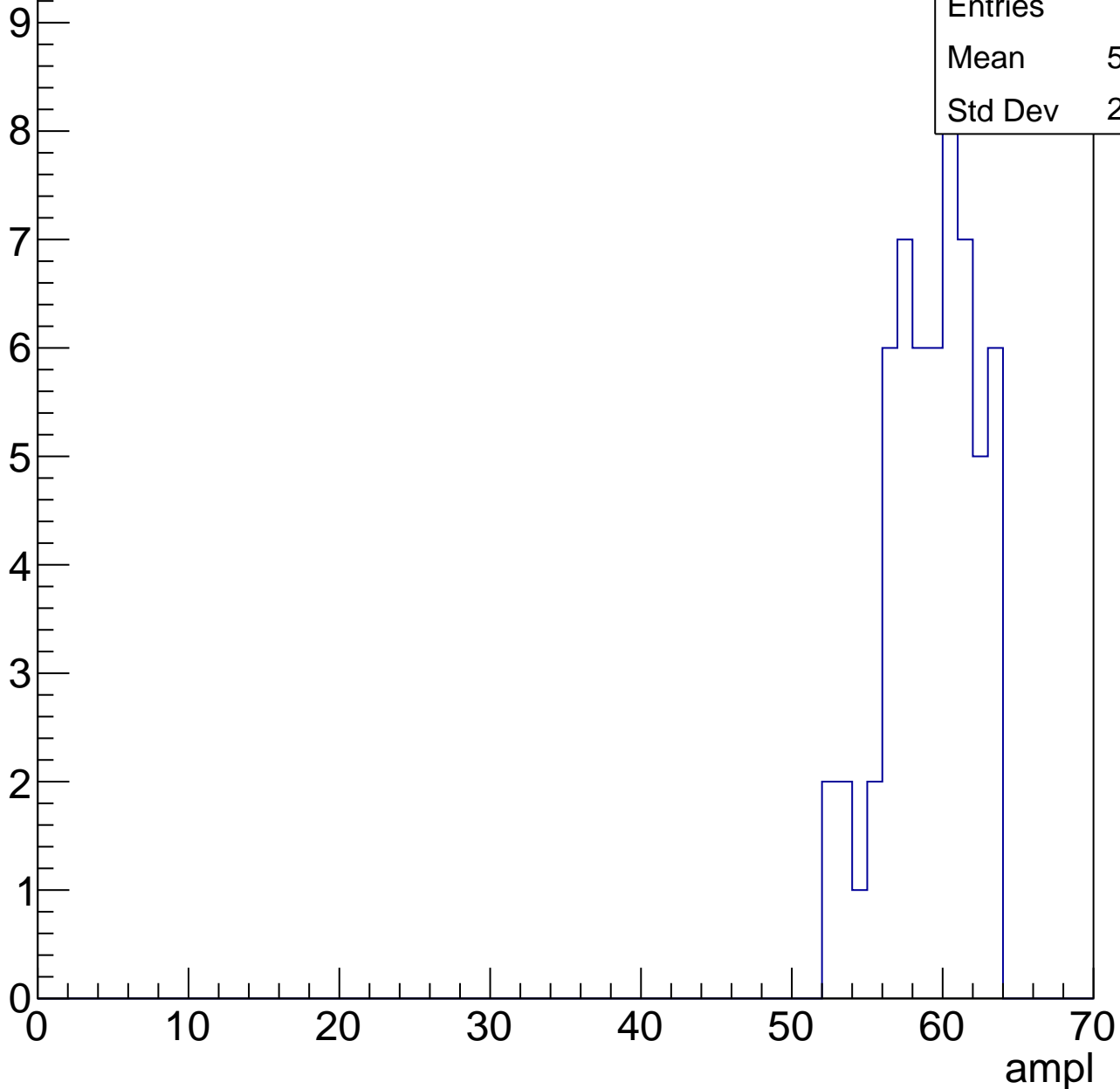


B1L103S, U18-ch126, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

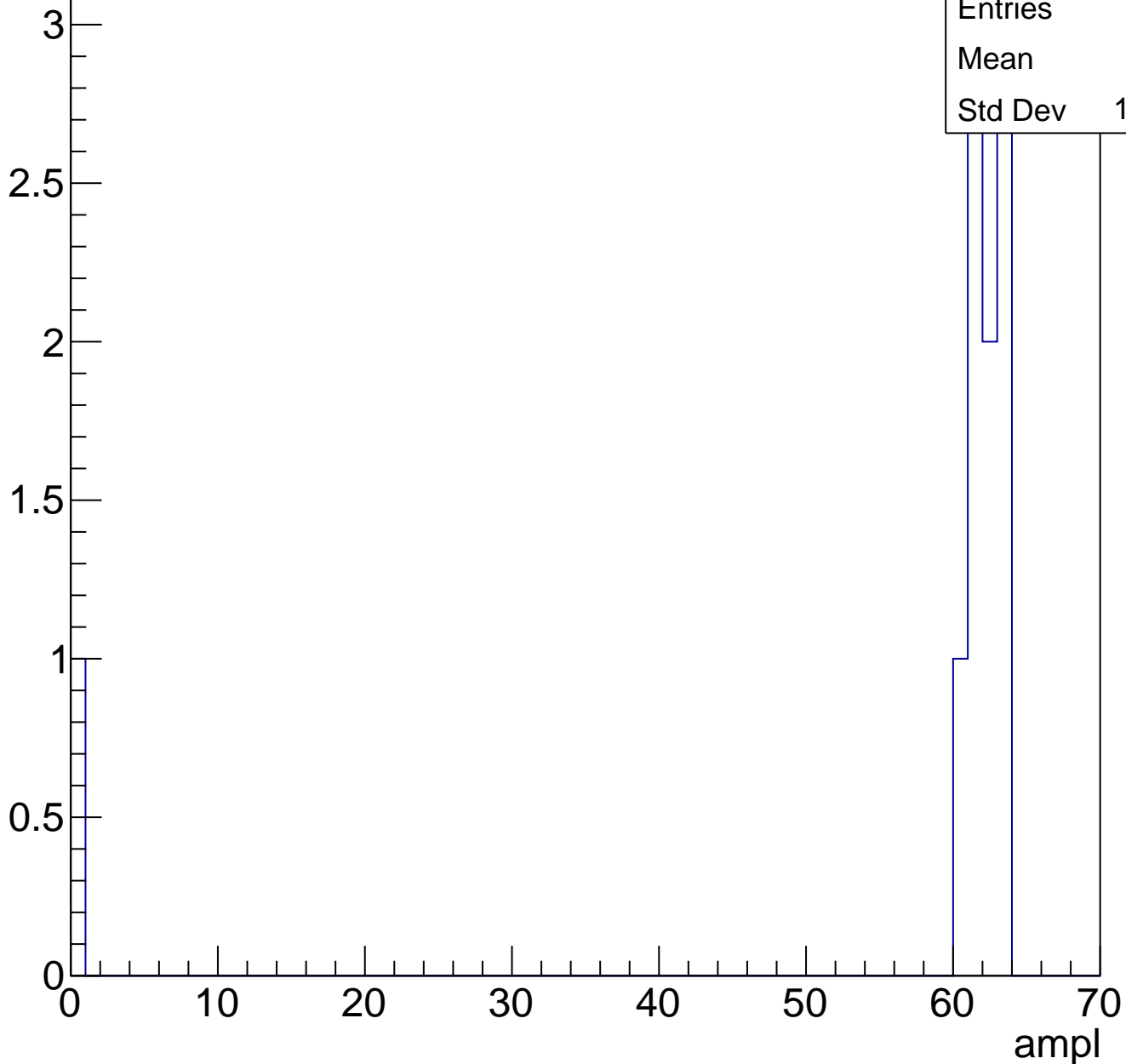
Entries	59
Mean	58.75
Std Dev	2.879



B1L103S, U18-ch126, adc6

calib_packv5_041523_1651.root, FC#0, port C2

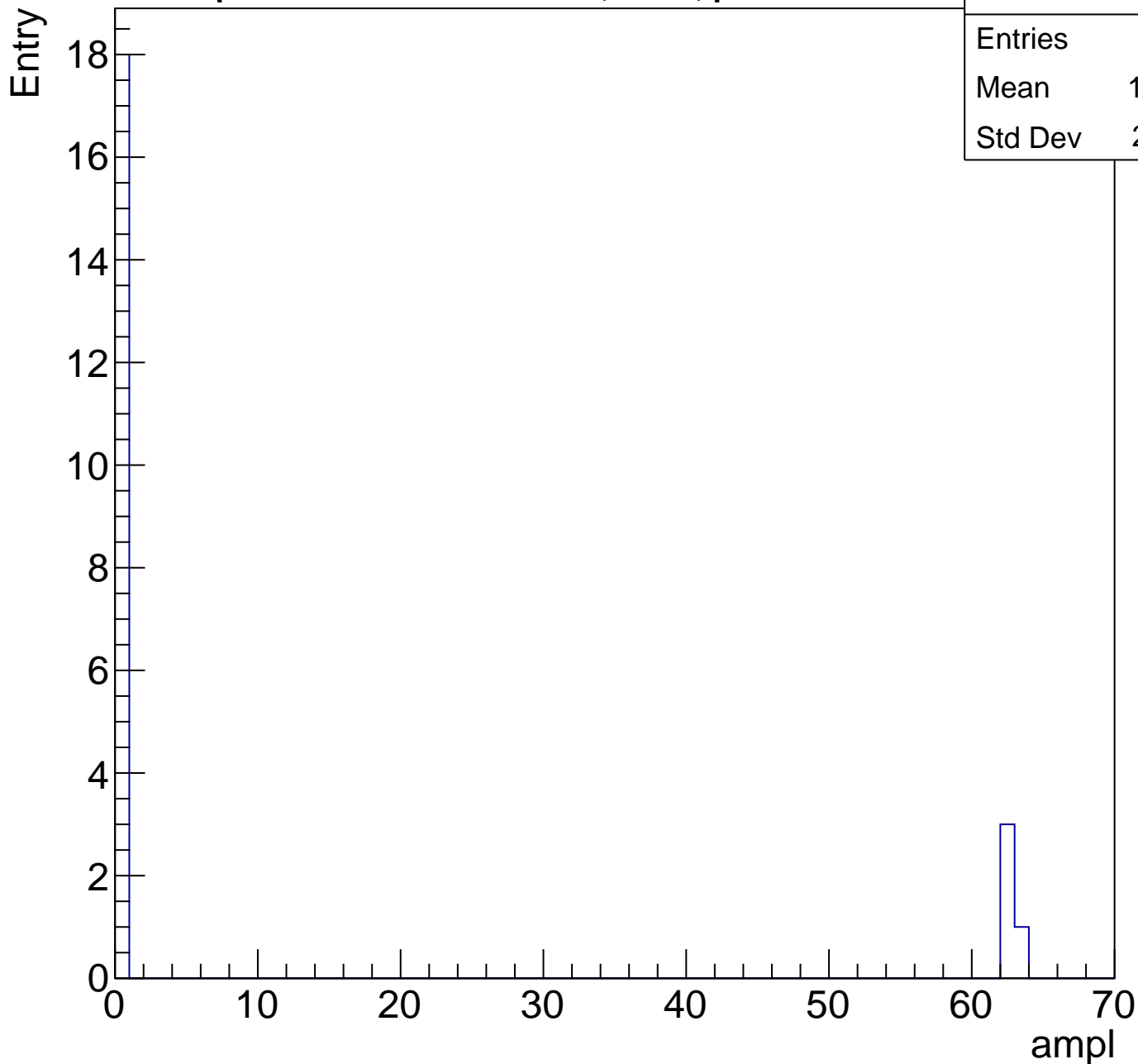
Entry



B1L103S, U18-ch126, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	22
Mean	11.32
Std Dev	24.01



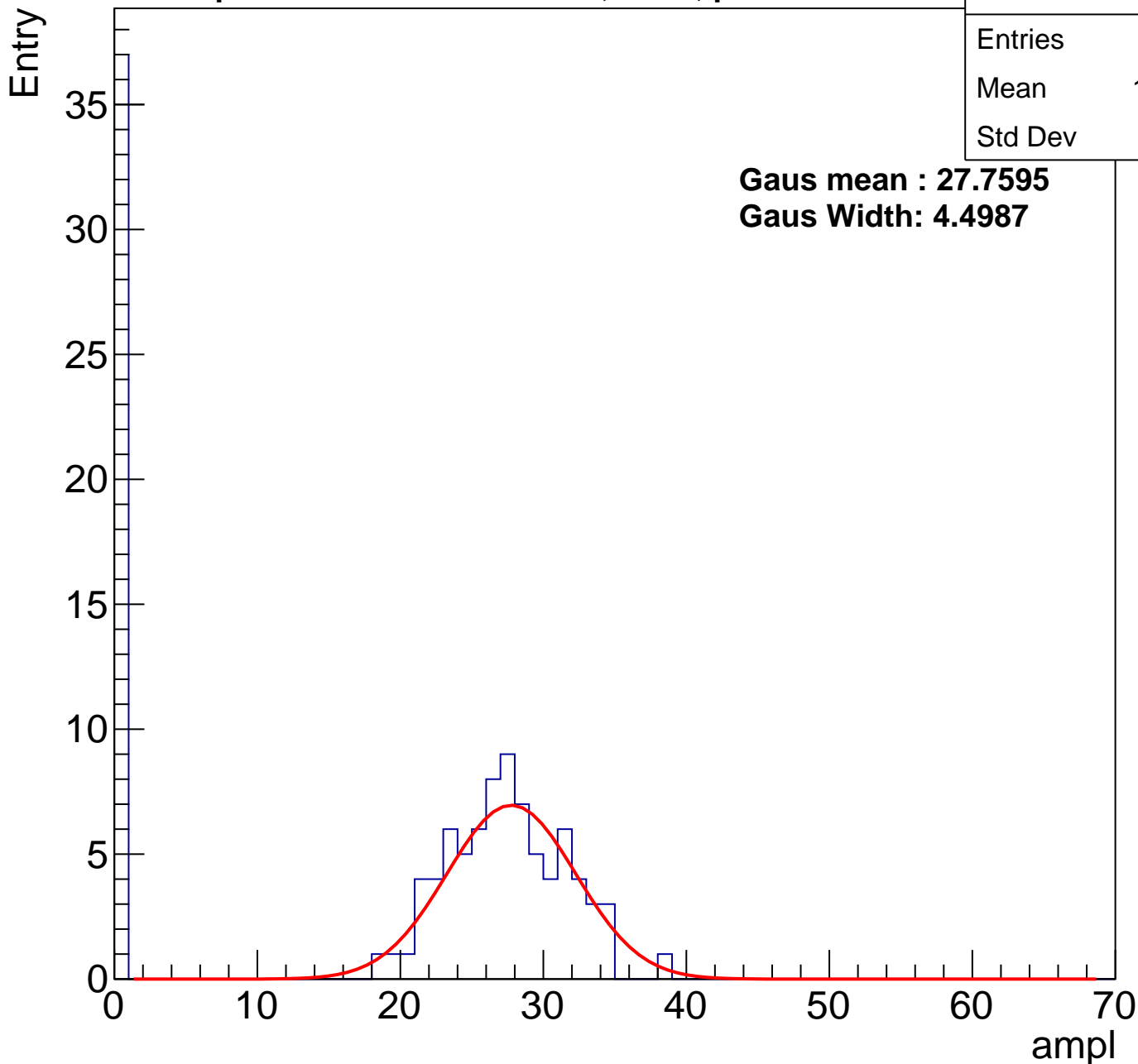
B1L103S, U18-ch127, adc0

calib_packv5_041523_1651.root, FC#0, port C2

Entries	115
Mean	18.28
Std Dev	13.01

Gaus mean : 27.7595

Gaus Width: 4.4987



B1L103S, U18-ch127, adc1

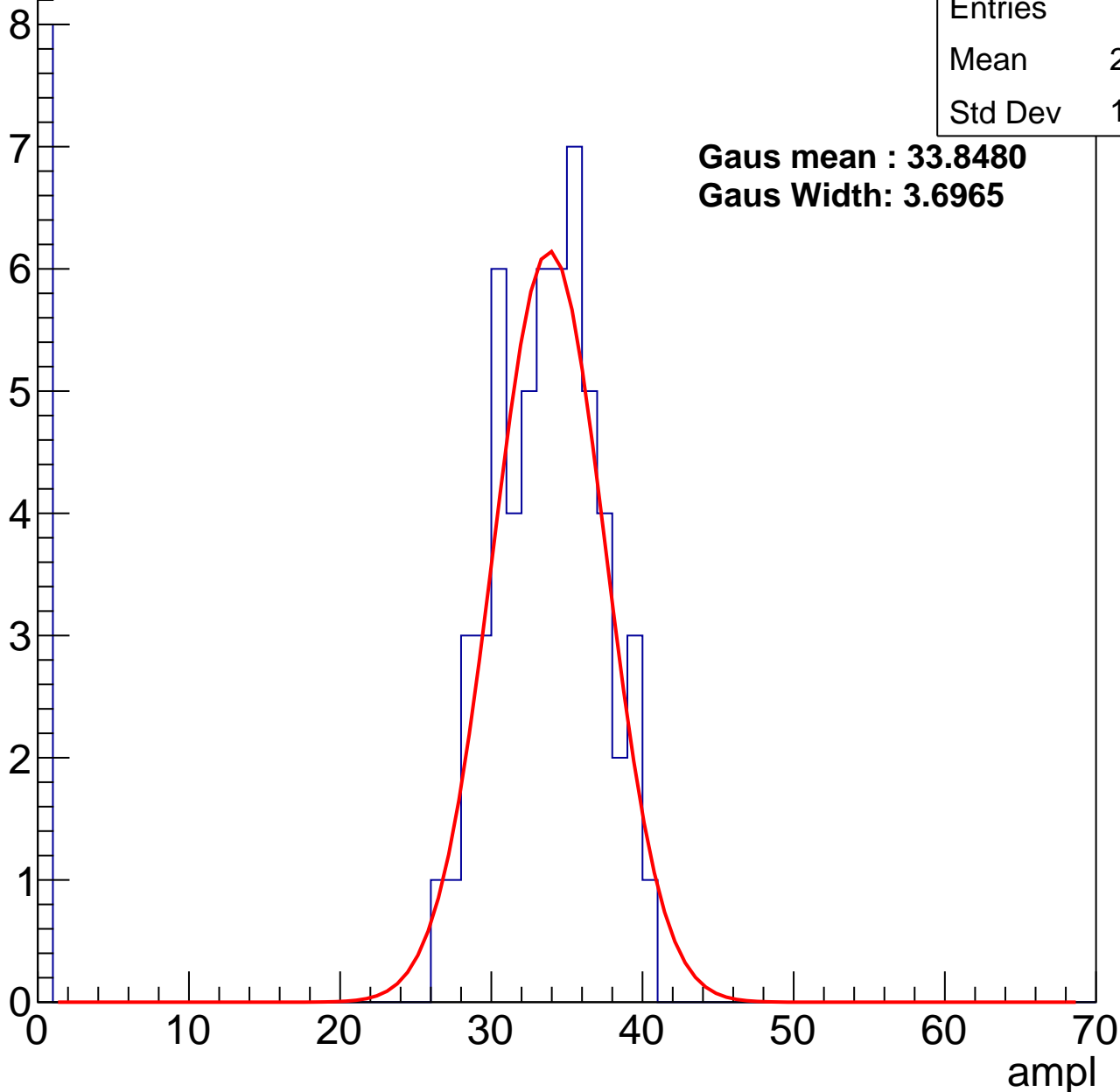
calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	65
Mean	29.17
Std Dev	11.36

Gaus mean : 33.8480

Gaus Width: 3.6965



B1L103S, U18-ch127, adc2

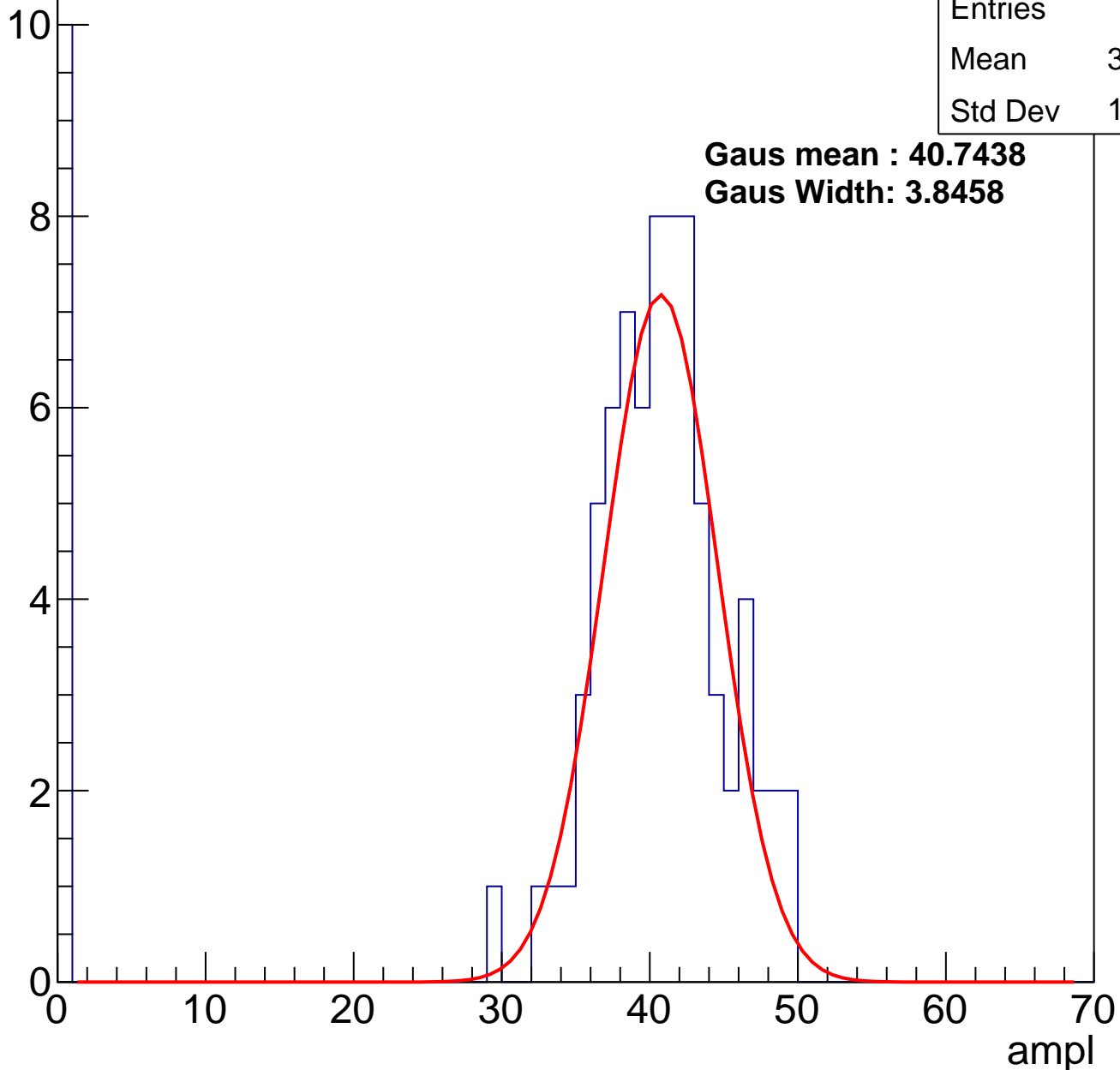
calib_packv5_041523_1651.root, FC#0, port C2

Entries	85
Mean	35.62
Std Dev	13.55

Gaus mean : 40.7438

Gaus Width: 3.8458

Entry

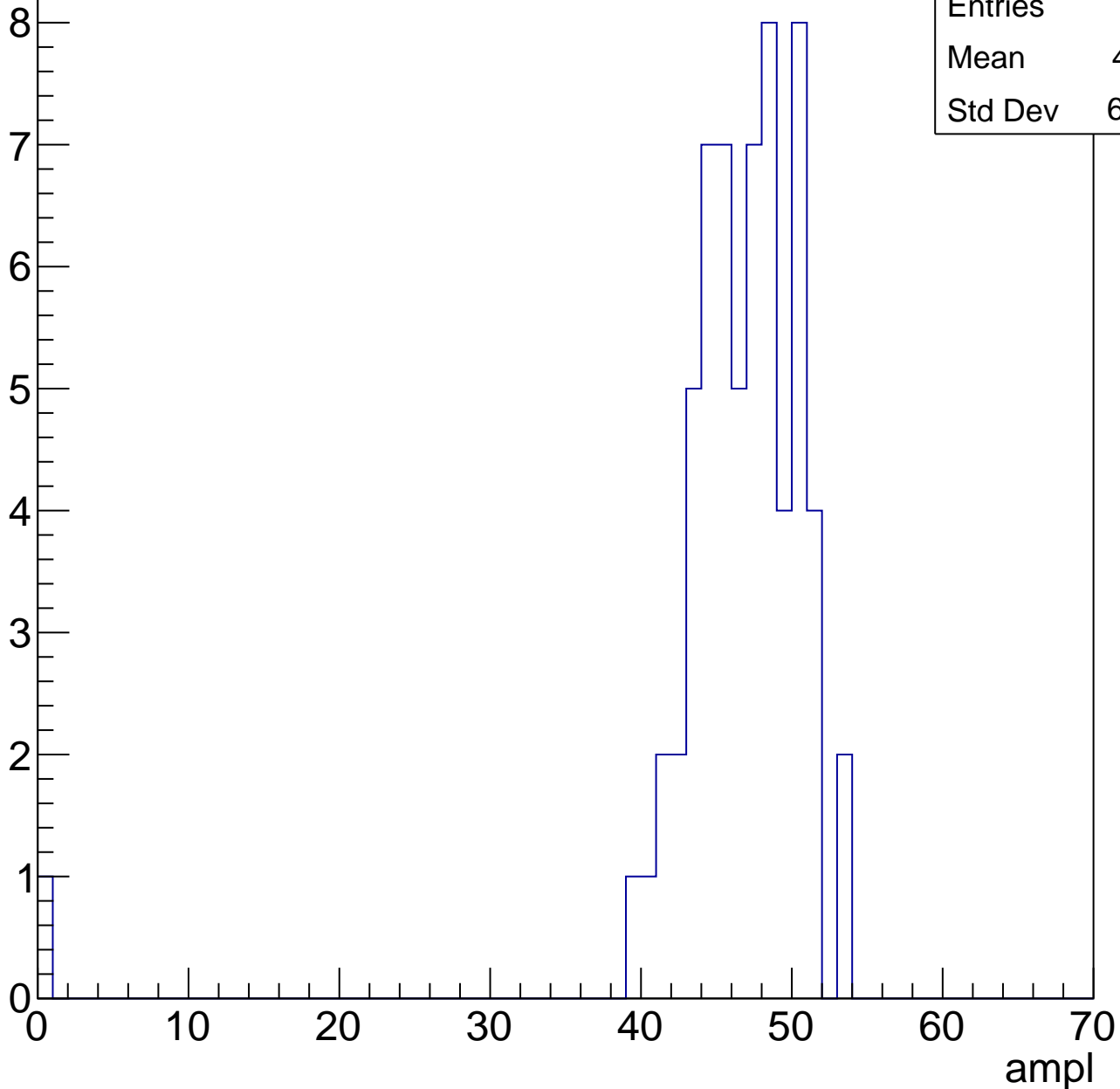


B1L103S, U18-ch127, adc3

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	64
Mean	45.81
Std Dev	6.569

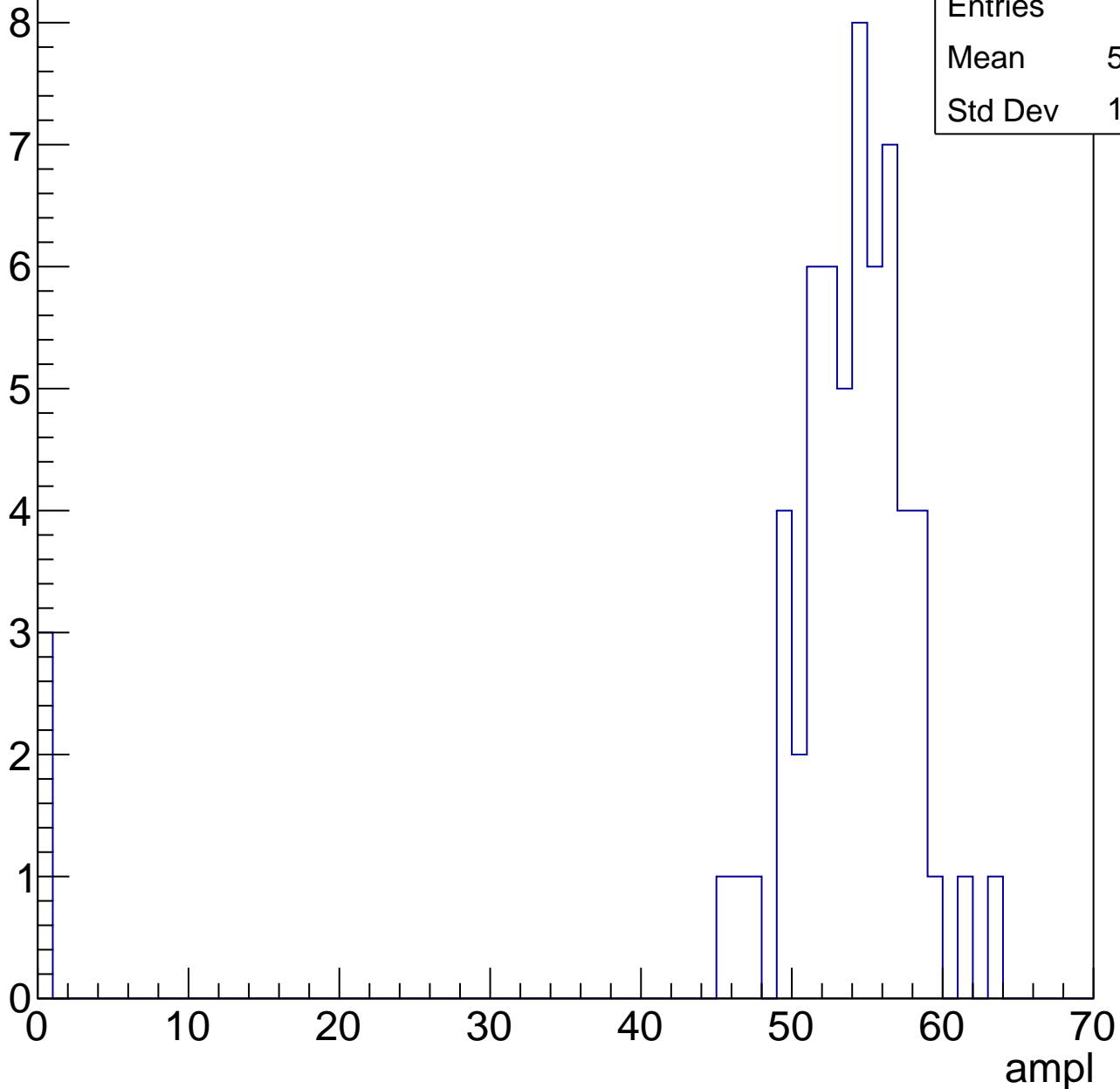


B1L103S, U18-ch127, adc4

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	61
Mean	51.05
Std Dev	12.09

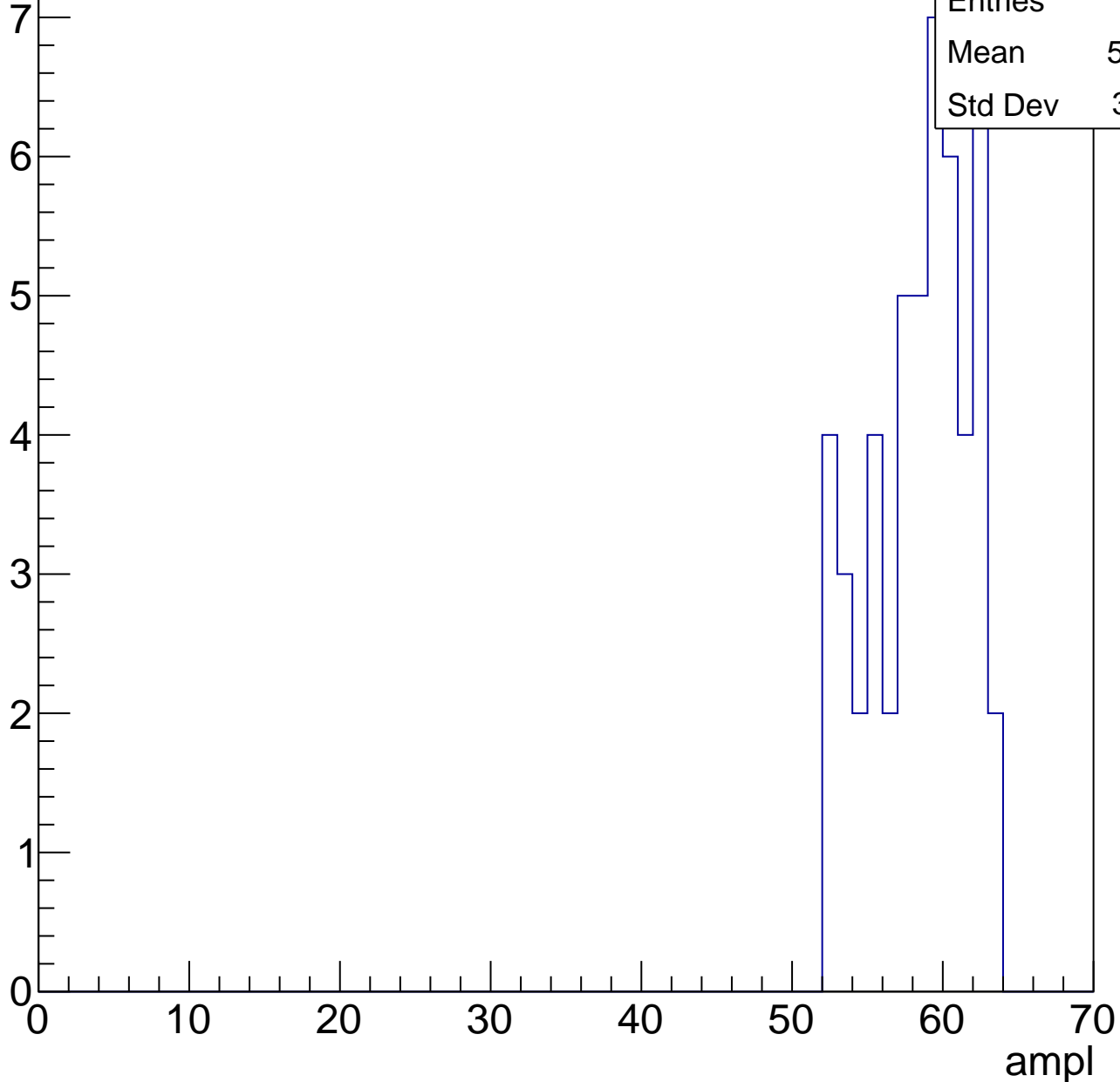


B1L103S, U18-ch127, adc5

calib_packv5_041523_1651.root, FC#0, port C2

Entry

Entries	51
Mean	58.02
Std Dev	3.221

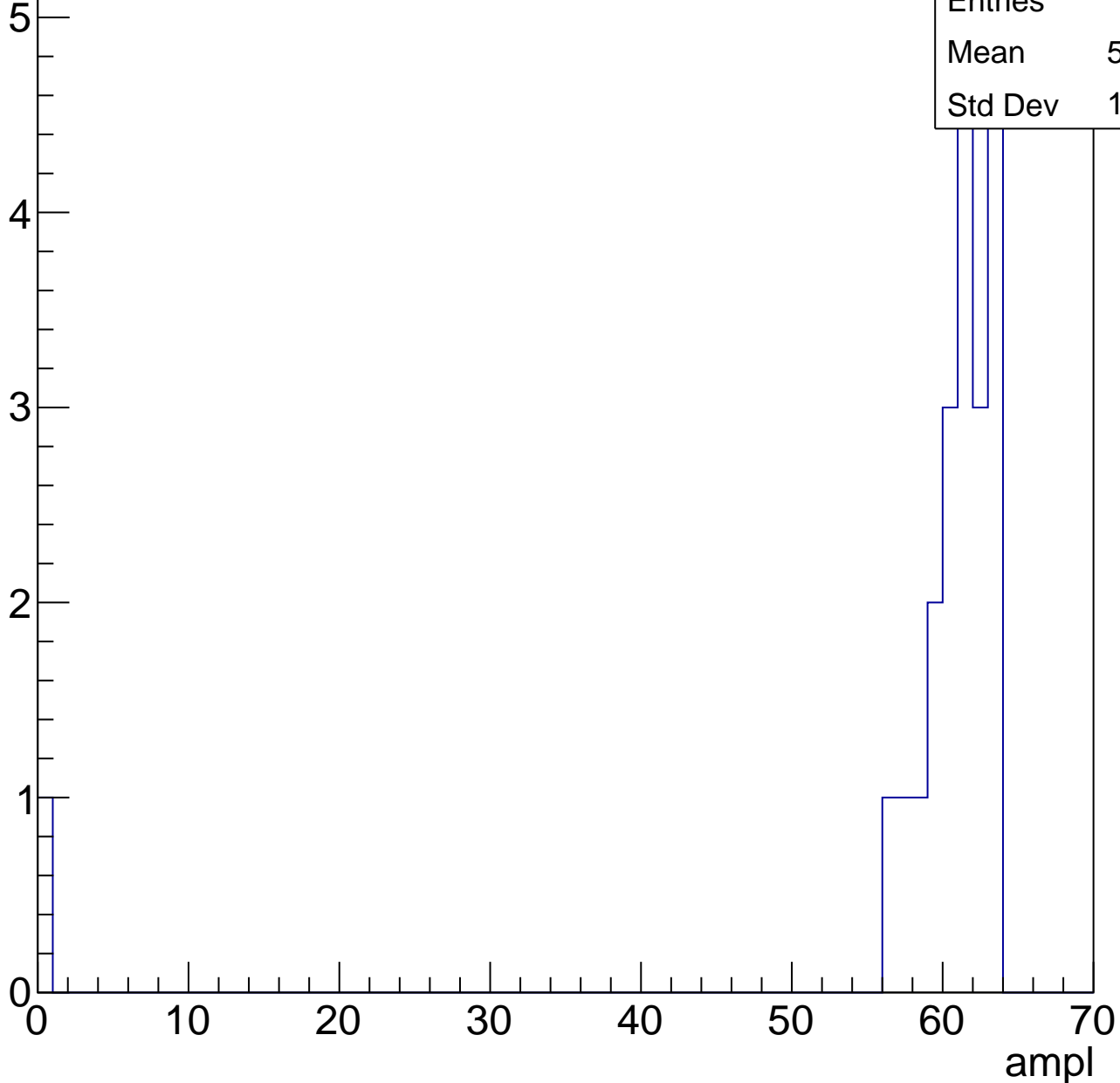


B1L103S, U18-ch127, adc6

calib_packv5_041523_1651.root, FC#0, port C2

Entry

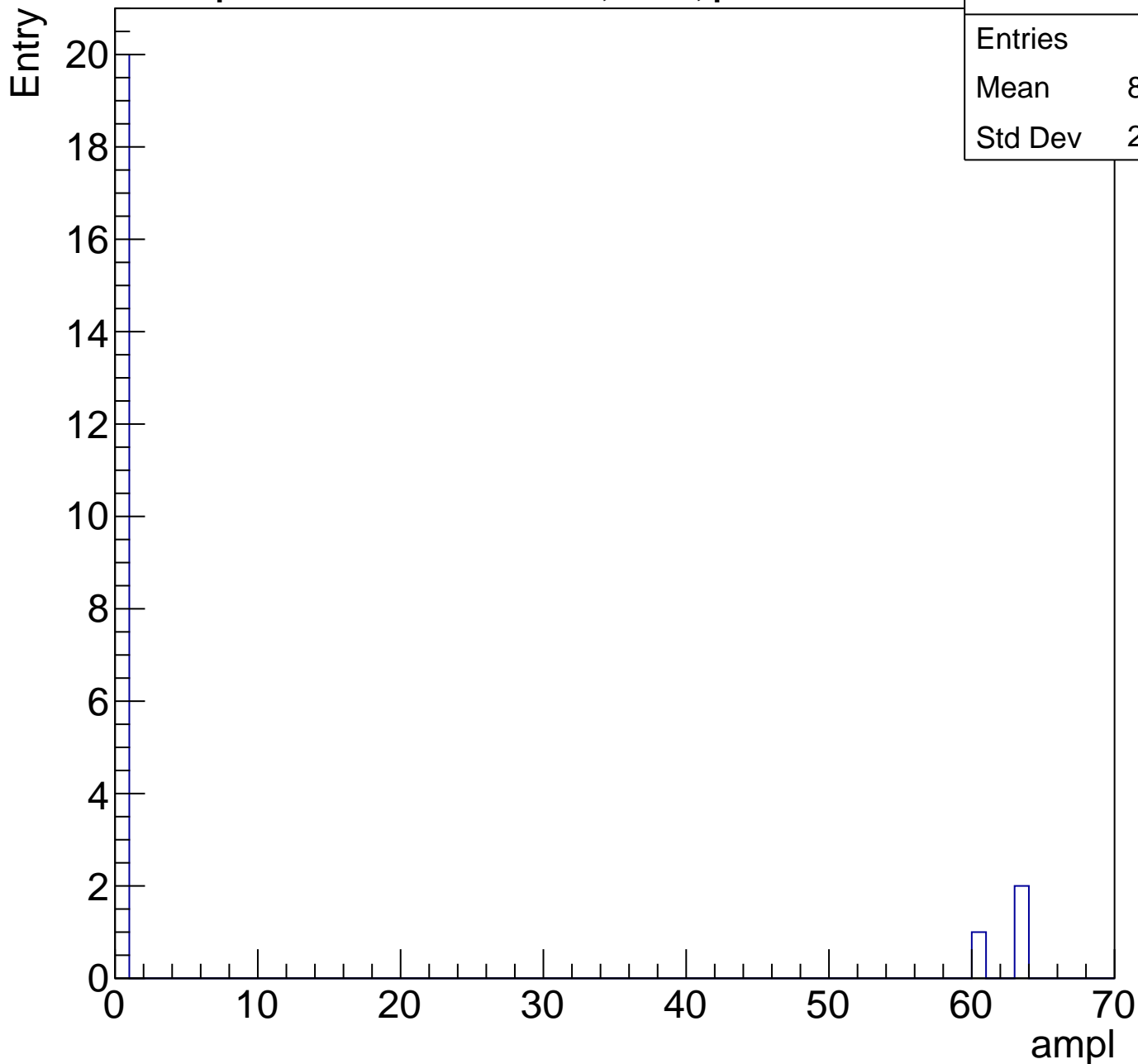
Entries	22
Mean	57.95
Std Dev	12.79



B1L103S, U18-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	8.087
Std Dev	20.89



B1L103S, U18-ch127, adc7

calib_packv5_041523_1651.root, FC#0, port C2

Entries	23
Mean	8.087
Std Dev	20.89

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

