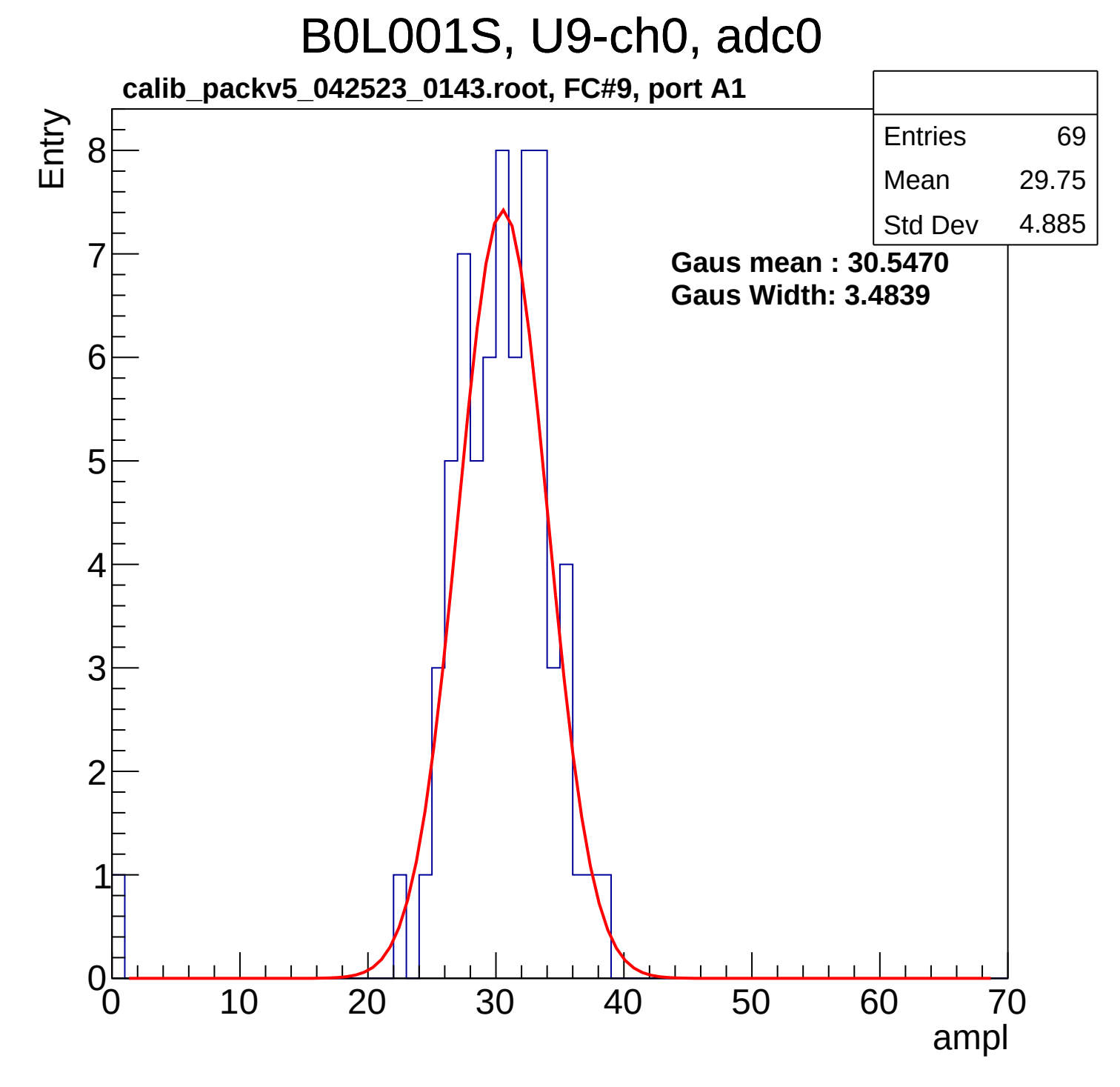
B0L001S, U9-ch0, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.75
Std Dev 4.885
Gaus mean : 30.5470
Gaus Width: 3.4839
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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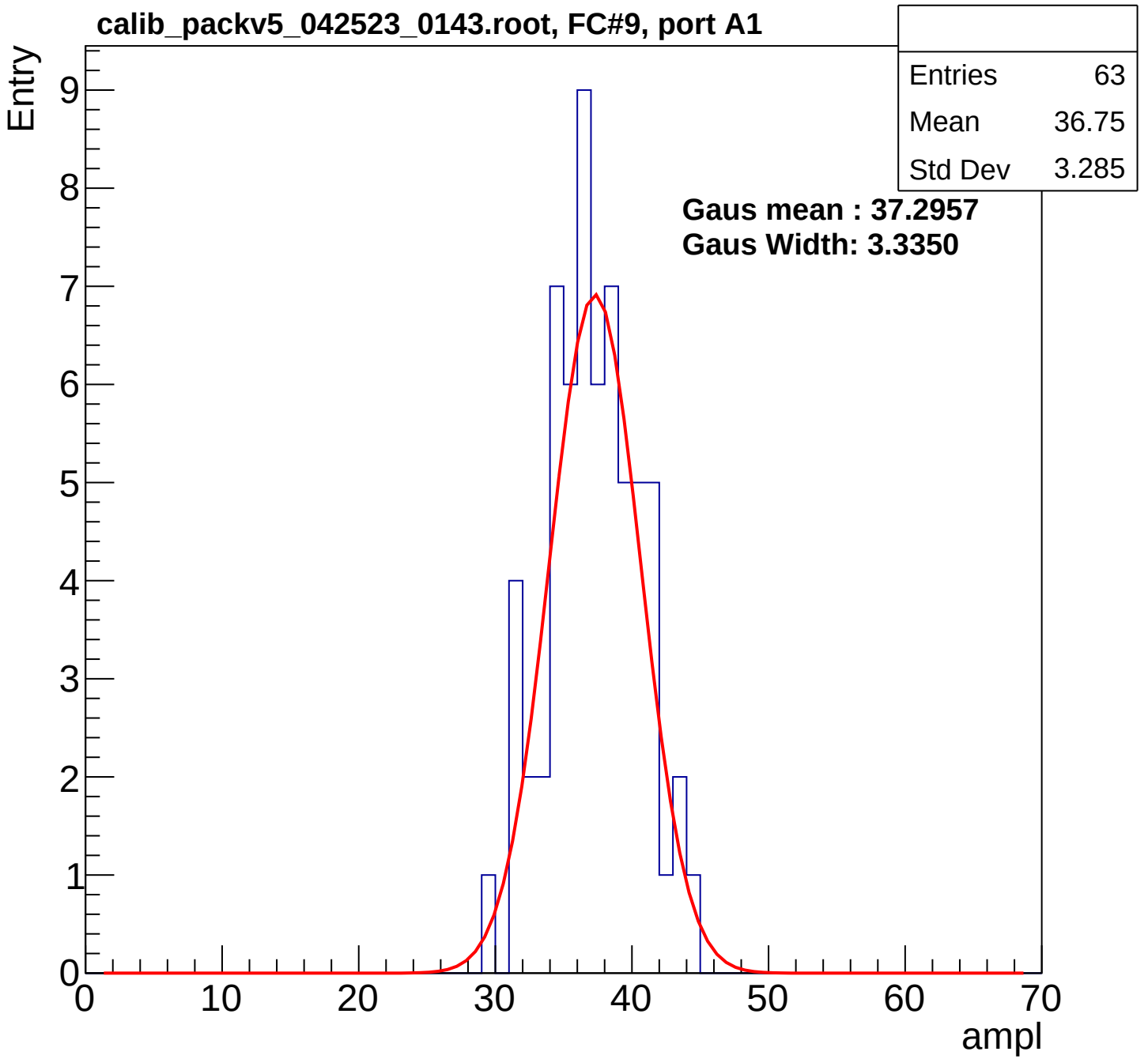
B0L001S, U9-ch0, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 36.75
Std Dev 3.285
Gaus mean : 37.2957
Gaus Width: 3.3350
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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40
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60
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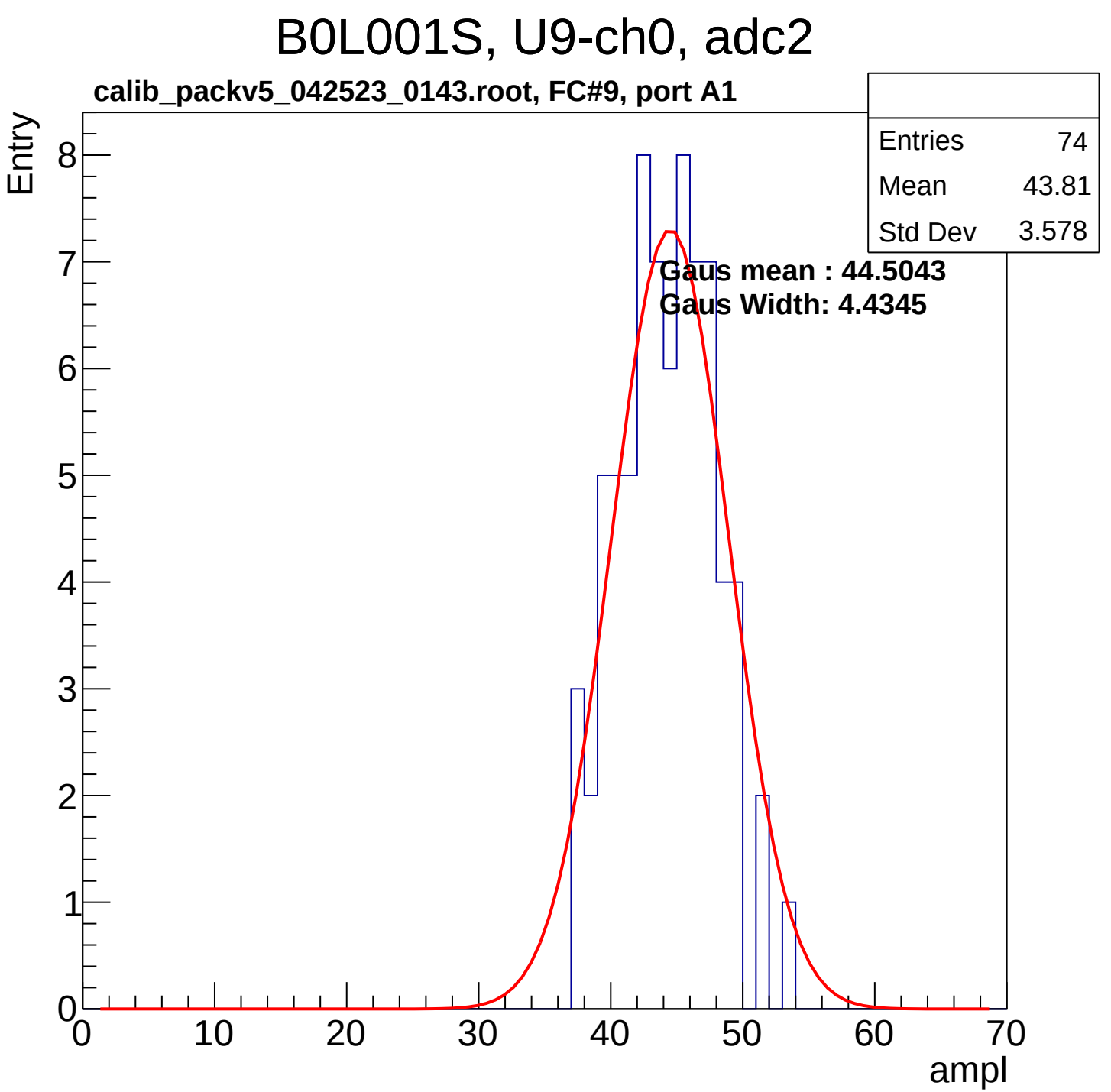
B0L001S, U9-ch0, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 43.81
Std Dev 3.578
Gaus mean : 44.5043
Gaus Width: 4.4345
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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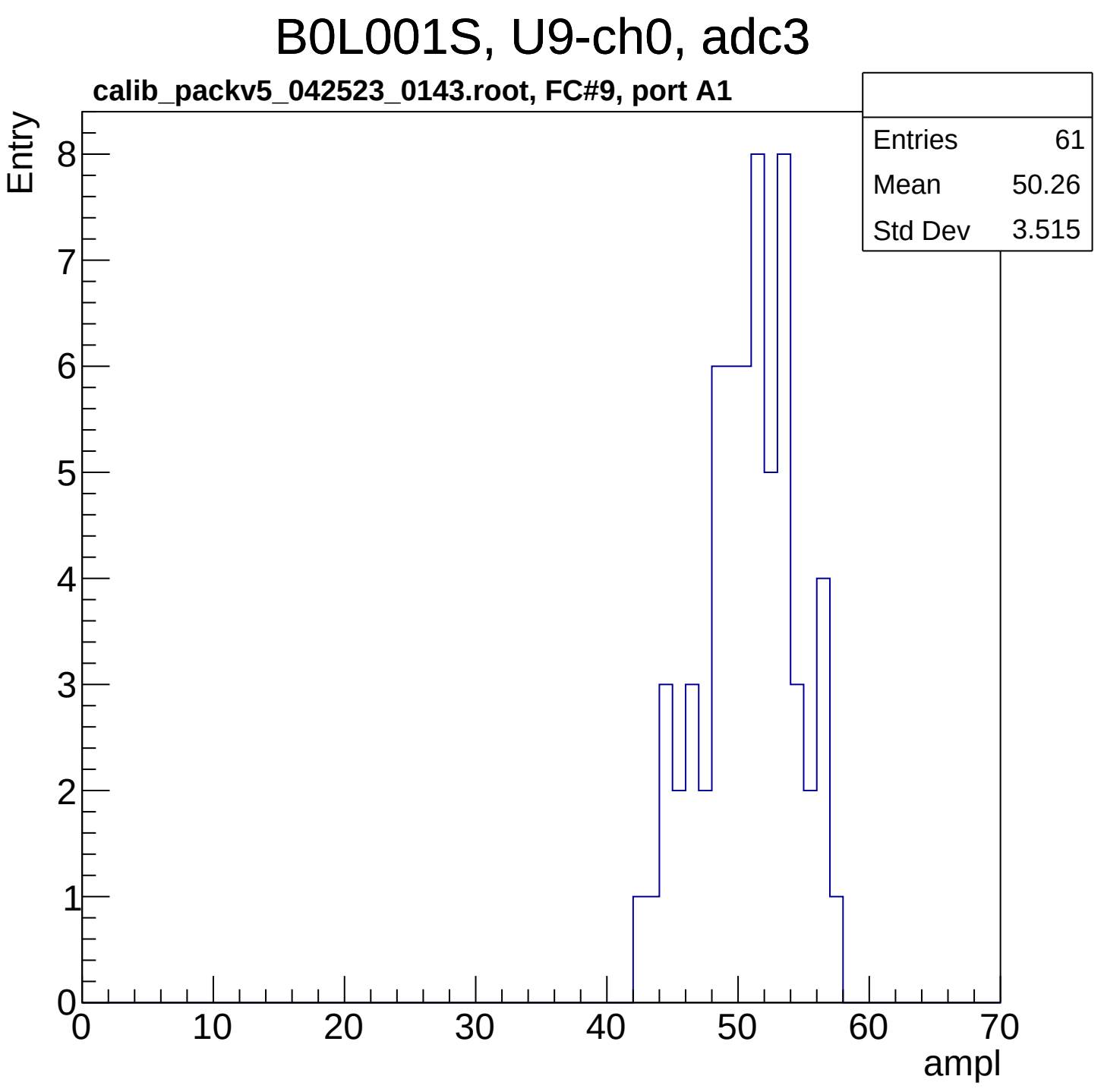
B0L001S, U9-ch0, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 50.26
Std Dev 3.515
Entry
ampl
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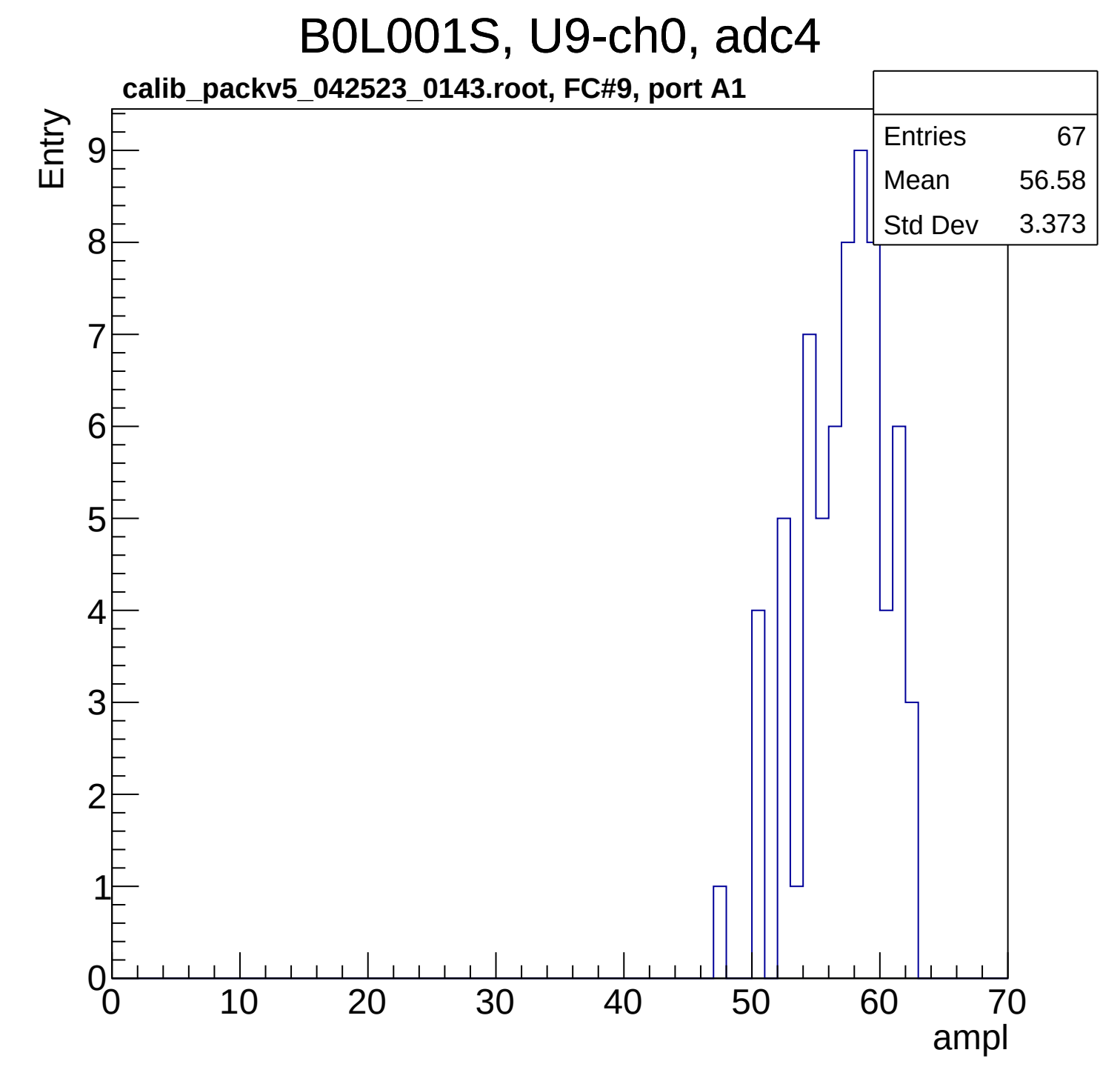
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch0, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.58
Std Dev 3.373
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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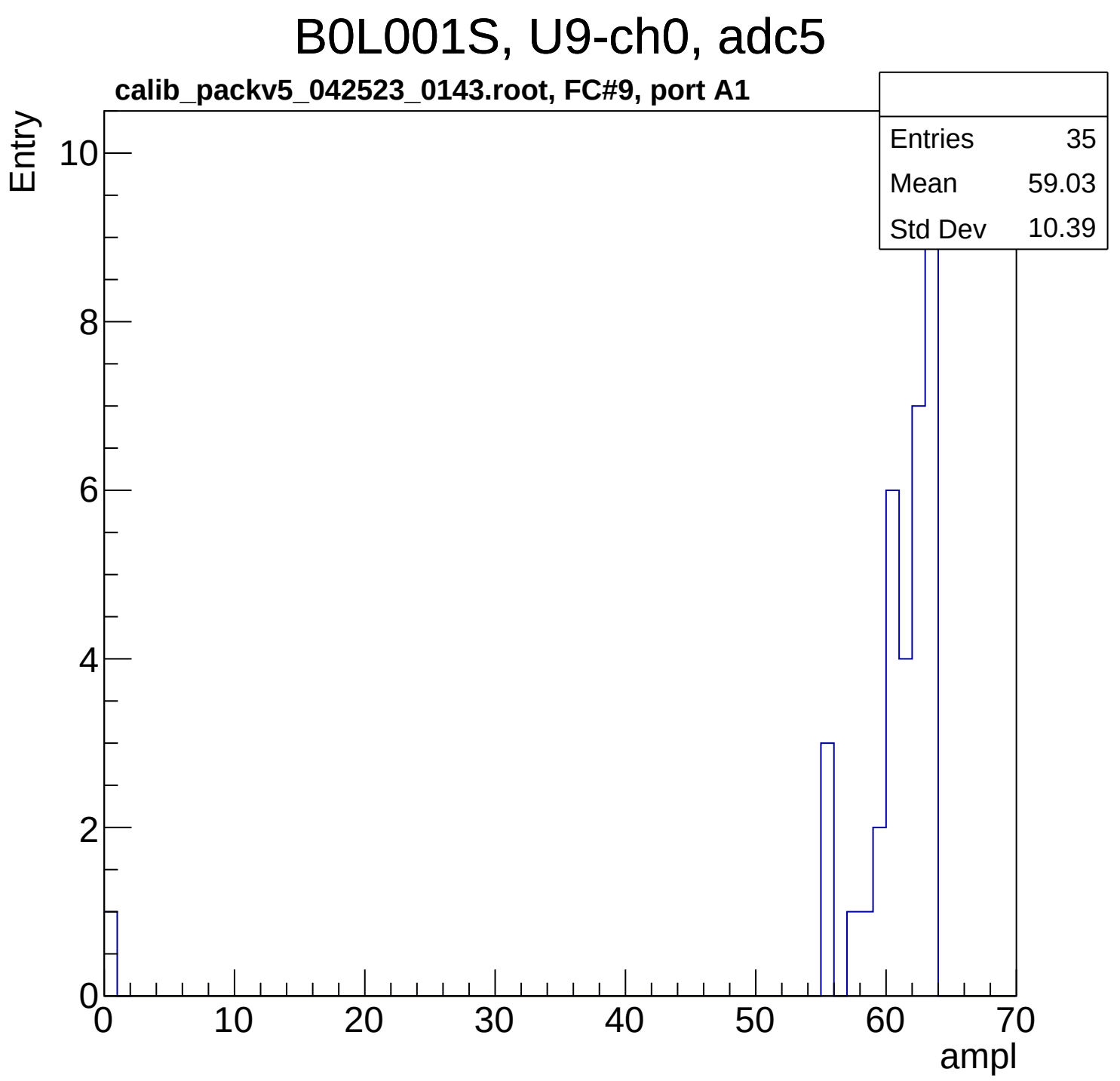
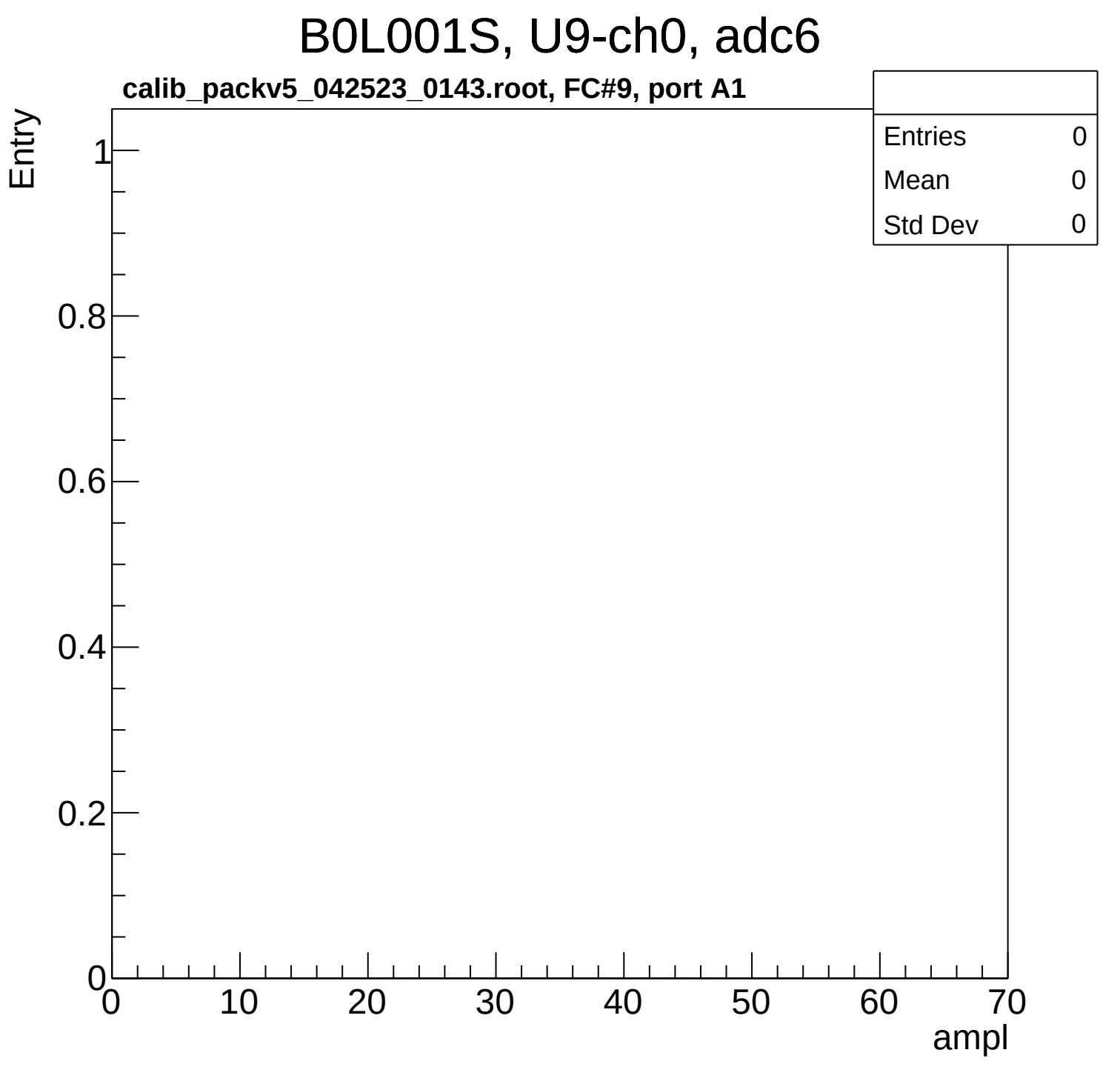
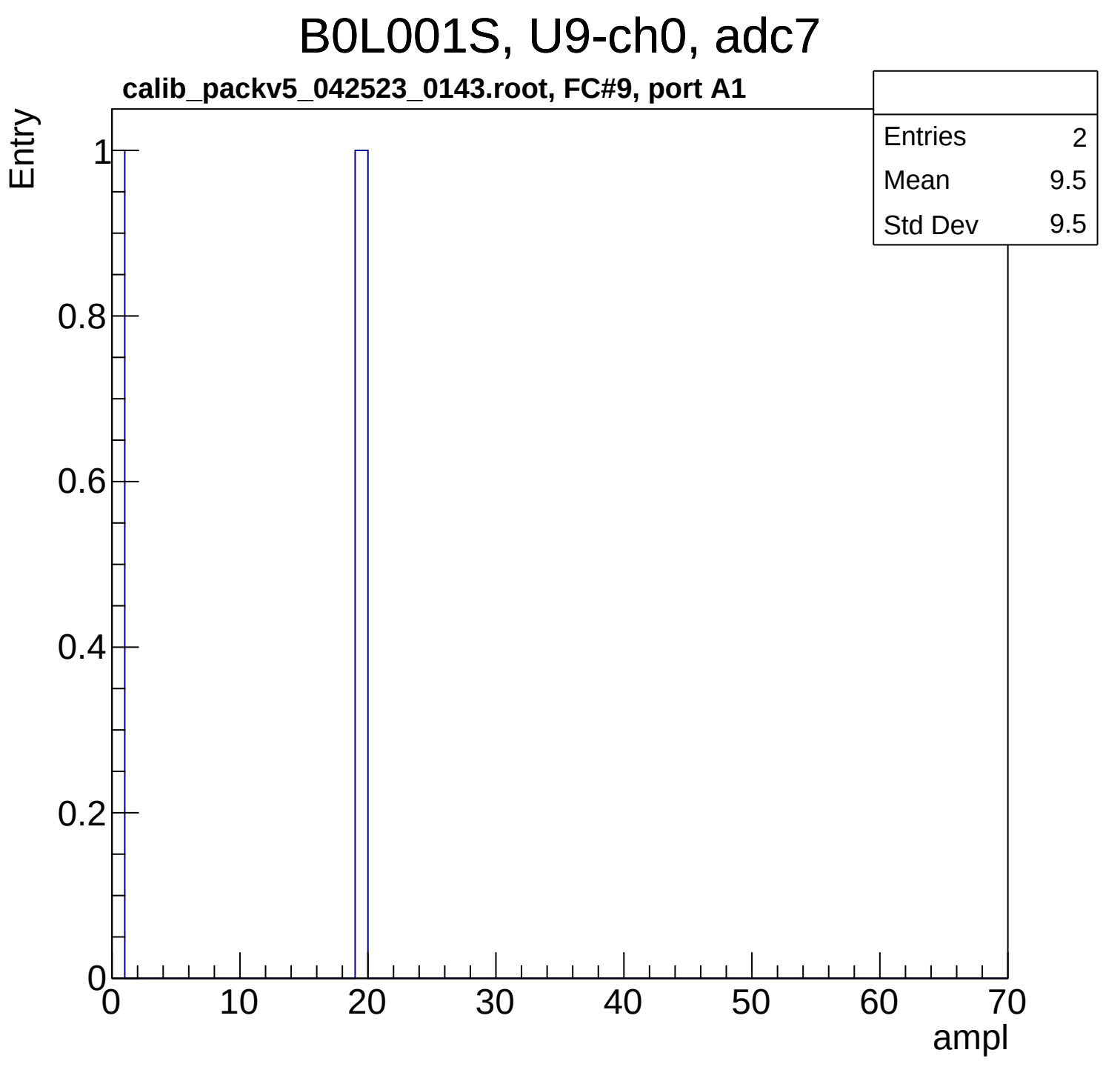
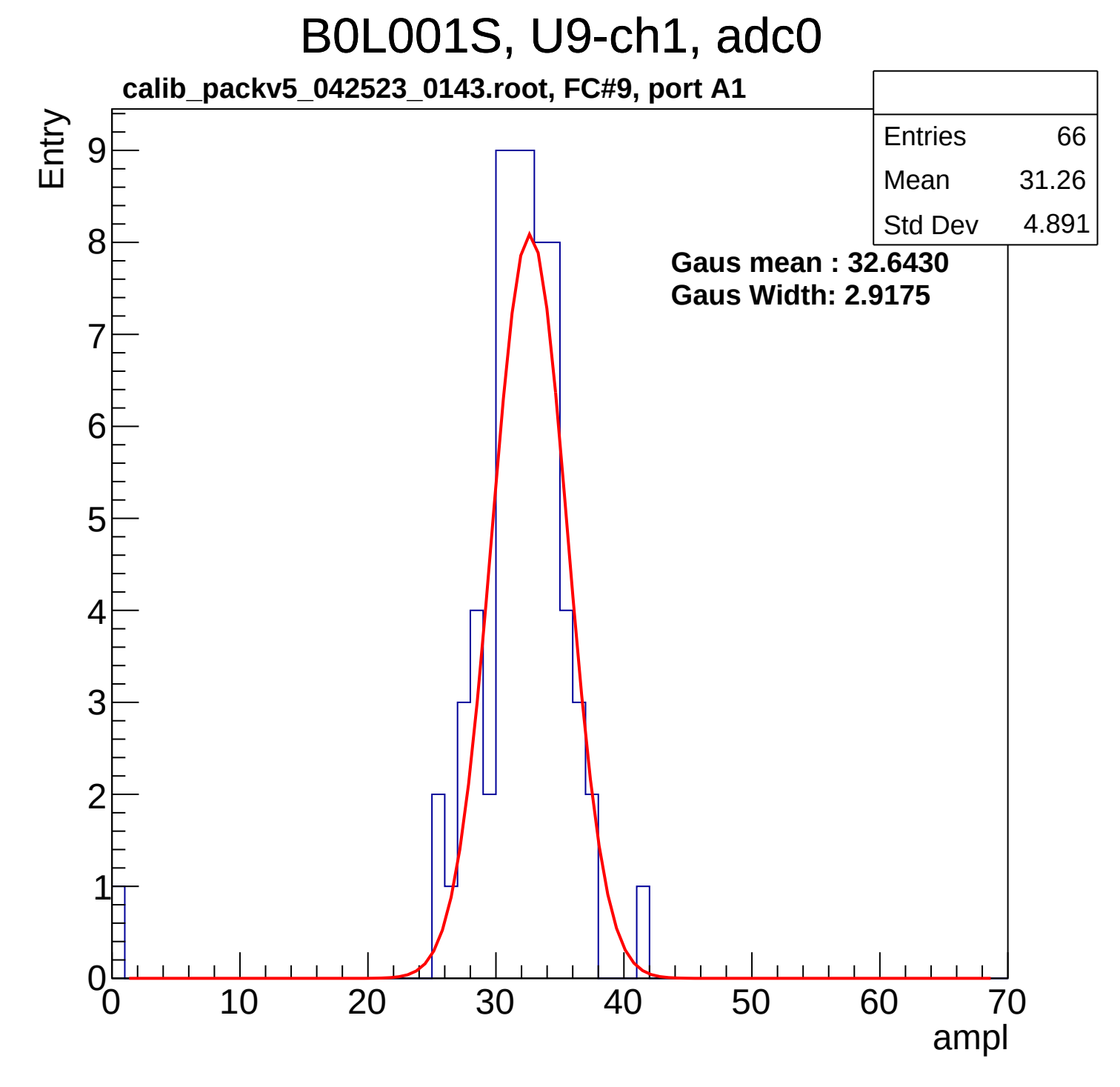
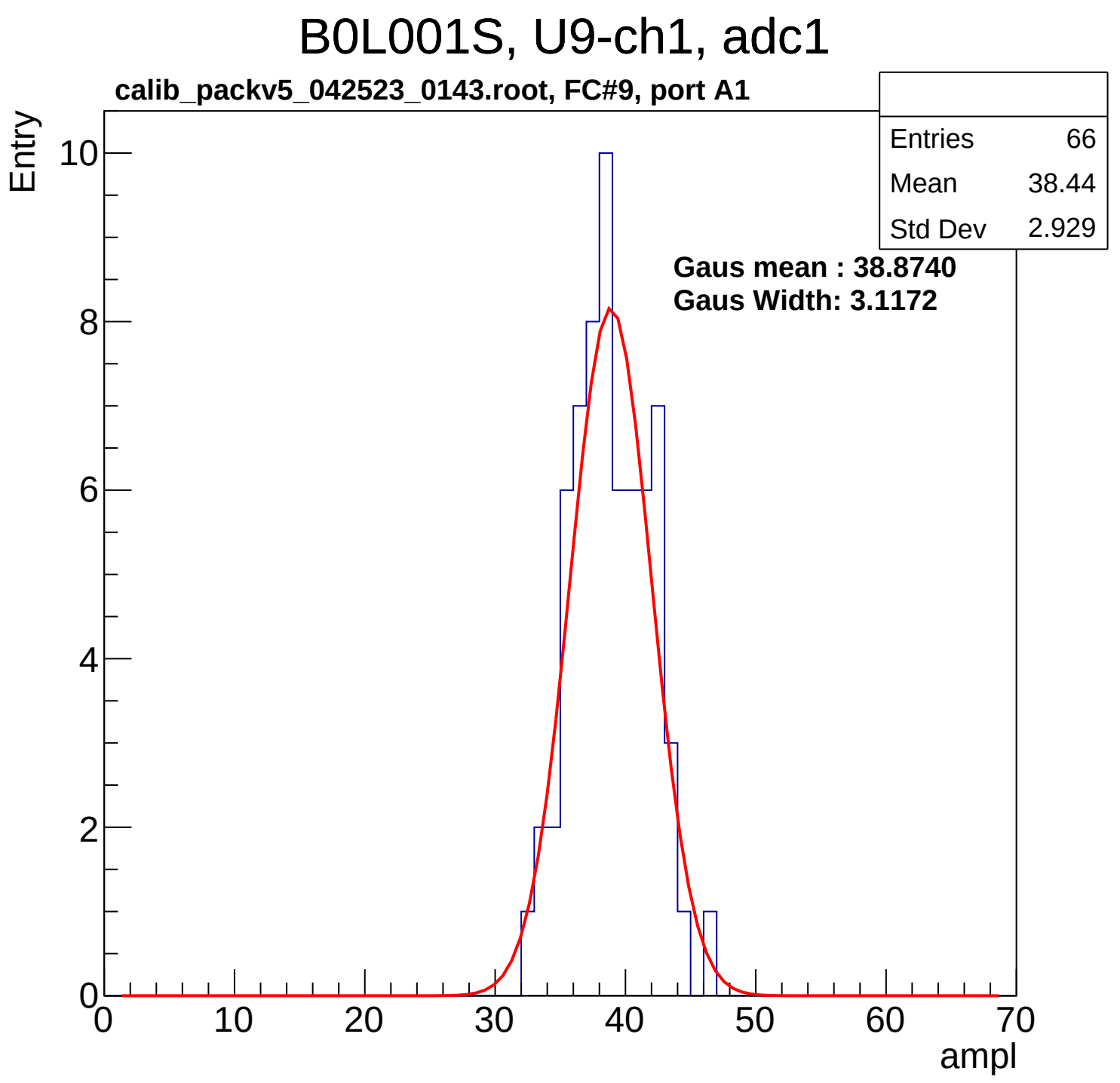
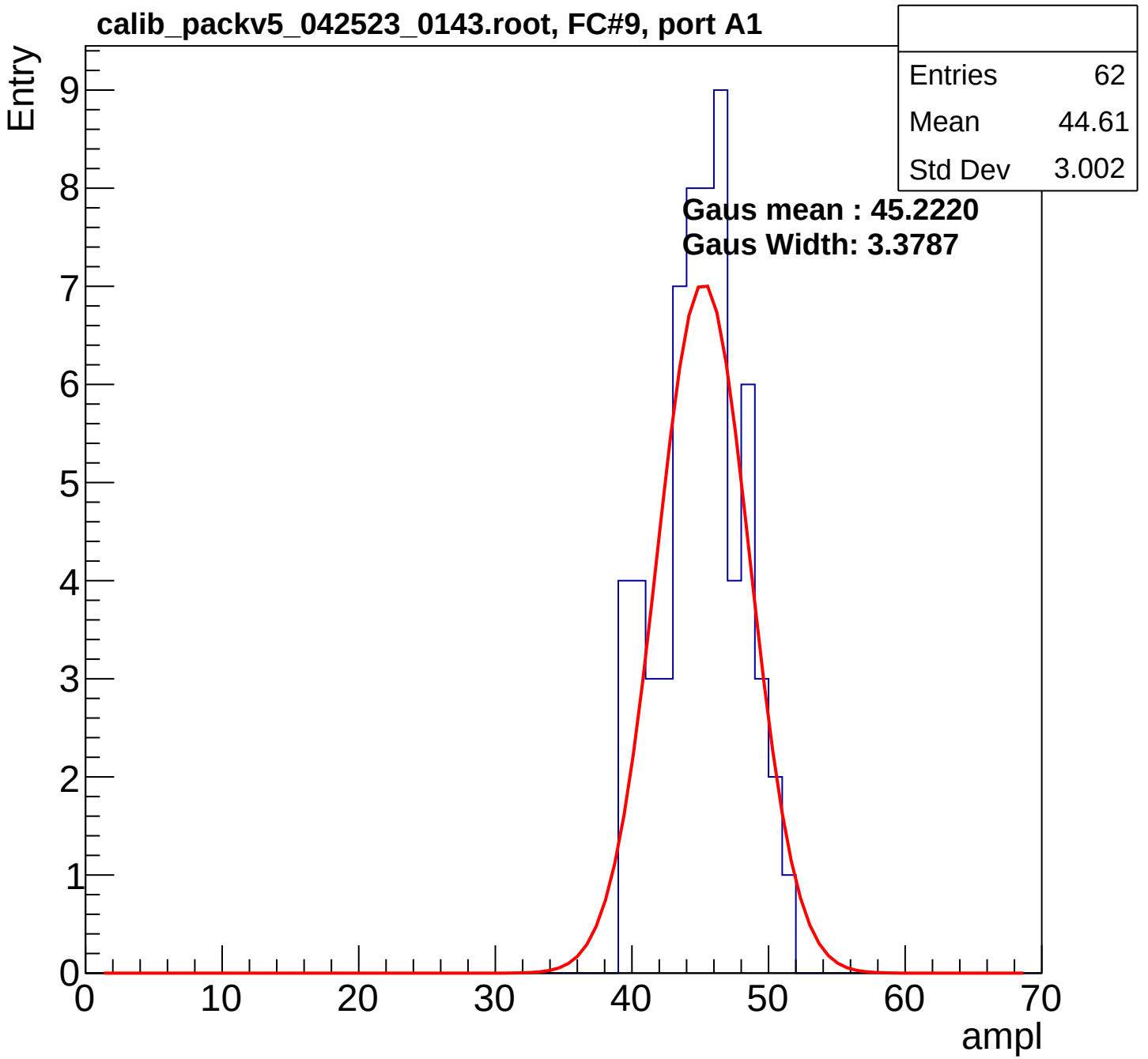

B0L001S, U9-ch0, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 59.03
Std Dev 10.39
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

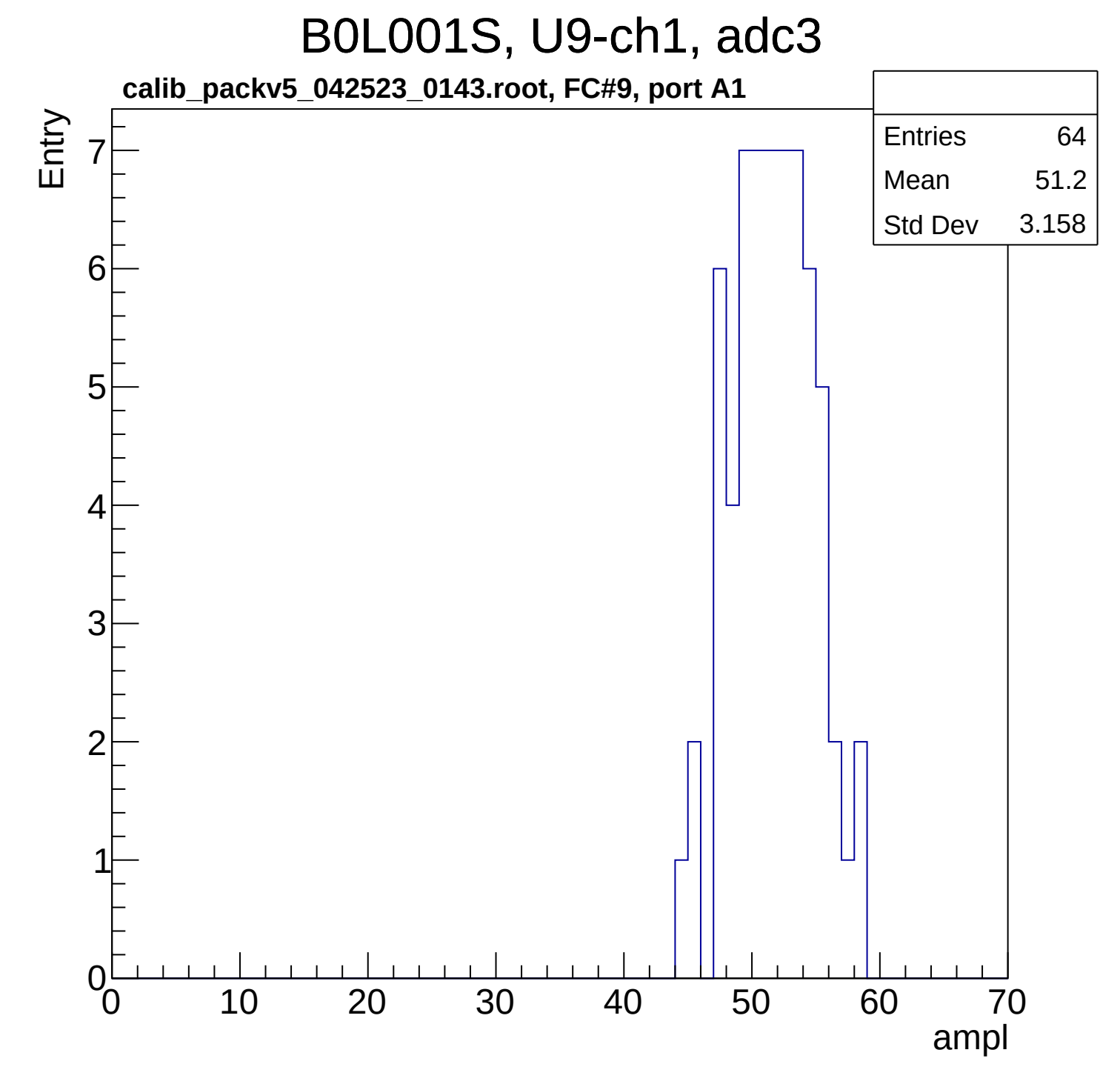

B0L001S, U9-ch0, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

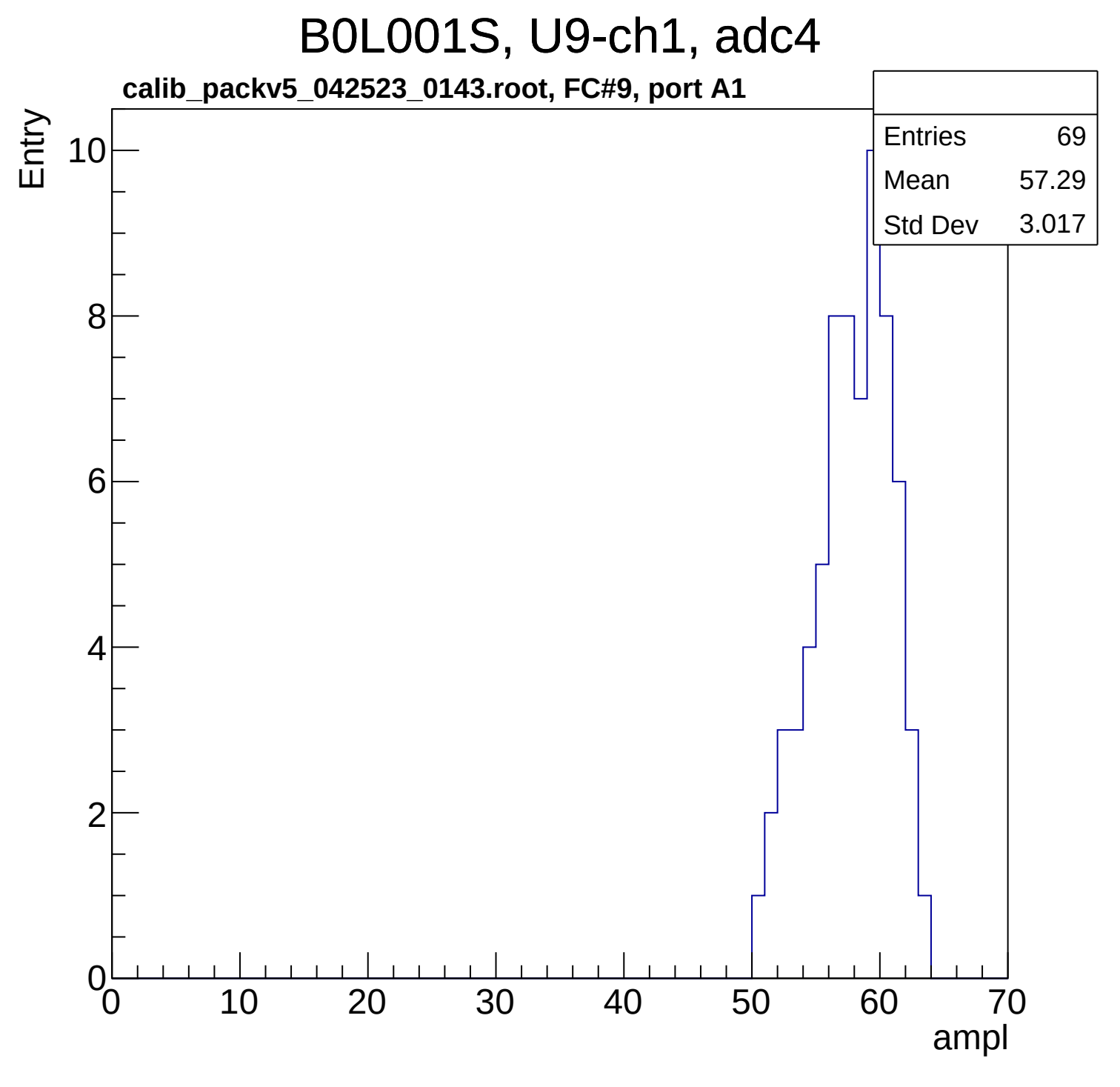

B0L001S, U9-ch0, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

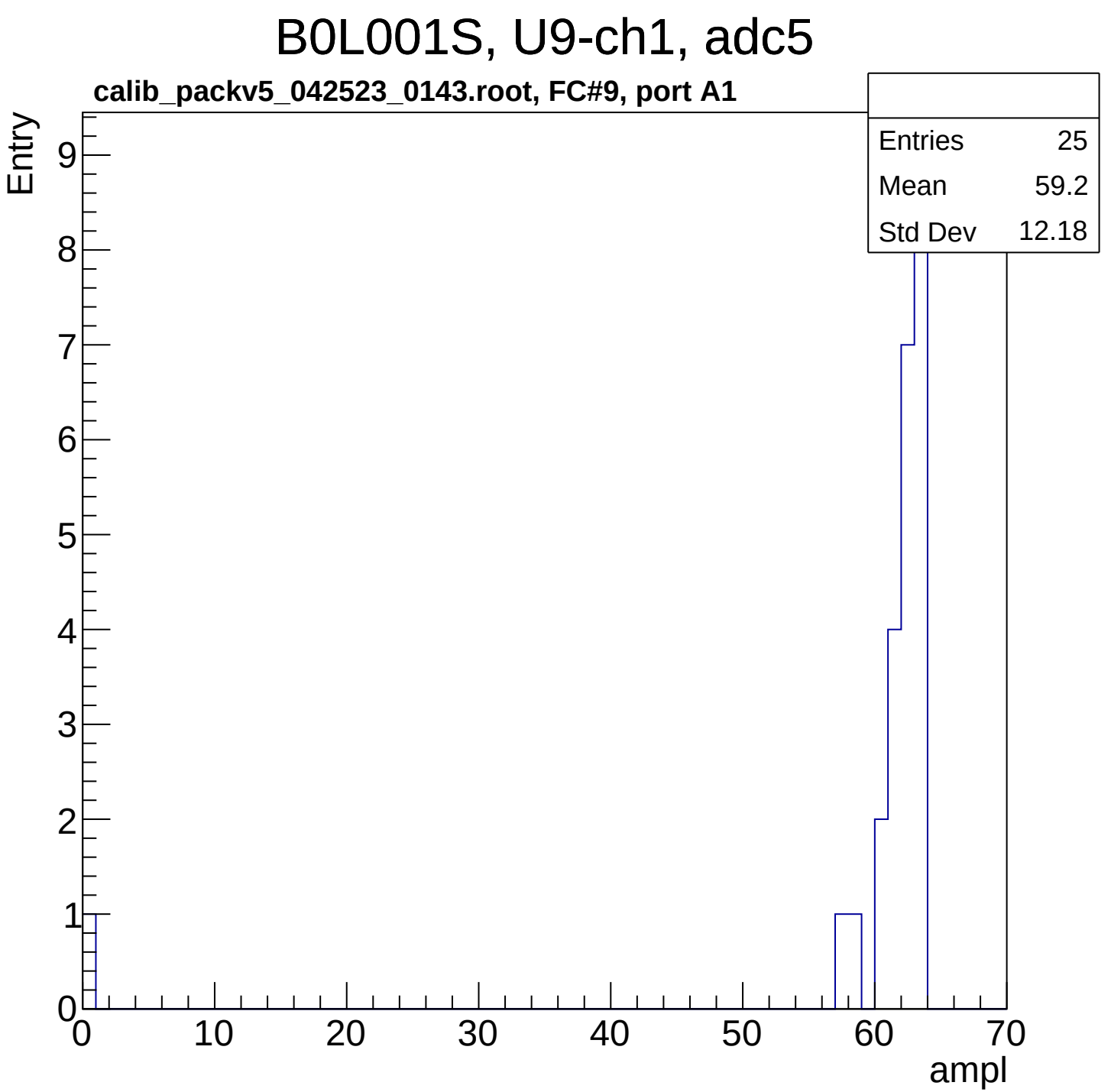

B0L001S, U9-ch1, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 31.26
Std Dev 4.891
Gaus mean : 32.6430
Gaus Width: 2.9175
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

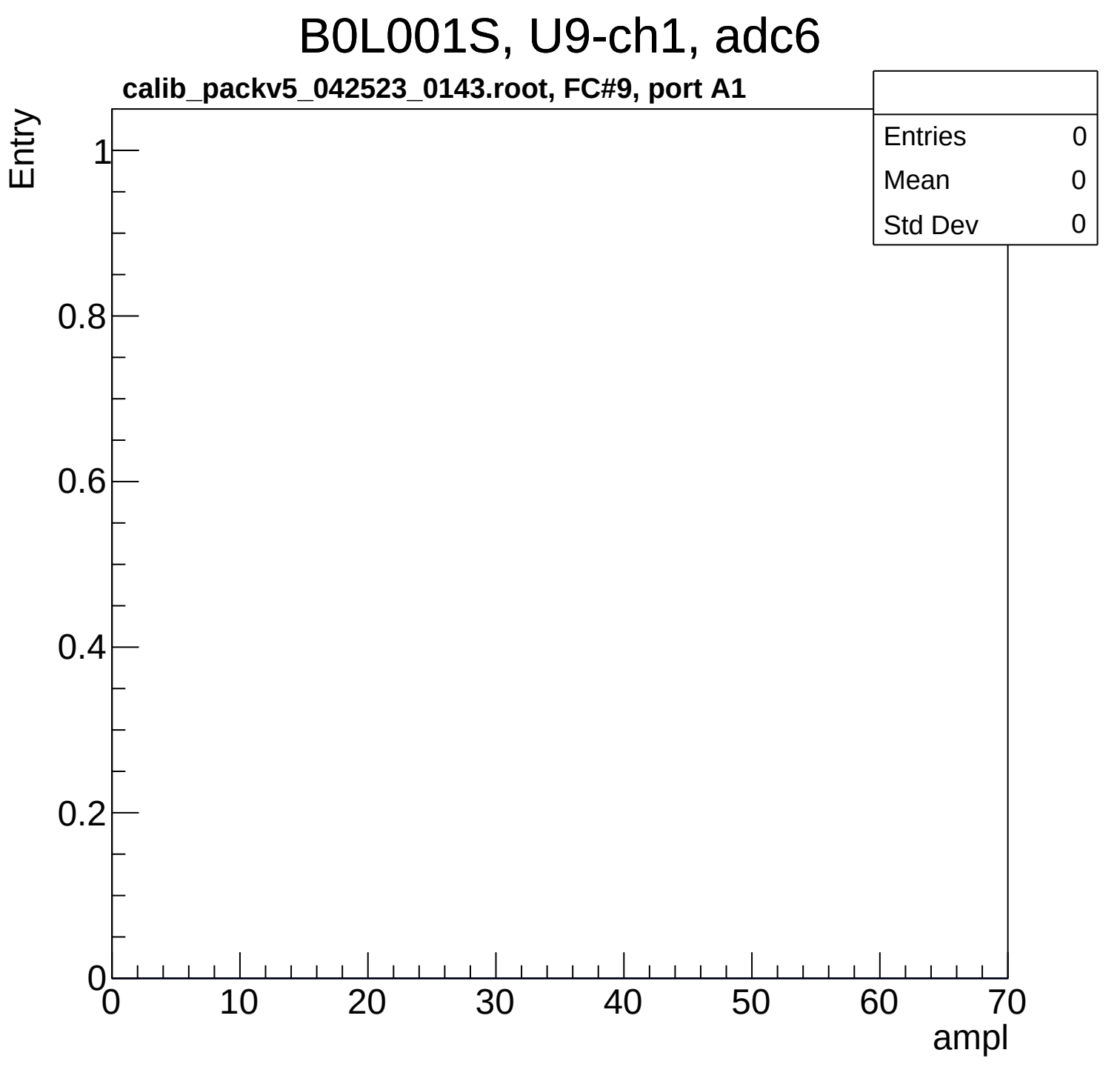

B0L001S, U9-ch1, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.44
Std Dev 2.929
Gaus mean : 38.8740
Gaus Width: 3.1172
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

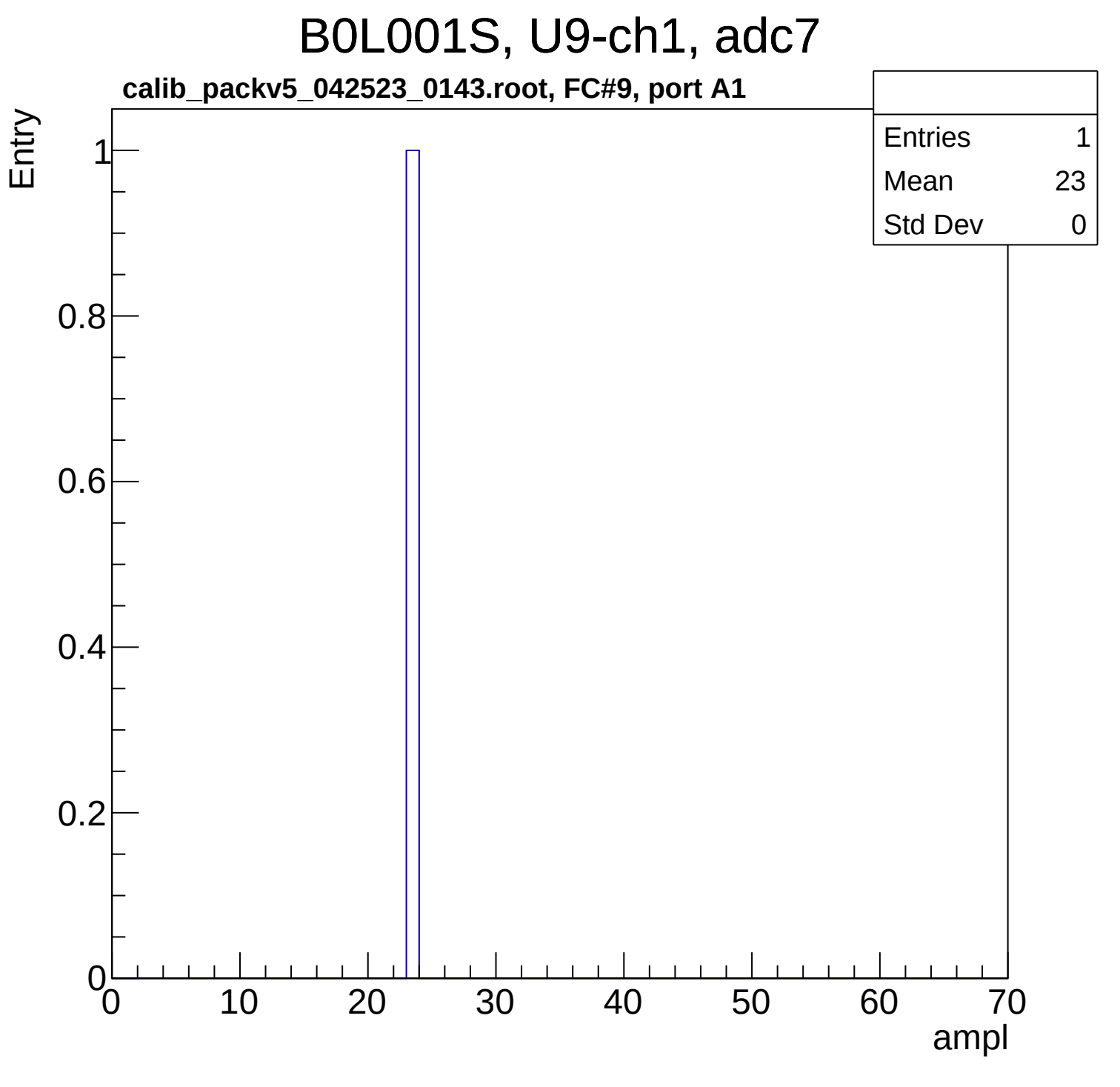

B0L001S, U9-ch1, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.61
Std Dev 3.002
Gaus mean : 45.2220
Gaus Width: 3.3787
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

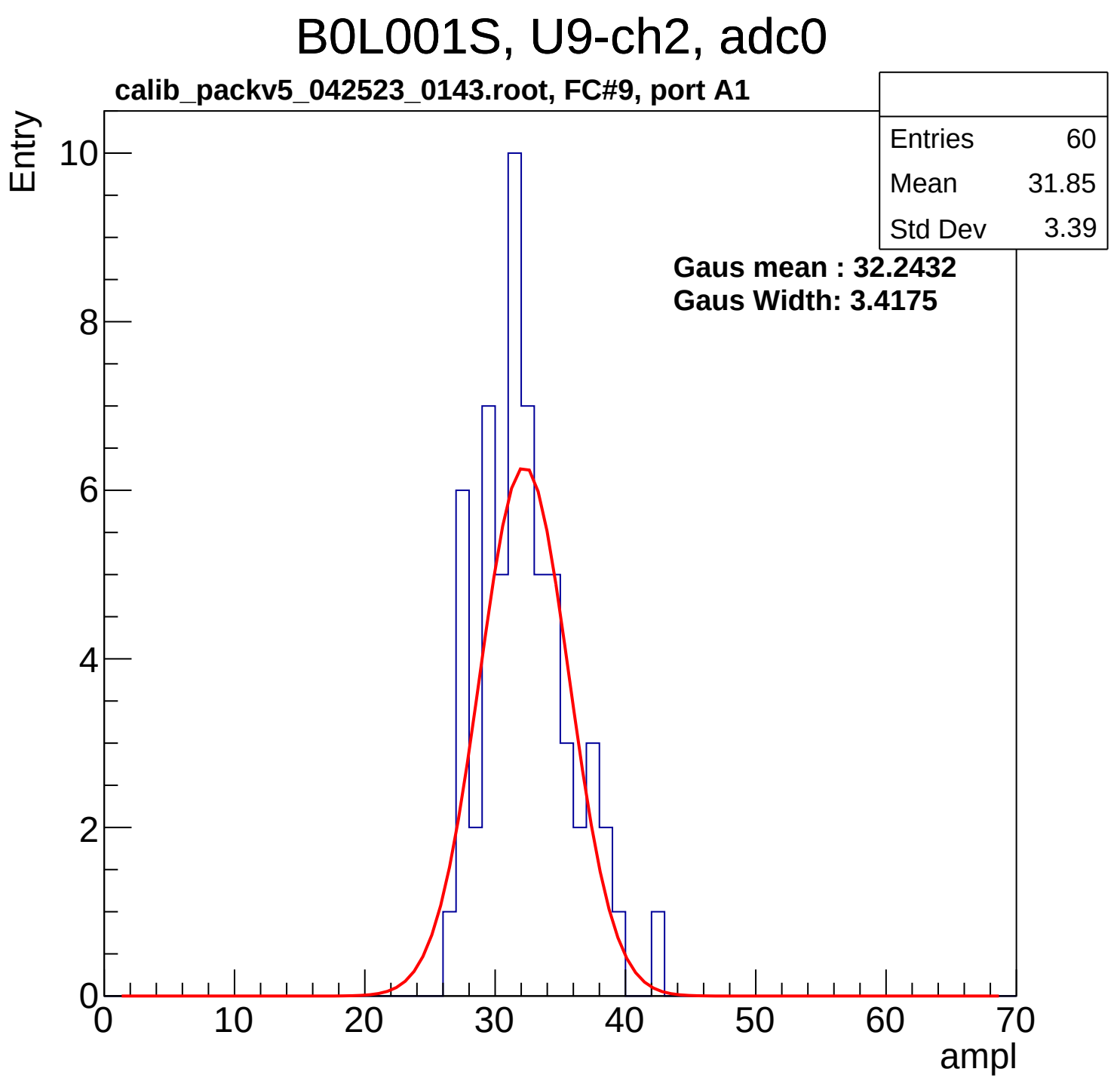

B0L001S, U9-ch1, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.2
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch1, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 57.29
Std Dev 3.017
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch1, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 59.2
Std Dev 12.18
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch1, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch1, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch2, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 31.85
Std Dev 3.39
Gaus mean : 32.2432
Gaus Width: 3.4175
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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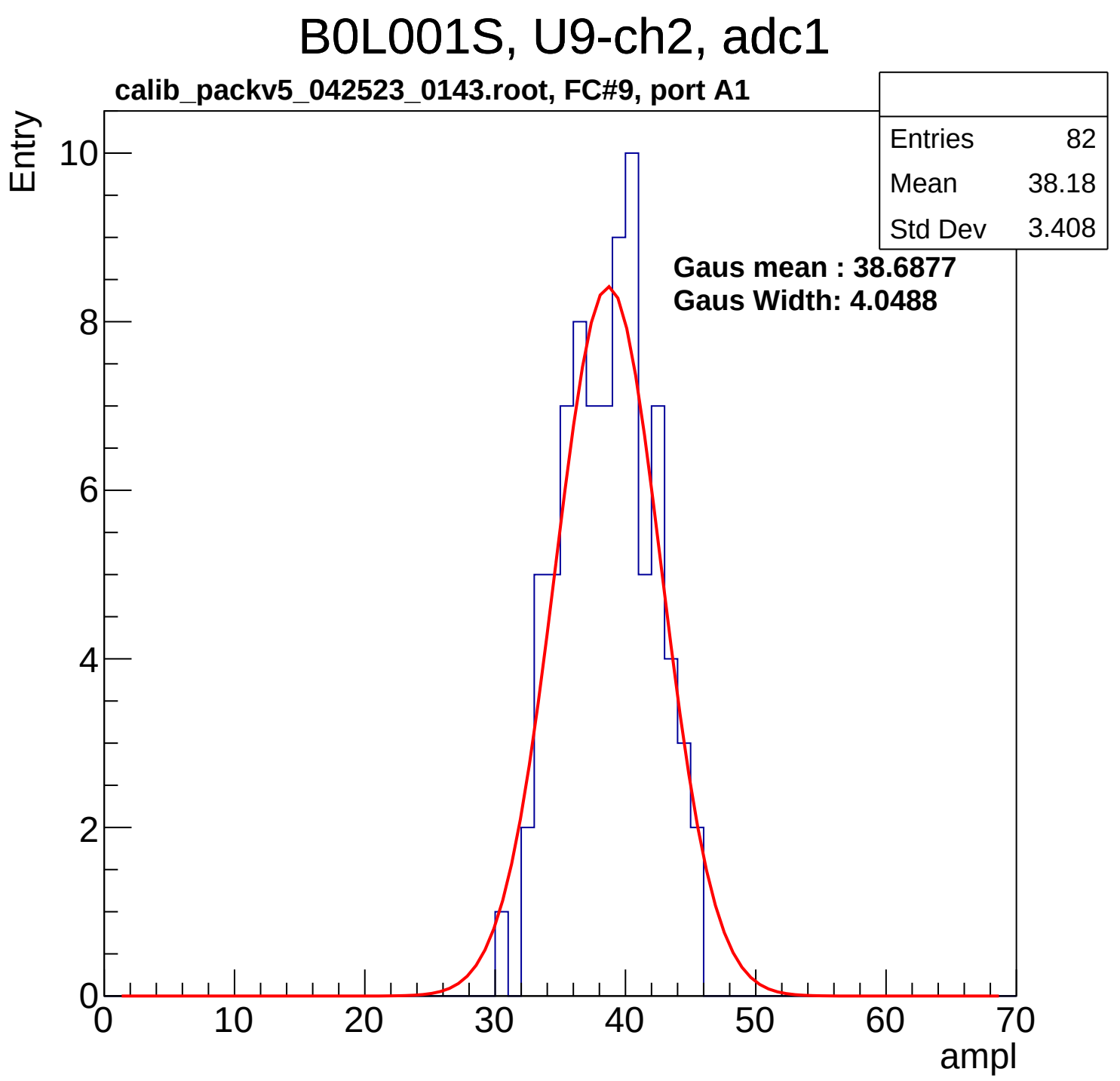
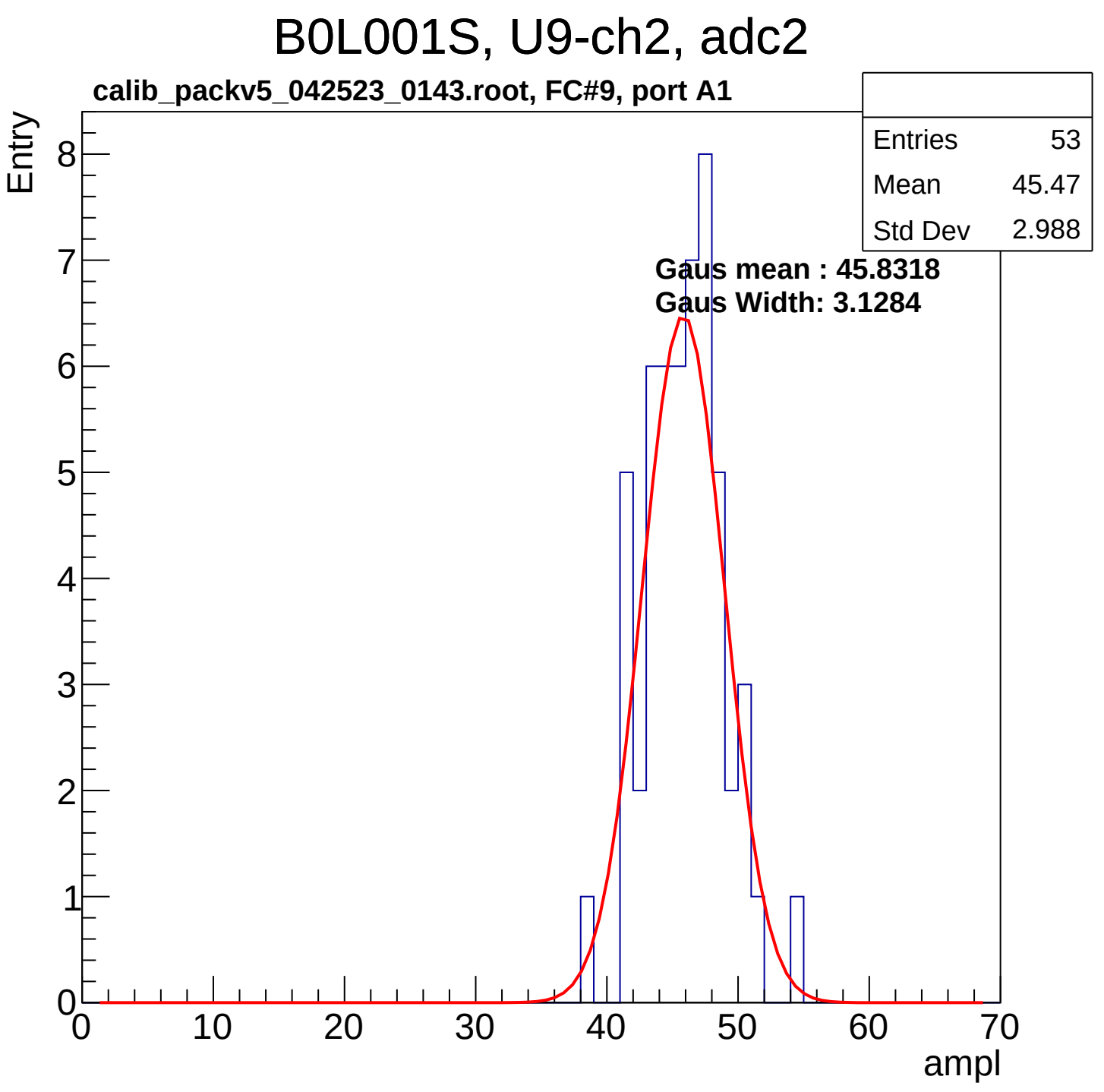

B0L001S, U9-ch2, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 38.18
Std Dev 3.408
Gaus mean : 38.6877
Gaus Width: 4.0488
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch2, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 45.47
Std Dev 2.988
Gaus mean : 45.8318
Gaus Width: 3.1284
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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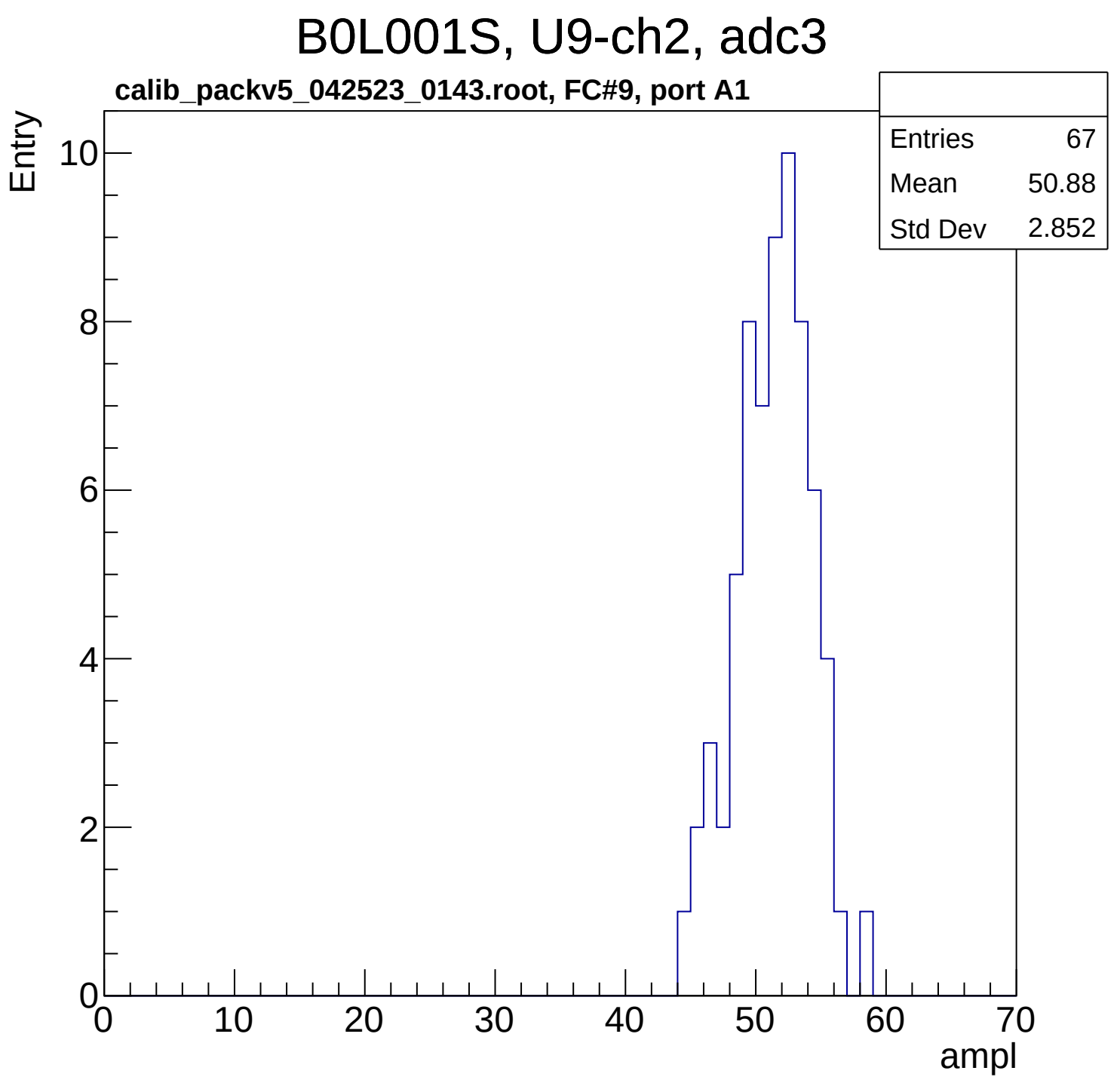
B0L001S, U9-ch2, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.88
Std Dev 2.852
Entry
0
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4
6
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10
0
10
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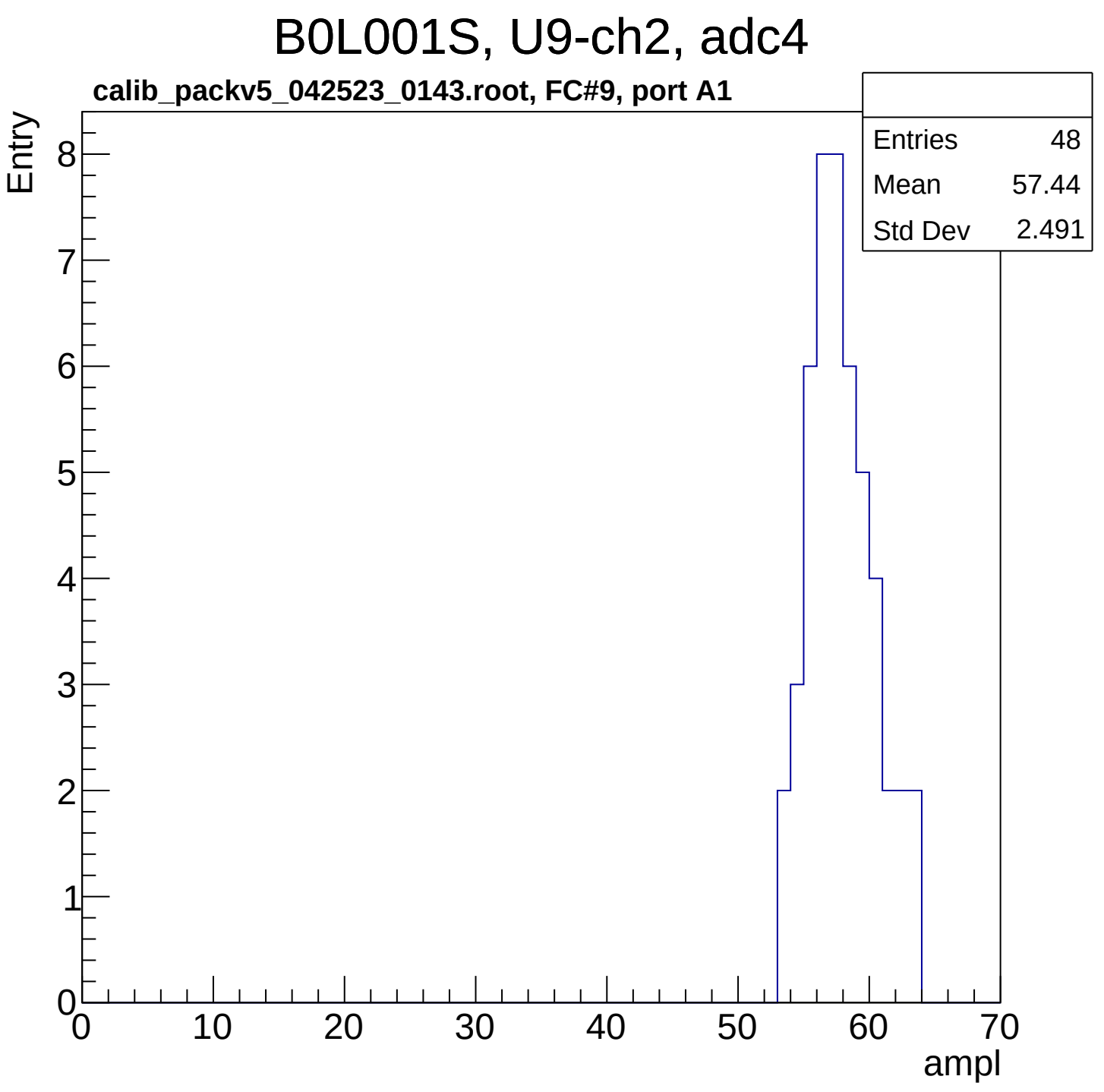
50
60
70
ampl


B0L001S, U9-ch2, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 57.44
Std Dev 2.491
Entry
ampl
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0
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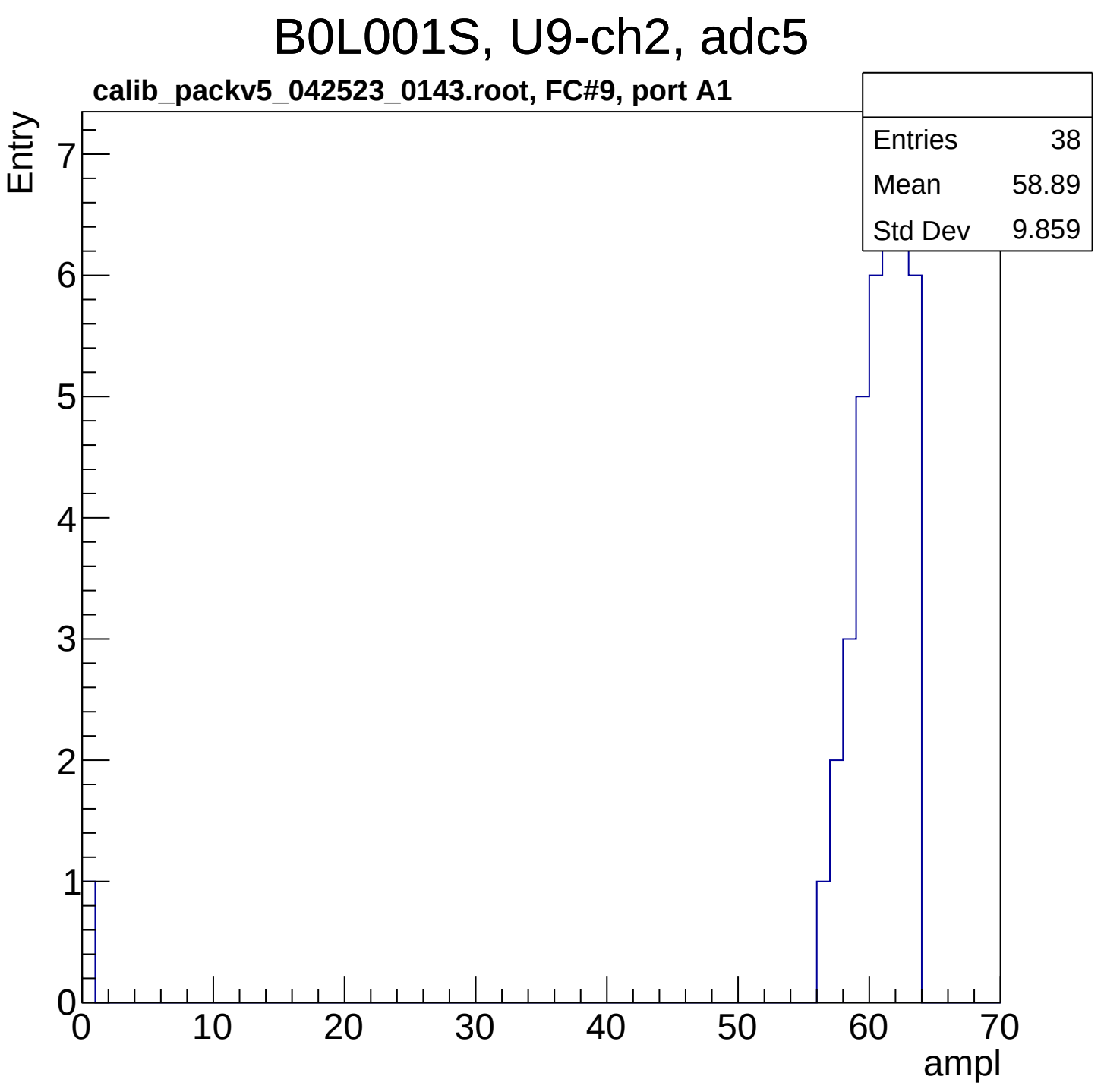
B0L001S, U9-ch2, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.89
Std Dev 9.859
Entry
ampl
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4
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6
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0
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60
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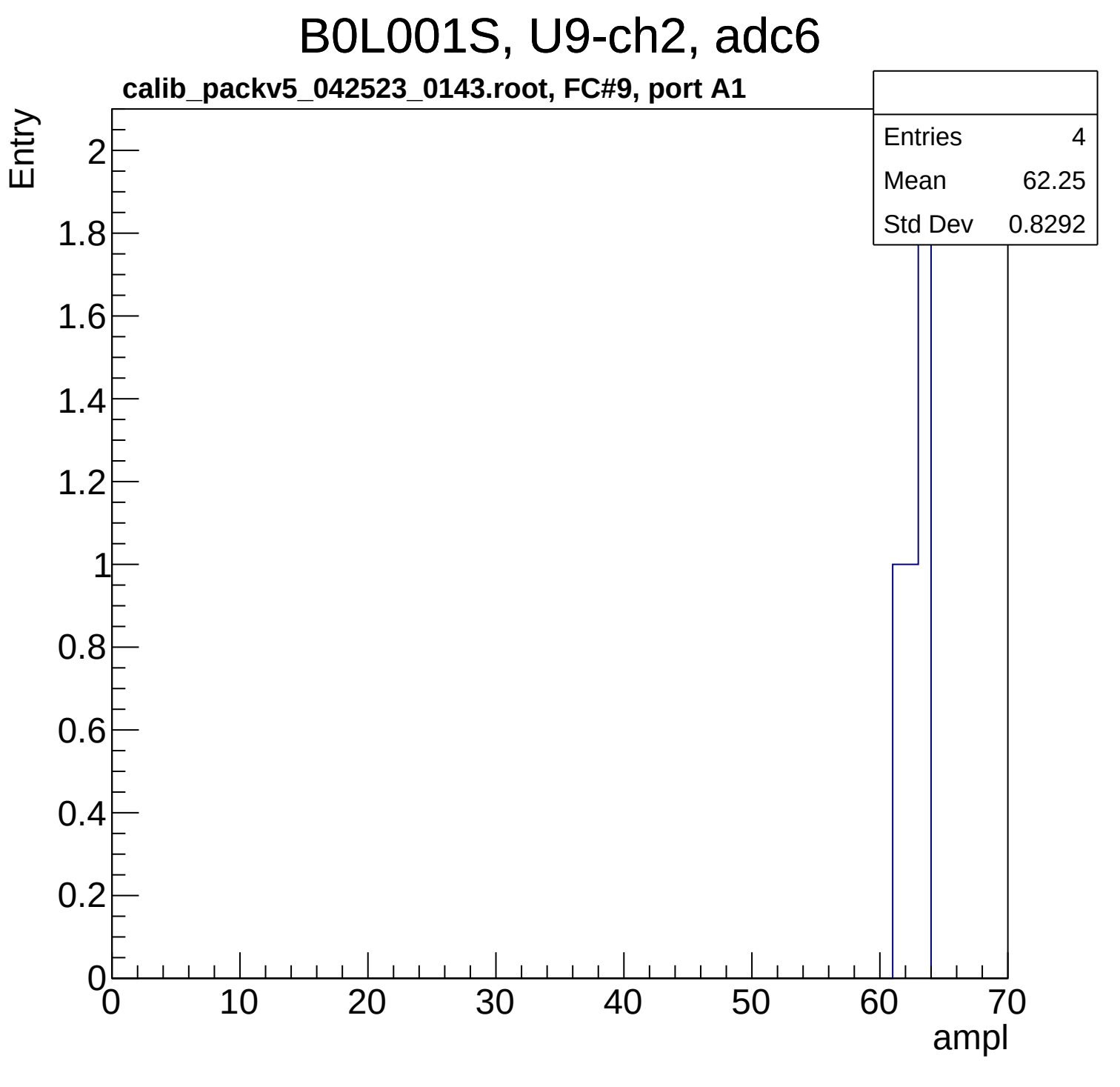
B0L001S, U9-ch2, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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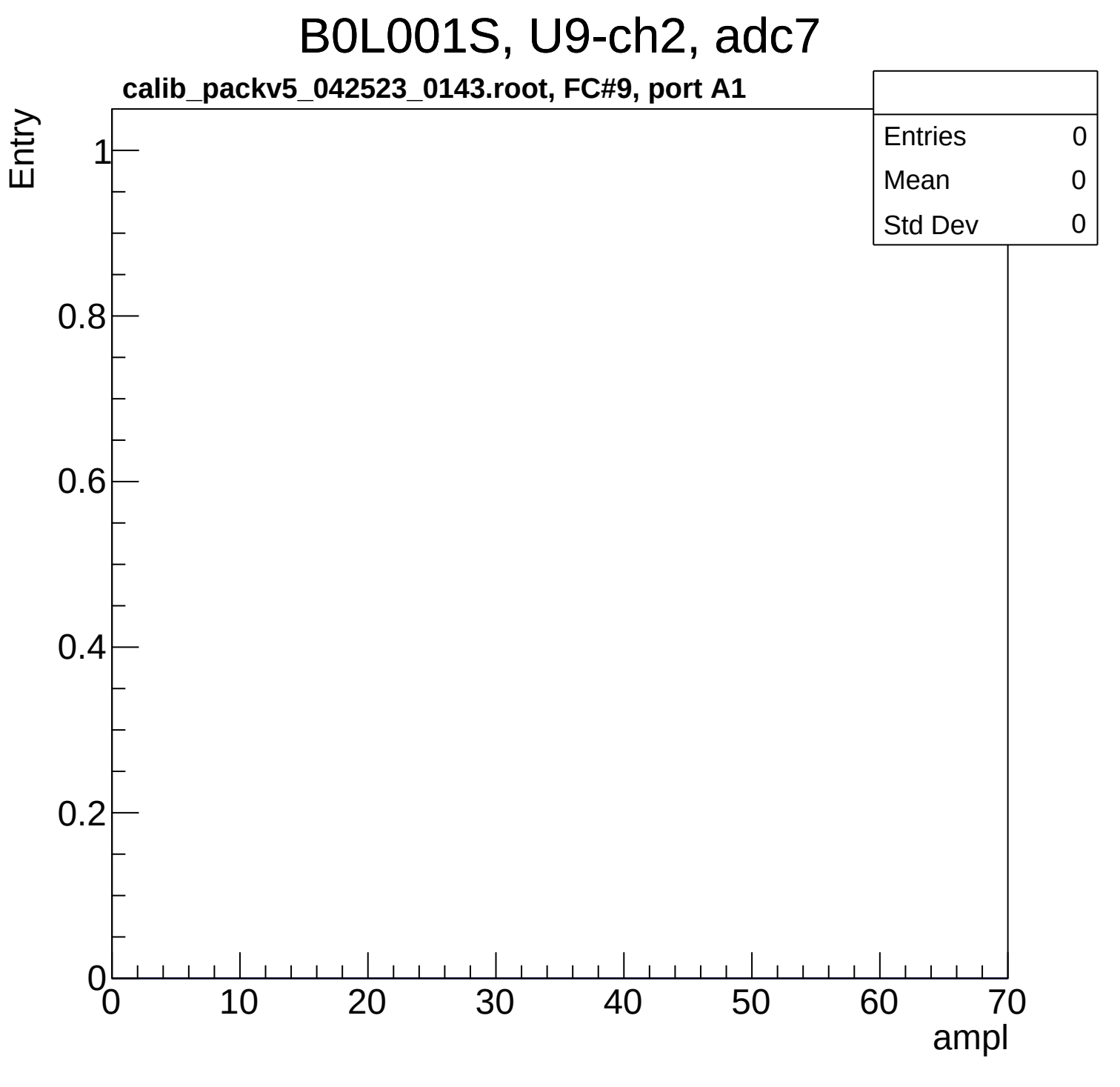
B0L001S, U9-ch2, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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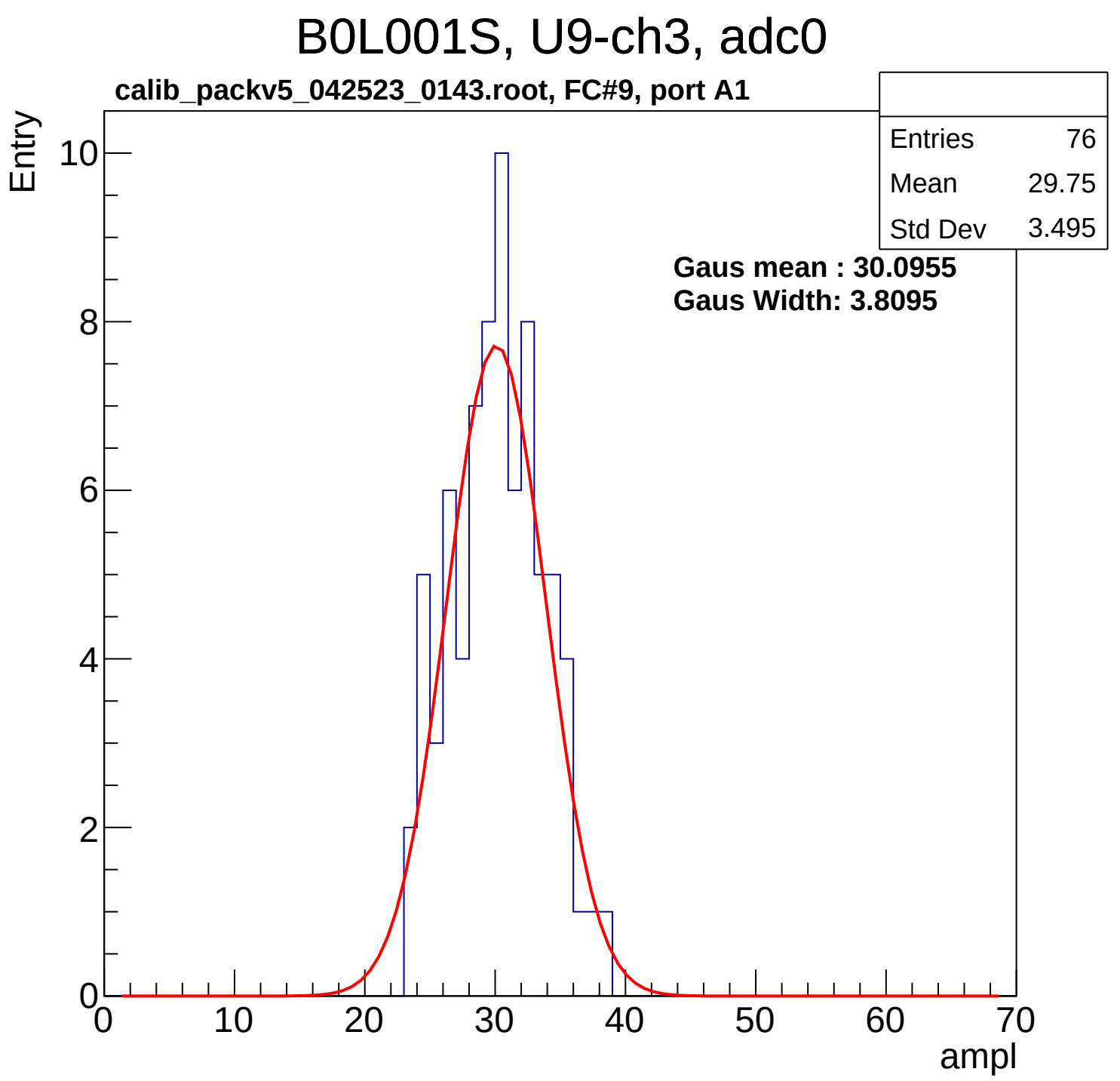
50
60
70
ampl


B0L001S, U9-ch3, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 29.75
Std Dev 3.495
Gaus mean : 30.0955
Gaus Width: 3.8095
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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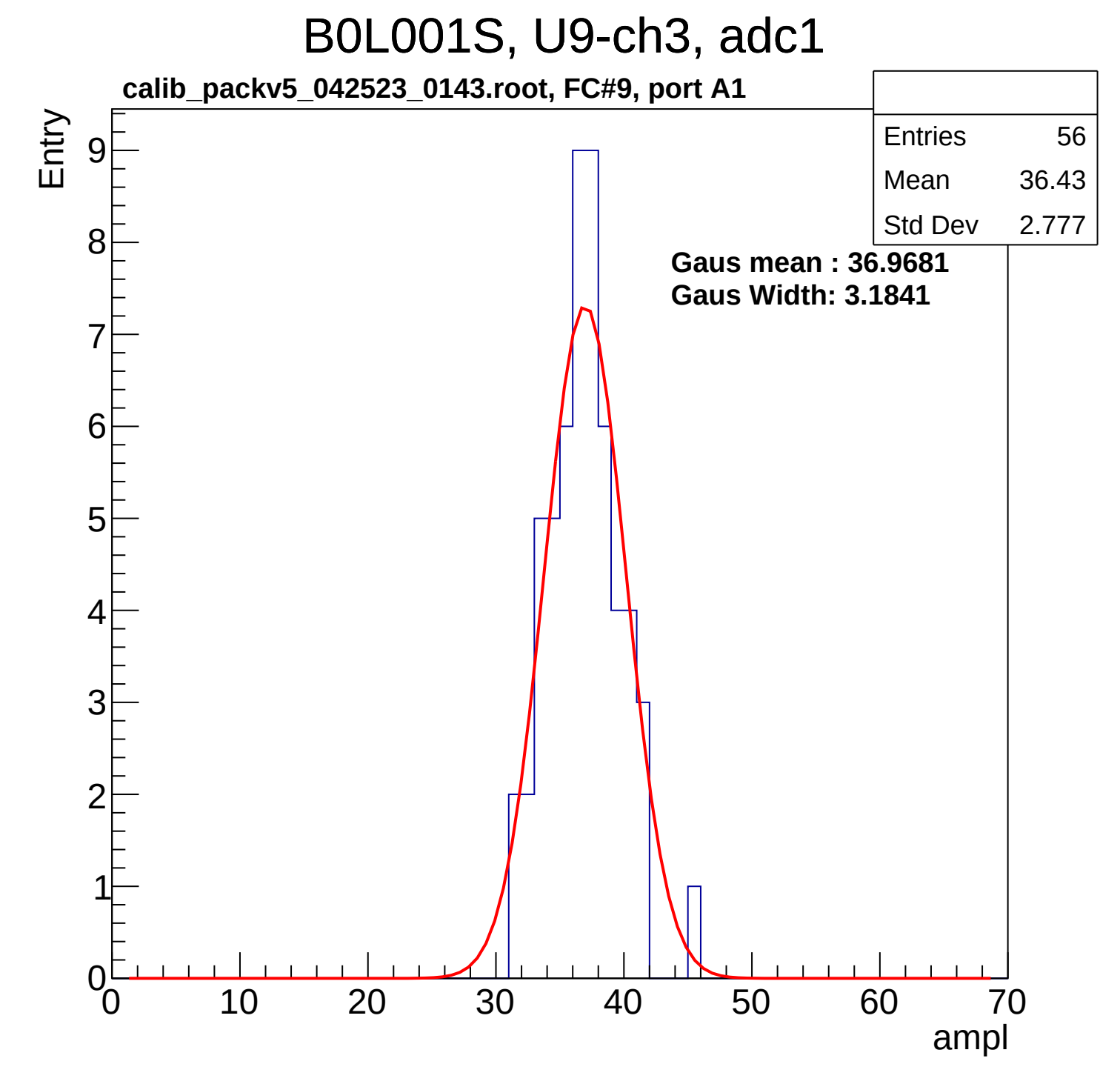
B0L001S, U9-ch3, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 36.43
Std Dev 2.777
Gaus mean : 36.9681
Gaus Width: 3.1841
Entry
ampl
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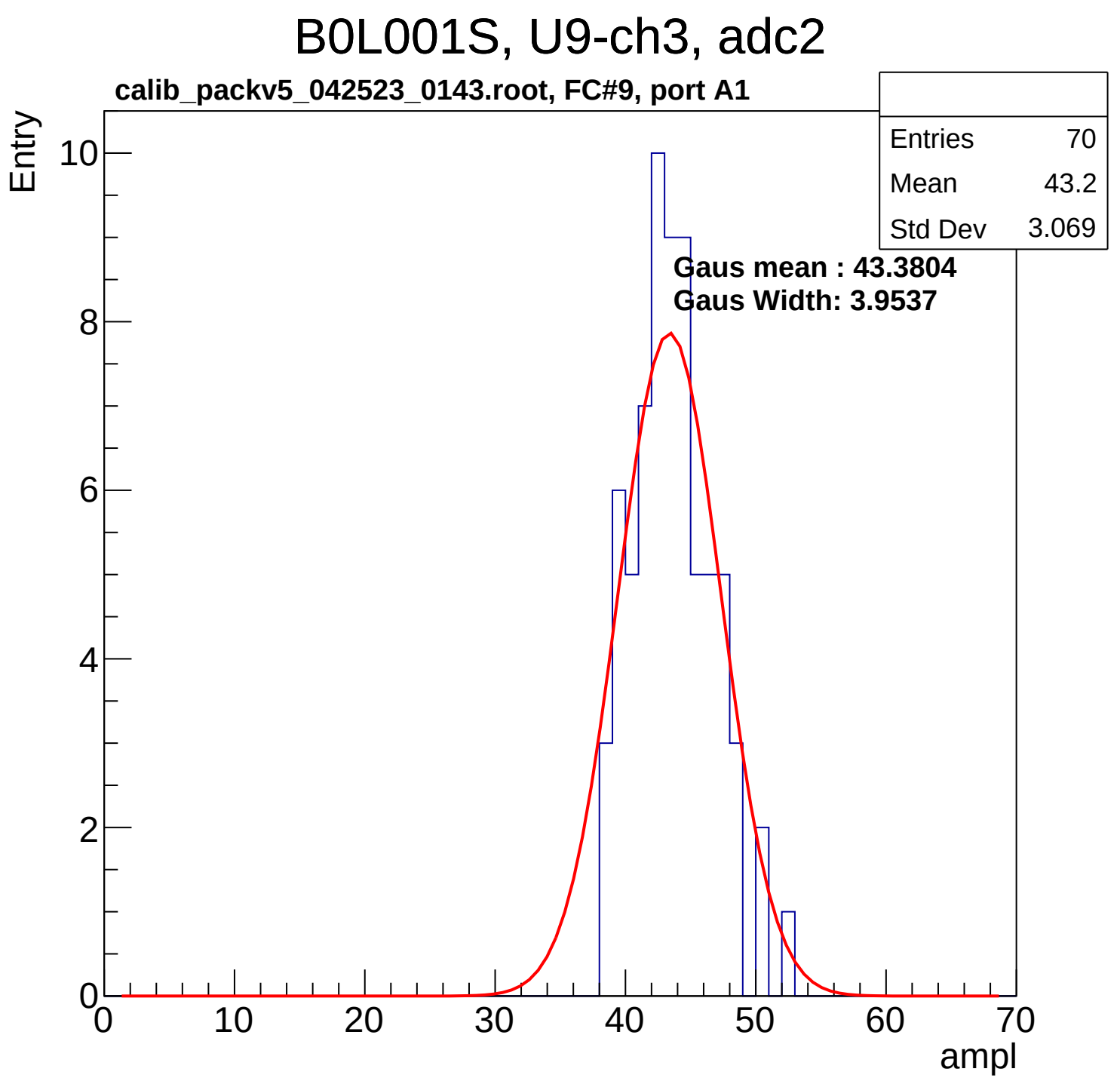
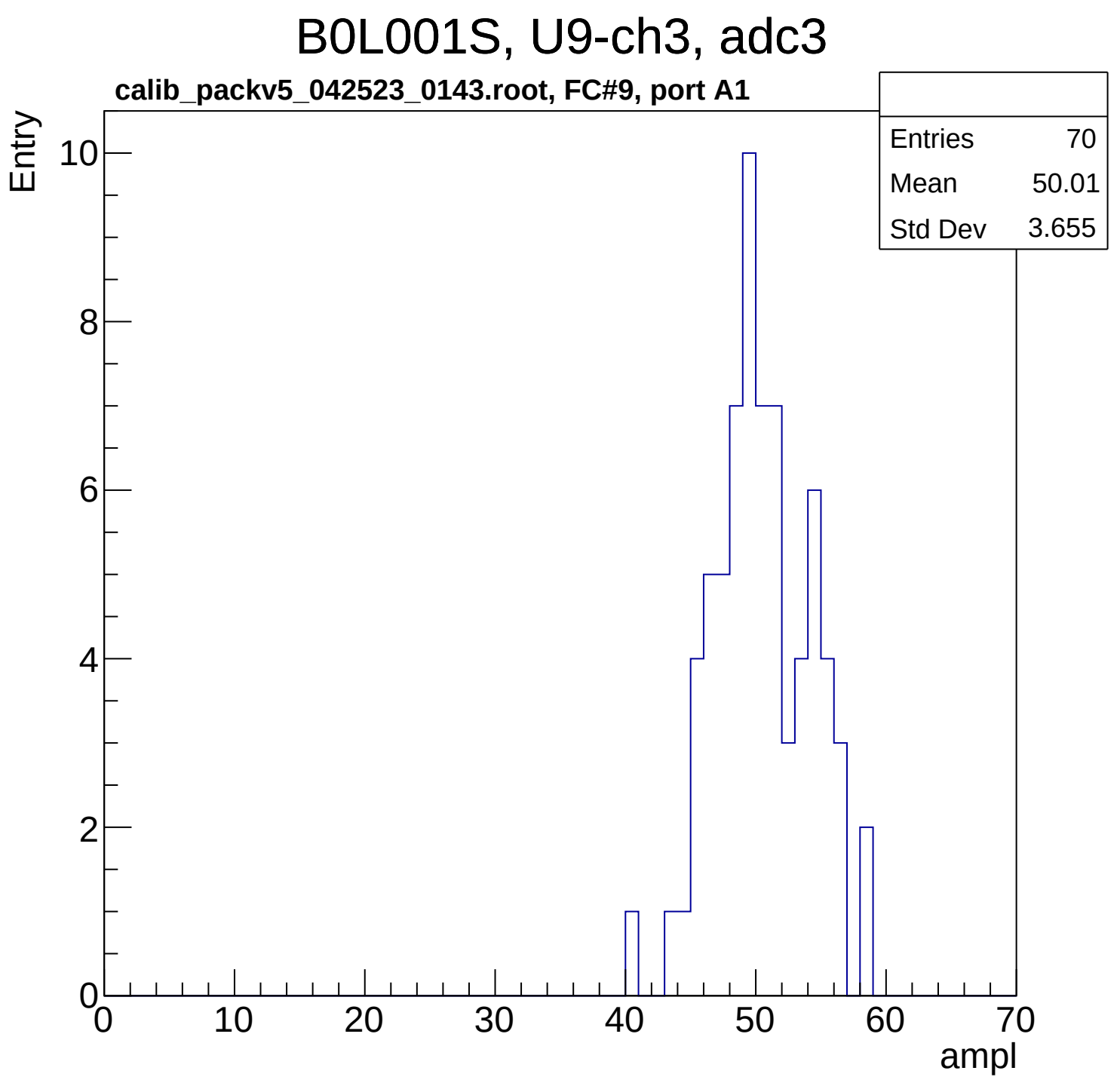
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch3, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.2
Std Dev 3.069
Gaus mean : 43.3804
Gaus Width: 3.9537
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch3, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.01
Std Dev 3.655
Entry
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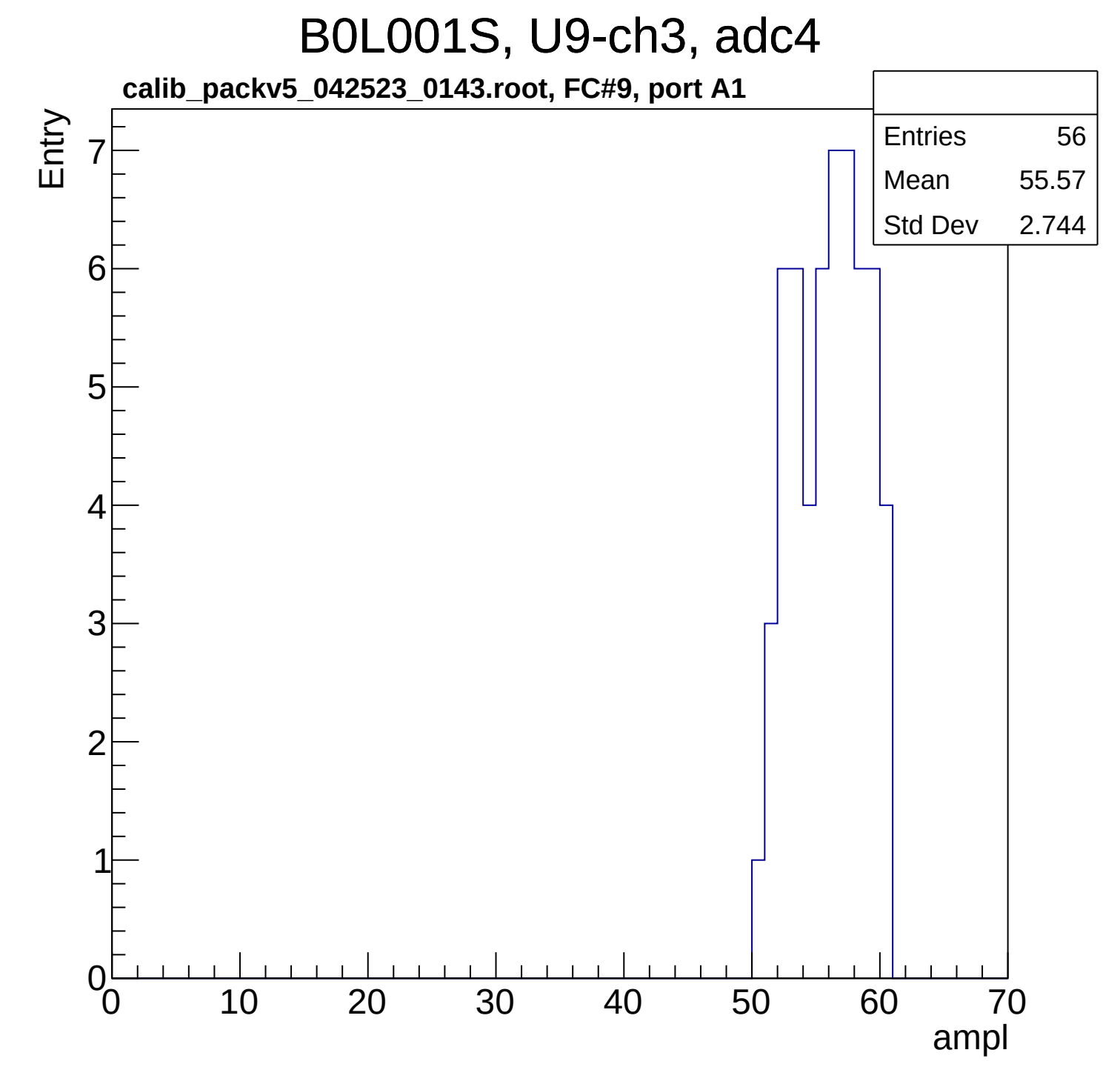
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch3, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.57
Std Dev 2.744
Entry
ampl
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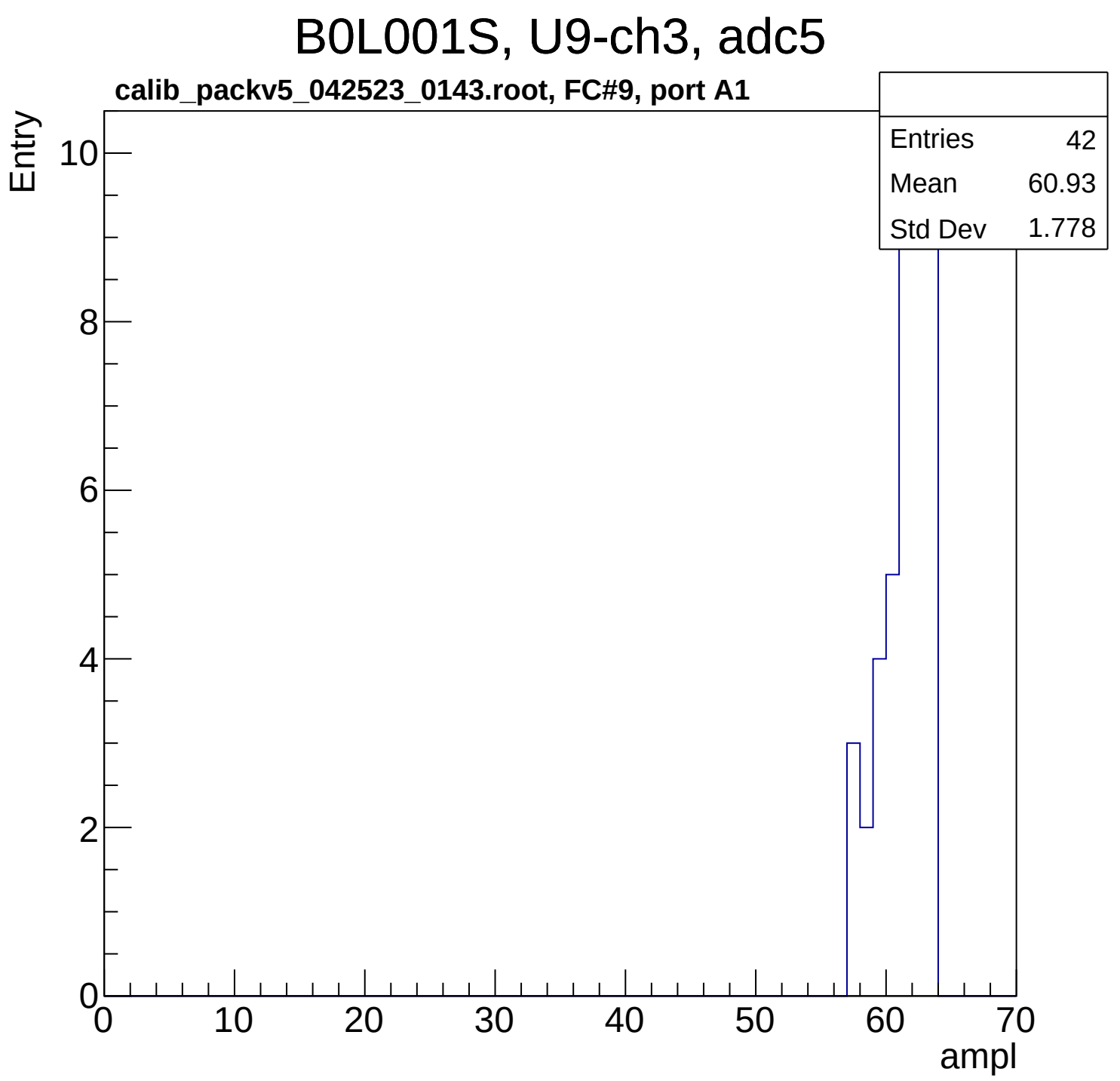
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch3, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 60.93
Std Dev 1.778
Entry
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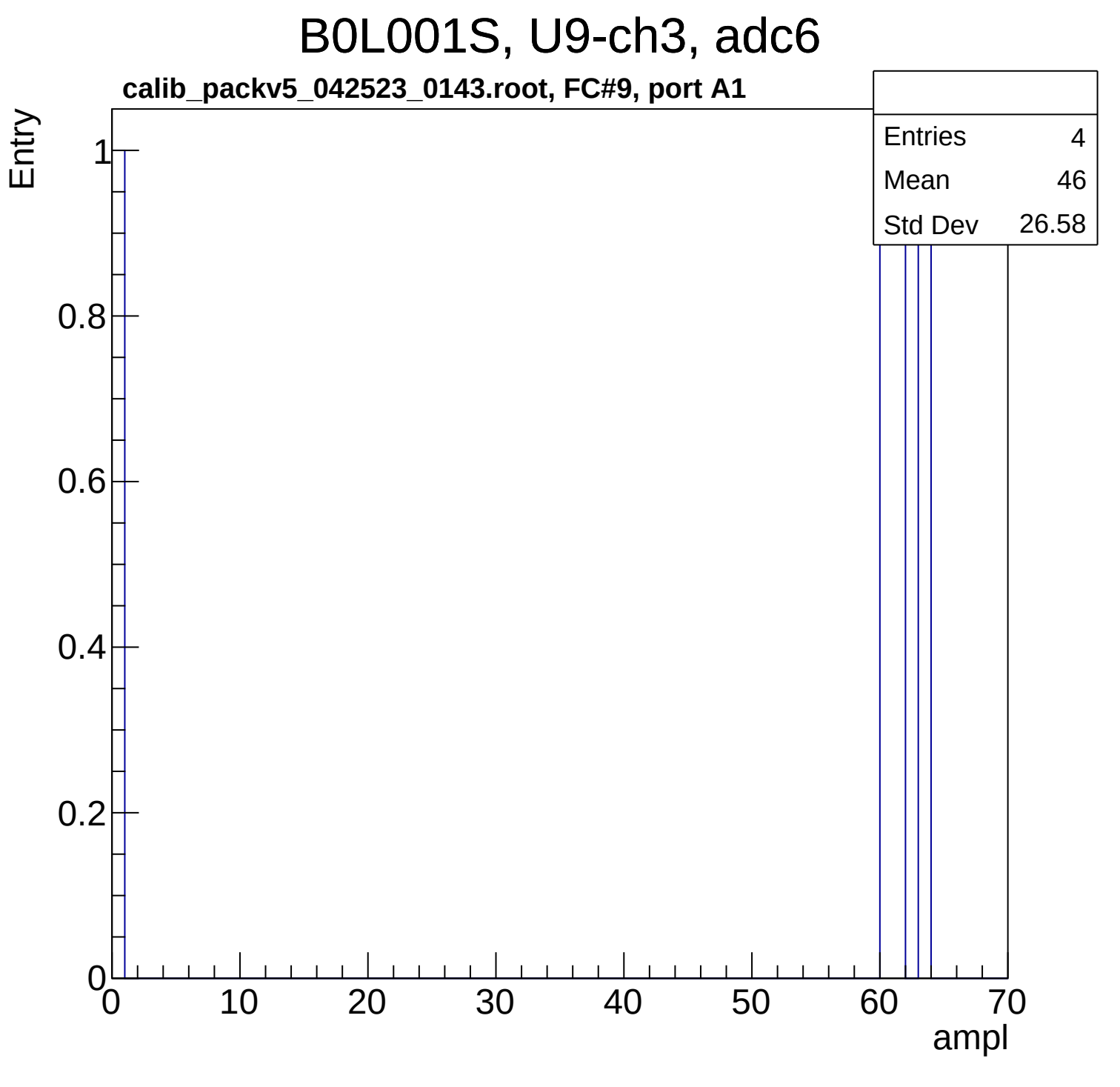
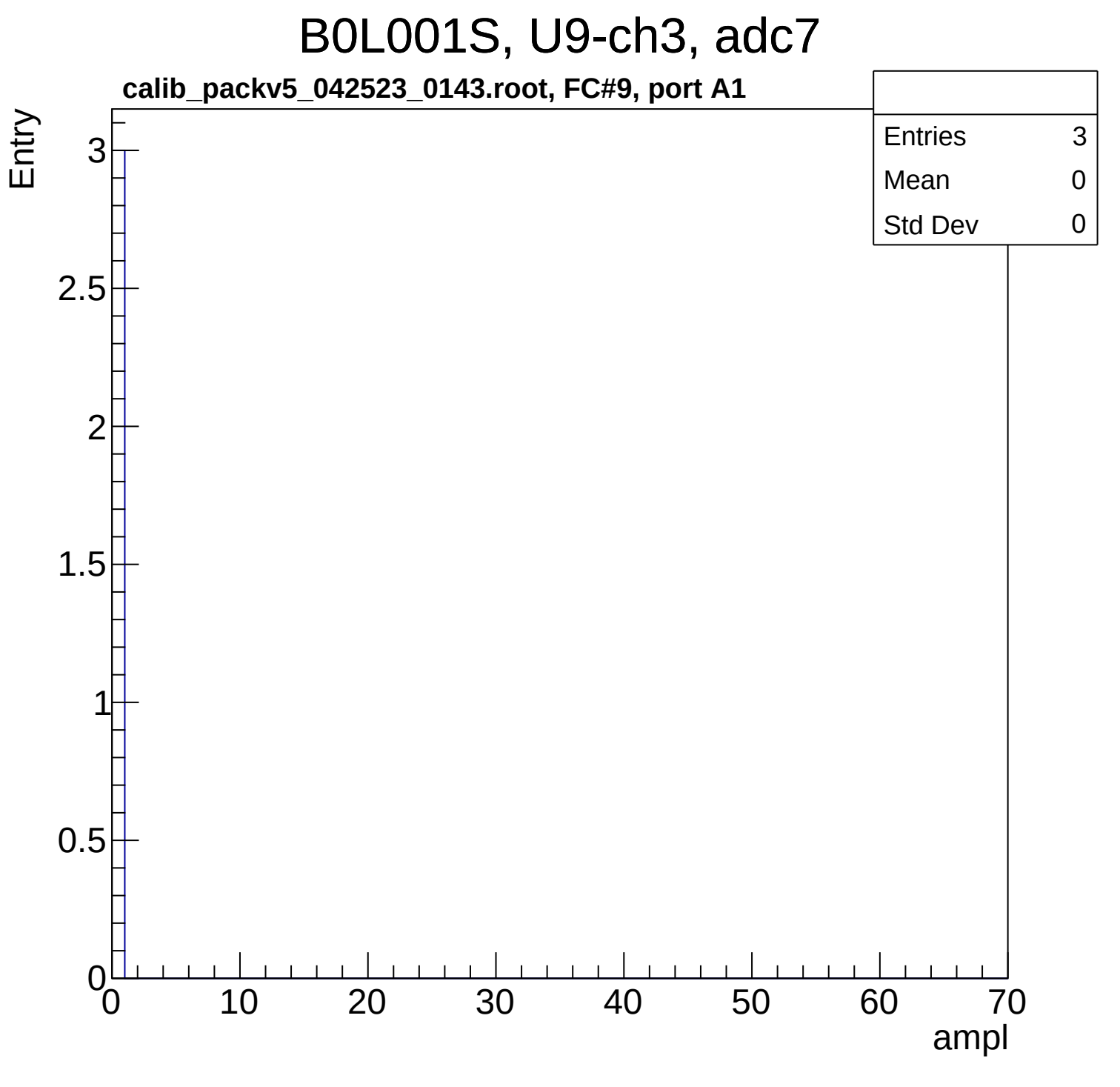
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch3, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46
Std Dev 26.58
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch3, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
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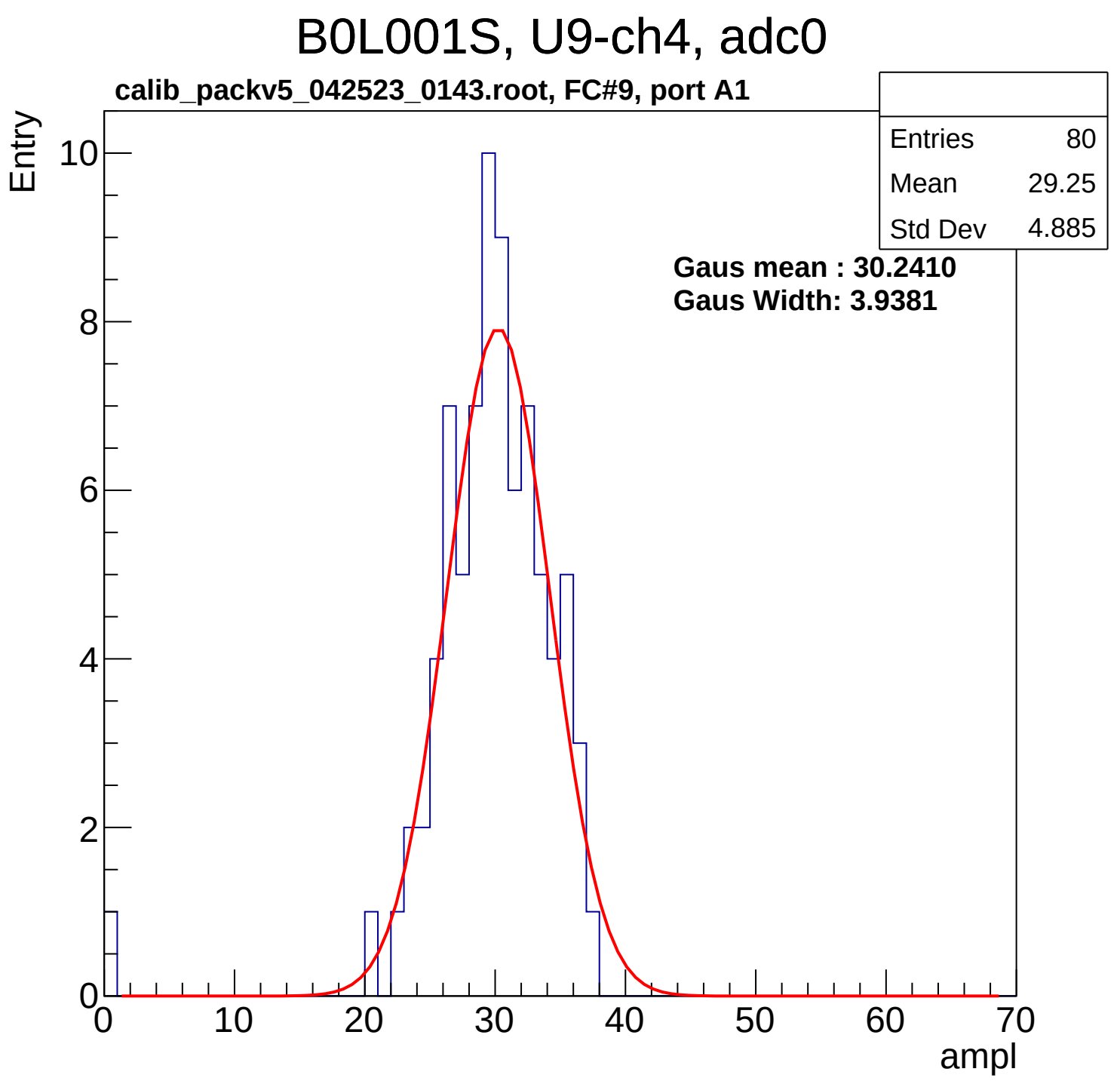
40
50
60
70
ampl


B0L001S, U9-ch4, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 29.25
Std Dev 4.885
Gaus mean : 30.2410
Gaus Width: 3.9381
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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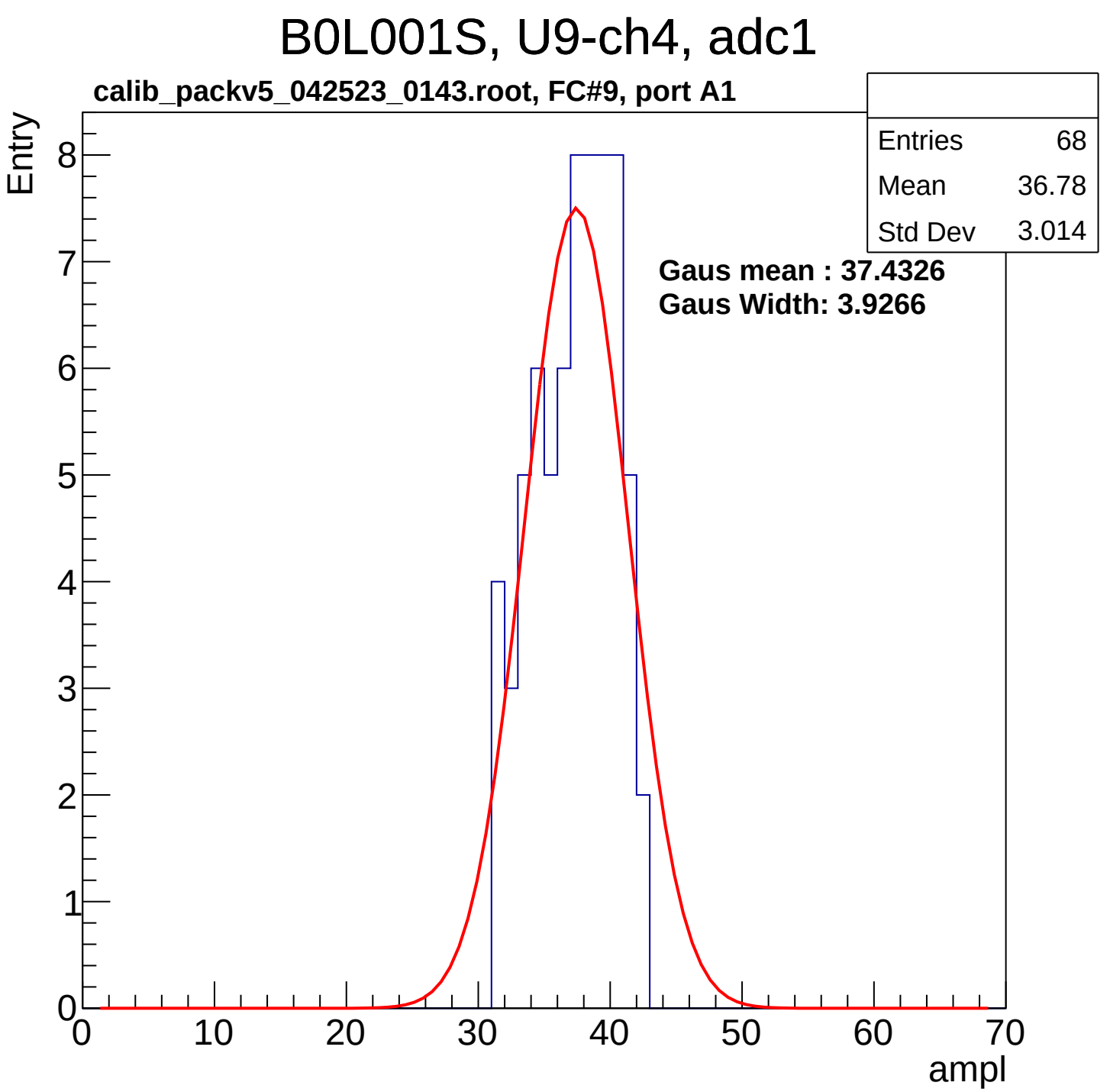
B0L001S, U9-ch4, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.78
Std Dev 3.014
Gaus mean : 37.4326
Gaus Width: 3.9266
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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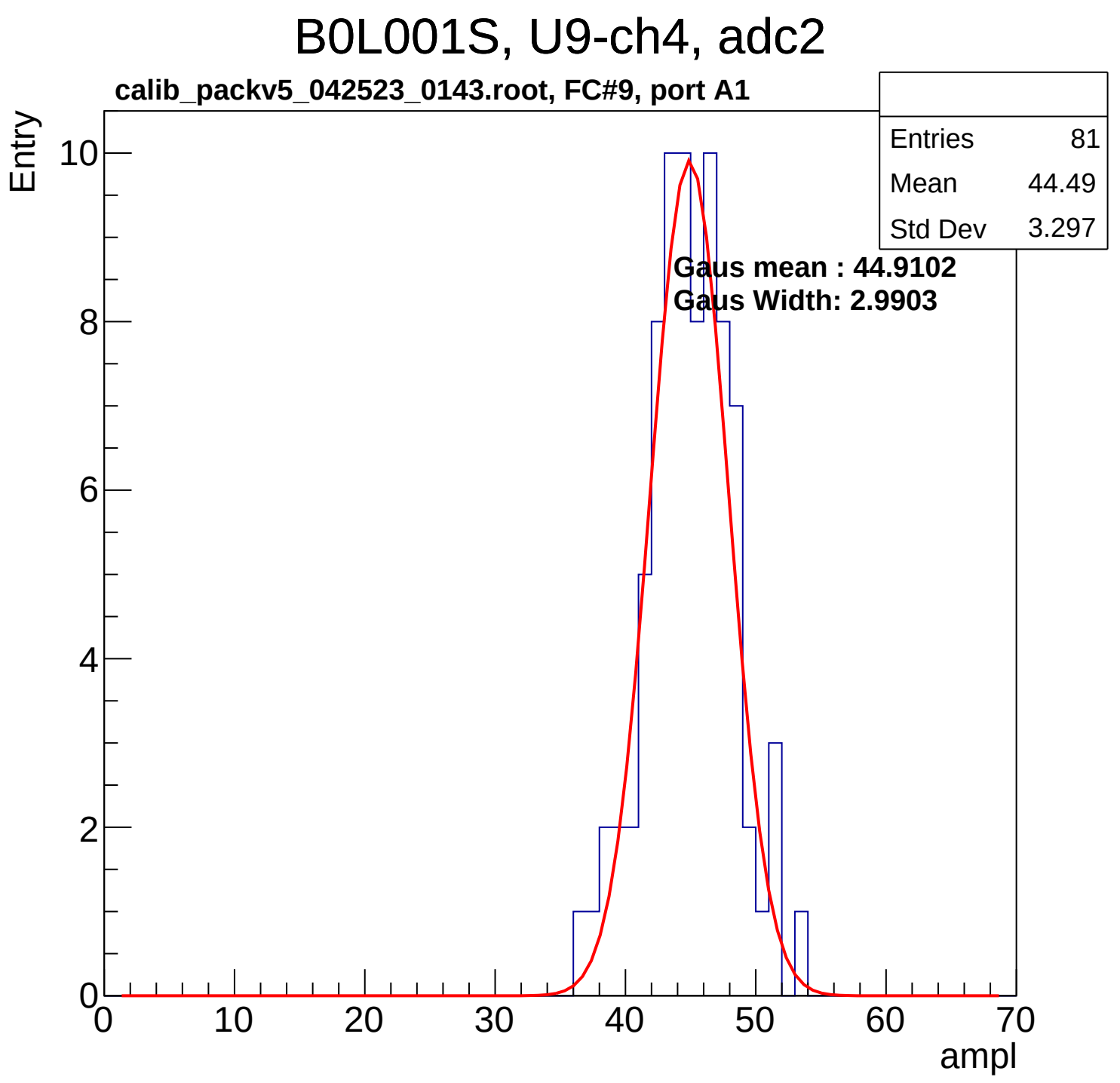
B0L001S, U9-ch4, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 44.49
Std Dev 3.297
Gaus mean : 44.9102
Gaus Width: 2.9903
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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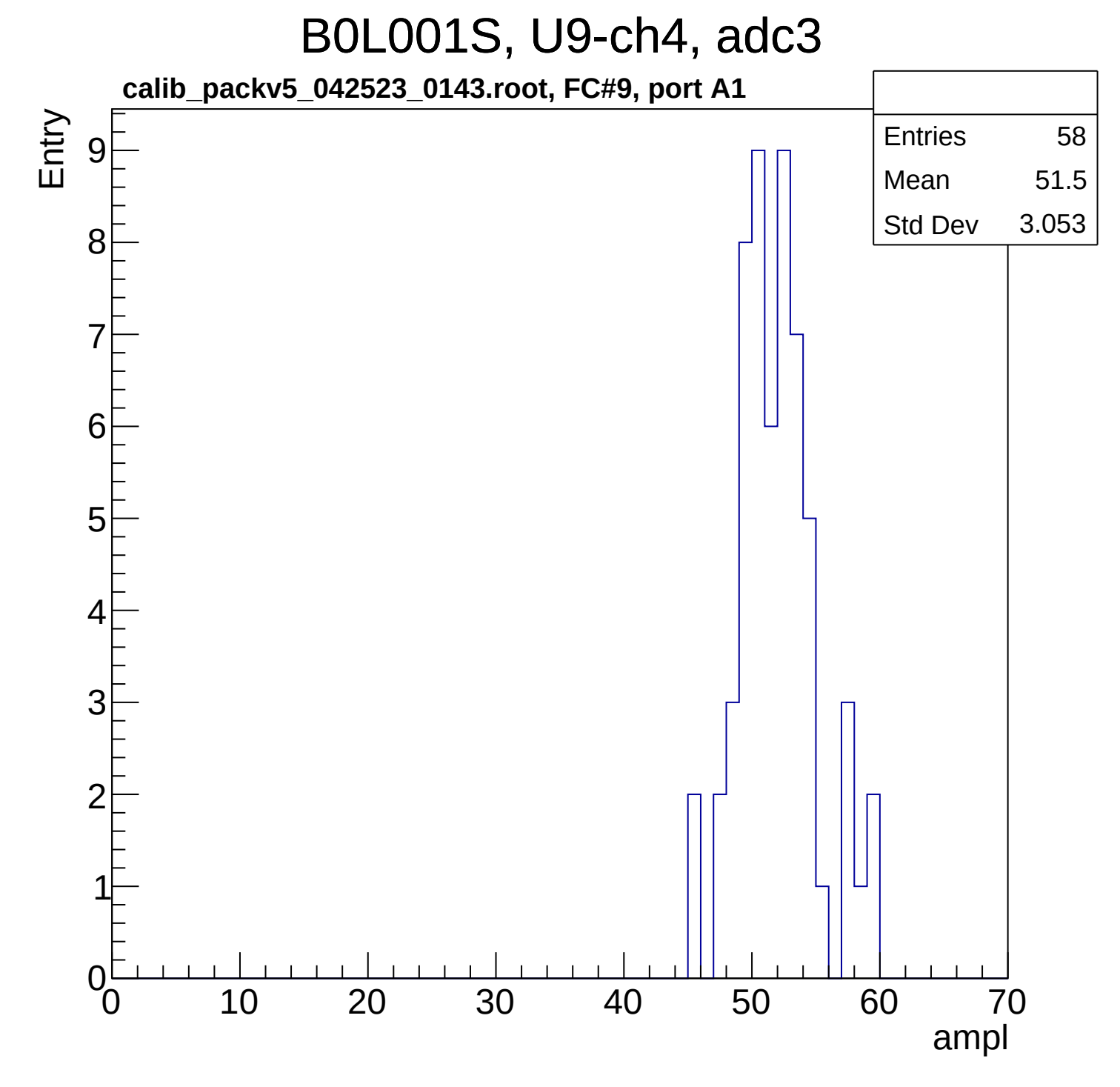
B0L001S, U9-ch4, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 51.5
Std Dev 3.053
Entry
ampl
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2
3
4
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6
7
8
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0
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60
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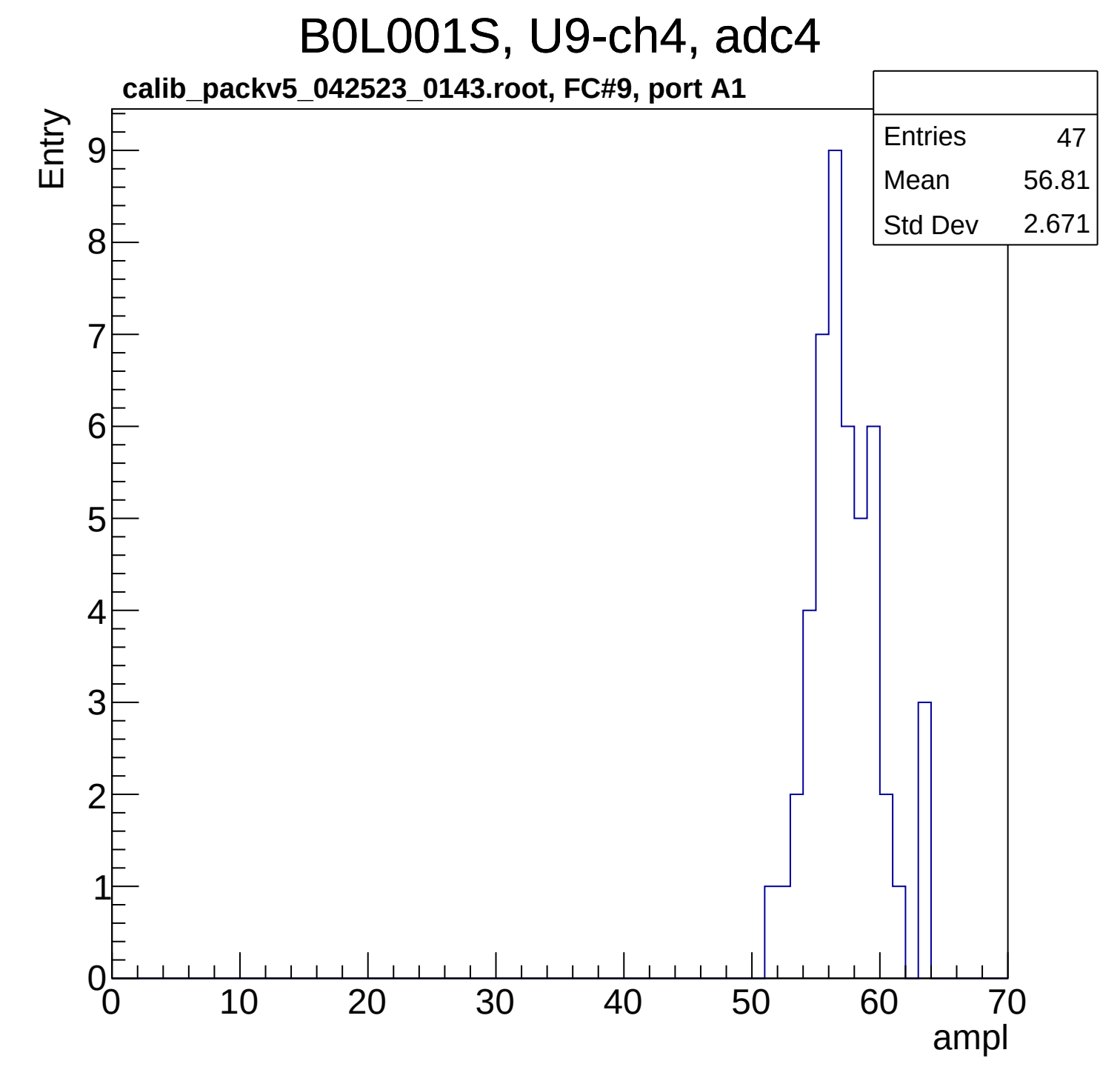
B0L001S, U9-ch4, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.81
Std Dev 2.671
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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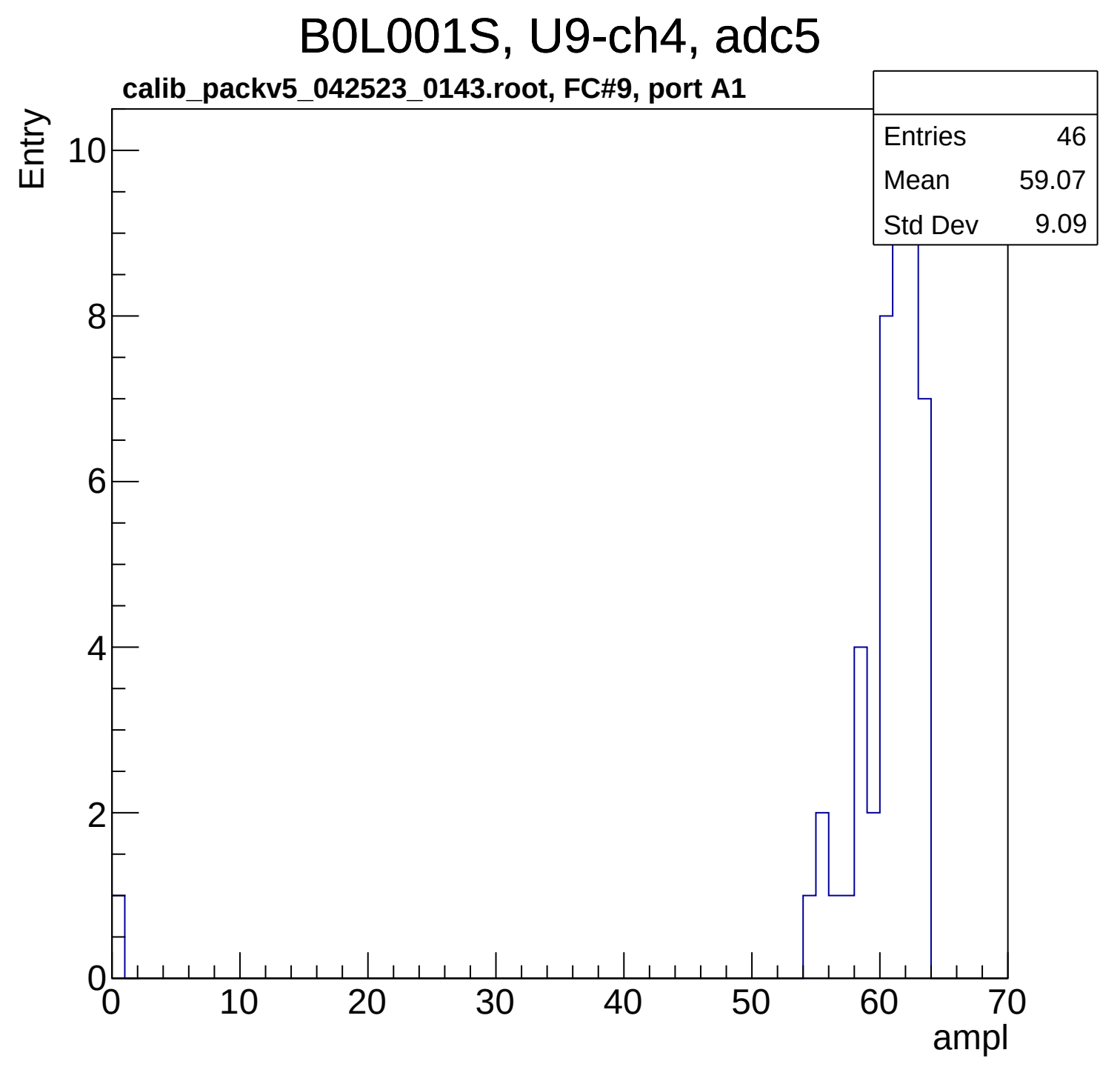
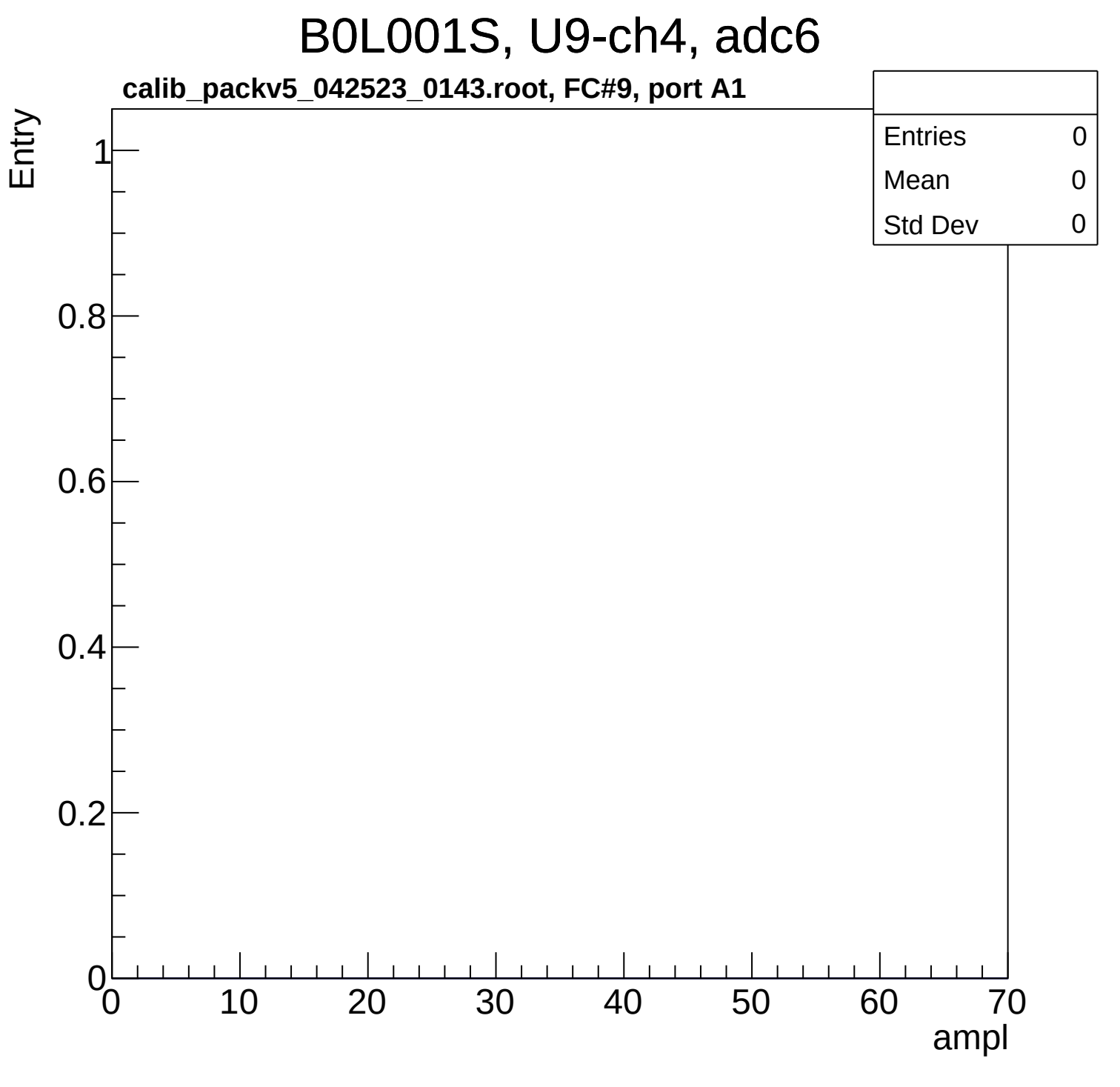
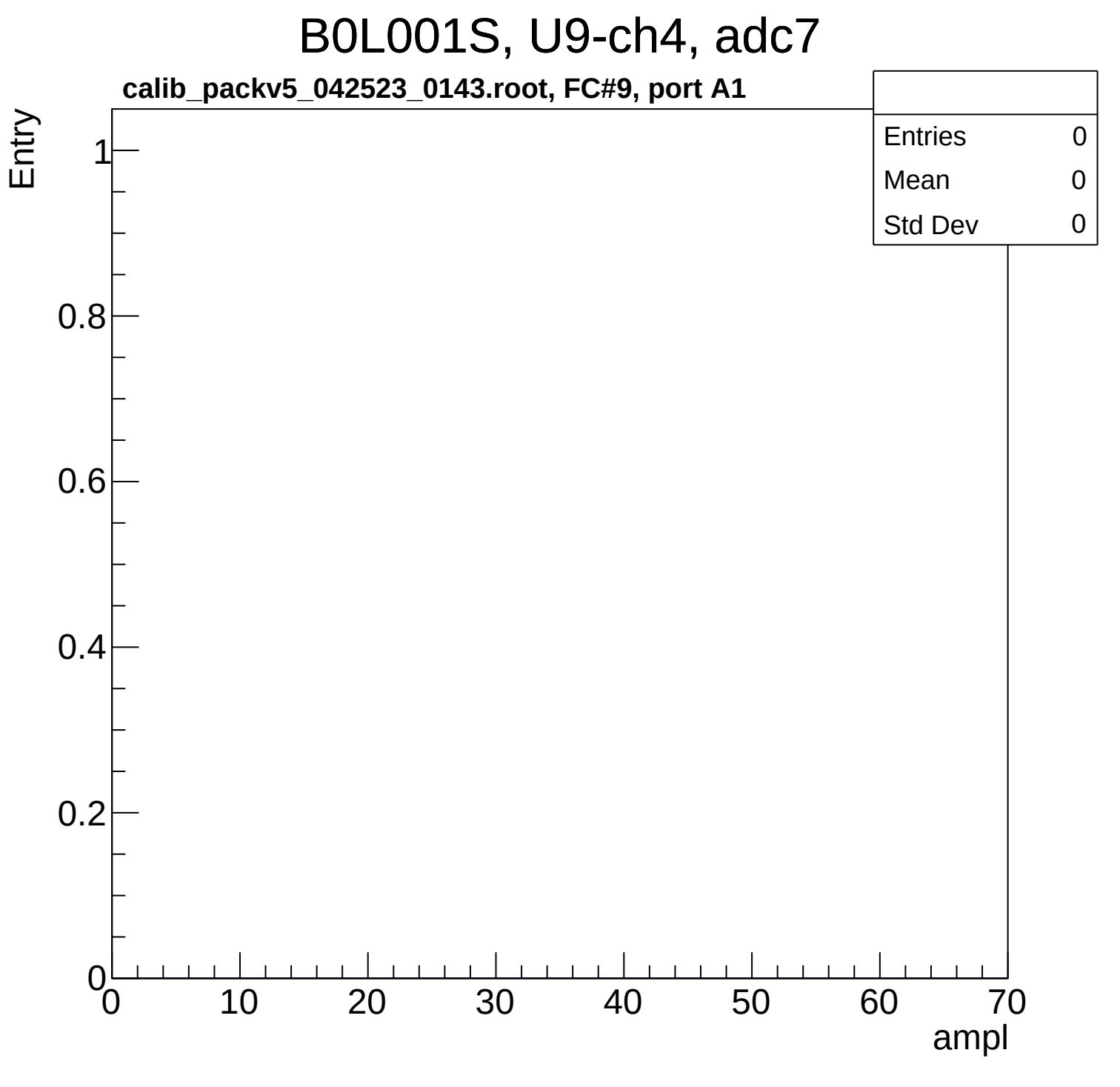
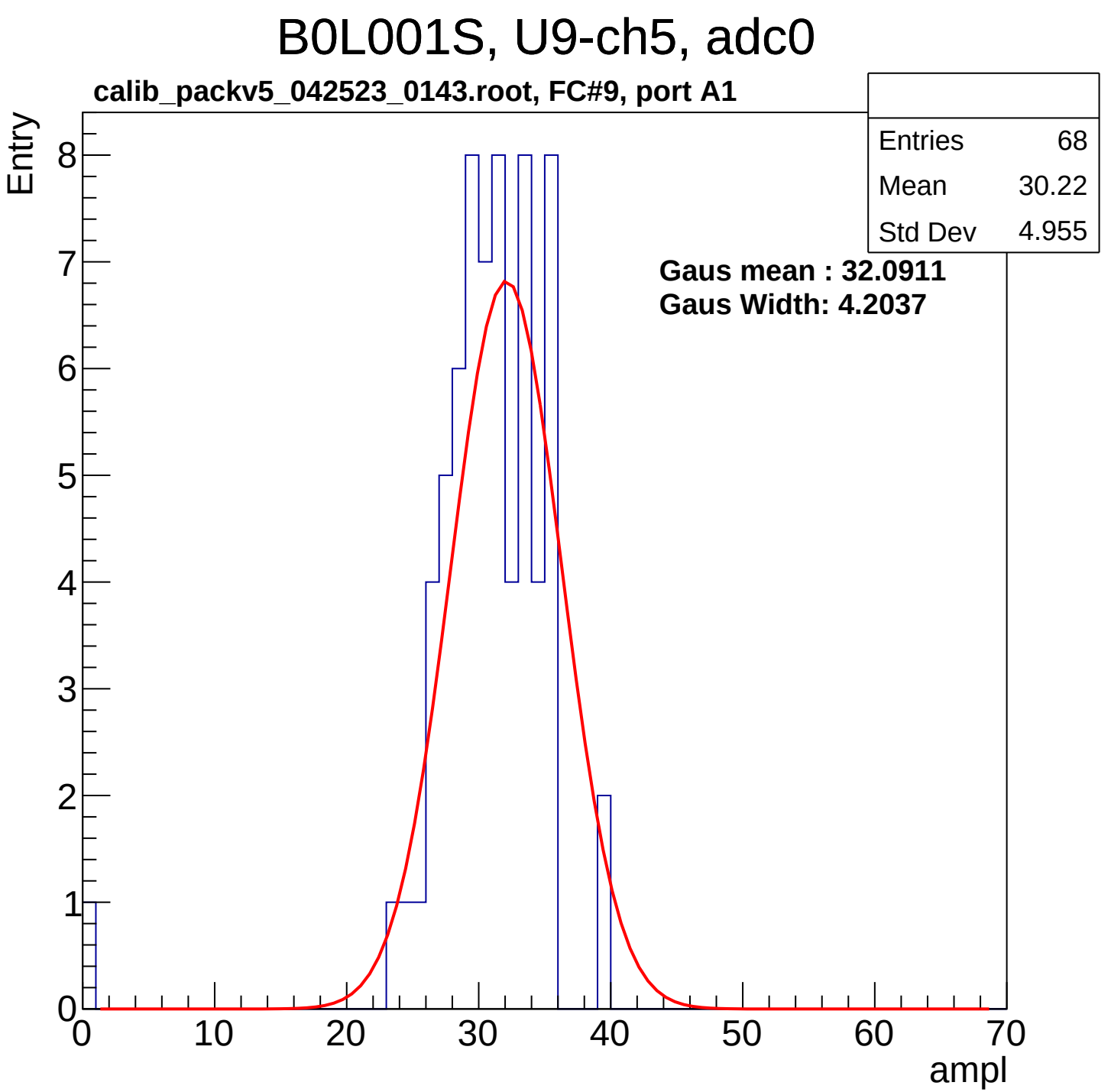
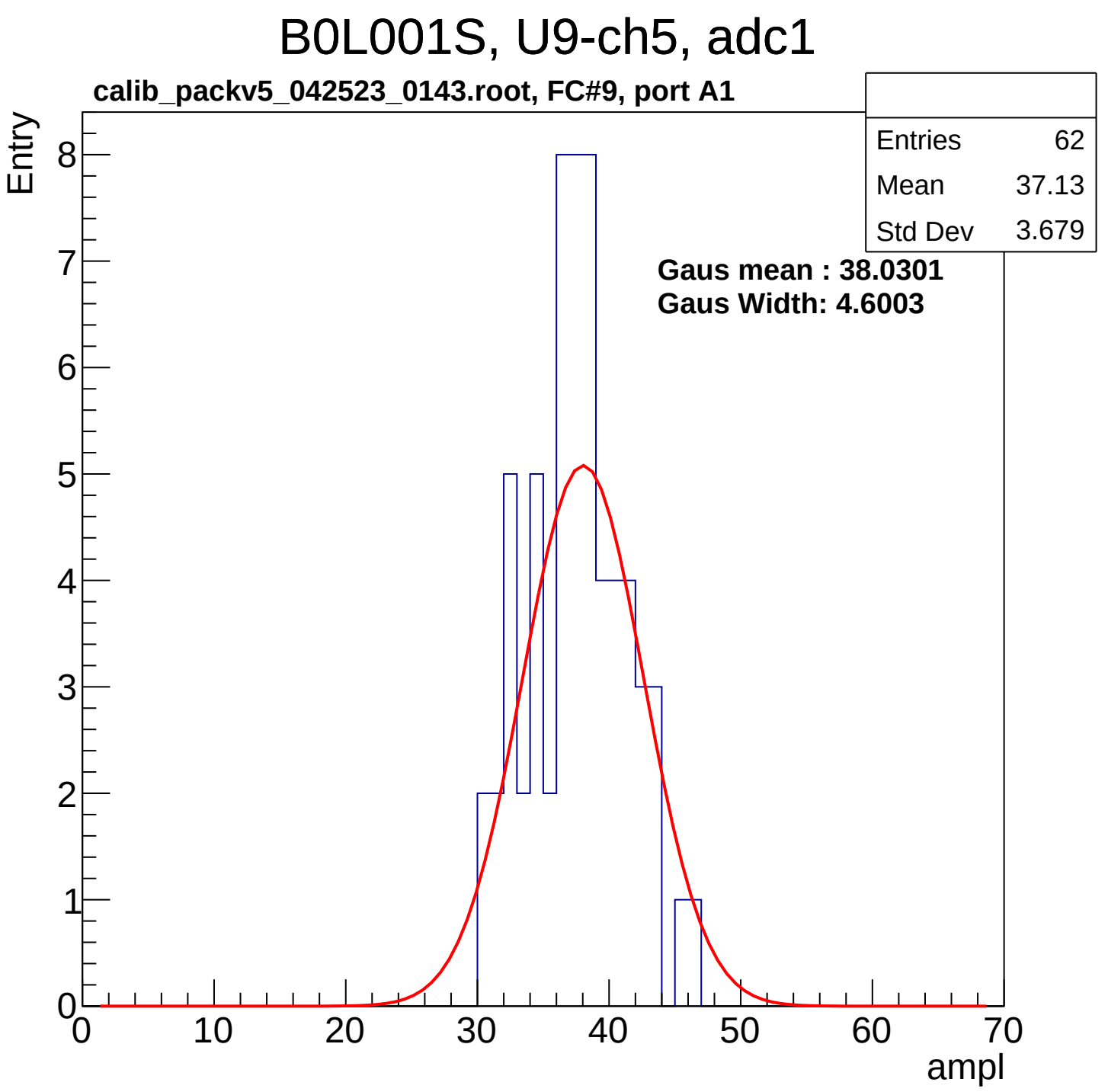
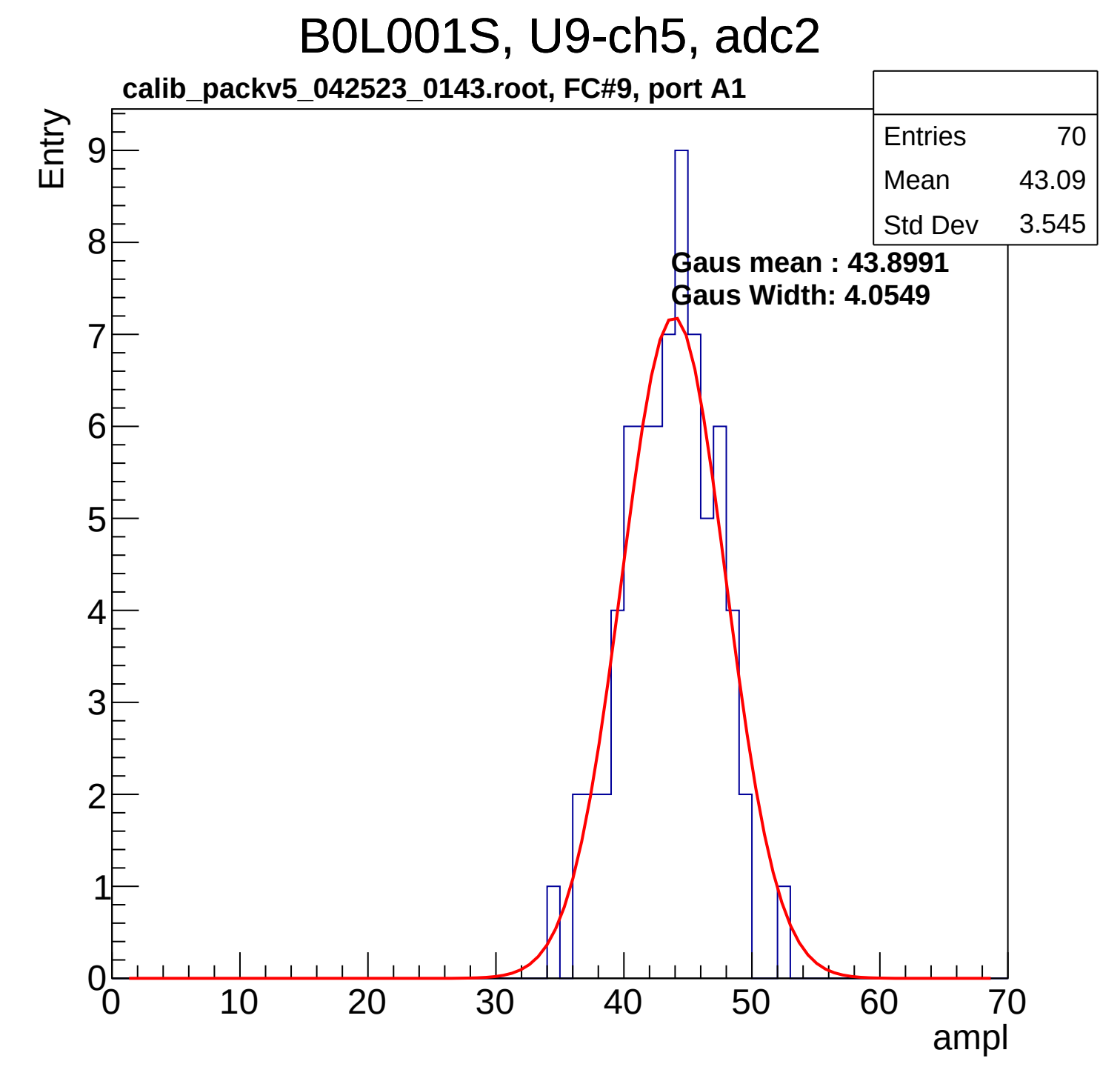

B0L001S, U9-ch4, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 59.07
Std Dev 9.09
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

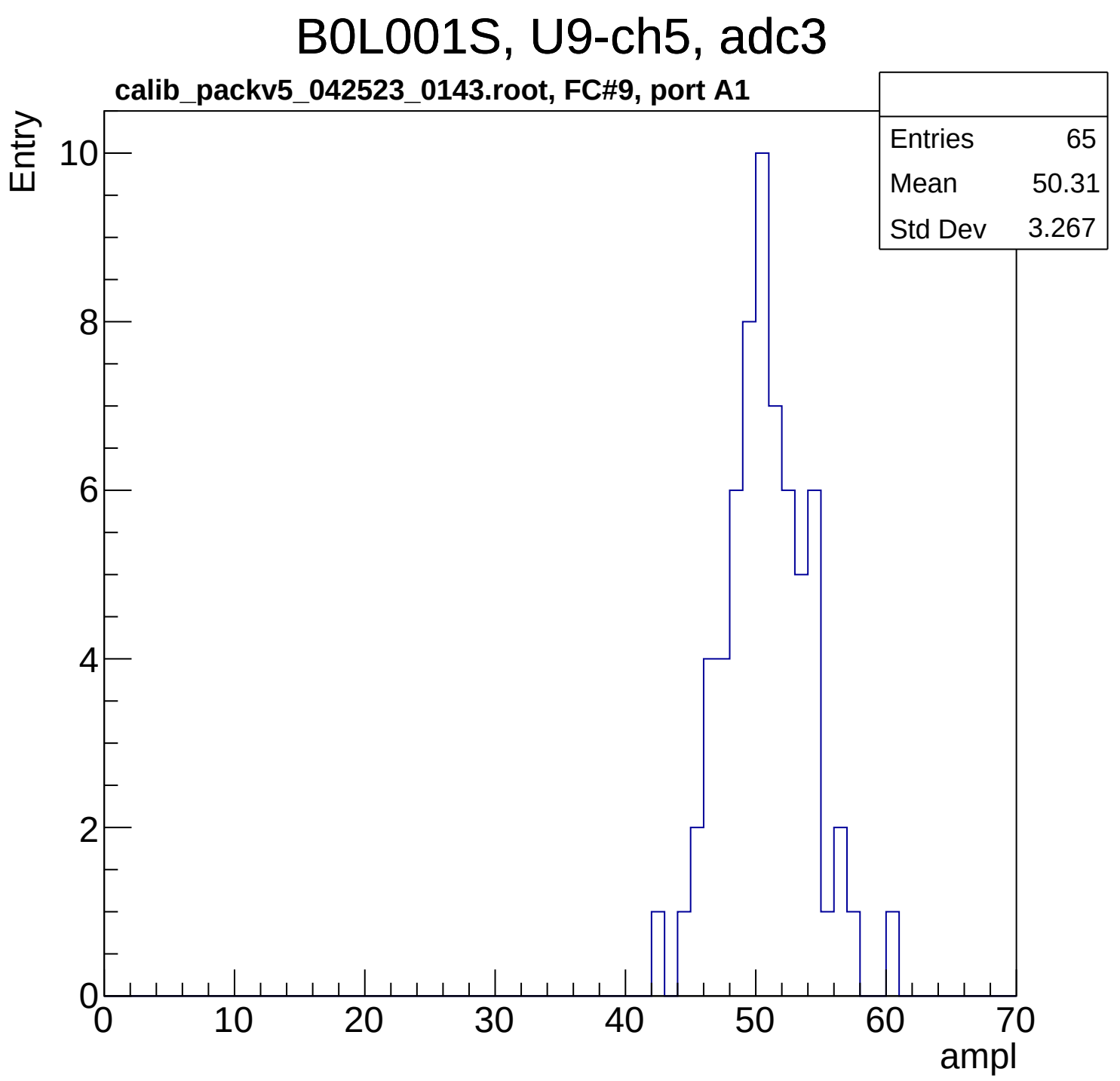

B0L001S, U9-ch4, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

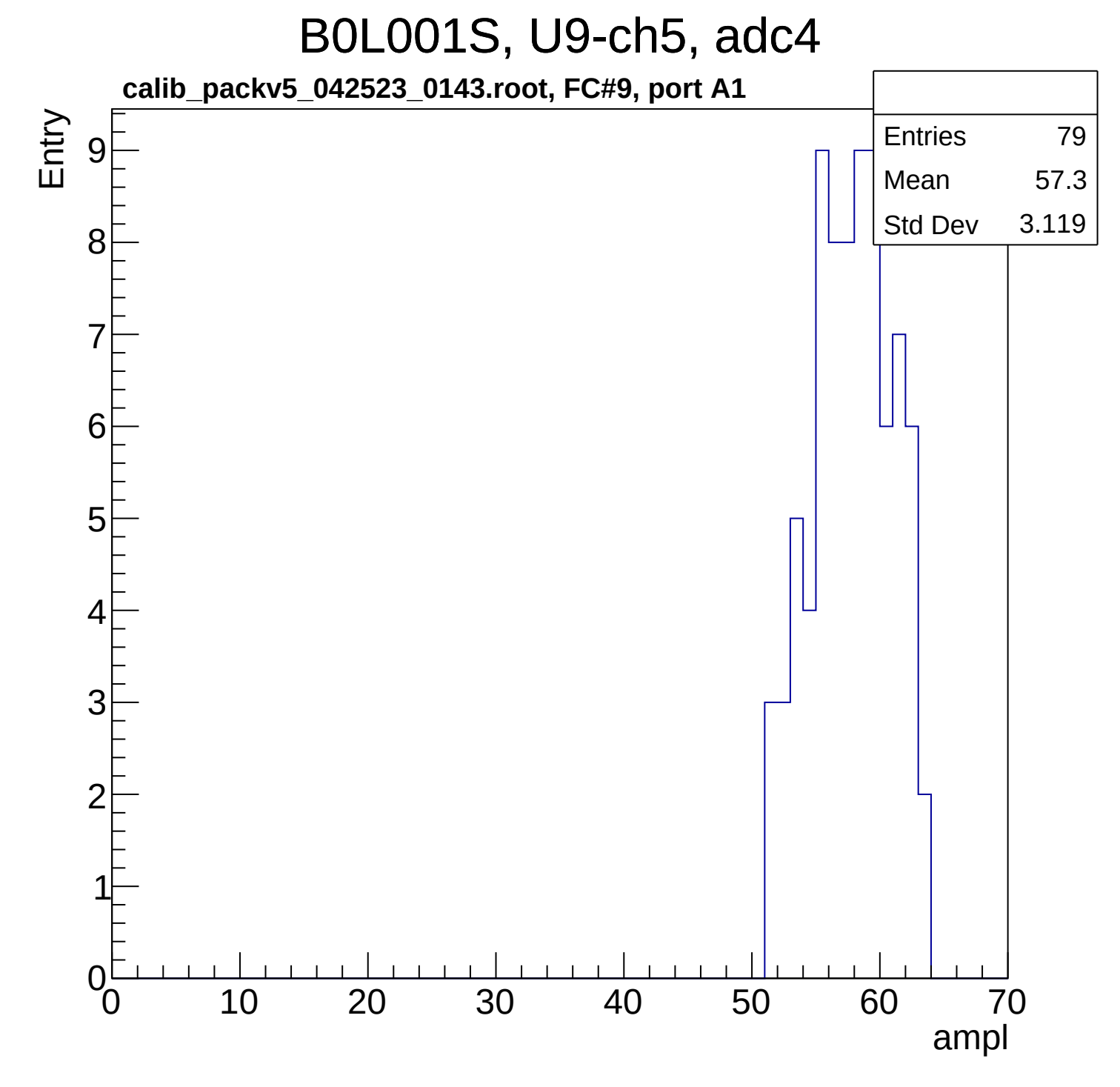

B0L001S, U9-ch4, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

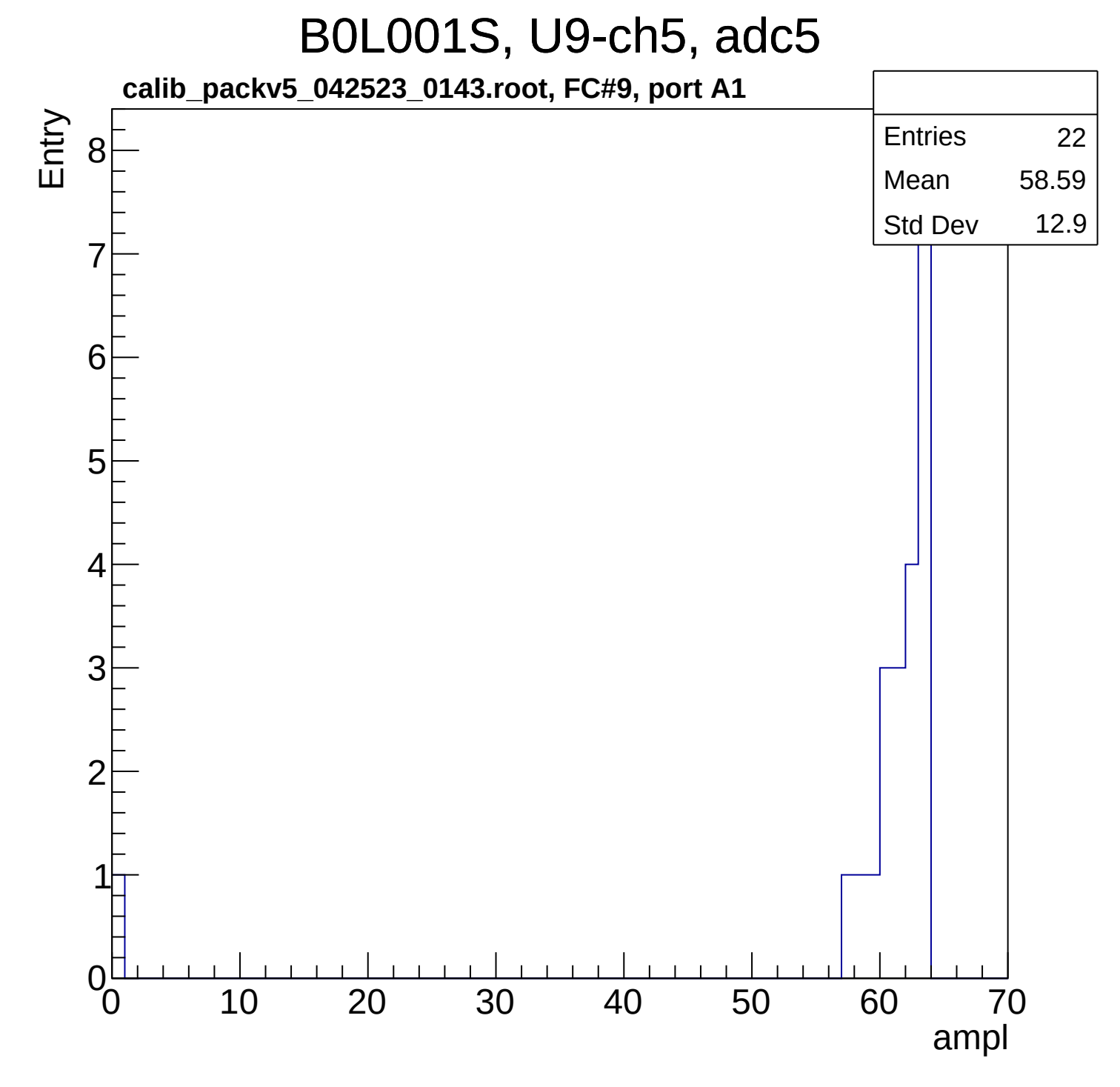

B0L001S, U9-ch5, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.22
Std Dev 4.955
Gaus mean : 32.0911
Gaus Width: 4.2037
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

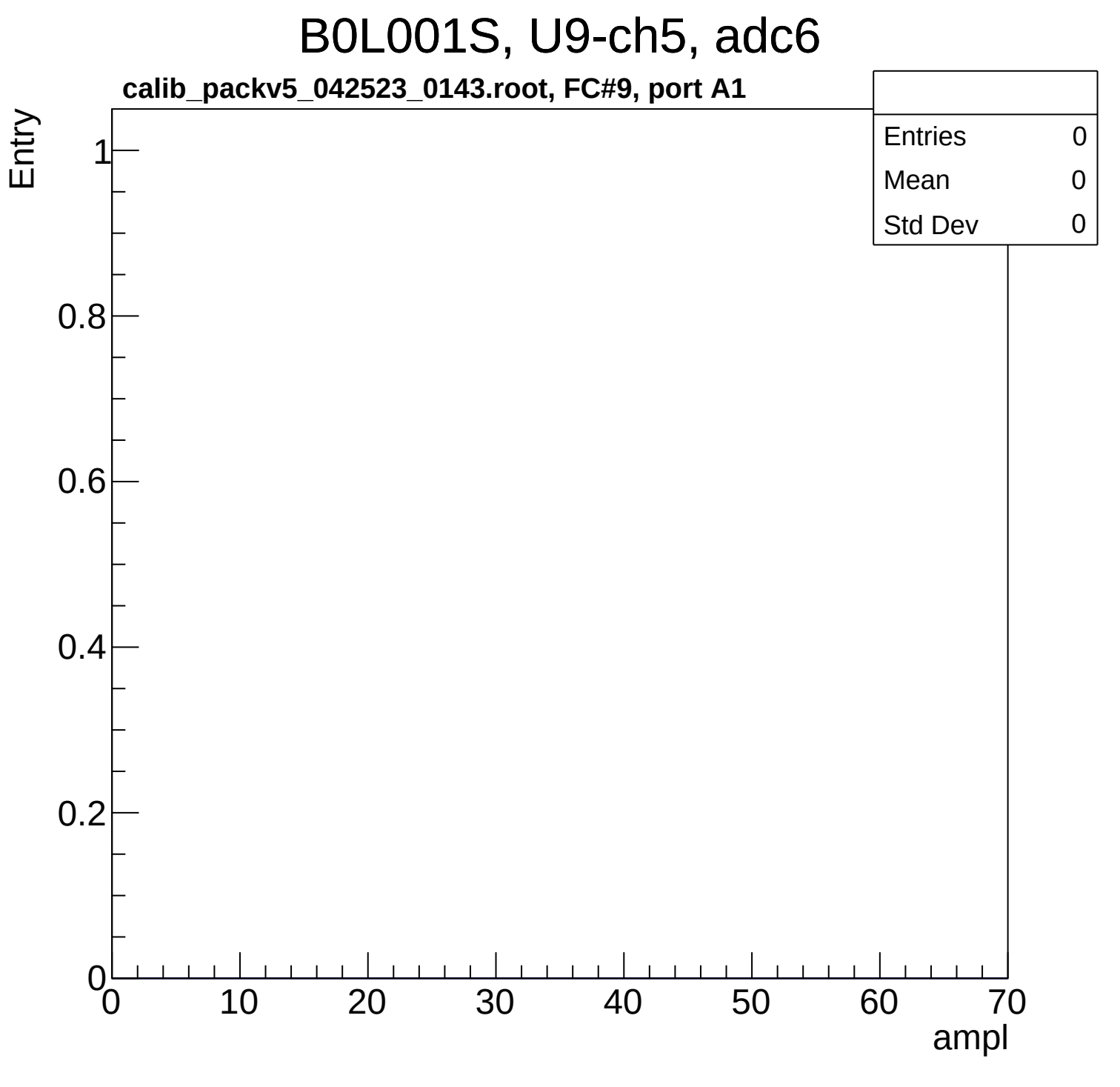

B0L001S, U9-ch5, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.13
Std Dev 3.679
Gaus mean : 38.0301
Gaus Width: 4.6003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

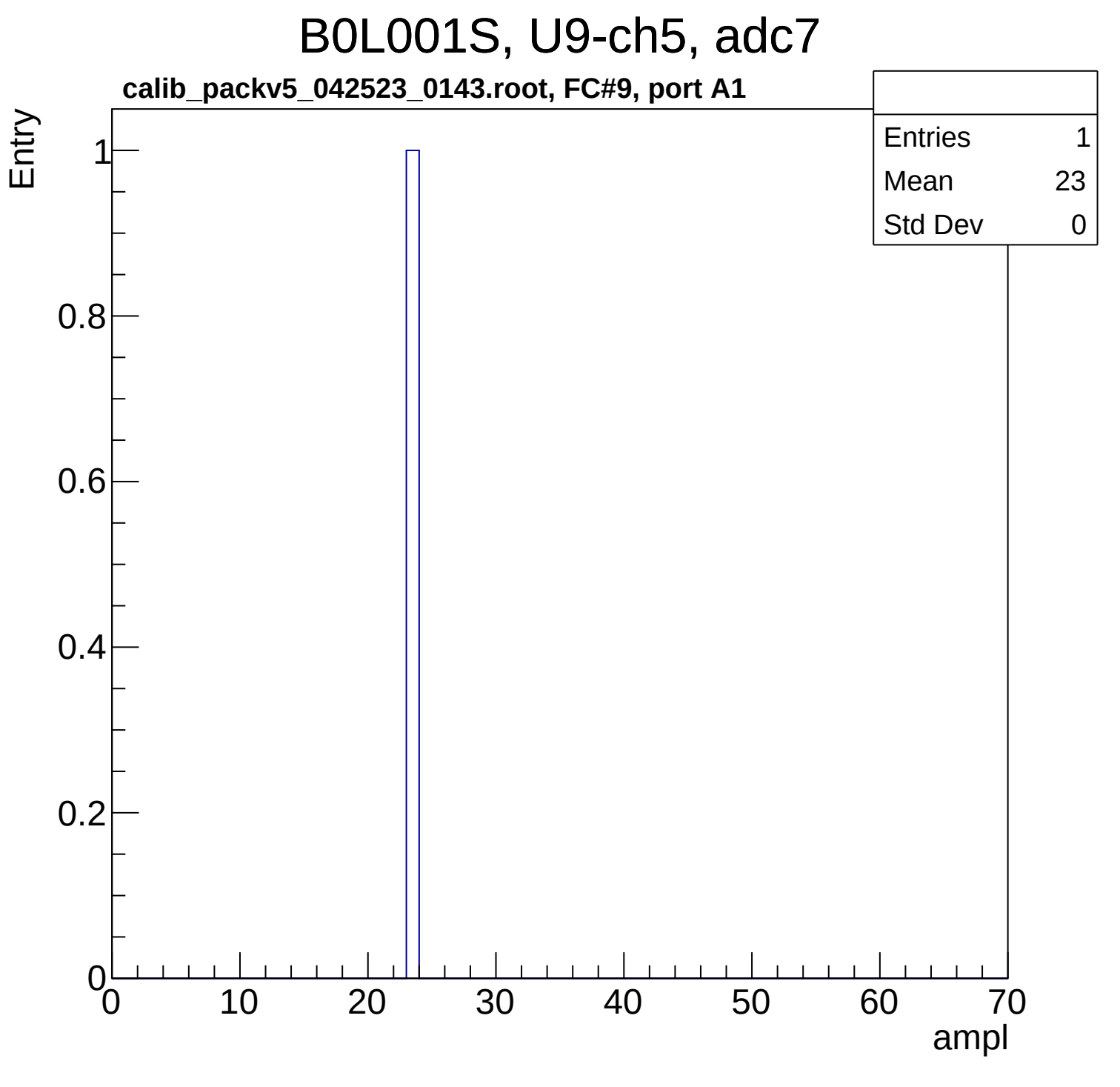

B0L001S, U9-ch5, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.09
Std Dev 3.545
Gaus mean : 43.8991
Gaus Width: 4.0549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

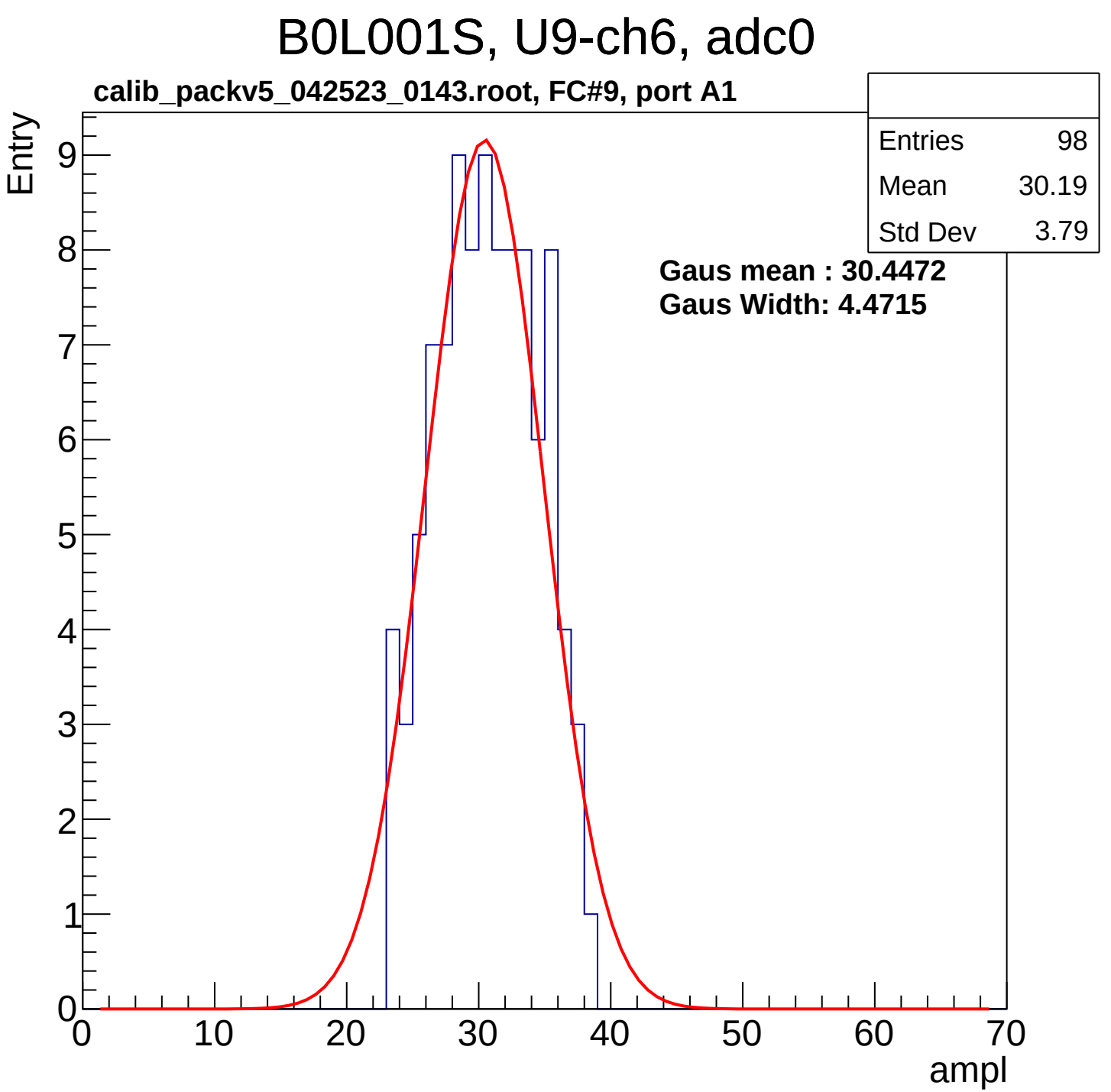

B0L001S, U9-ch5, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.31
Std Dev 3.267
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

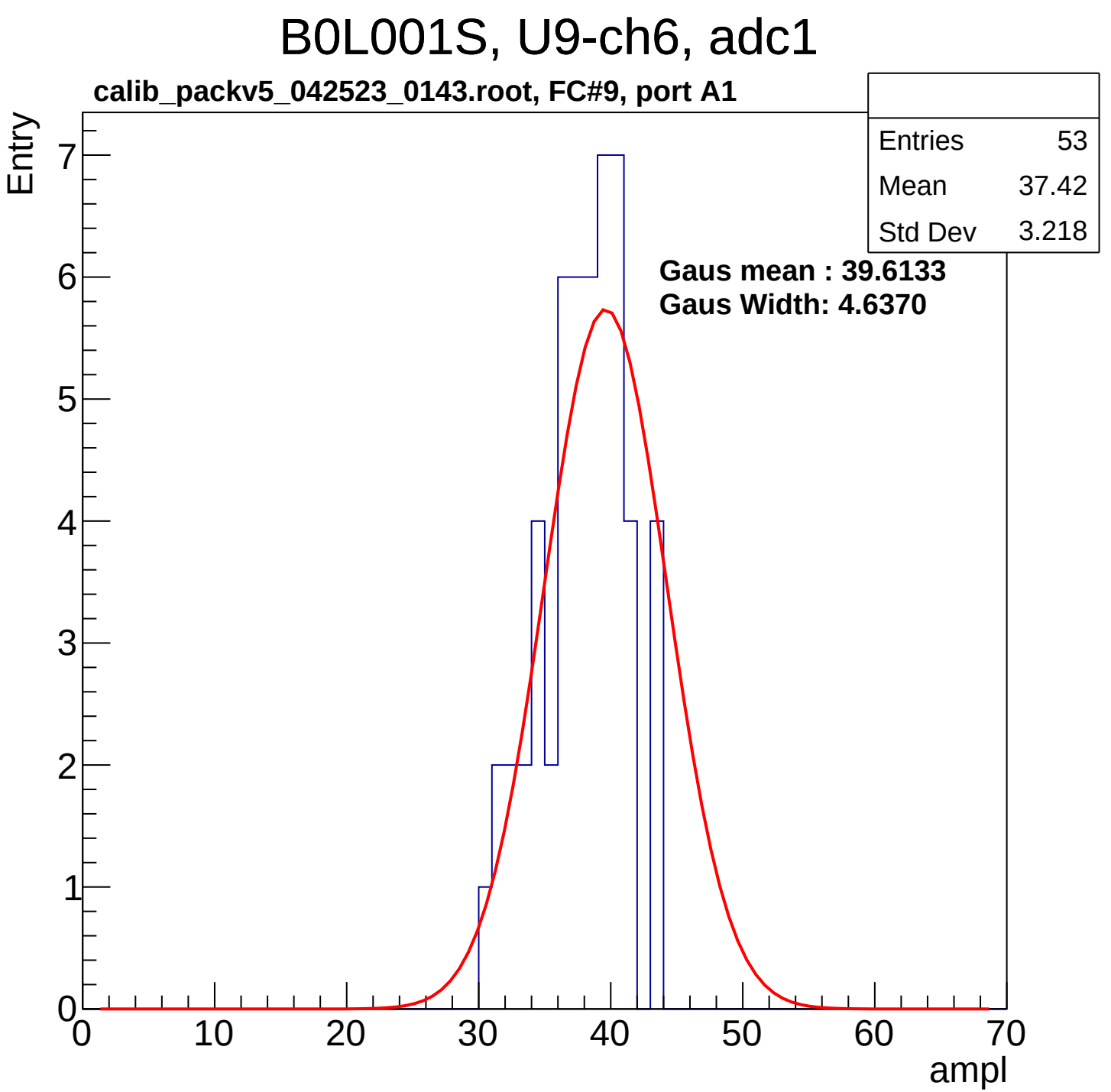

B0L001S, U9-ch5, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 57.3
Std Dev 3.119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

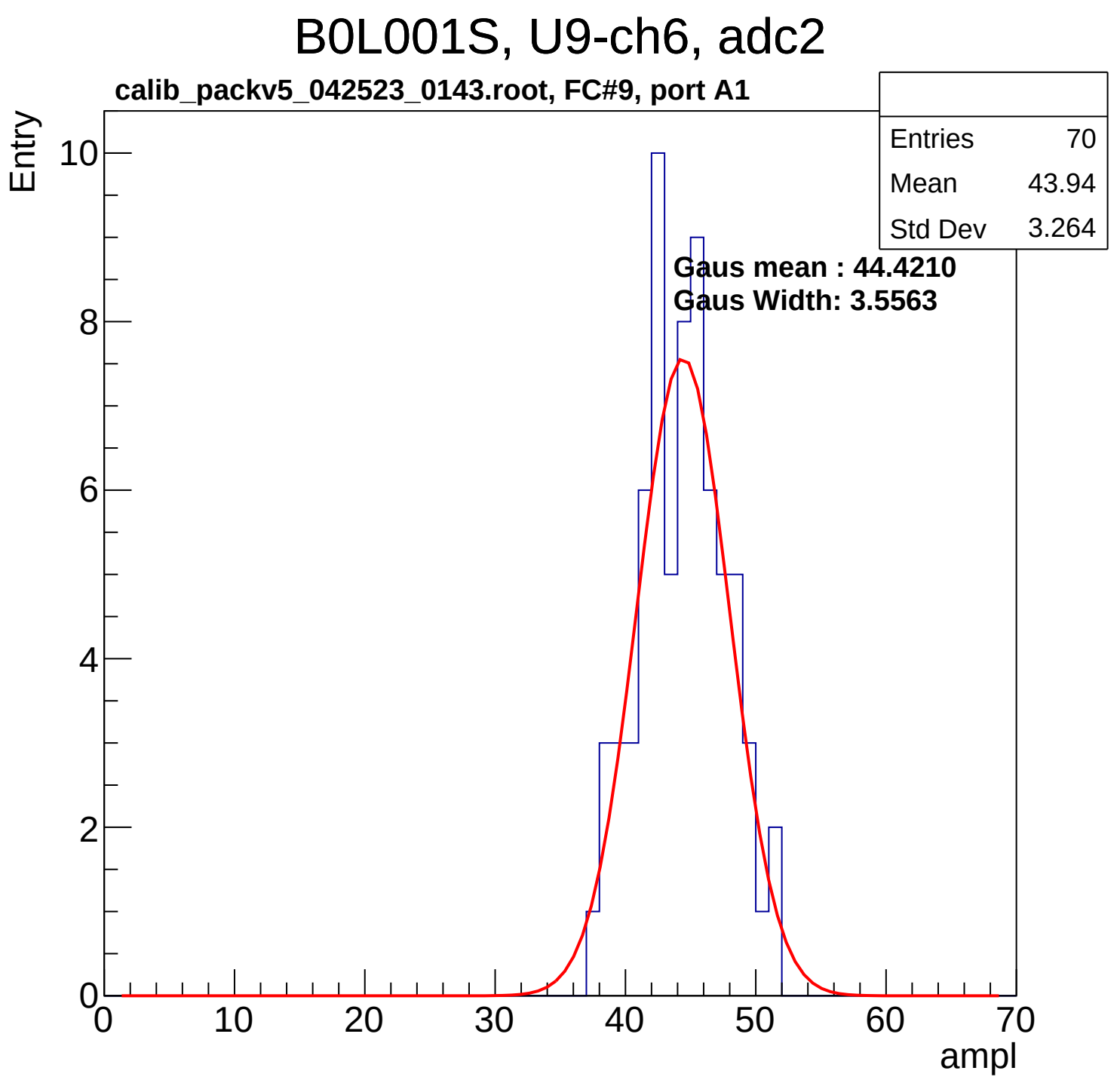

B0L001S, U9-ch5, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 22
Mean 58.59
Std Dev 12.9
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

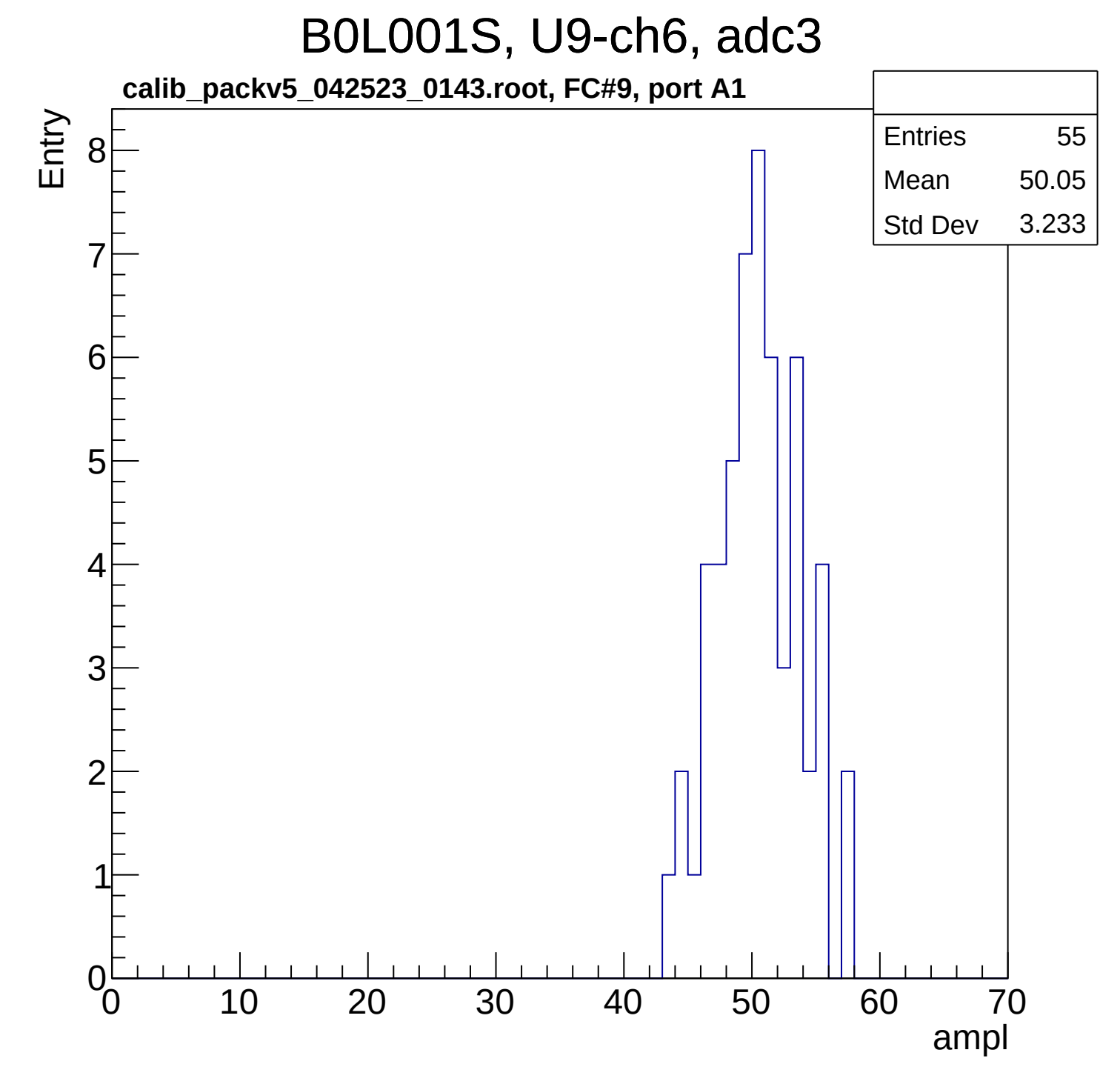

B0L001S, U9-ch5, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch5, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch6, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 98
Mean 30.19
Std Dev 3.79
Gaus mean : 30.4472
Gaus Width: 4.4715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch6, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 37.42
Std Dev 3.218
Gaus mean : 39.6133
Gaus Width: 4.6370
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch6, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.94
Std Dev 3.264
Gaus mean : 44.4210
Gaus Width: 3.5563
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch6, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 50.05
Std Dev 3.233
Entry
ampl
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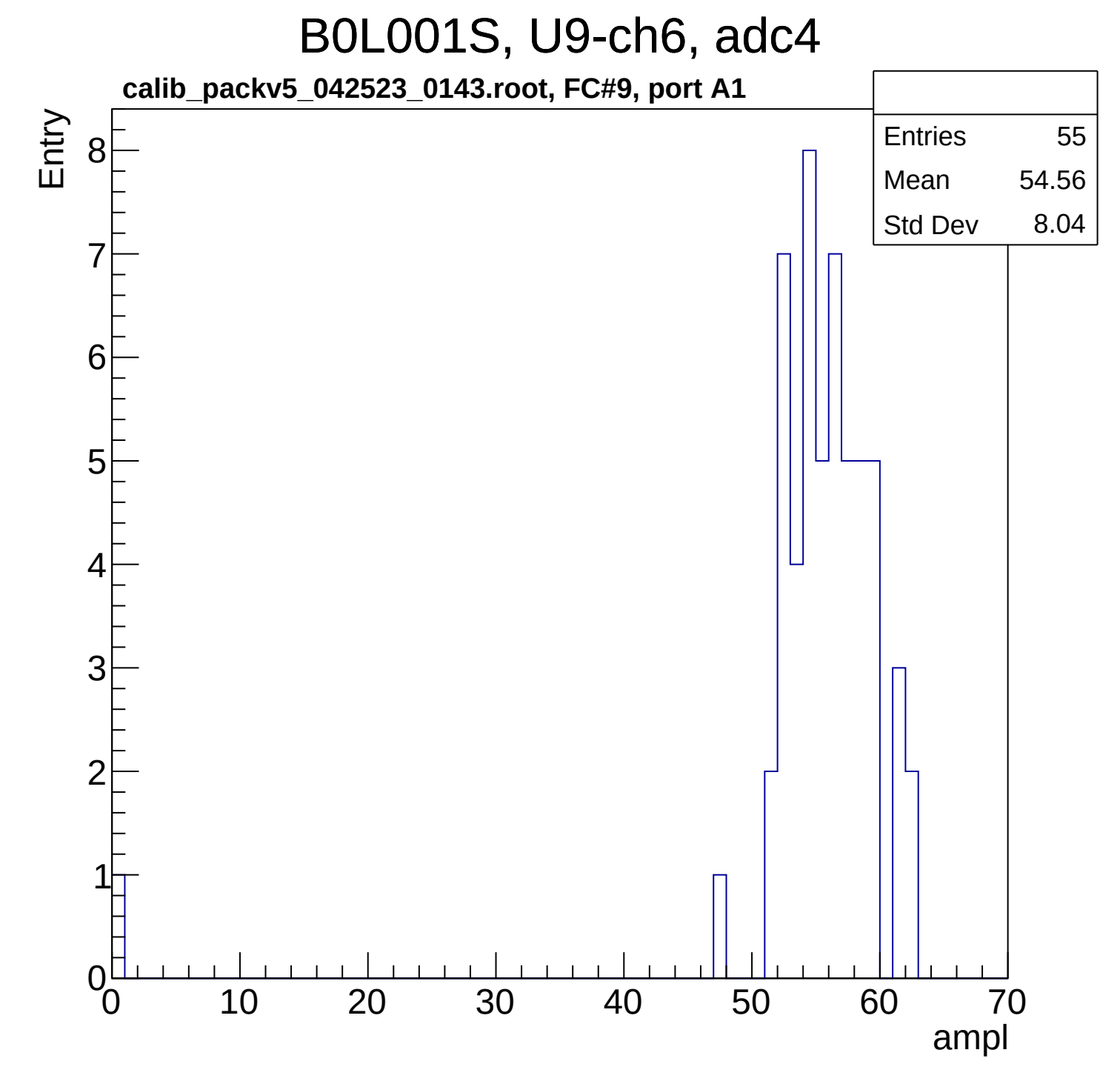
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch6, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 54.56
Std Dev 8.04
Entry
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2
3
4
5
6
7
8
0
10
20
30
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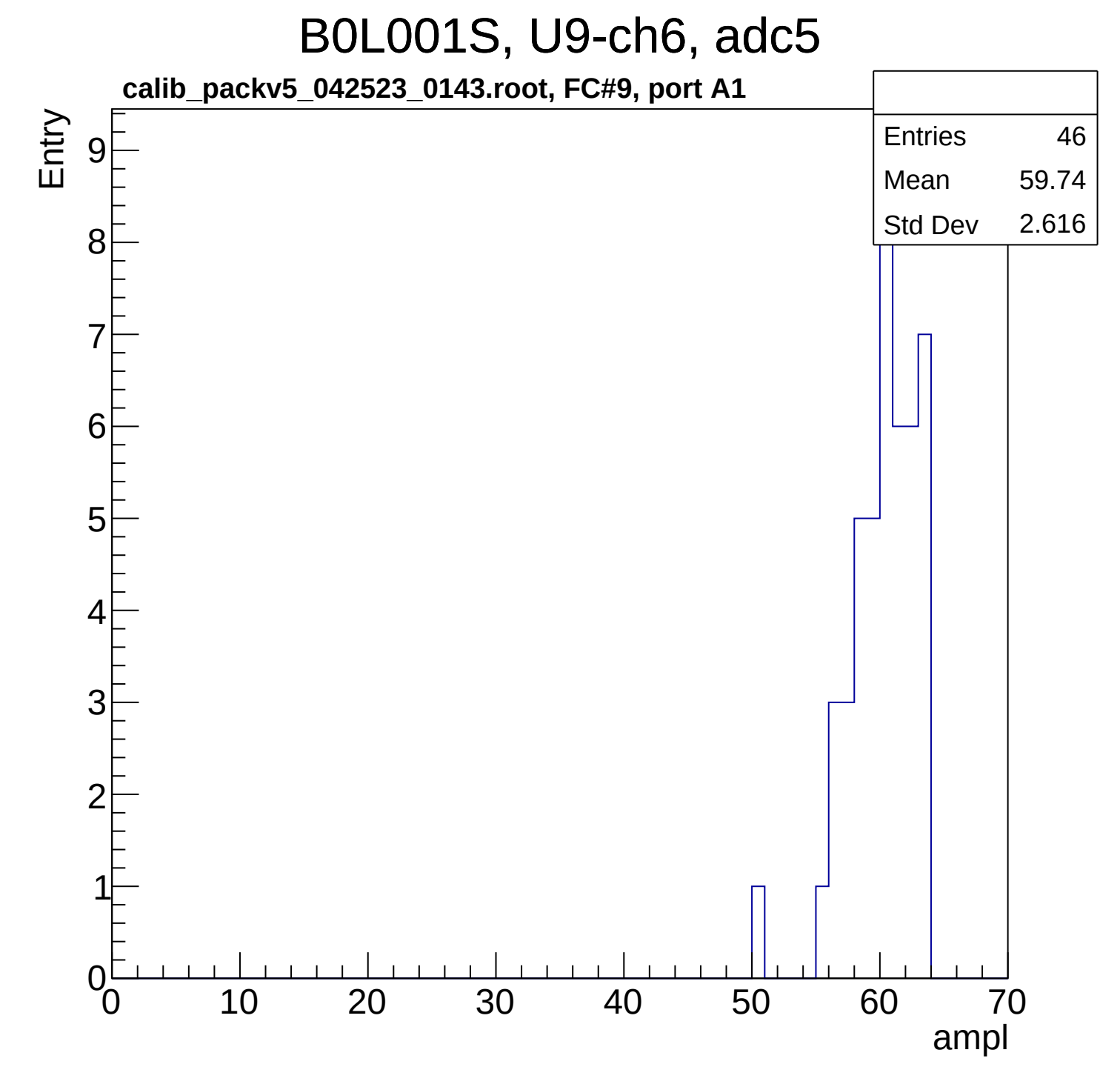
50
60
70
ampl


B0L001S, U9-ch6, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 59.74
Std Dev 2.616
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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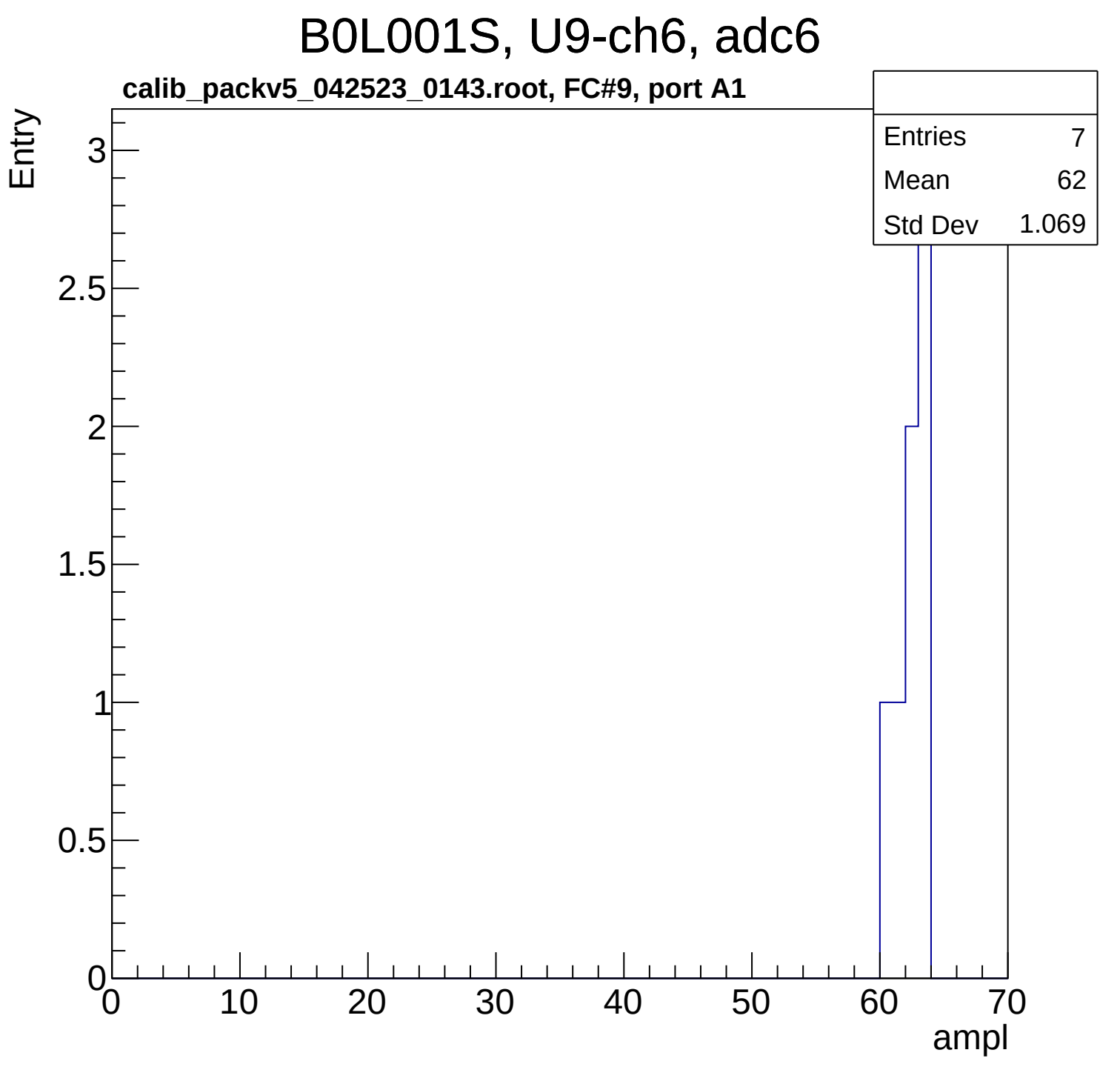
B0L001S, U9-ch6, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62
Std Dev 1.069
Entry
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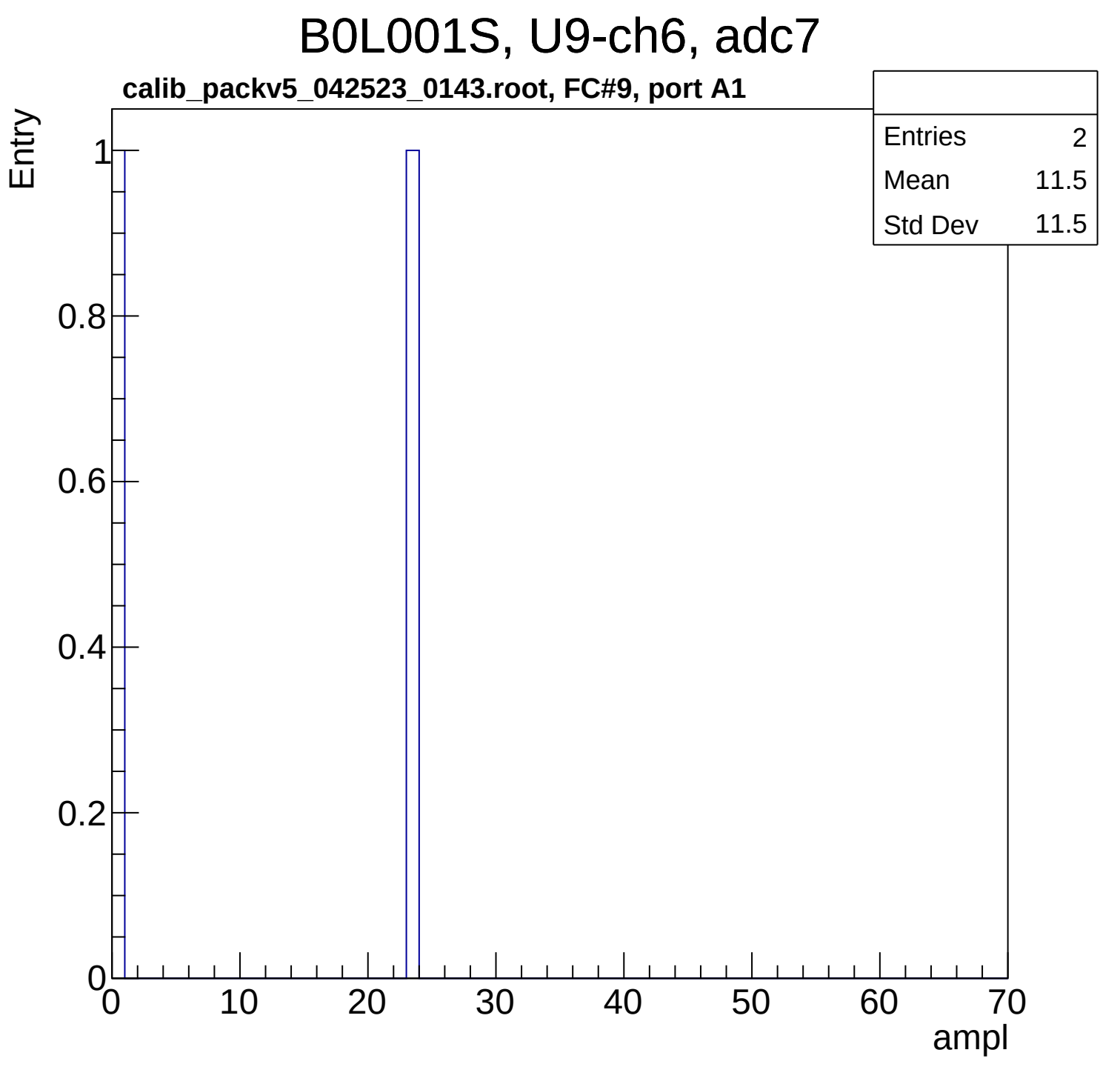
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch6, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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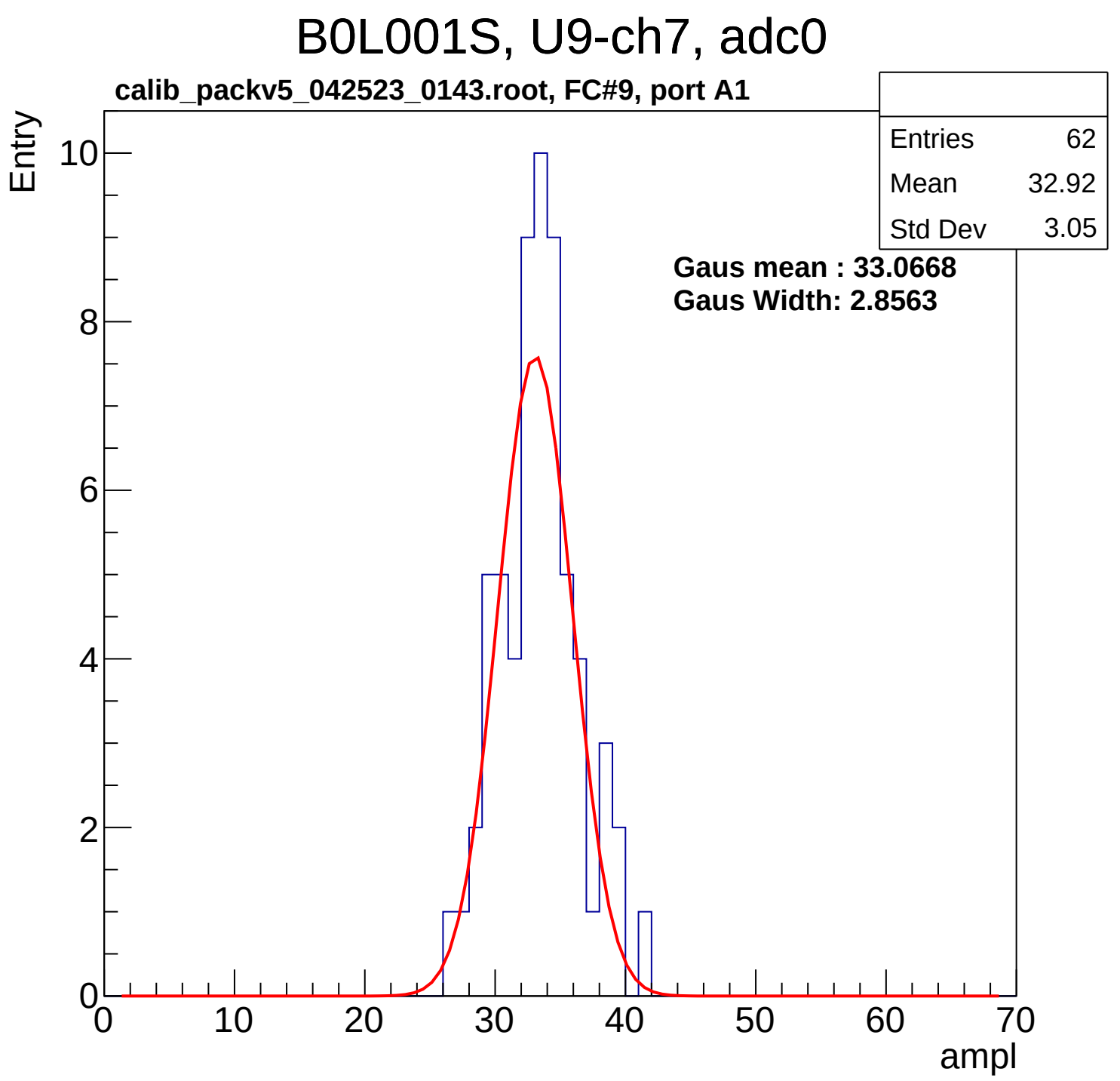
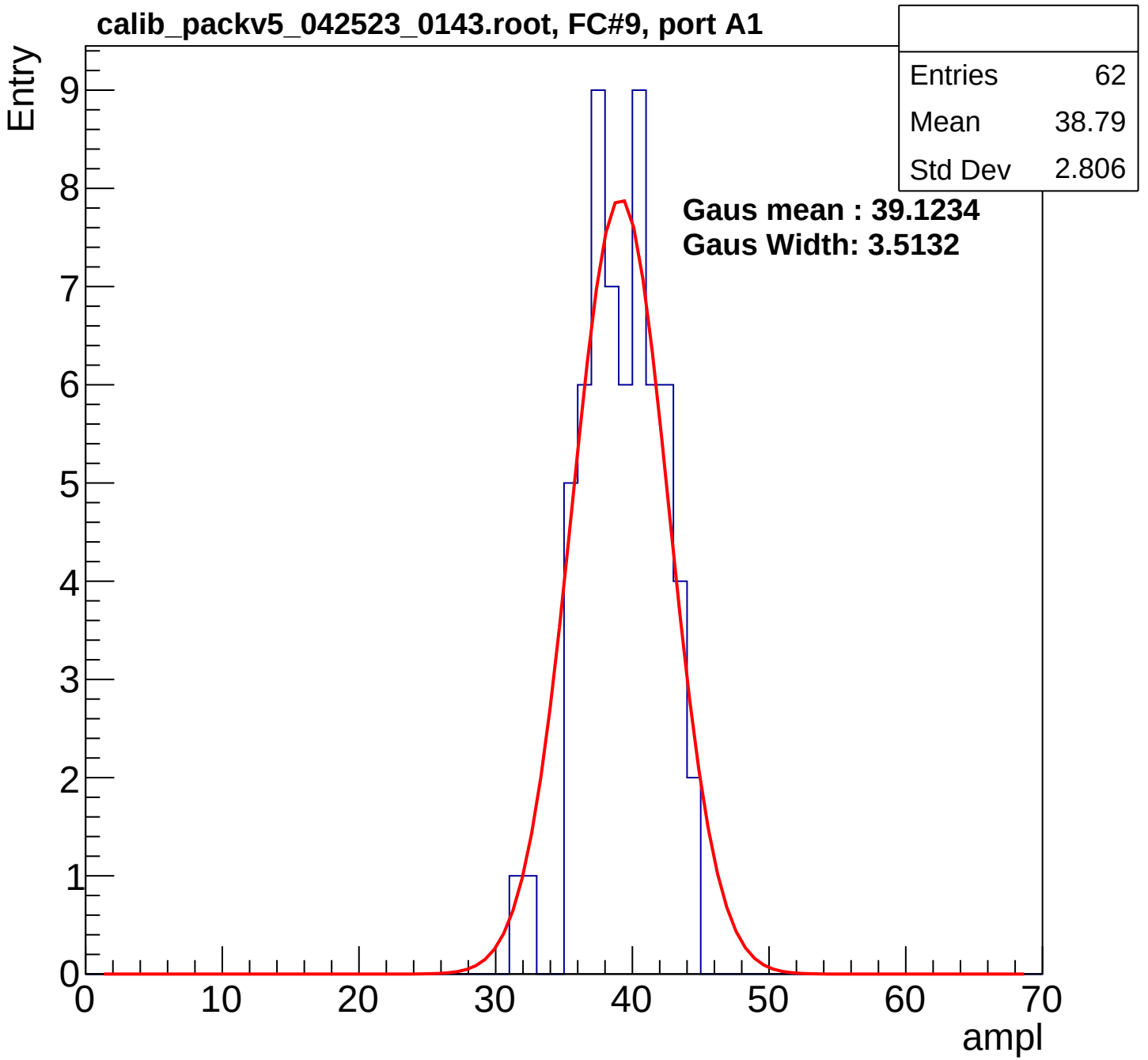
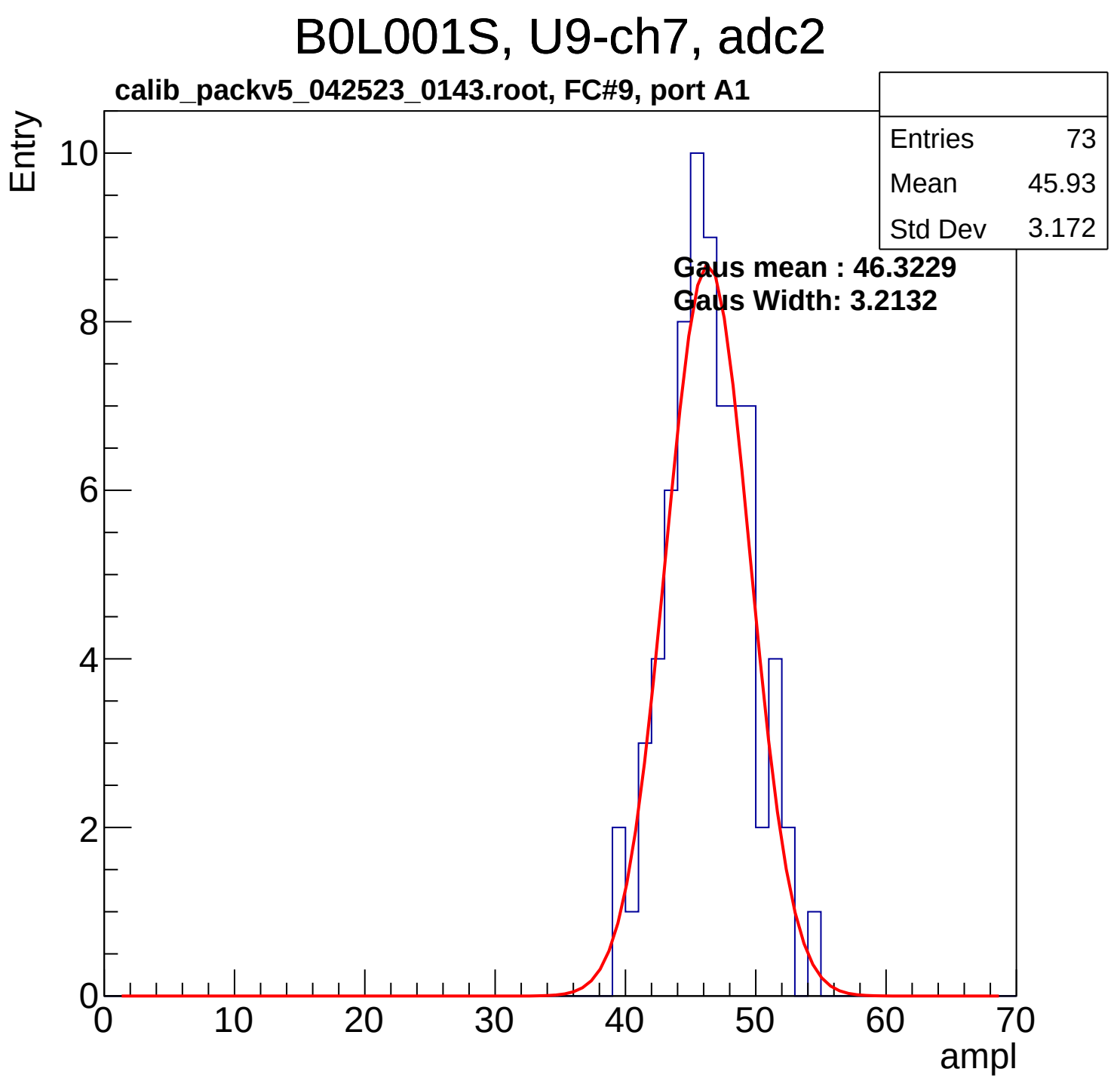
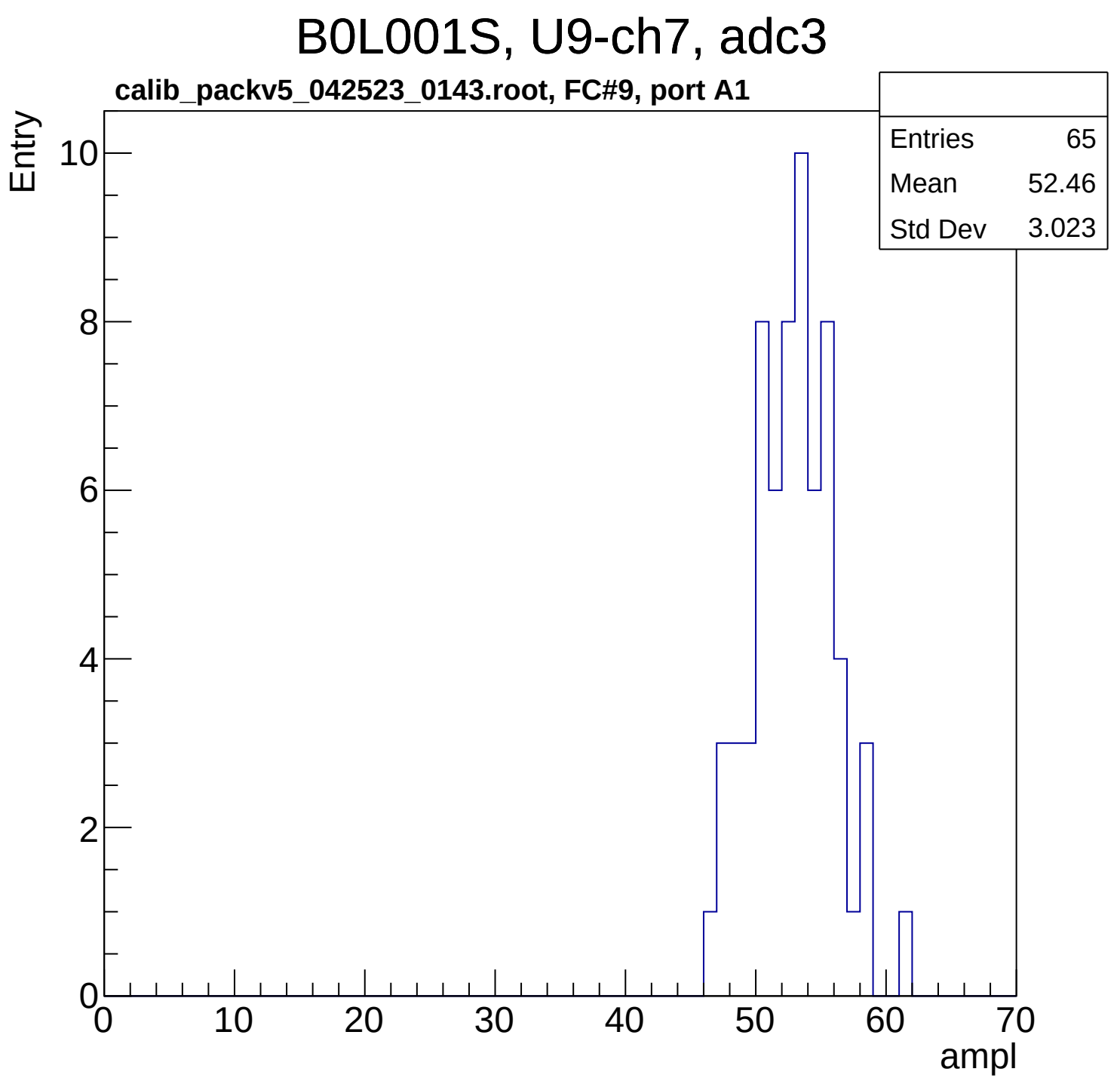
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch7, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 32.92
Std Dev 3.05
Gaus mean : 33.0668
Gaus Width: 2.8563
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch7, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.79
Std Dev 2.806
Gaus mean : 39.1234
Gaus Width: 3.5132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch7, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 45.93
Std Dev 3.172
Gaus mean : 46.3229
Gaus Width: 3.2132
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch7, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 52.46
Std Dev 3.023
Entry
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6
8
10
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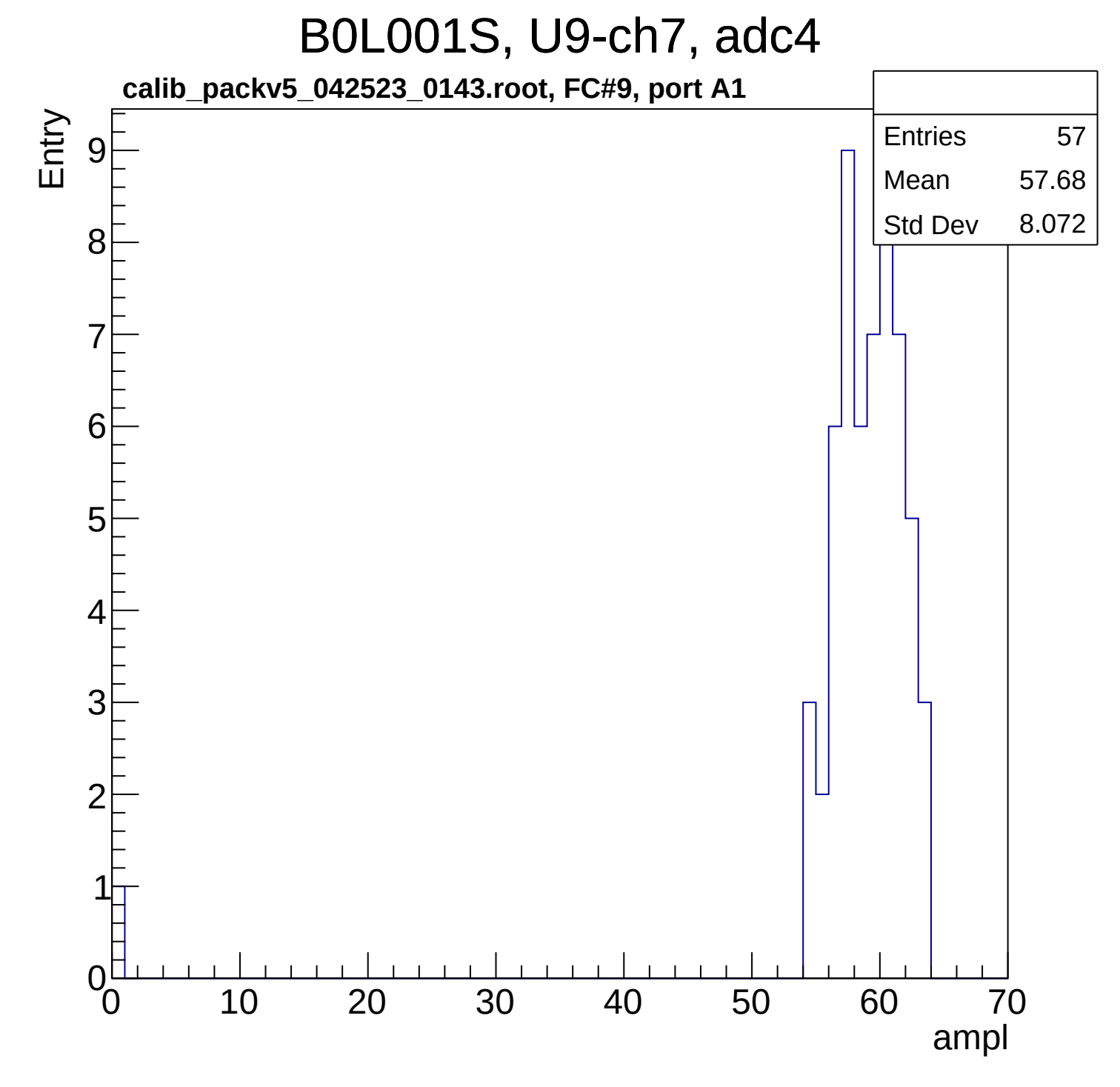
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch7, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.68
Std Dev 8.072
Entry
ampl
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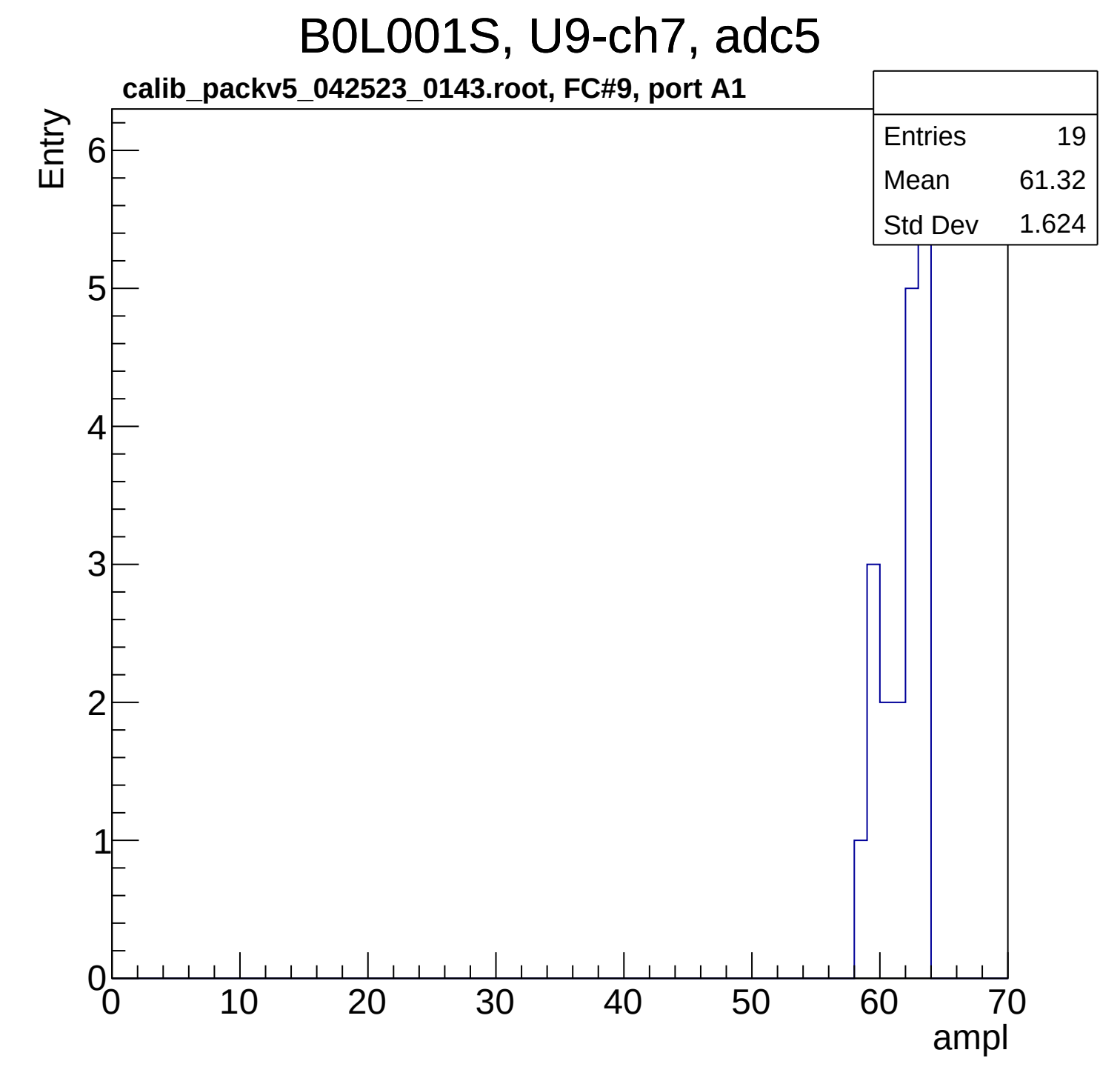
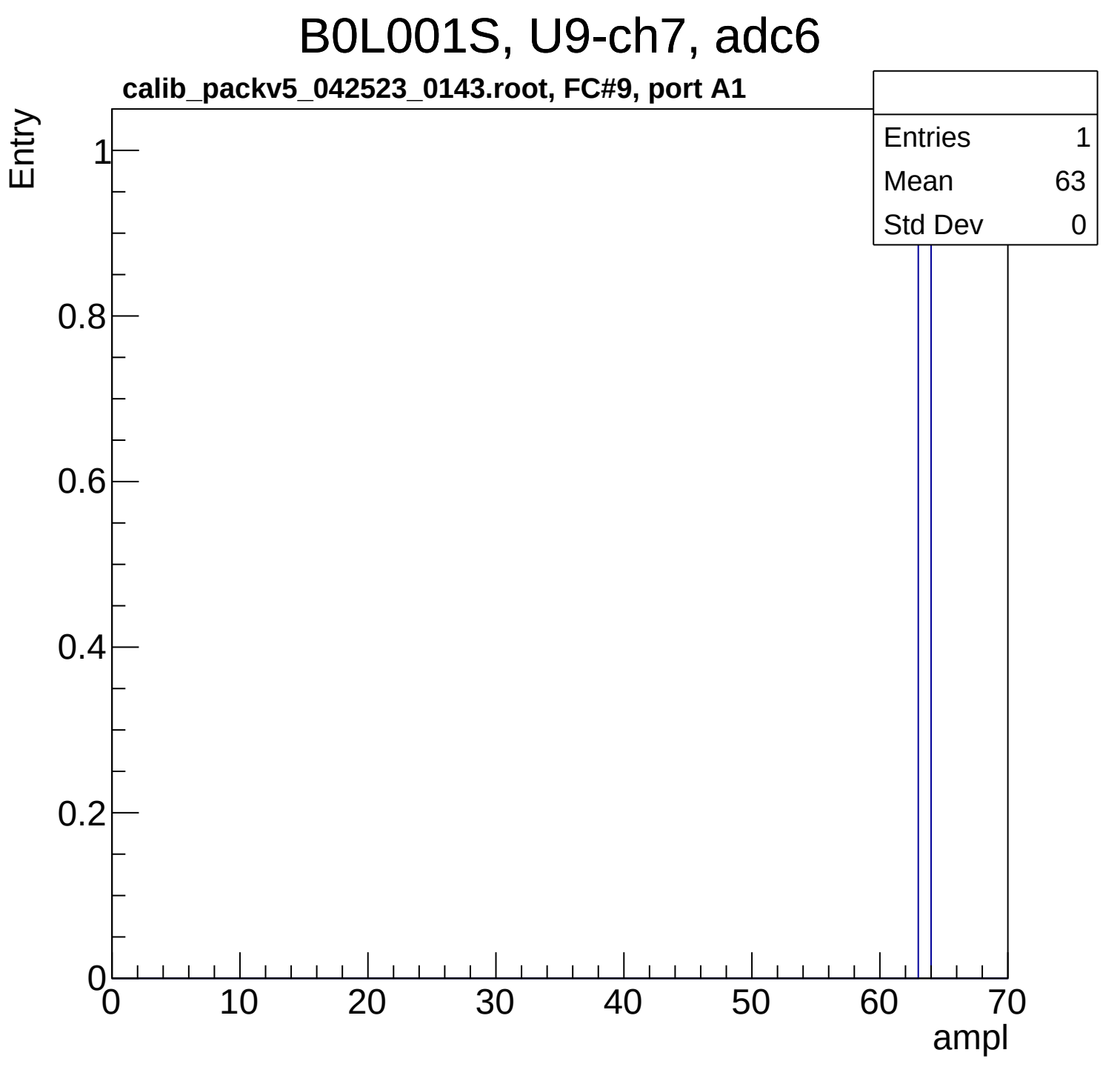
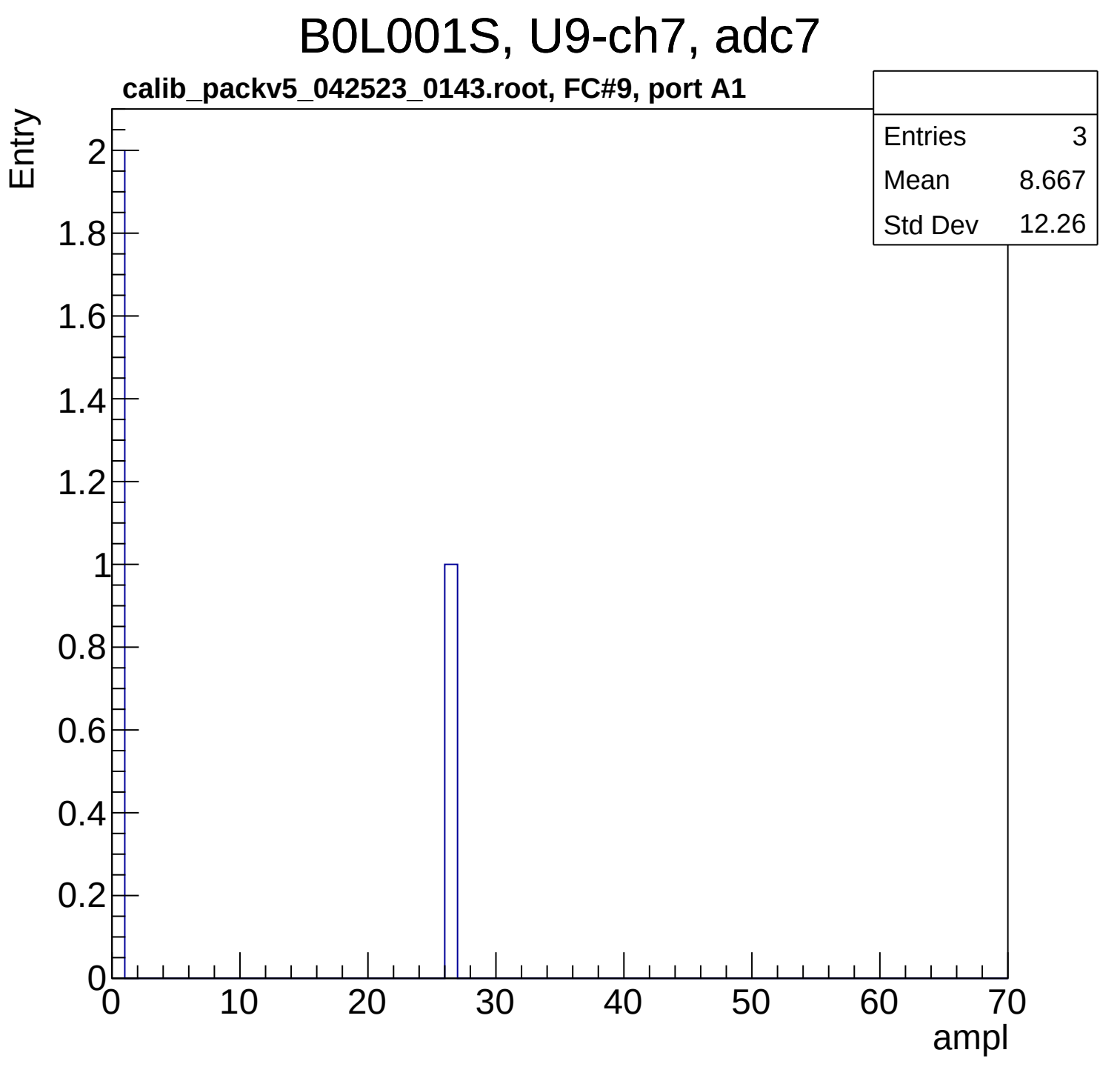
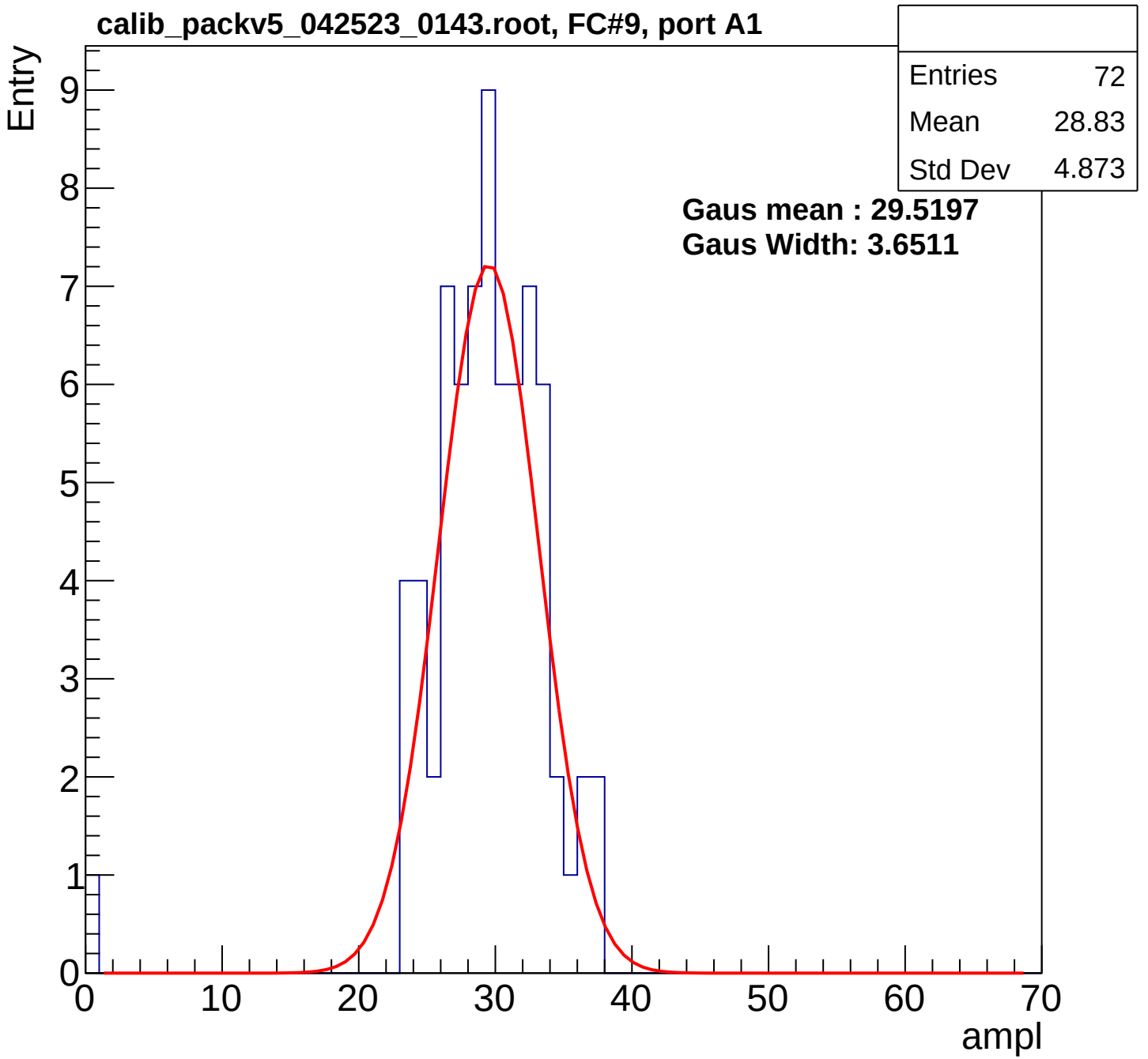
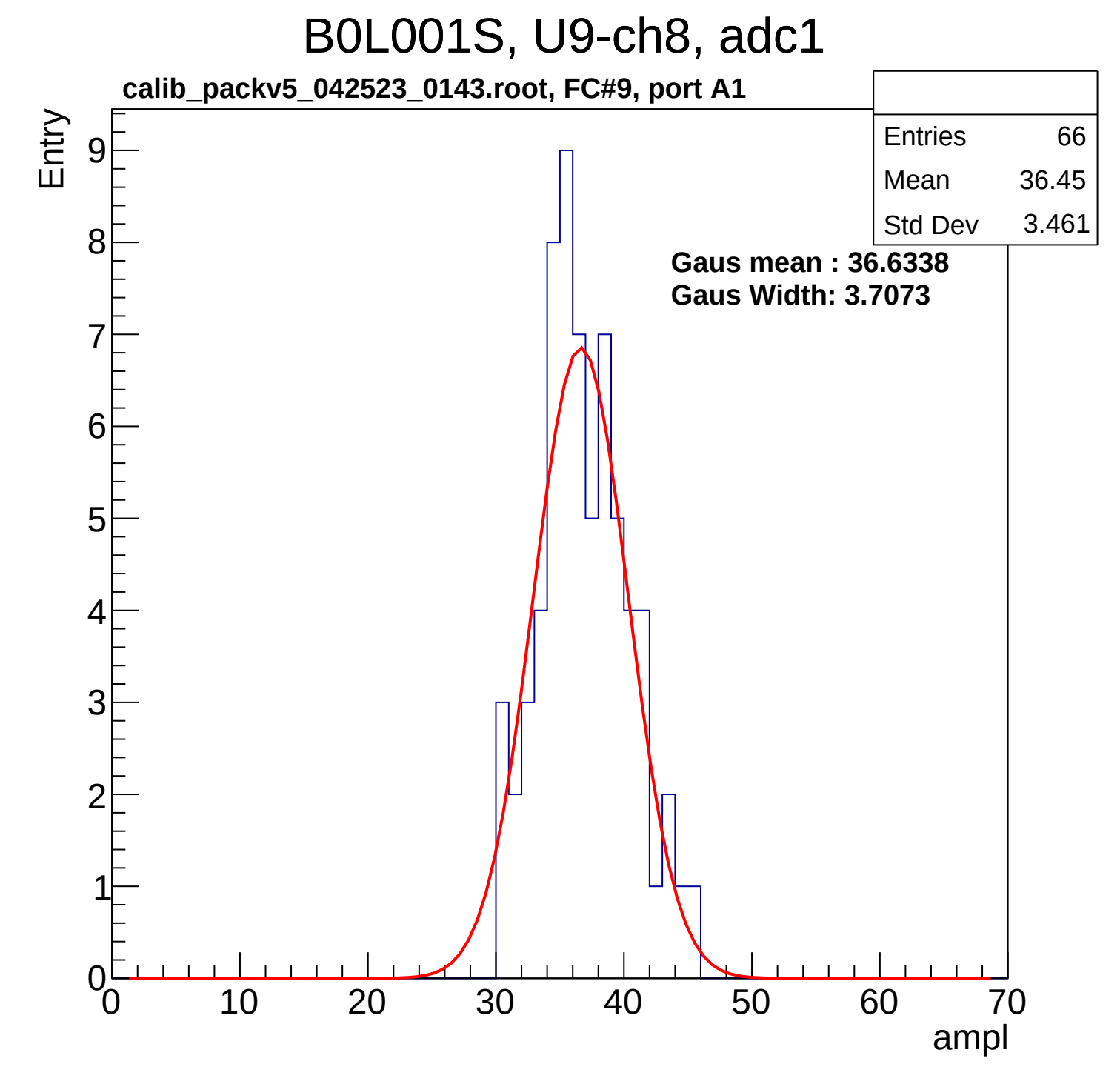
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

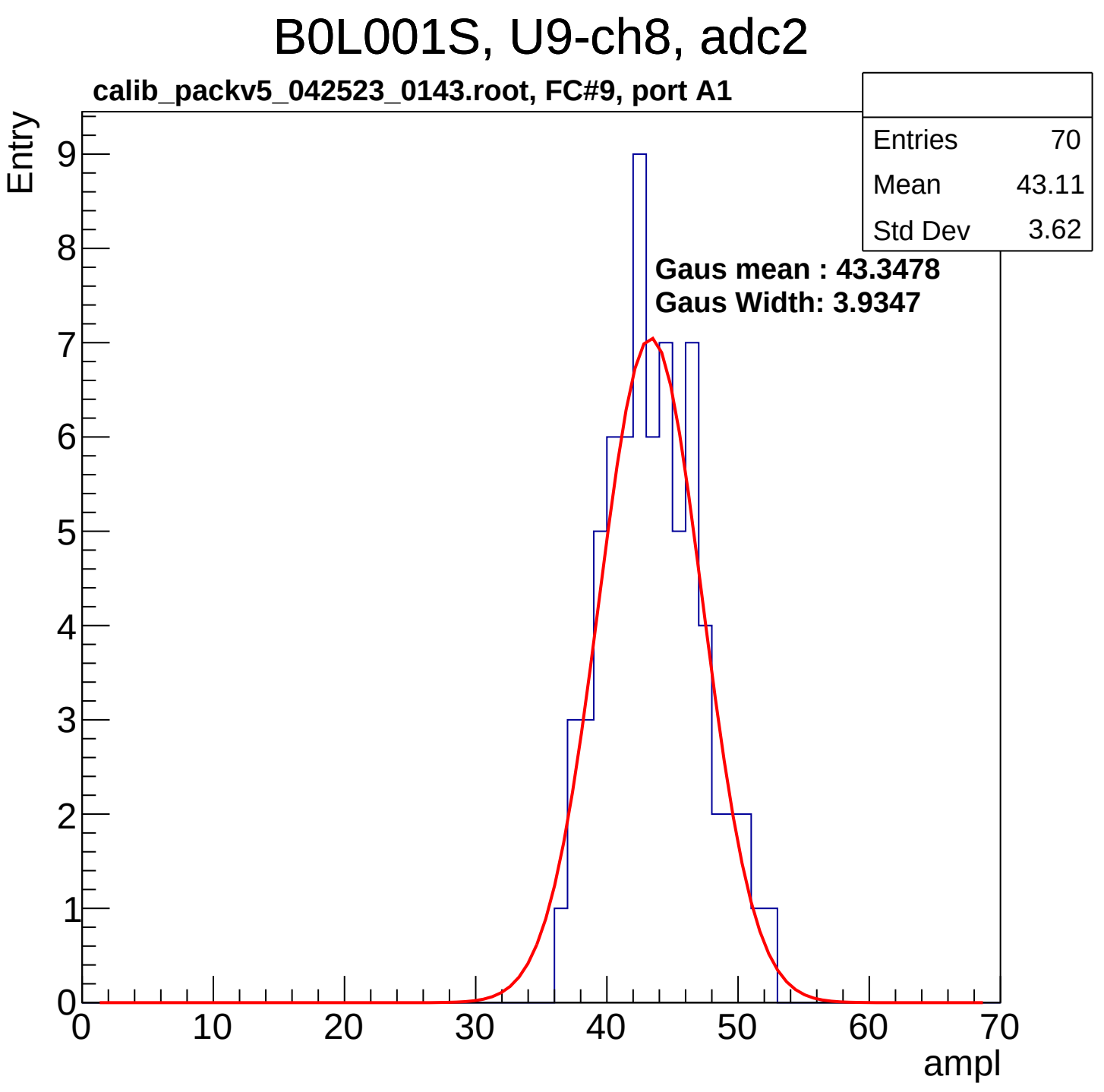

B0L001S, U9-ch7, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 61.32
Std Dev 1.624
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

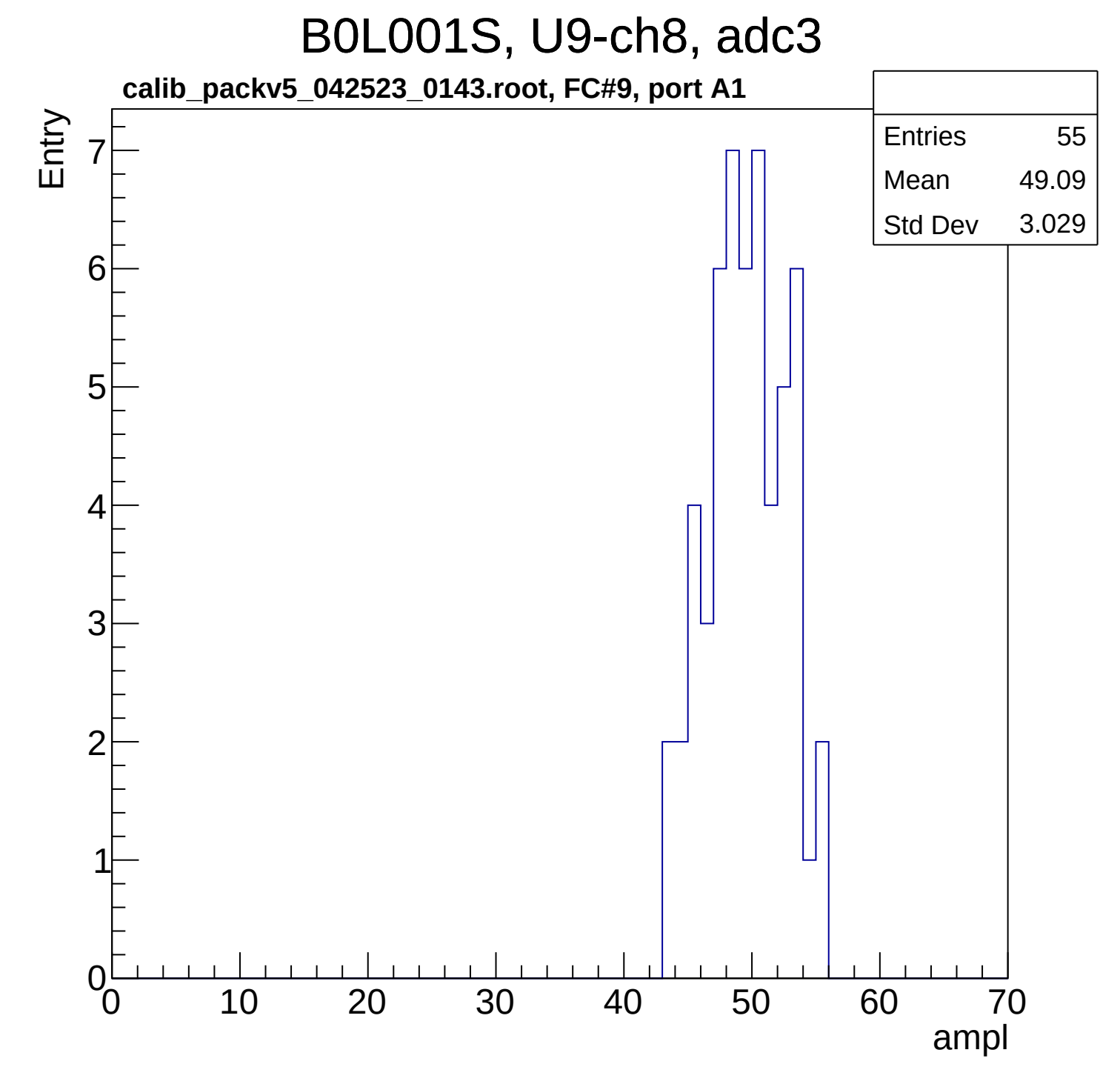

B0L001S, U9-ch7, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

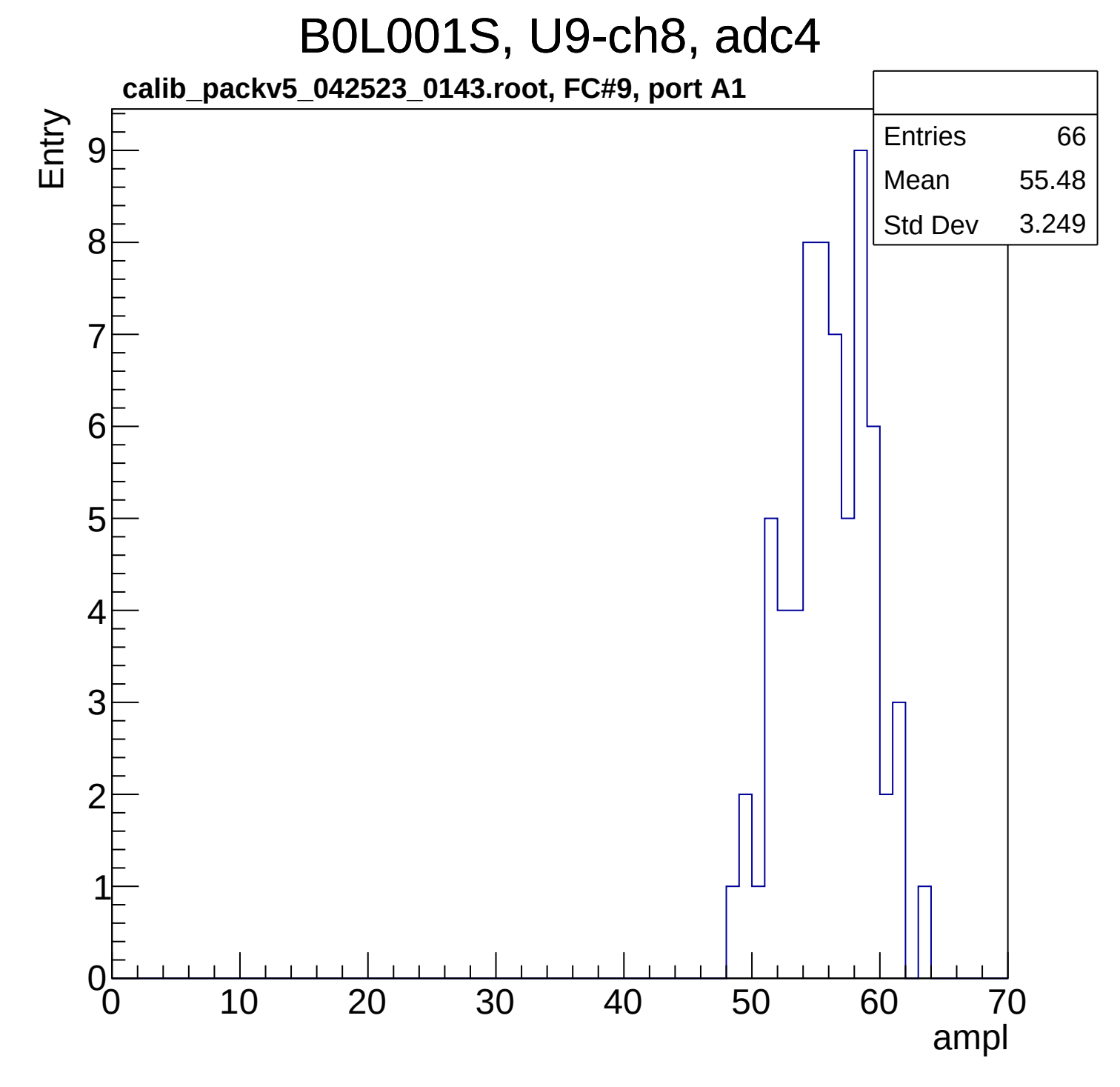

B0L001S, U9-ch7, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 8.667
Std Dev 12.26
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

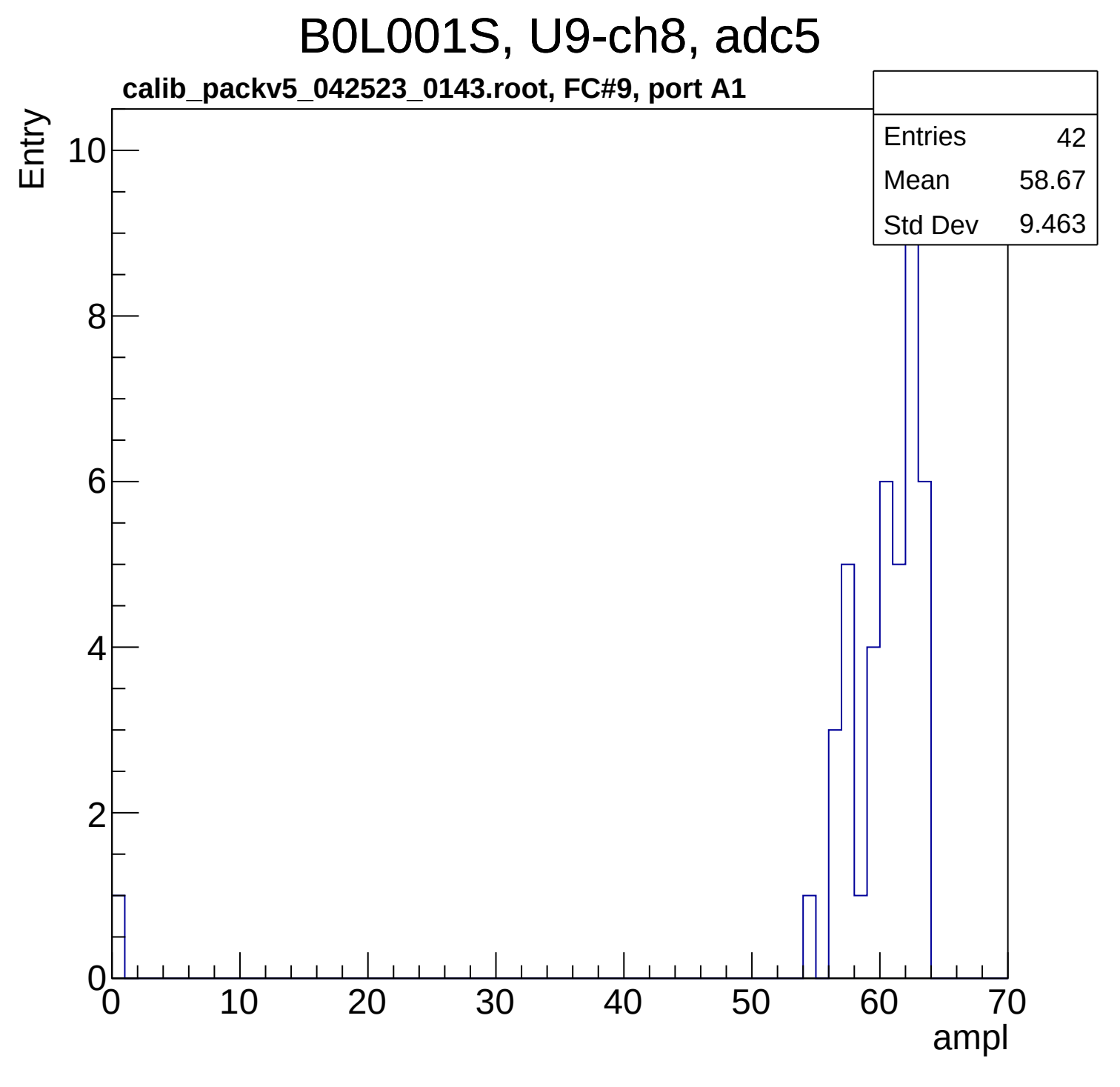

B0L001S, U9-ch8, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 28.83
Std Dev 4.873
Gaus mean : 29.5197
Gaus Width: 3.6511
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

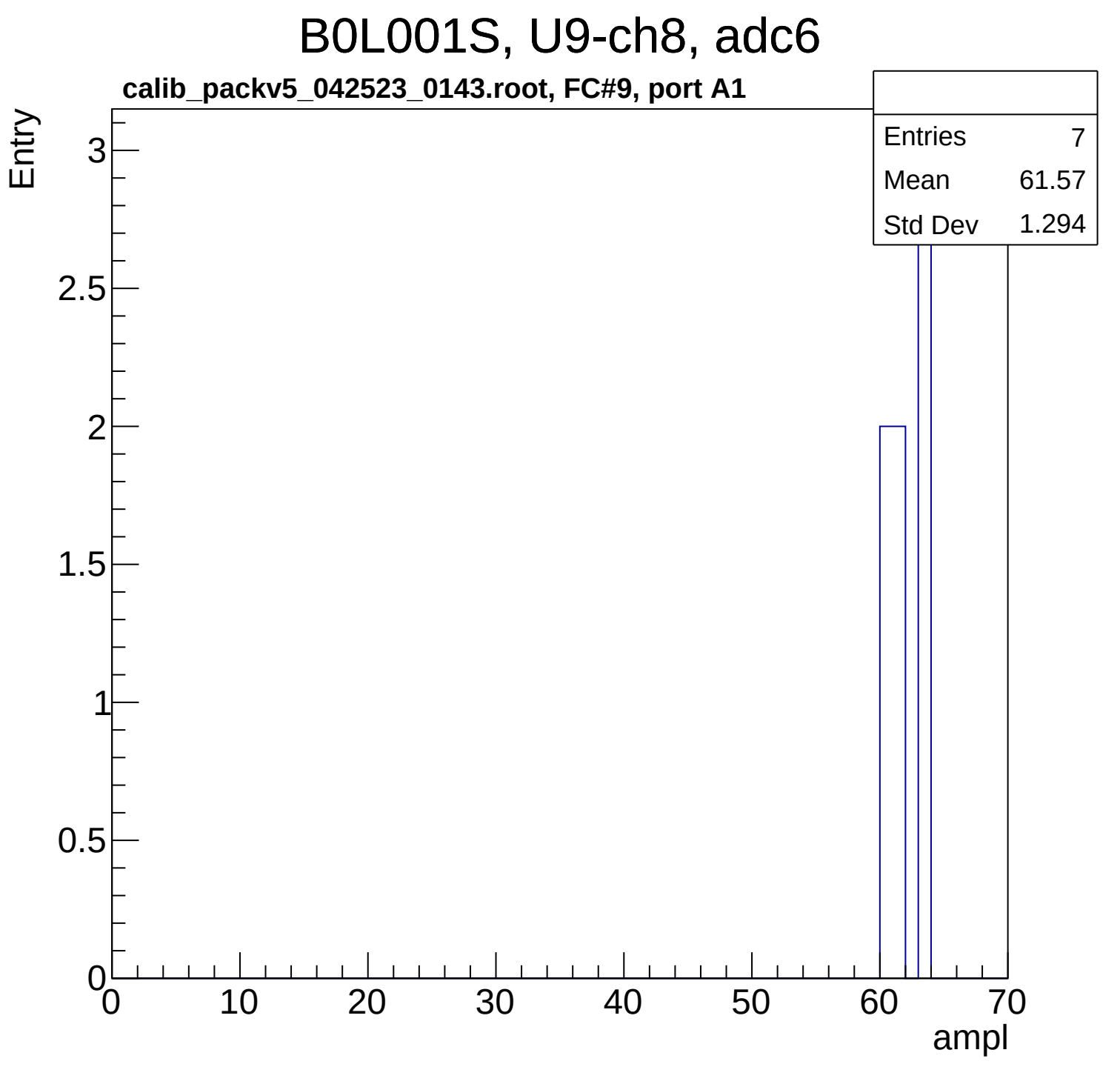

B0L001S, U9-ch8, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.45
Std Dev 3.461
Gaus mean : 36.6338
Gaus Width: 3.7073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

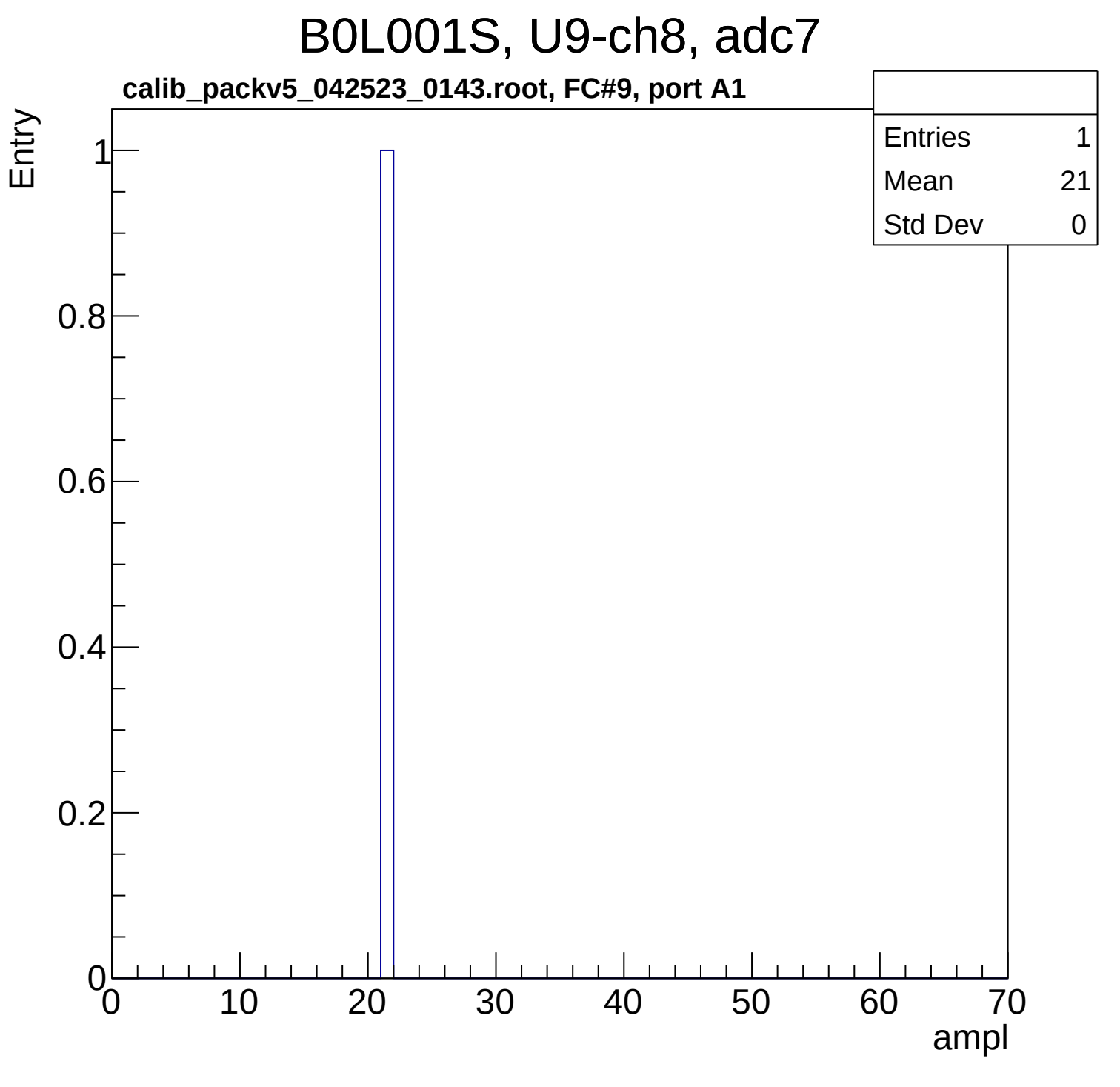

B0L001S, U9-ch8, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.11
Std Dev 3.62
Gaus mean : 43.3478
Gaus Width: 3.9347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

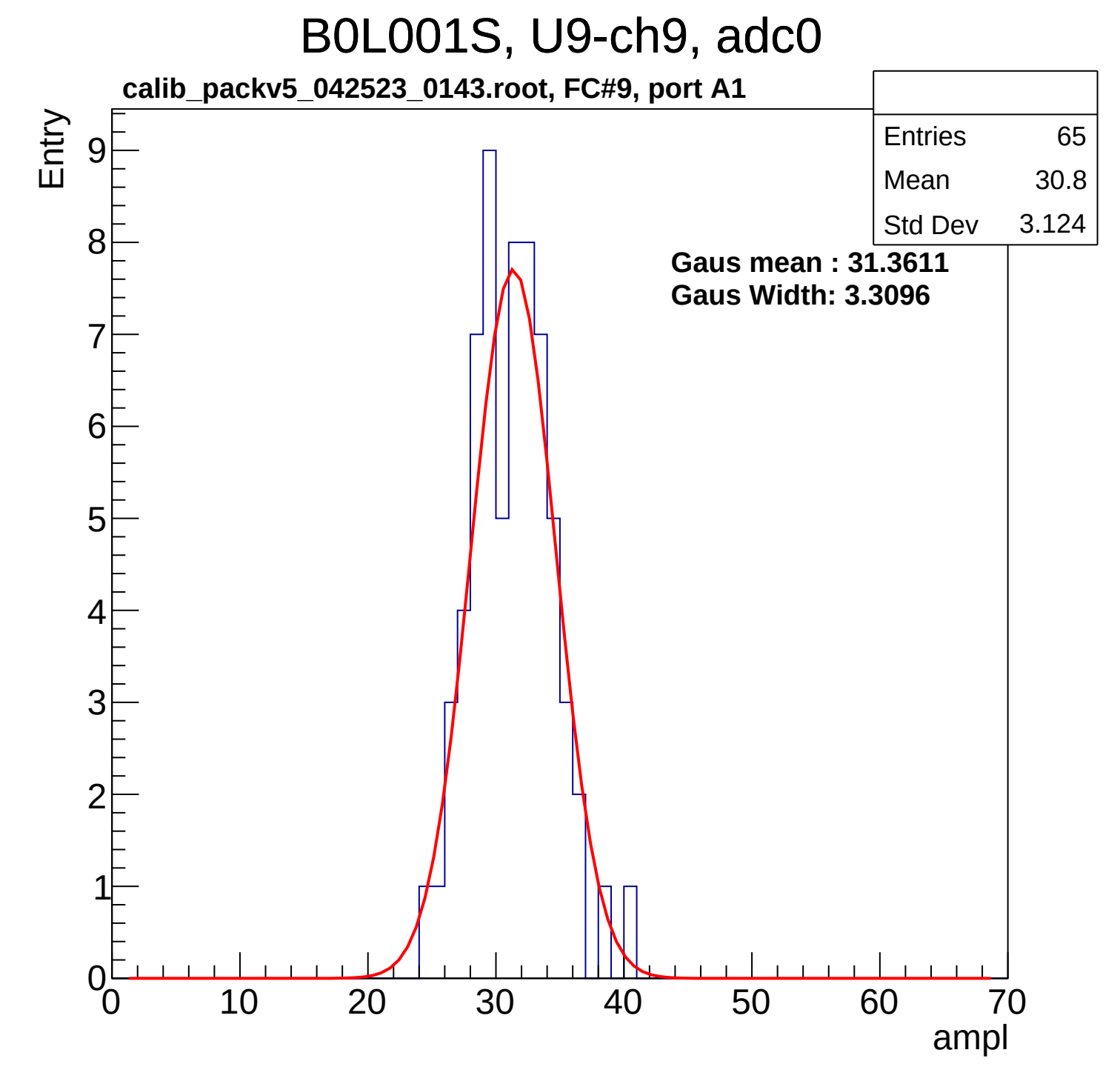

B0L001S, U9-ch8, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 49.09
Std Dev 3.029
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

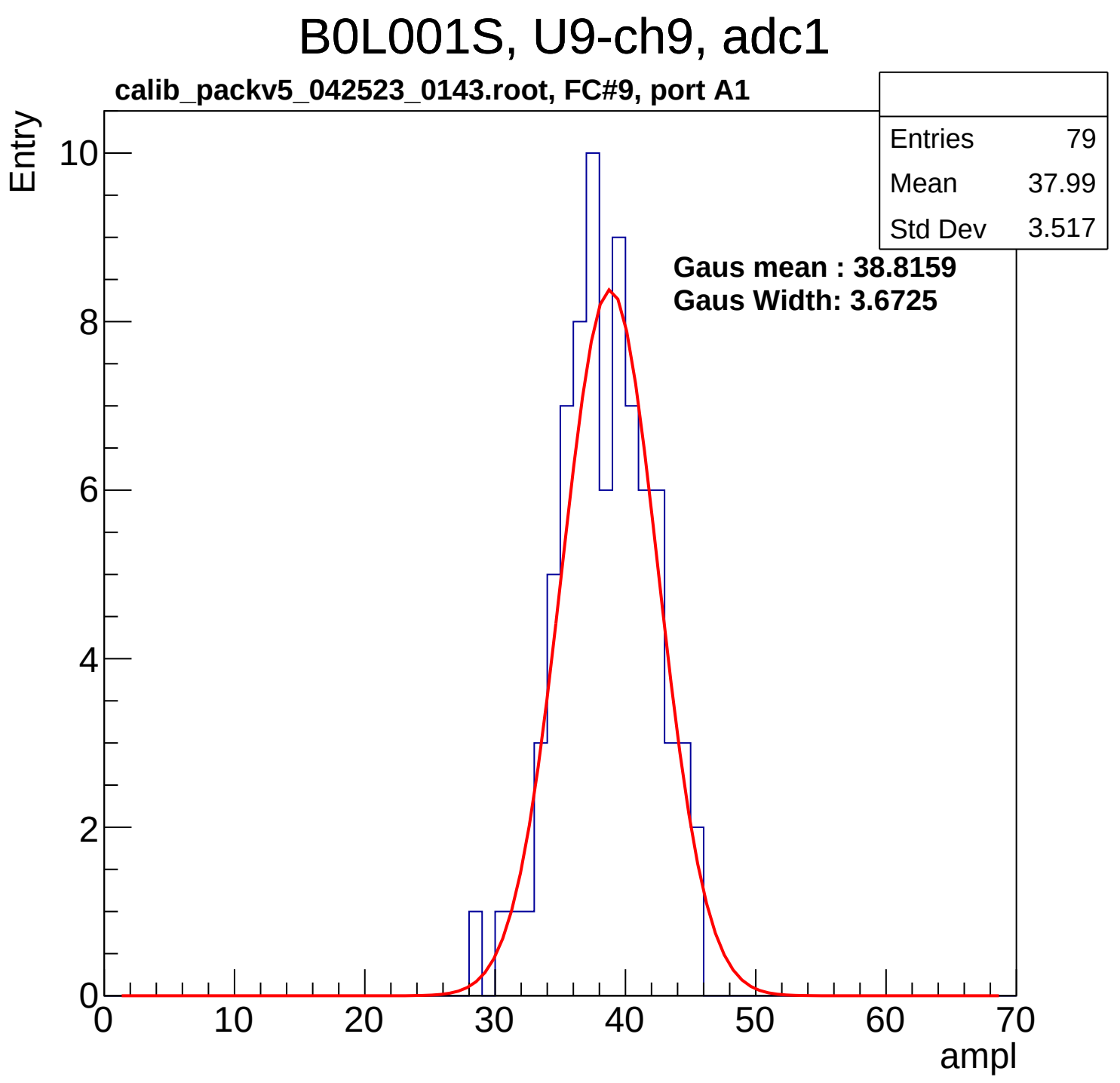

B0L001S, U9-ch8, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.48
Std Dev 3.249
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

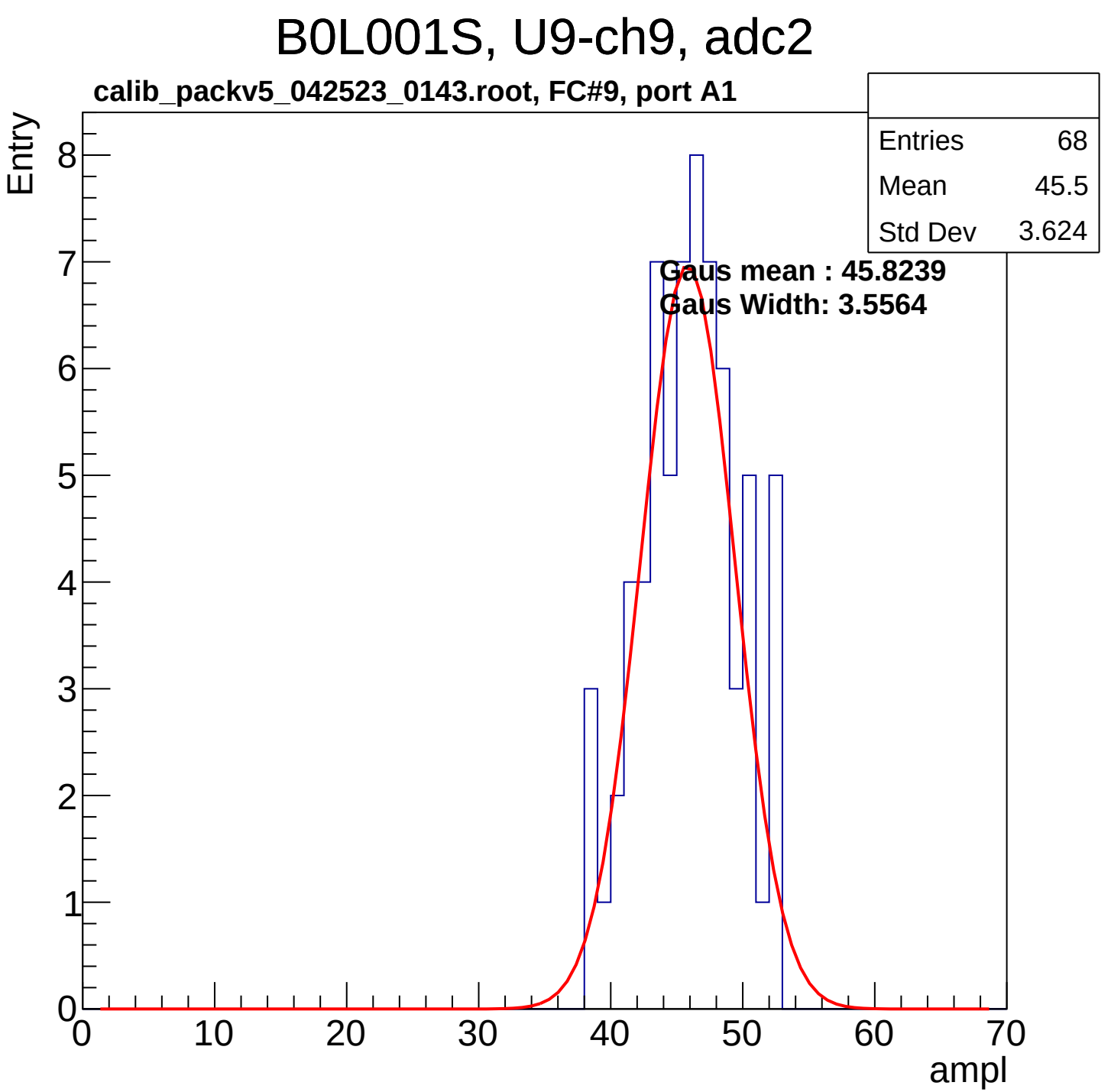

B0L001S, U9-ch8, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.67
Std Dev 9.463
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

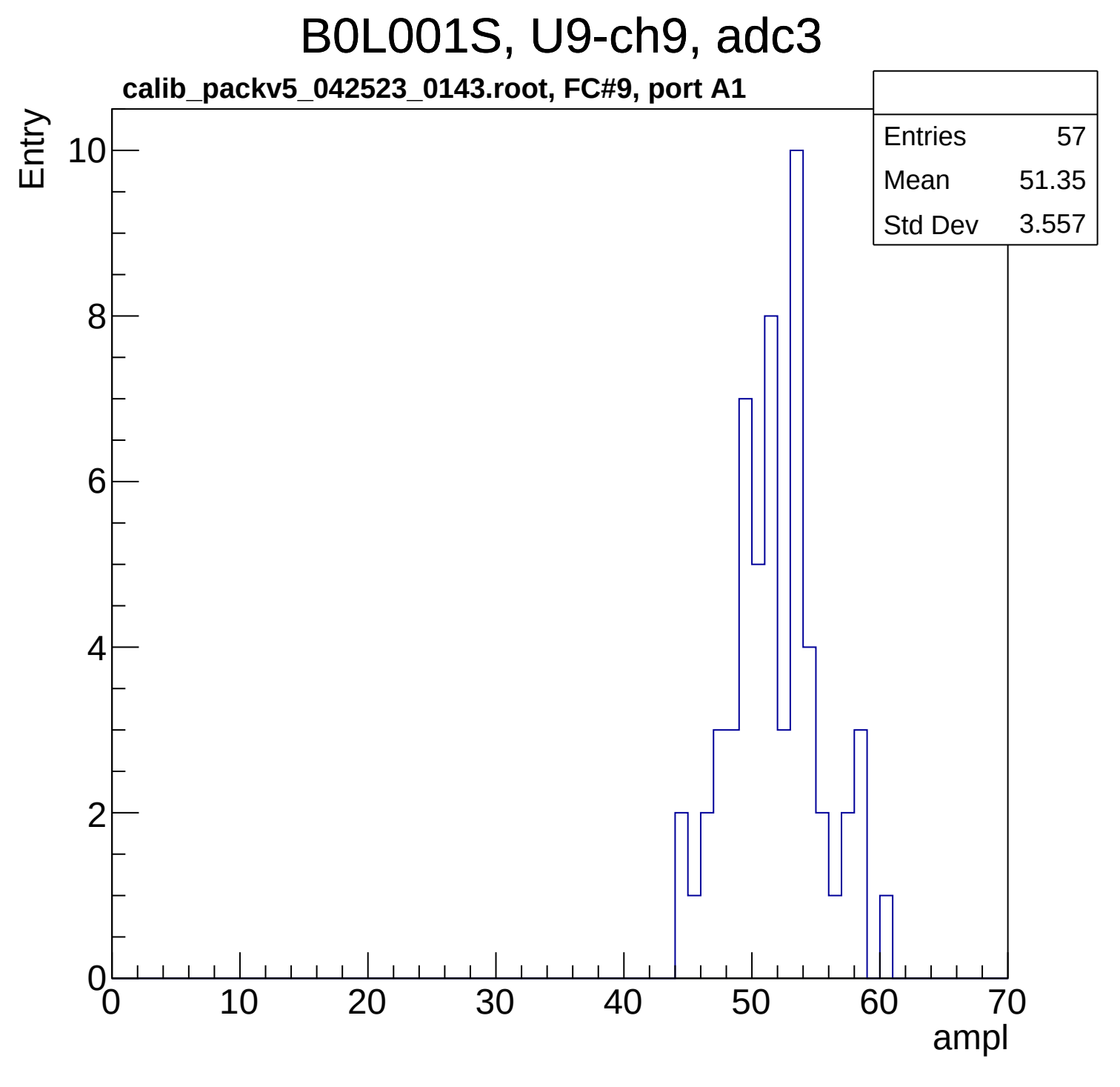

B0L001S, U9-ch8, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.57
Std Dev 1.294
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

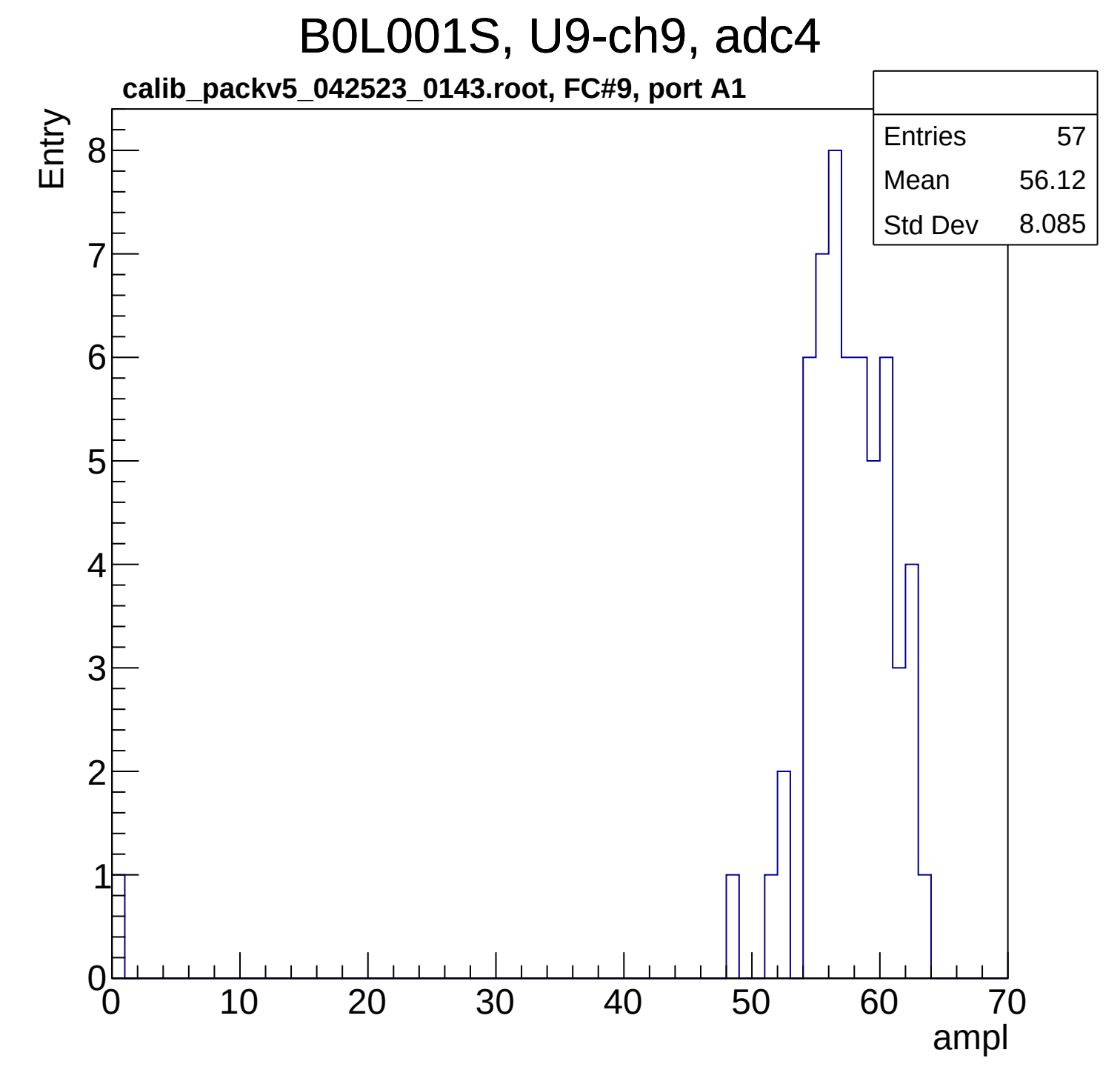

B0L001S, U9-ch8, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

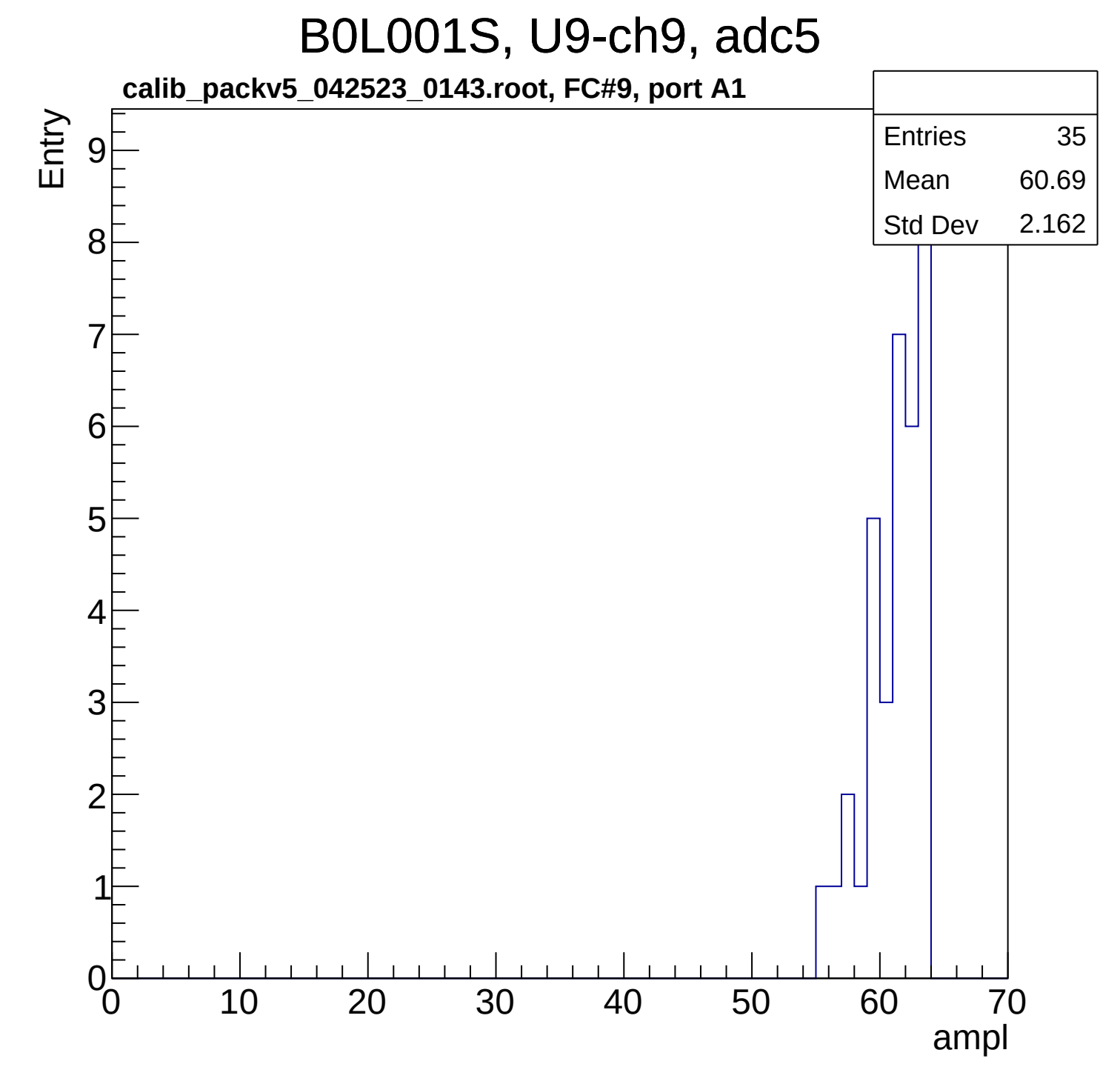

B0L001S, U9-ch9, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.8
Std Dev 3.124
Gaus mean : 31.3611
Gaus Width: 3.3096
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch9, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 37.99
Std Dev 3.517
Gaus mean : 38.8159
Gaus Width: 3.6725
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch9, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 45.5
Std Dev 3.624
Gaus mean : 45.8239
Gaus Width: 3.5564
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch9, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 51.35
Std Dev 3.557
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch9, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.12
Std Dev 8.085
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch9, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.69
Std Dev 2.162
Entry
ampl
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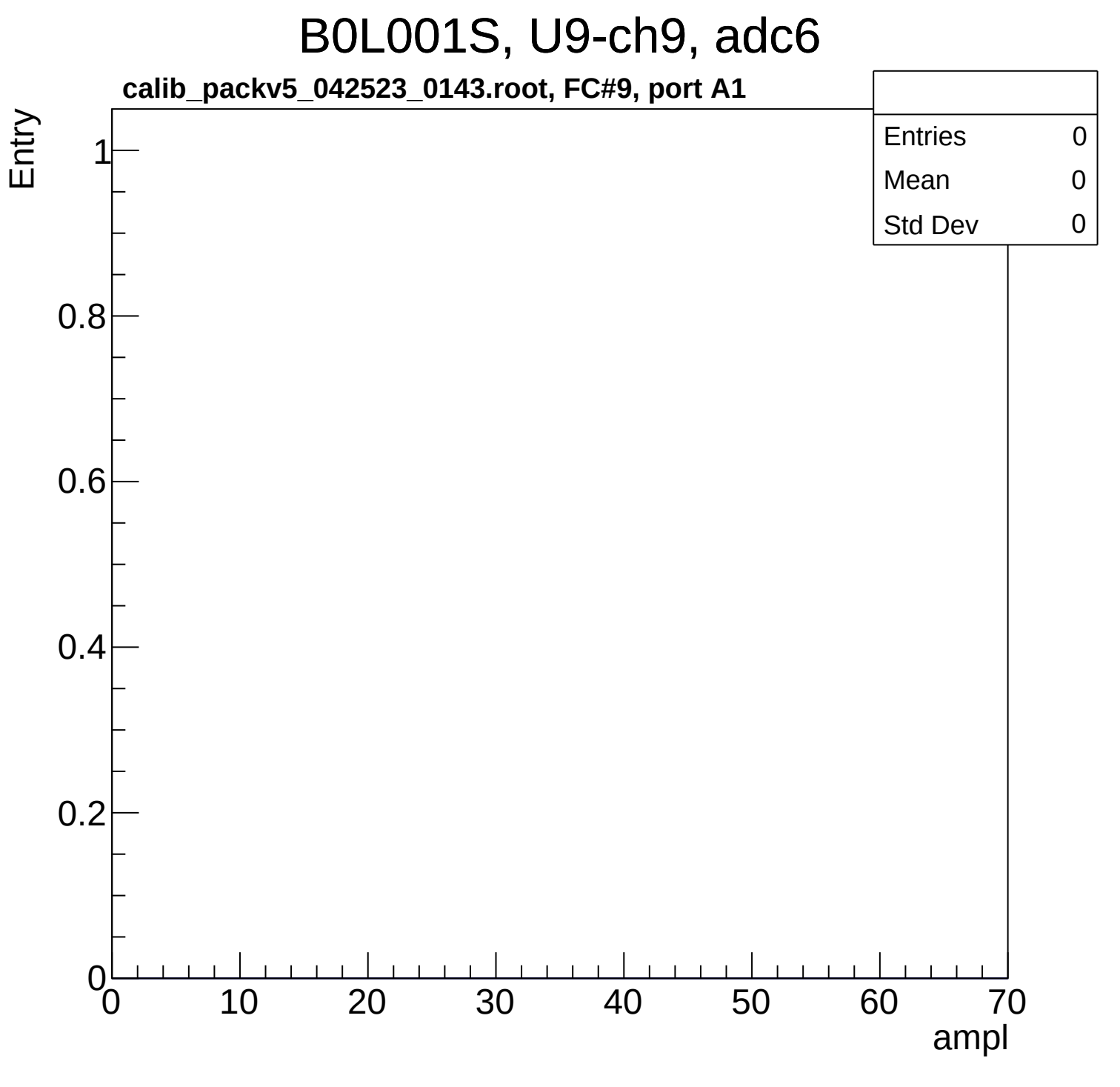
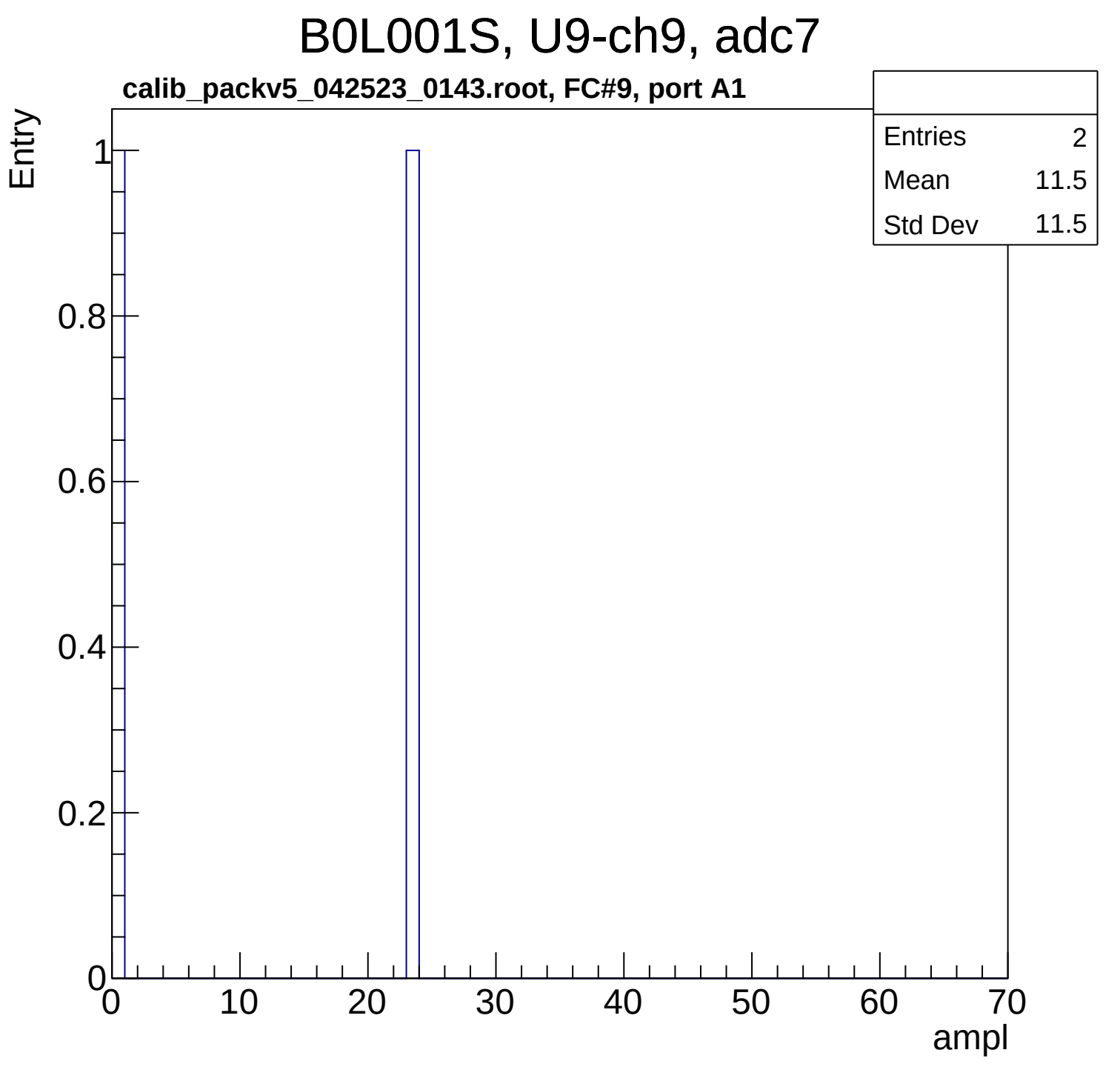
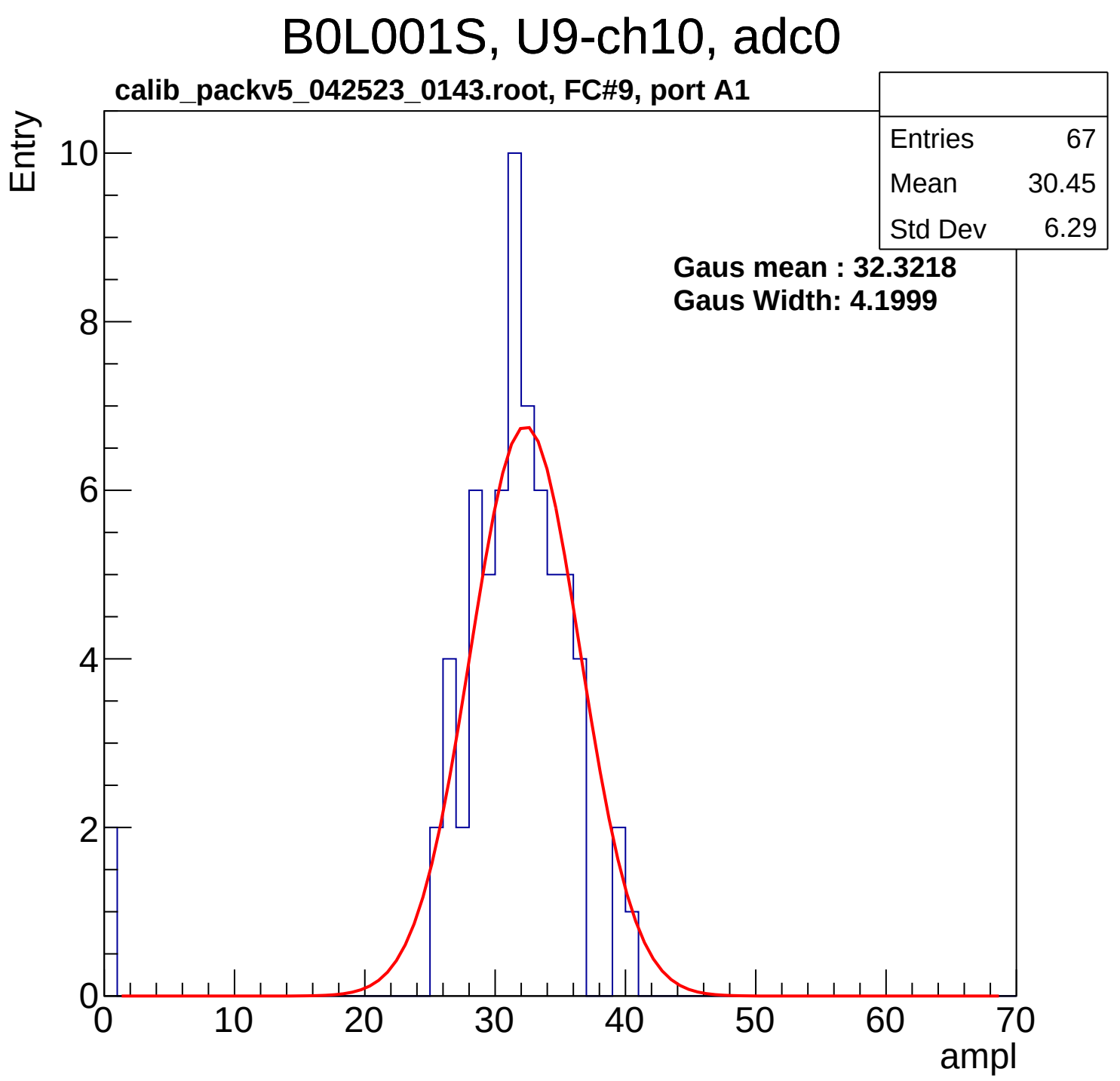
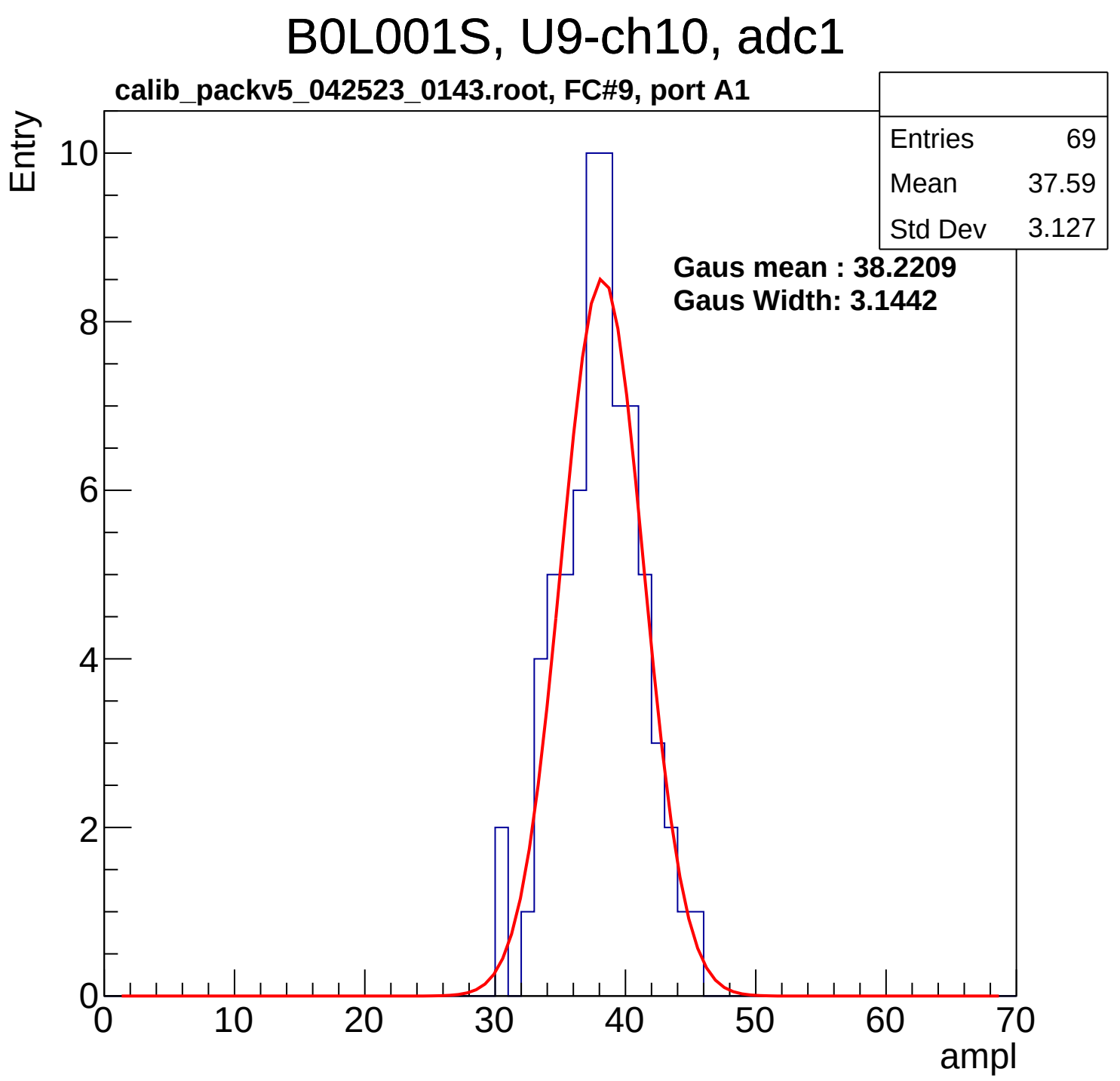
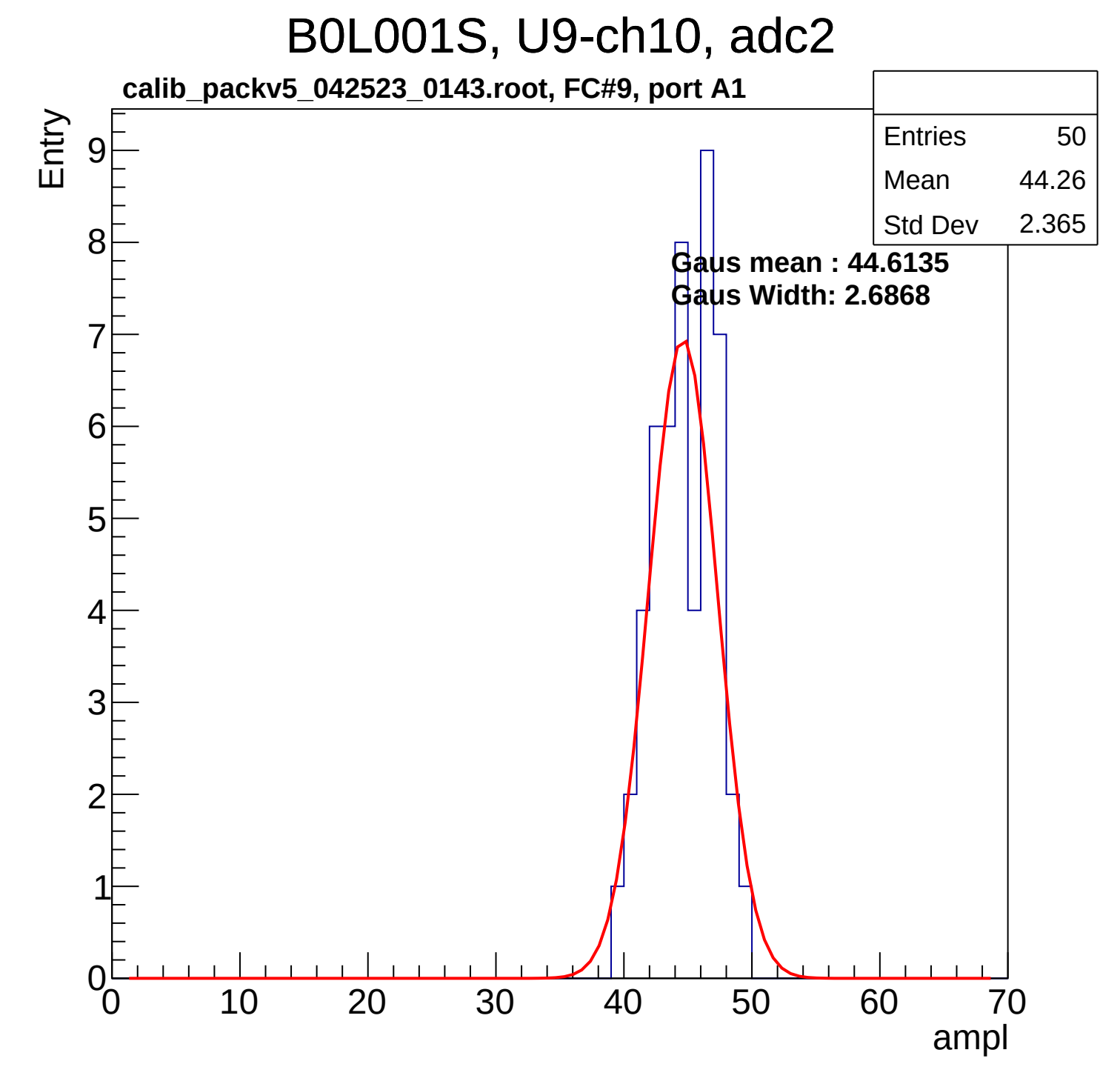
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch9, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch9, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L001S, U9-ch10, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.45
Std Dev 6.29
Gaus mean : 32.3218
Gaus Width: 4.1999
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch10, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.59
Std Dev 3.127
Gaus mean : 38.2209
Gaus Width: 3.1442
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch10, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 44.26
Std Dev 2.365
Gaus mean : 44.6135
Gaus Width: 2.6868
Entry
ampl
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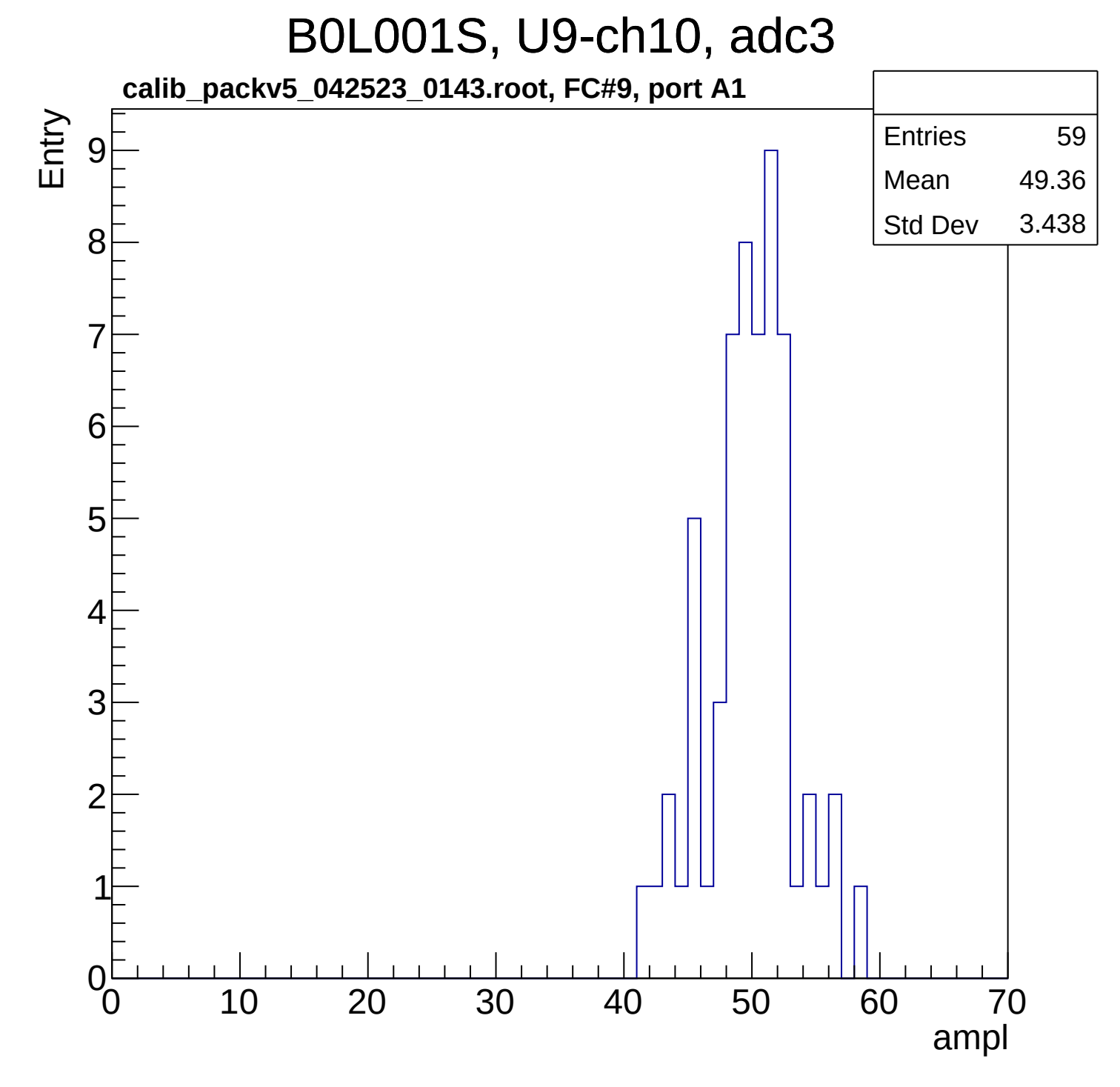
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch10, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 49.36
Std Dev 3.438
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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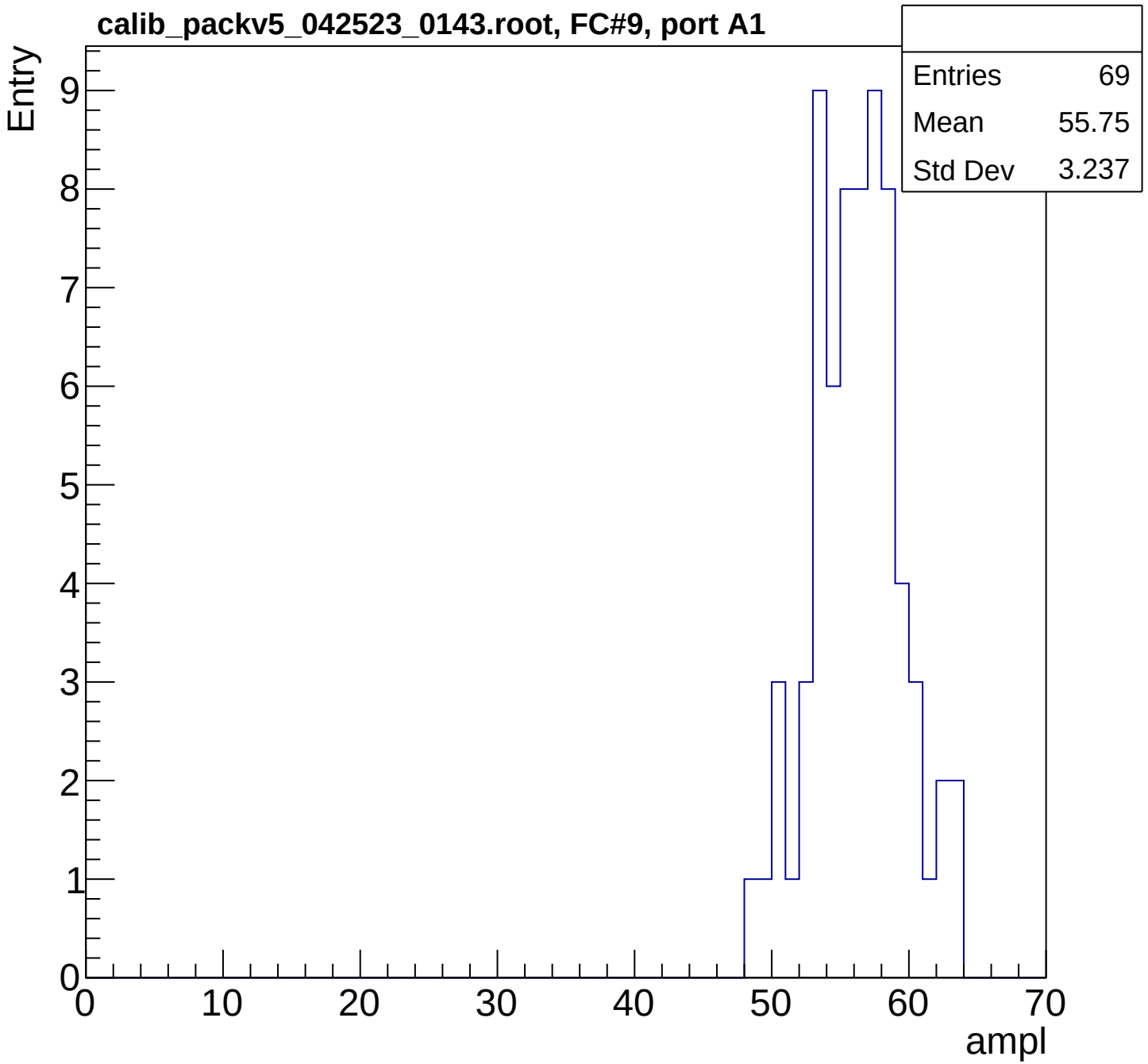
40
50
60
70


B0L001S, U9-ch10, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 55.75
Std Dev 3.237
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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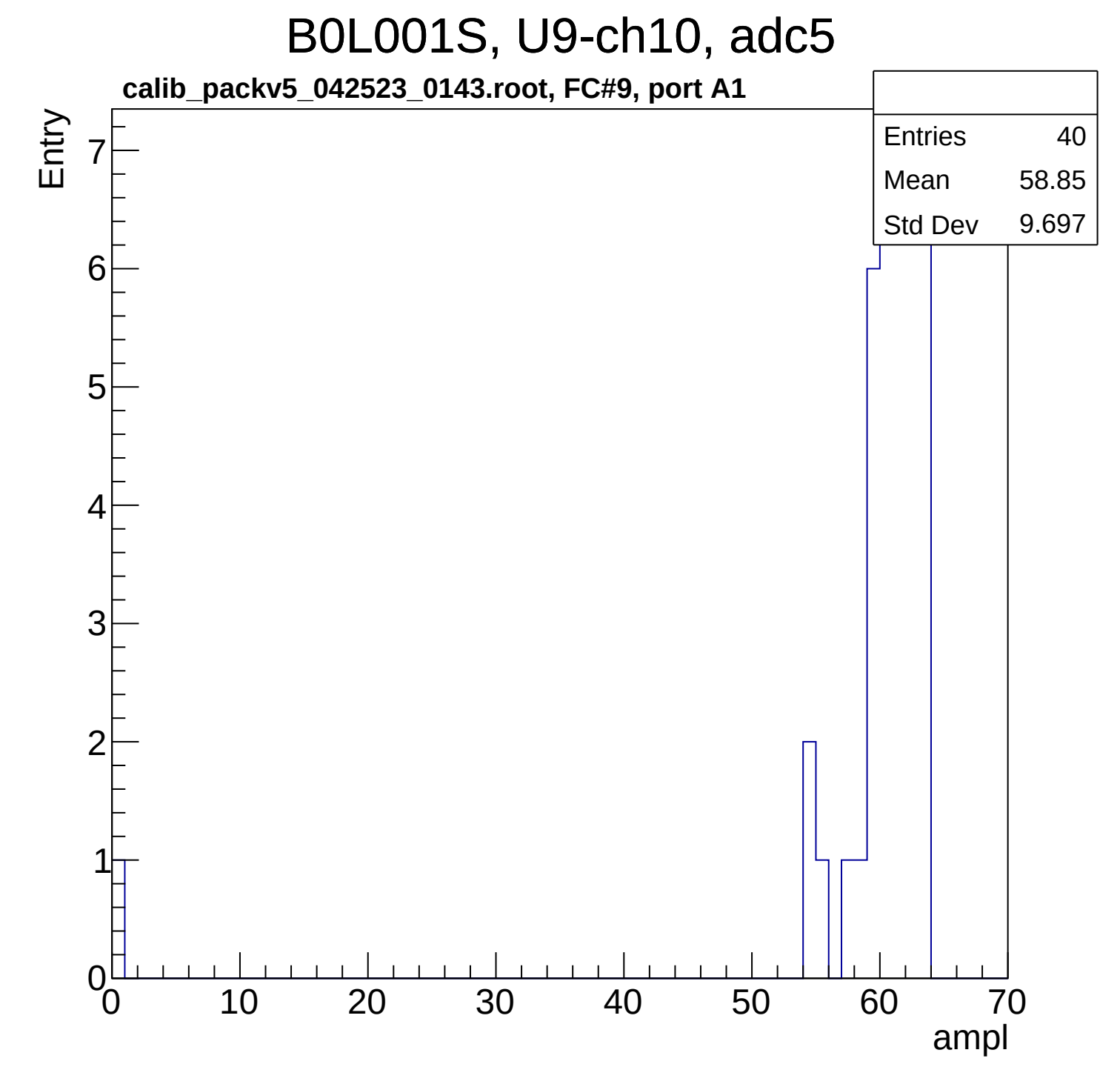
60
70


B0L001S, U9-ch10, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.85
Std Dev 9.697
Entry
ampl
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1
2
3
4
5
6
7
0
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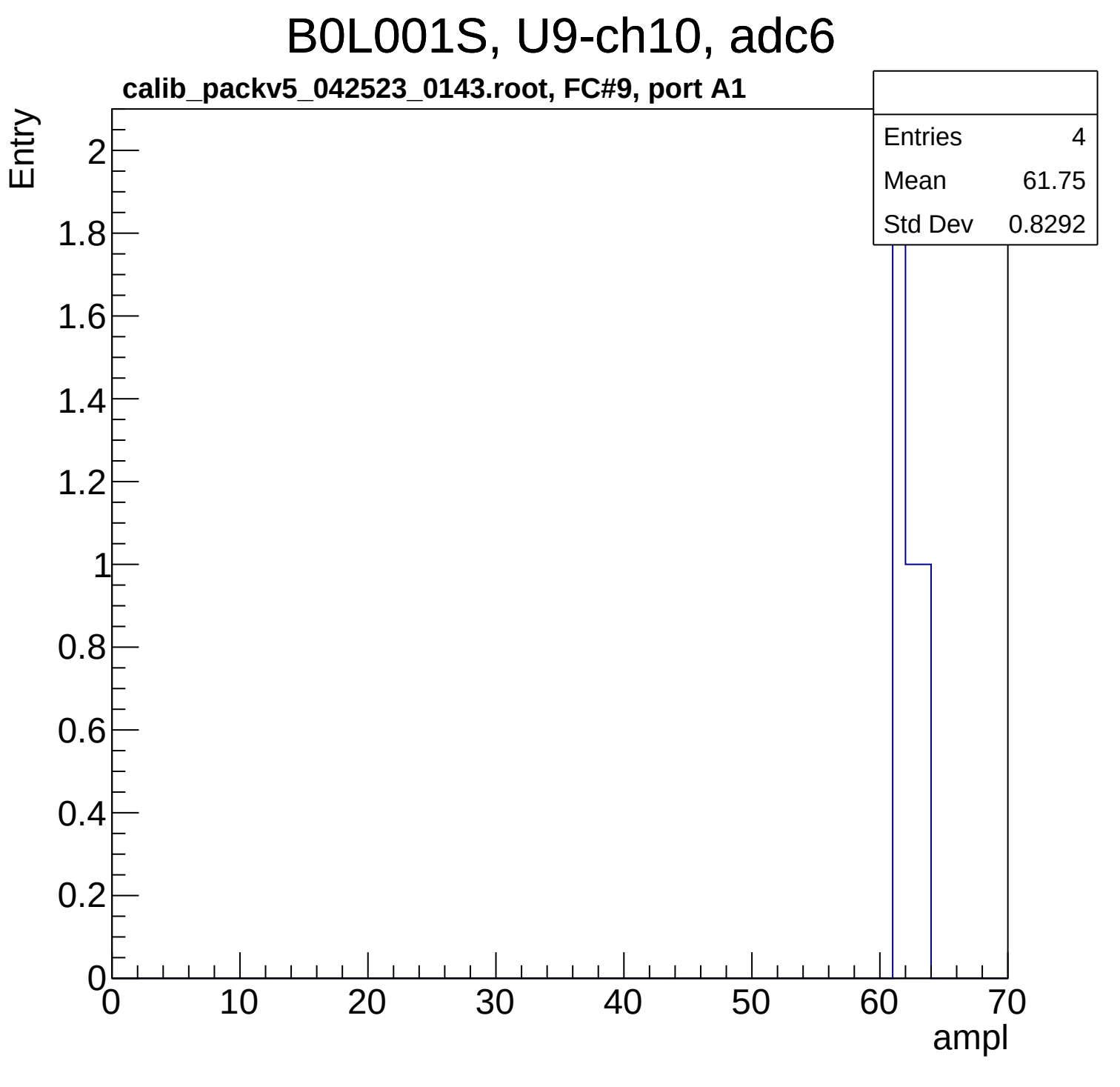
20
30
40
50
60
70


B0L001S, U9-ch10, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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0
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40
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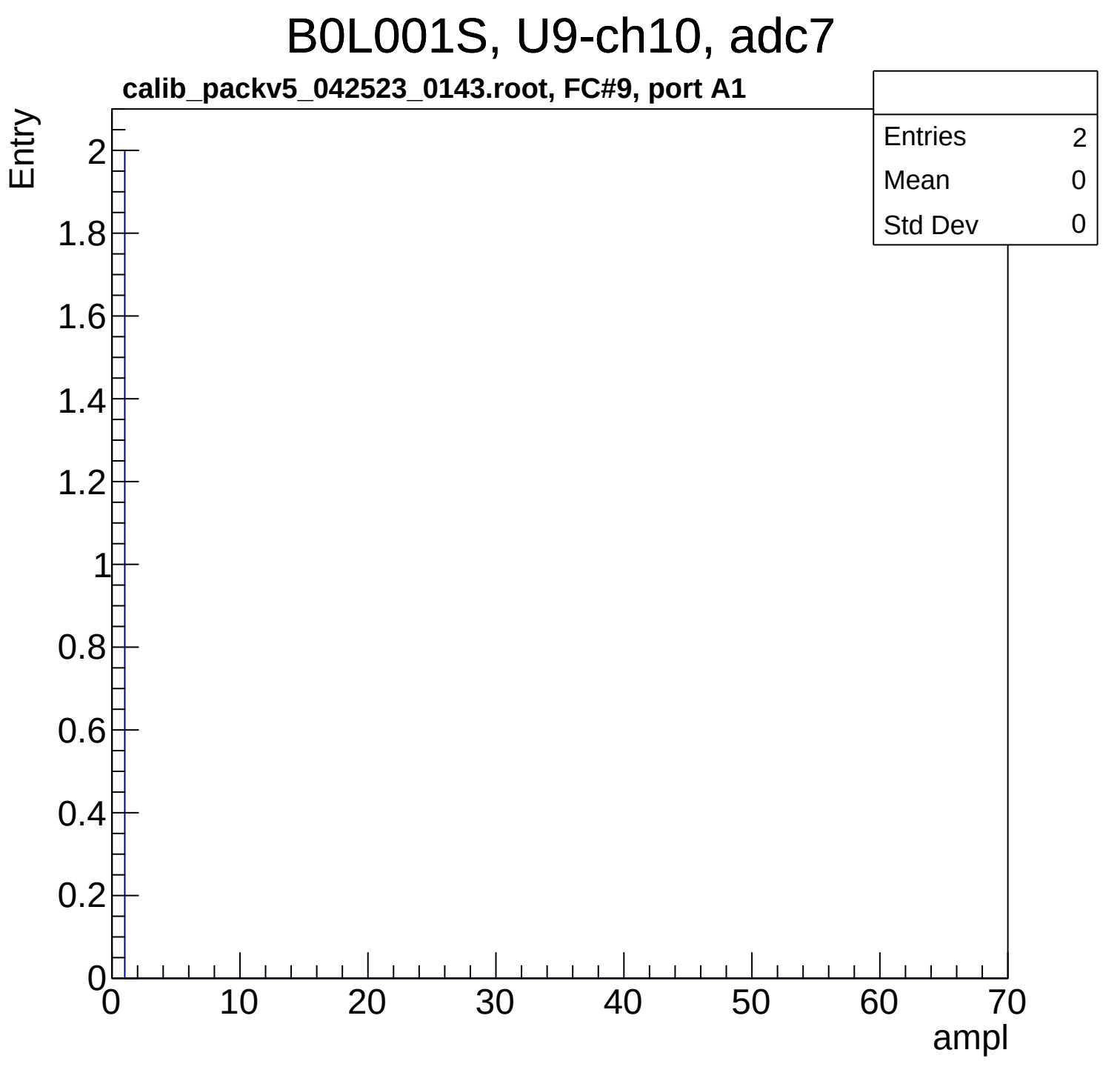
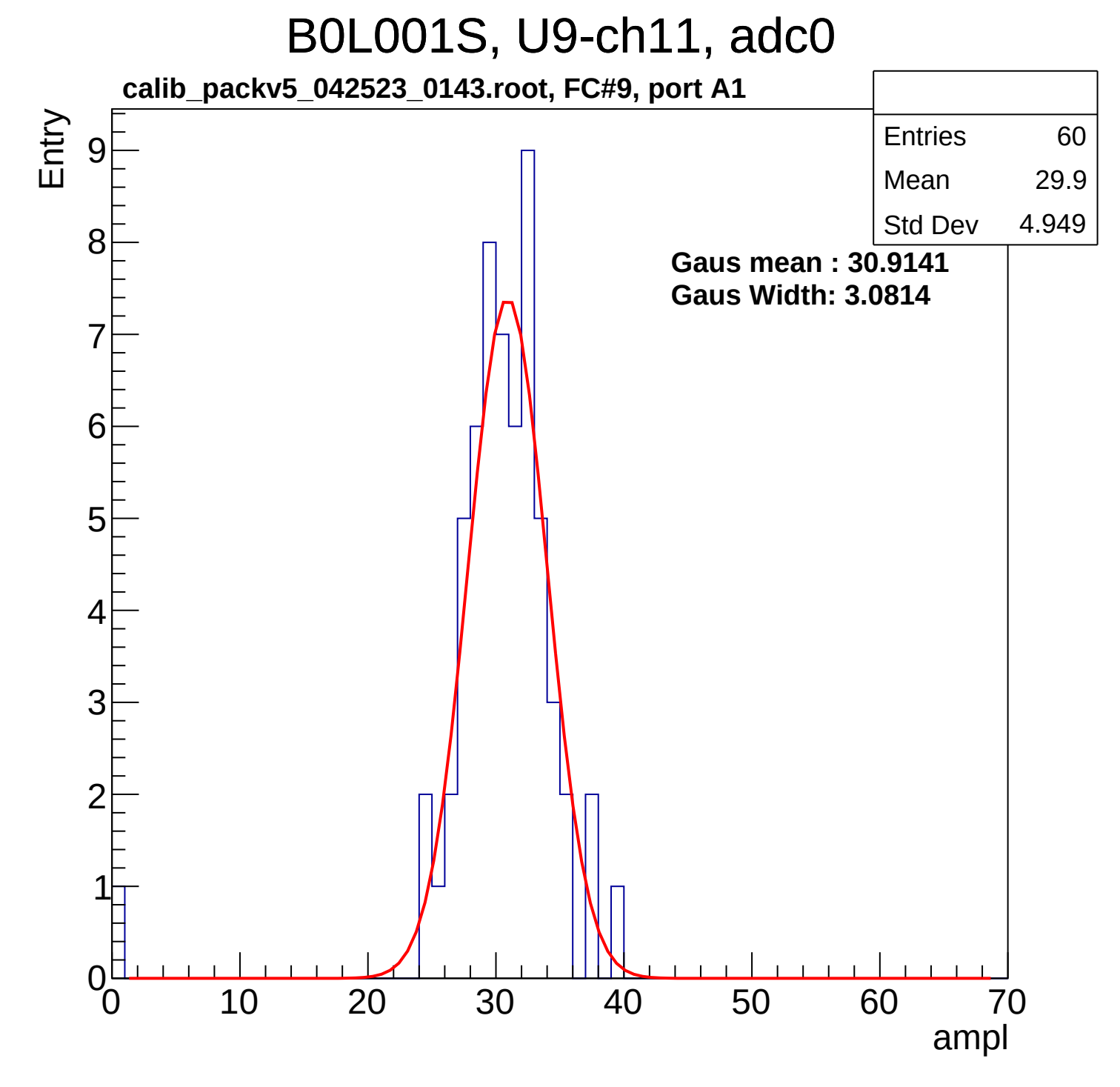
60
70


B0L001S, U9-ch10, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch11, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 29.9
Std Dev 4.949
Gaus mean : 30.9141
Gaus Width: 3.0814
Entry
ampl
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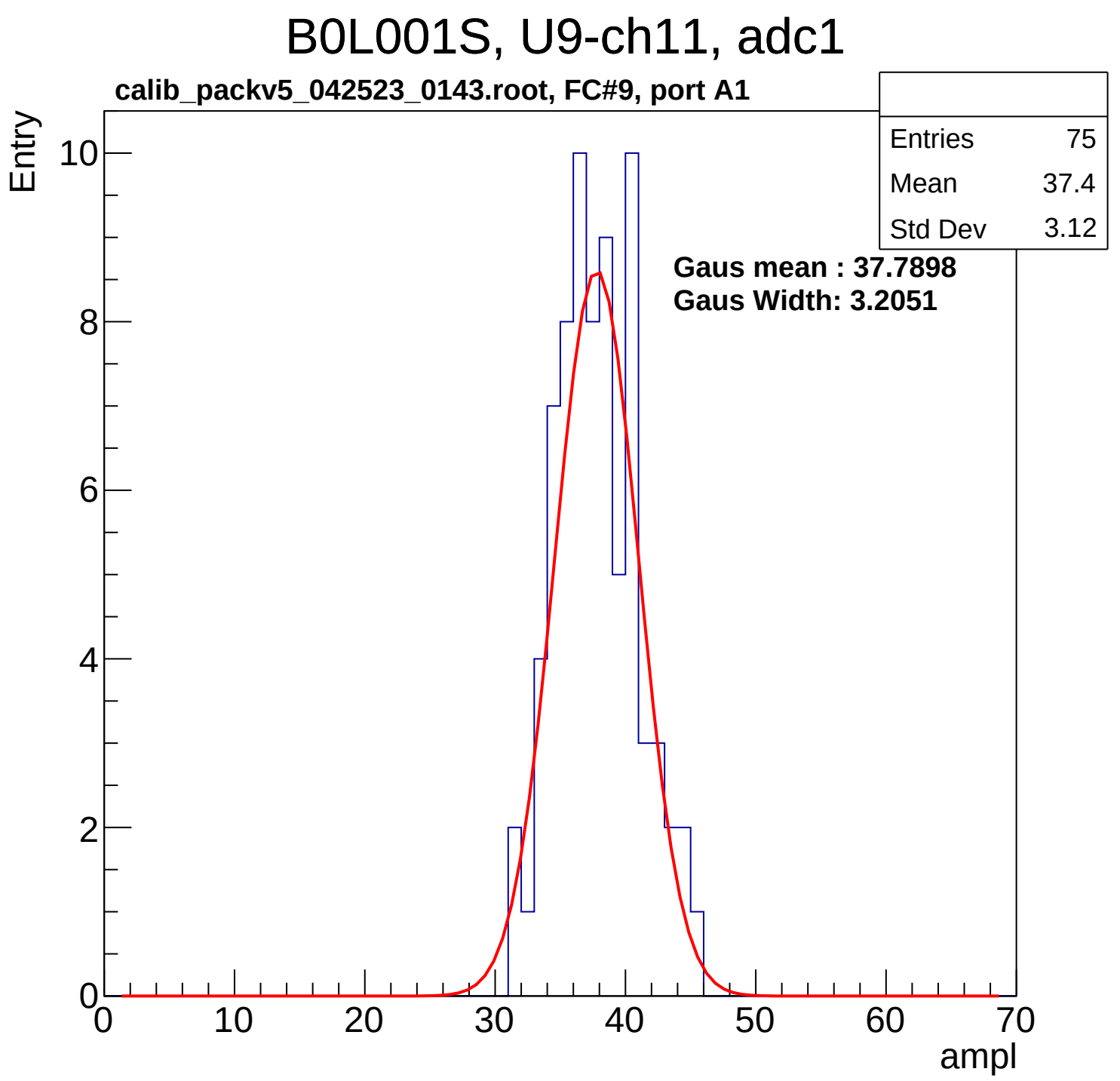
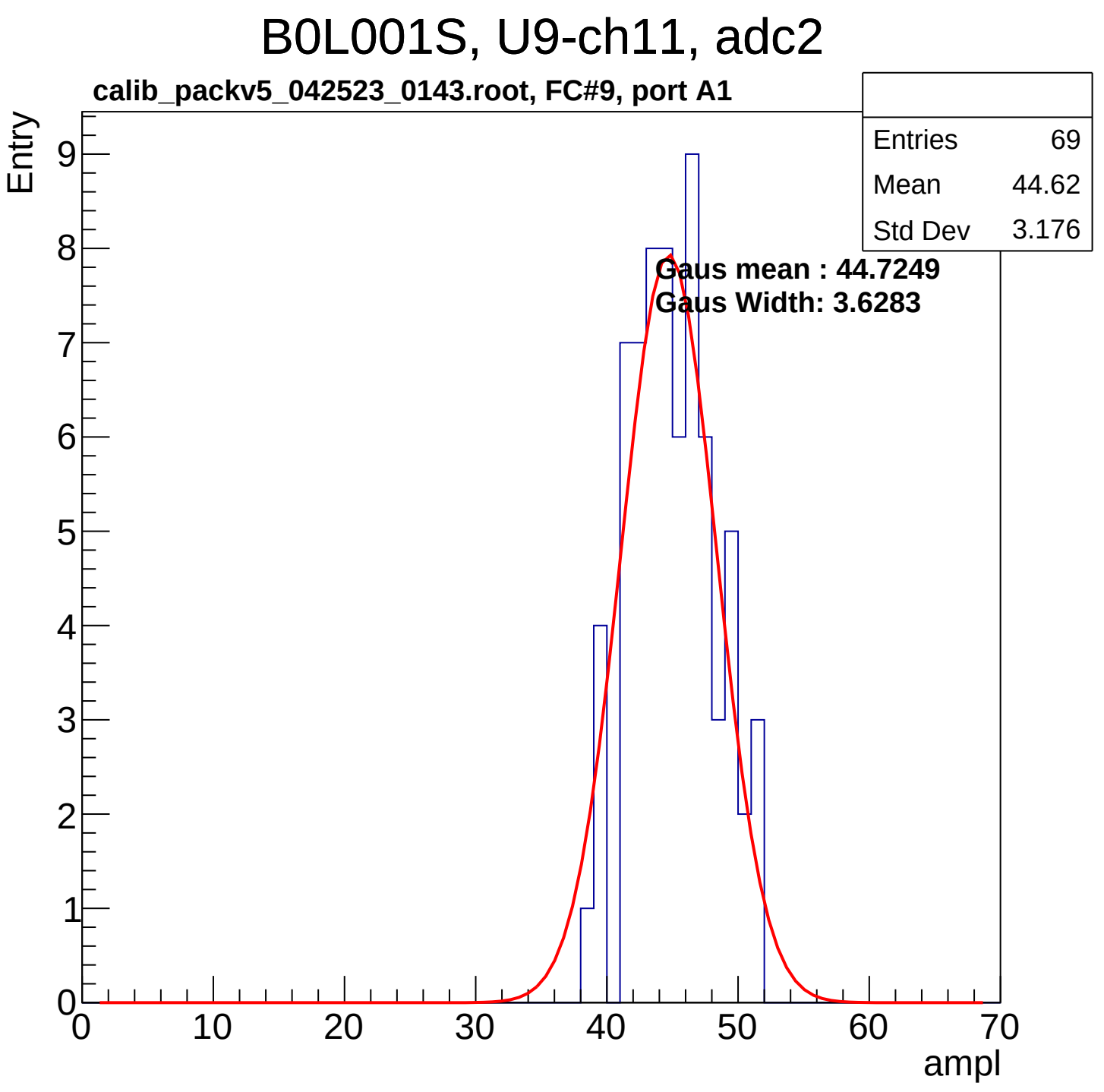
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch11, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.4
Std Dev 3.12
Gaus mean : 37.7898
Gaus Width: 3.2051
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch11, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.62
Std Dev 3.176
Gaus mean : 44.7249
Gaus Width: 3.6283
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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60
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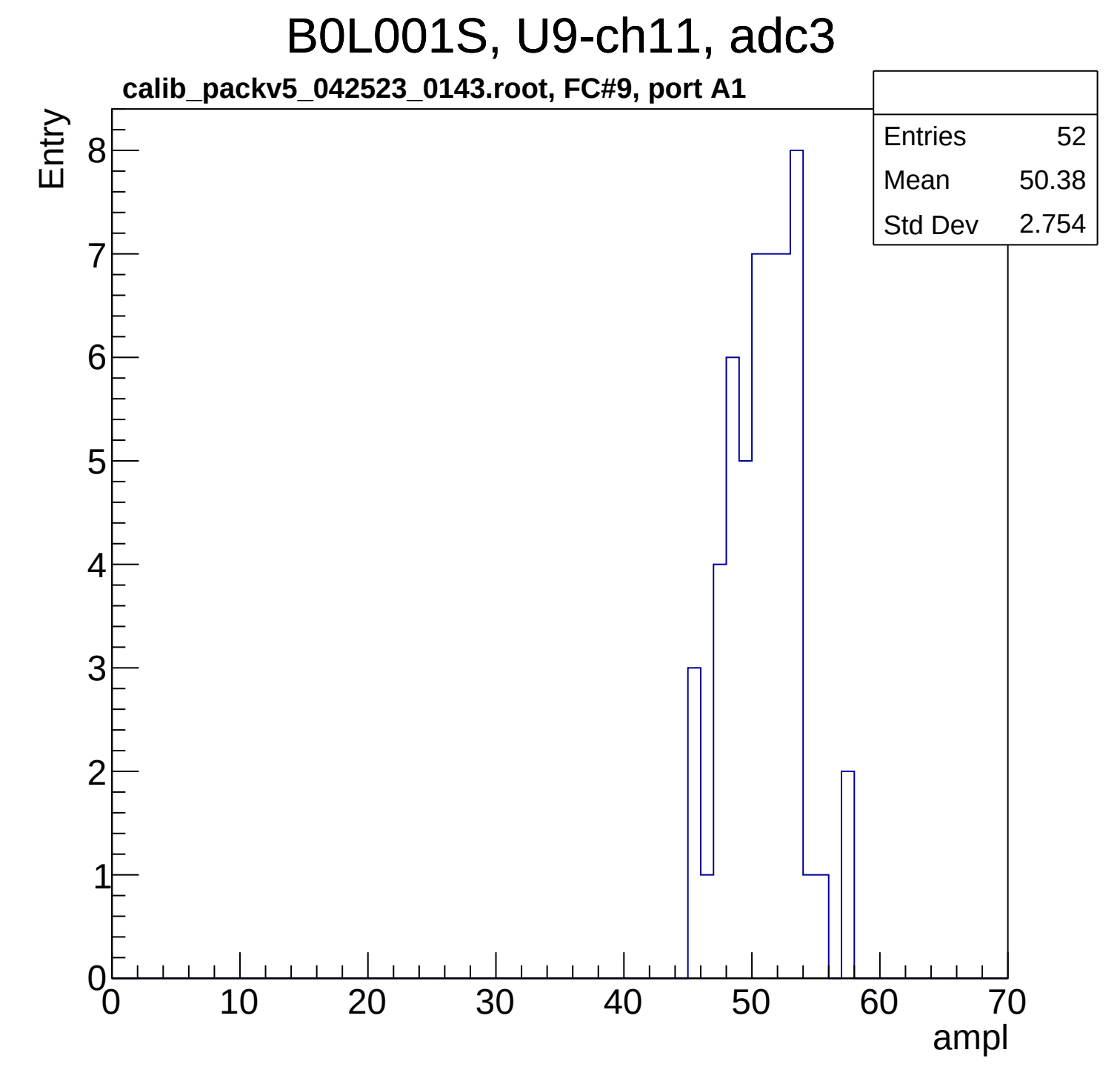
B0L001S, U9-ch11, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 50.38
Std Dev 2.754
Entry
ampl
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2
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4
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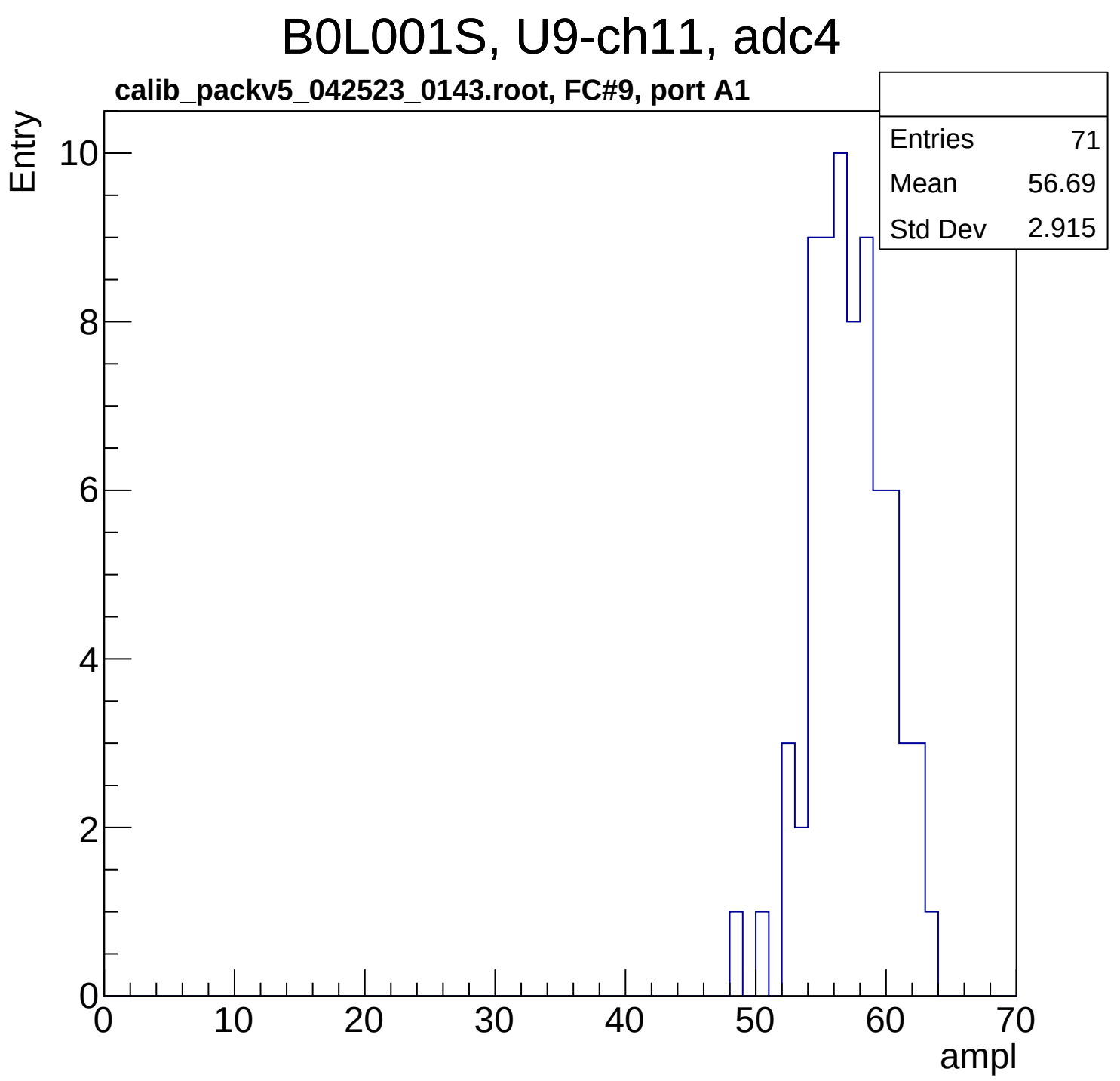
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch11, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 56.69
Std Dev 2.915
Entry
ampl
0
2
4
6
8
10
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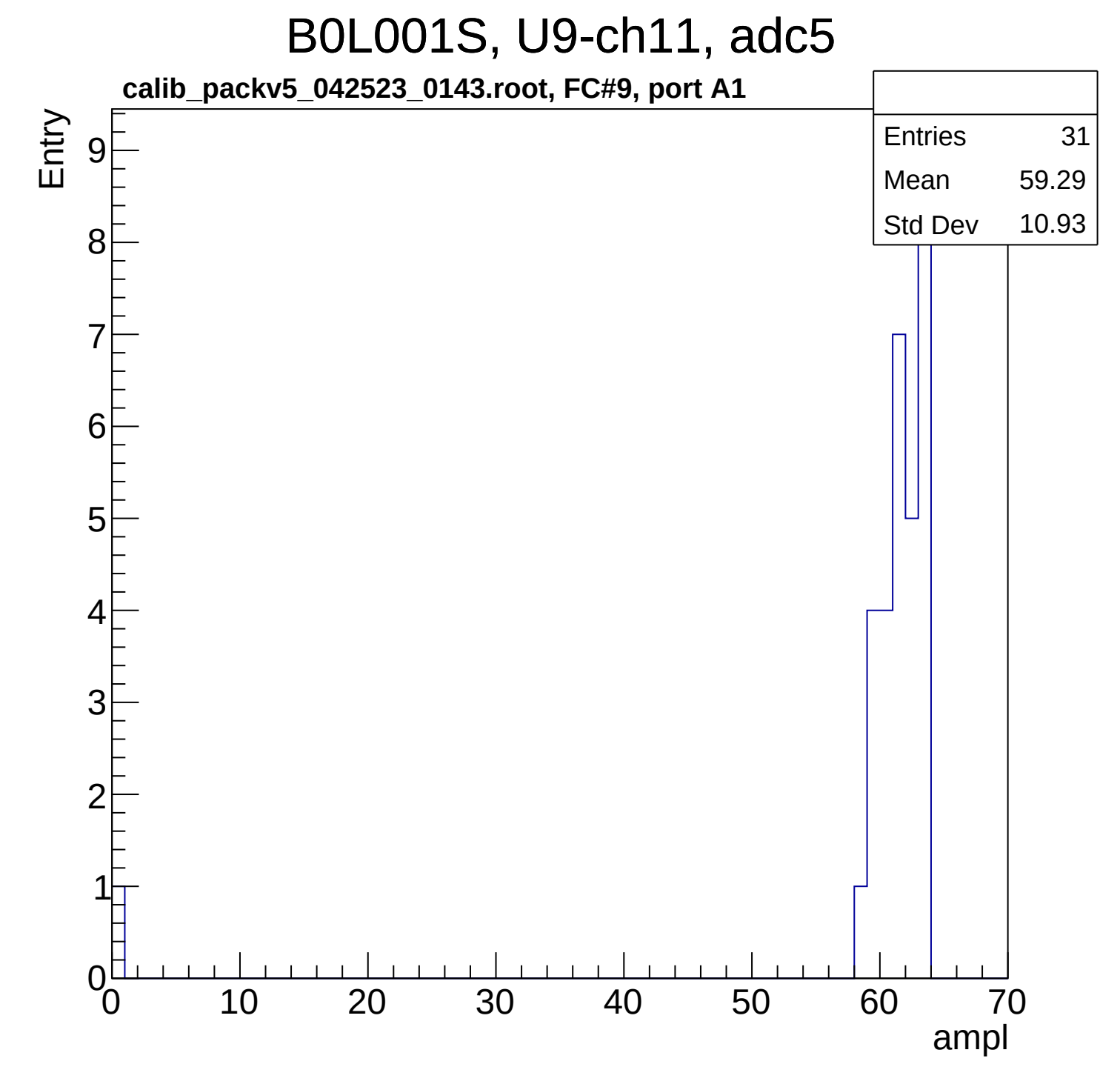
B0L001S, U9-ch11, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59.29
Std Dev 10.93
Entry
ampl
0
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2
3
4
5
6
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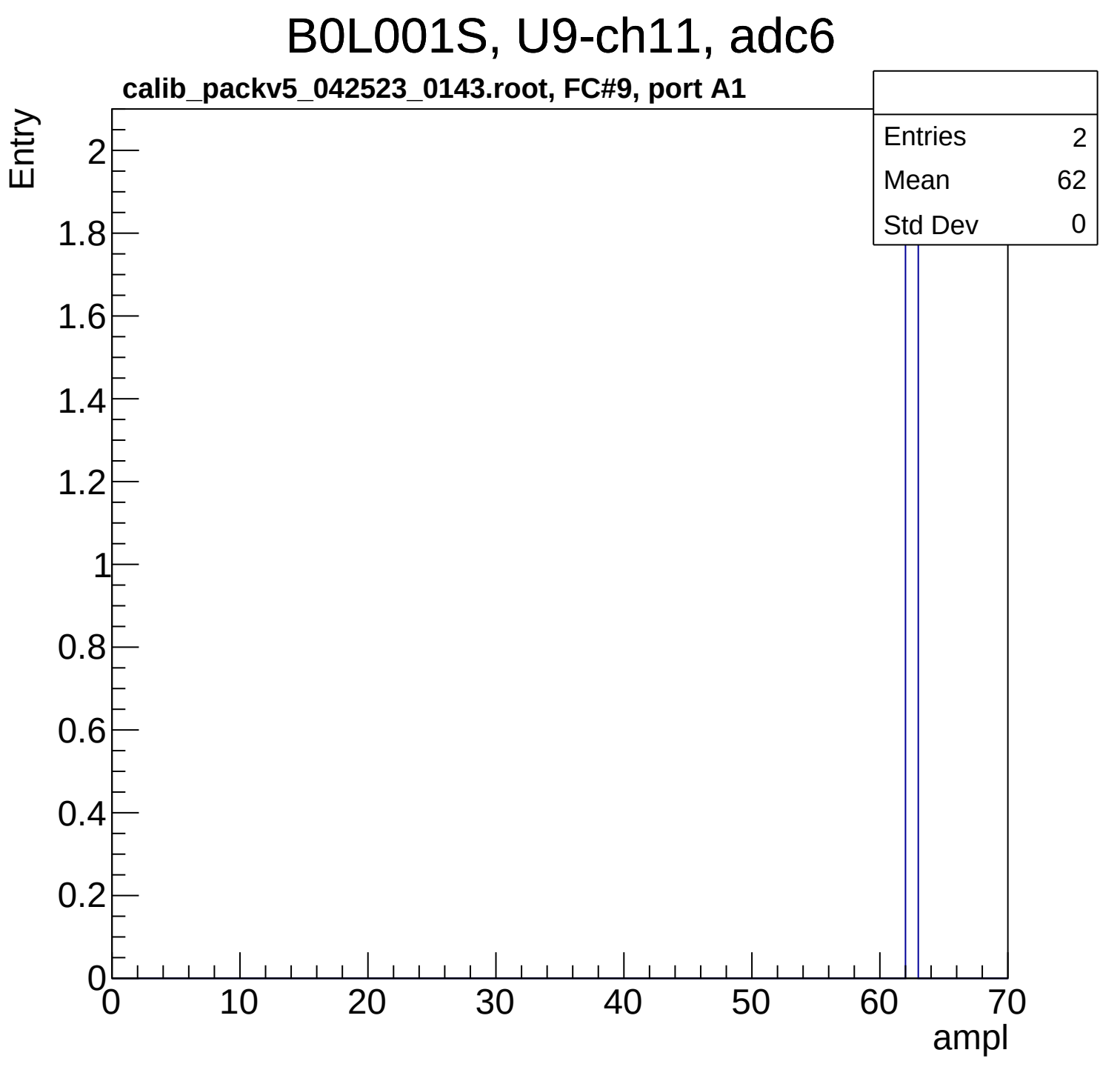
B0L001S, U9-ch11, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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40
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60
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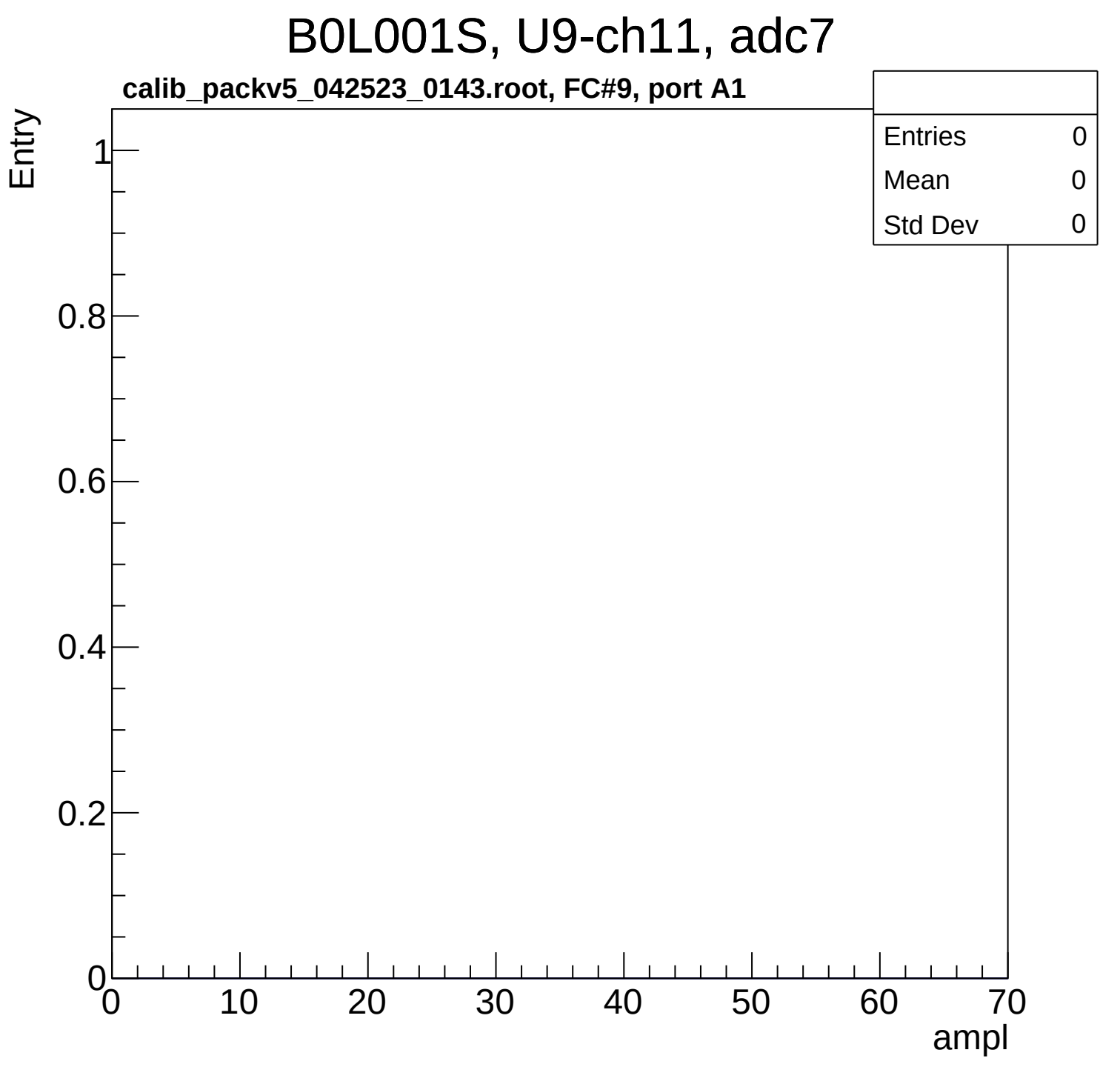
B0L001S, U9-ch11, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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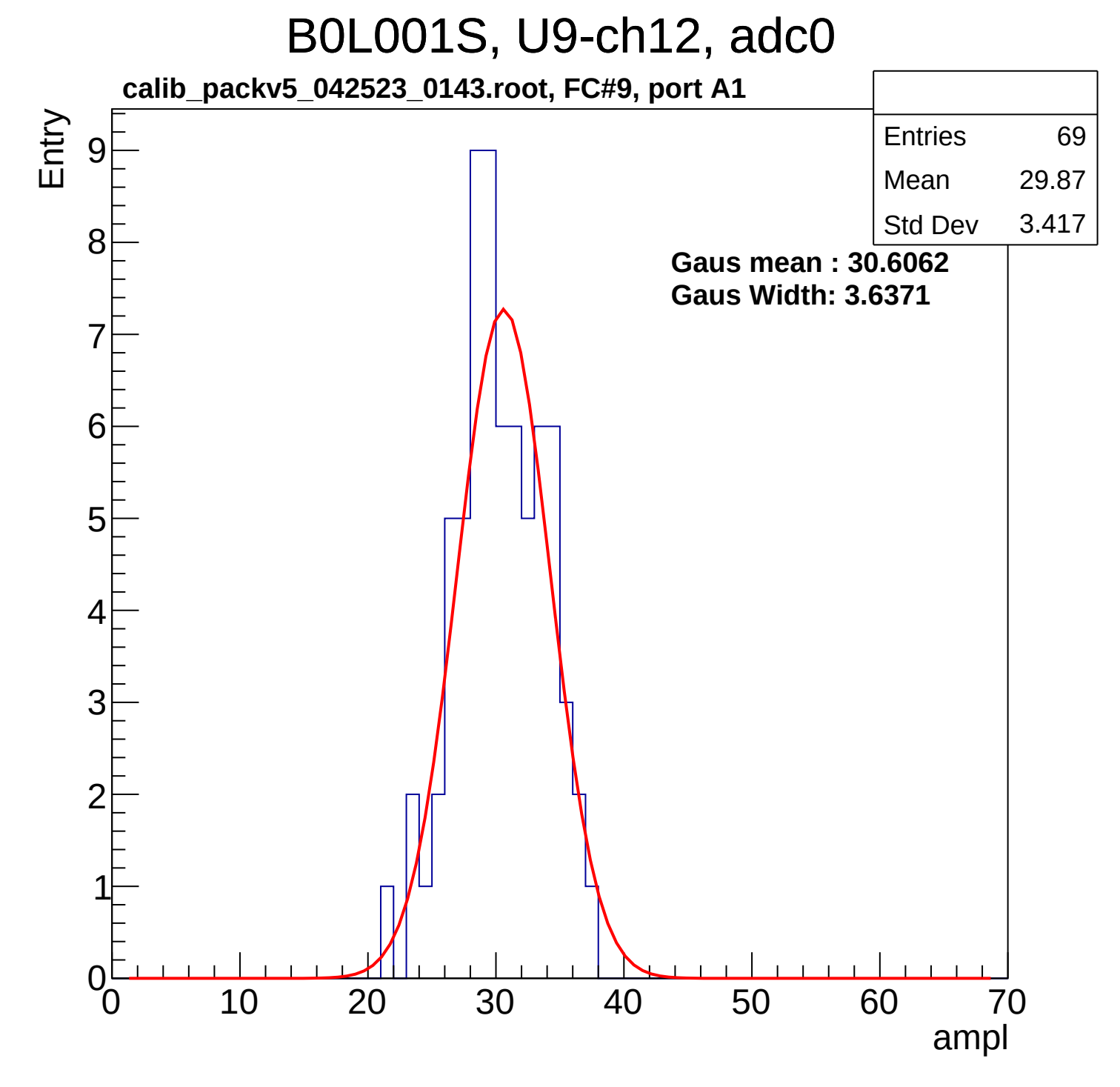
40
50
60
70
ampl


B0L001S, U9-ch12, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.87
Std Dev 3.417
Gaus mean : 30.6062
Gaus Width: 3.6371
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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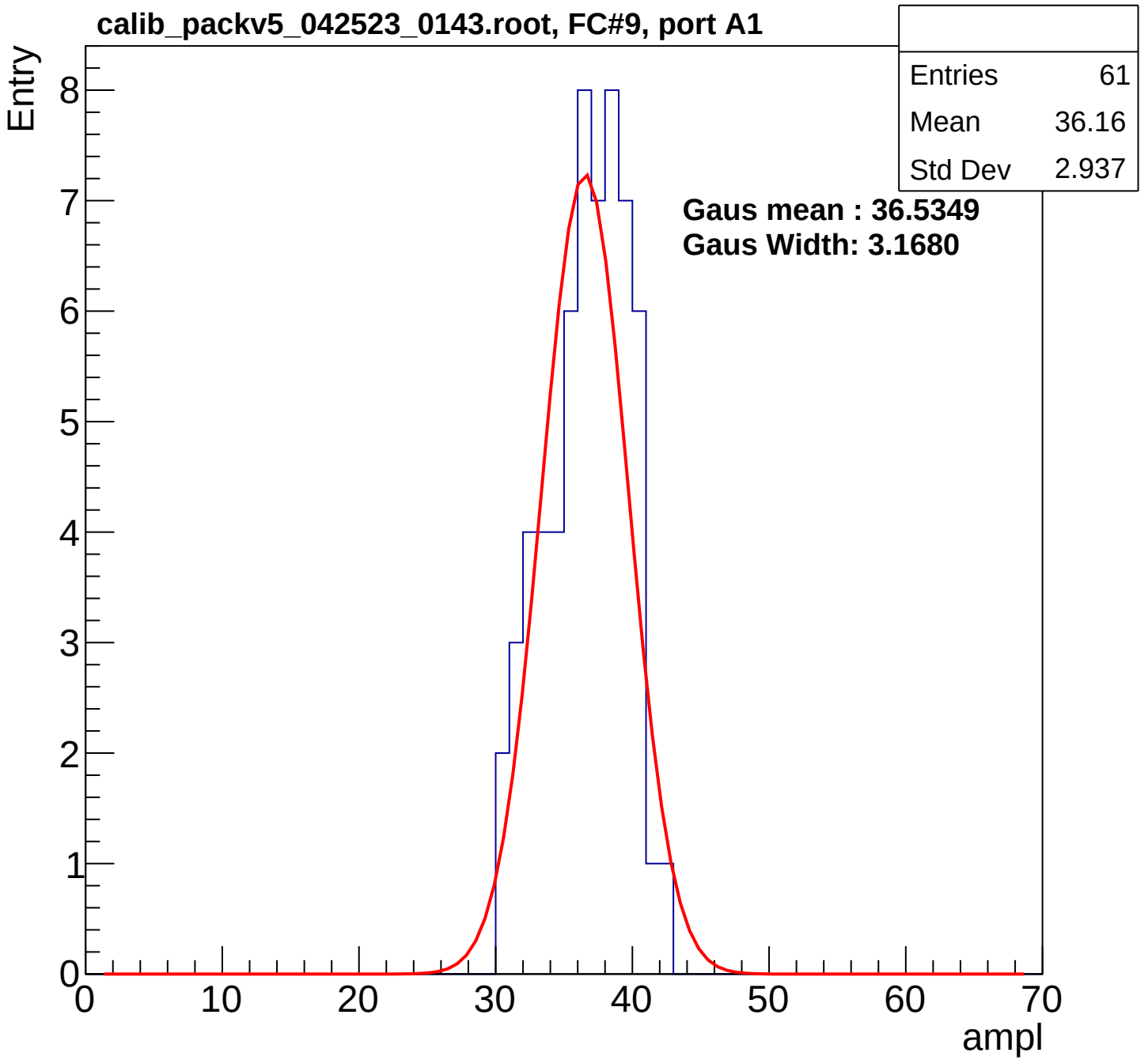
B0L001S, U9-ch12, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 36.16
Std Dev 2.937
Gaus mean : 36.5349
Gaus Width: 3.1680
Entry
ampl
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1
2
3
4
5
6
7
8
0
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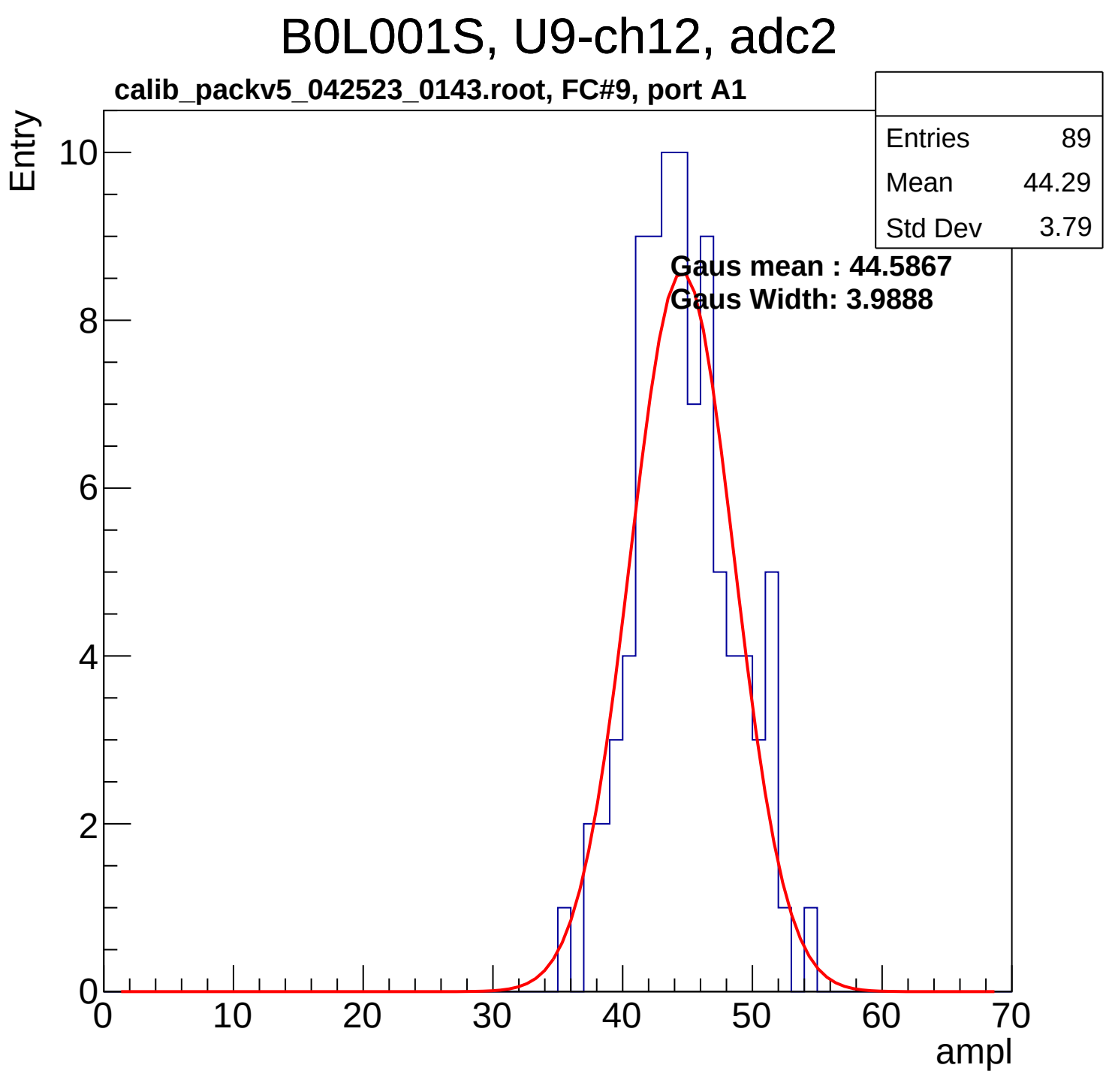
B0L001S, U9-ch12, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 89
Mean 44.29
Std Dev 3.79
Gaus mean : 44.5867
Gaus Width: 3.9888
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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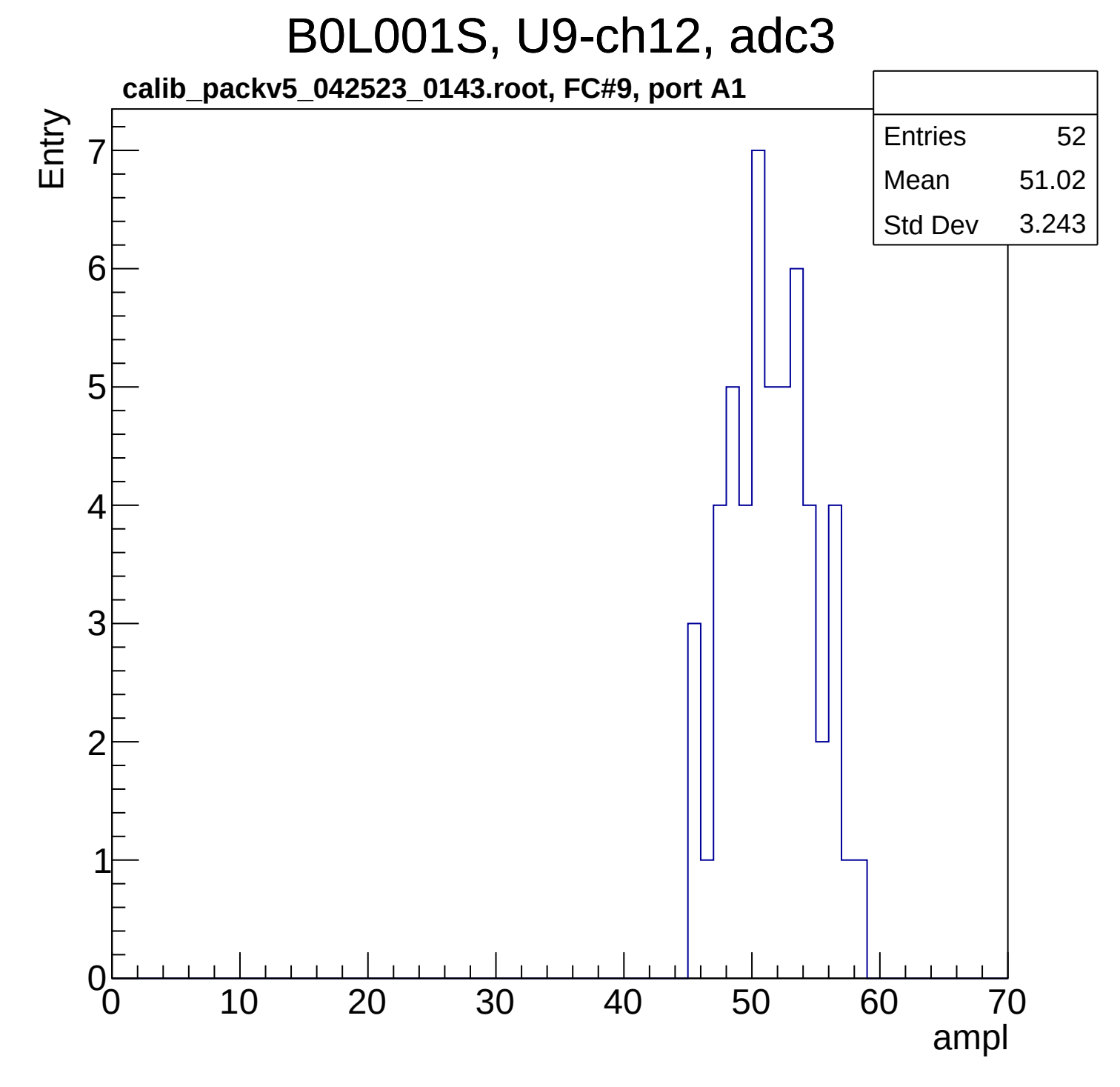
B0L001S, U9-ch12, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 51.02
Std Dev 3.243
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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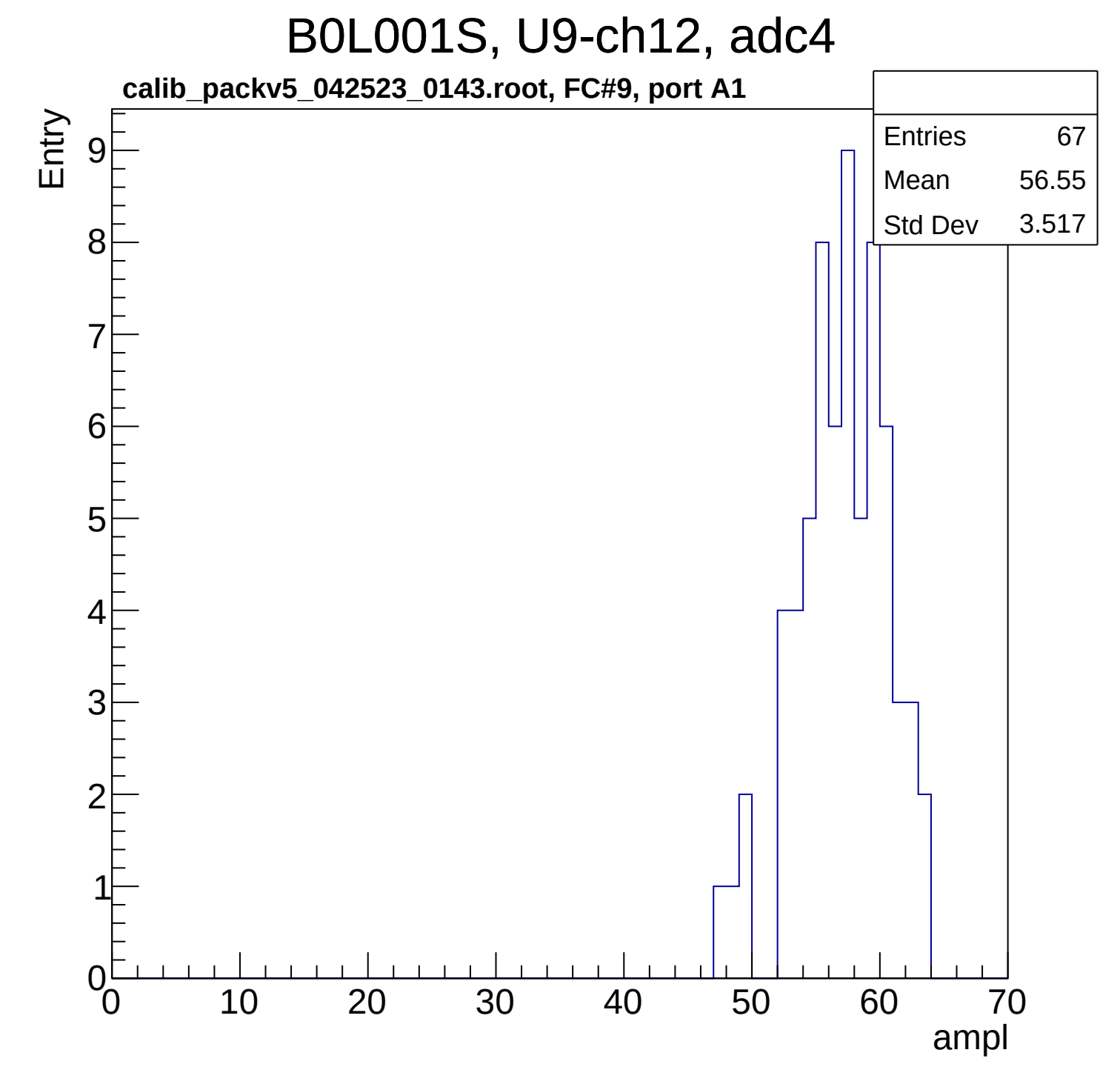
B0L001S, U9-ch12, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.55
Std Dev 3.517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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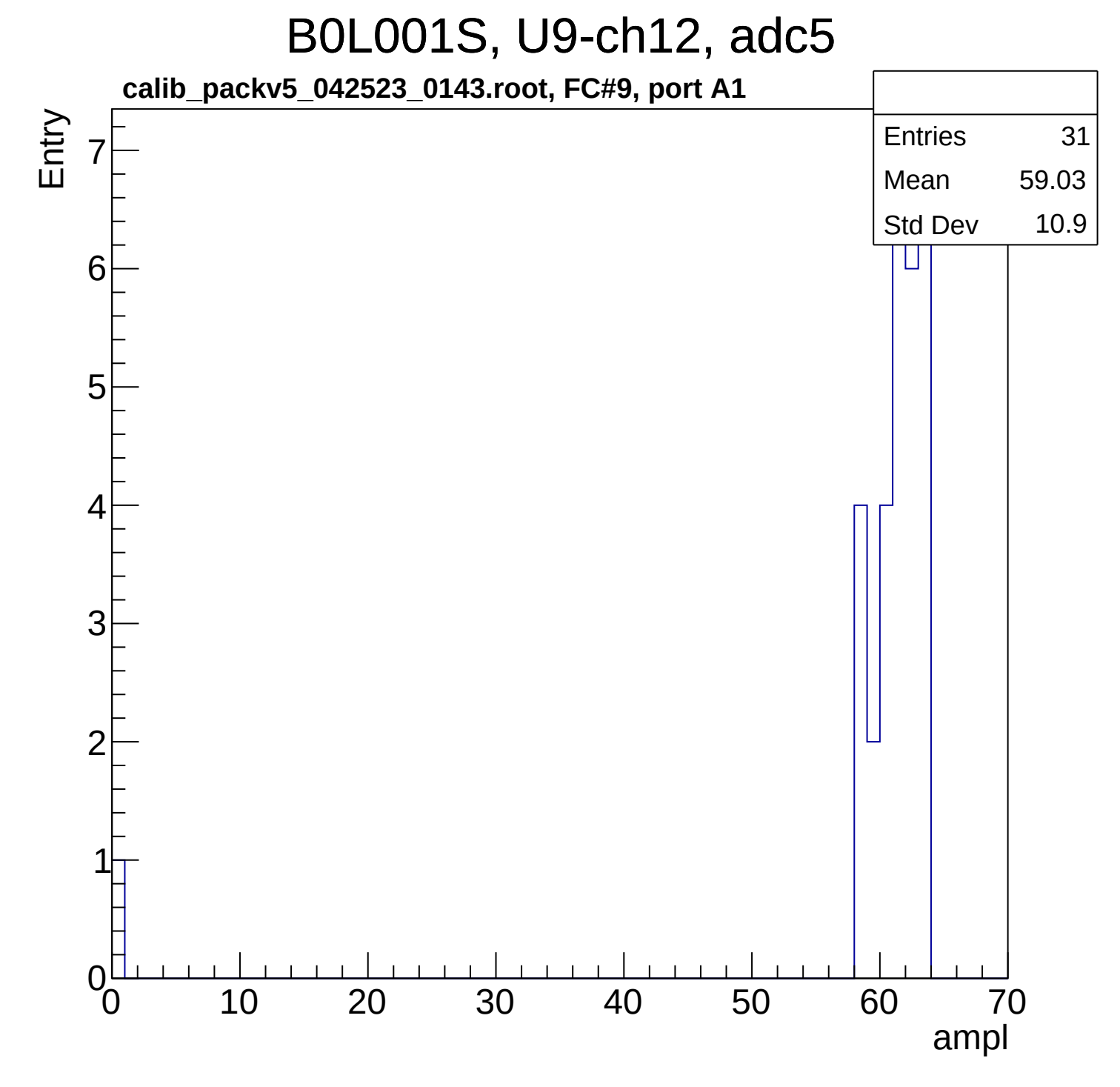
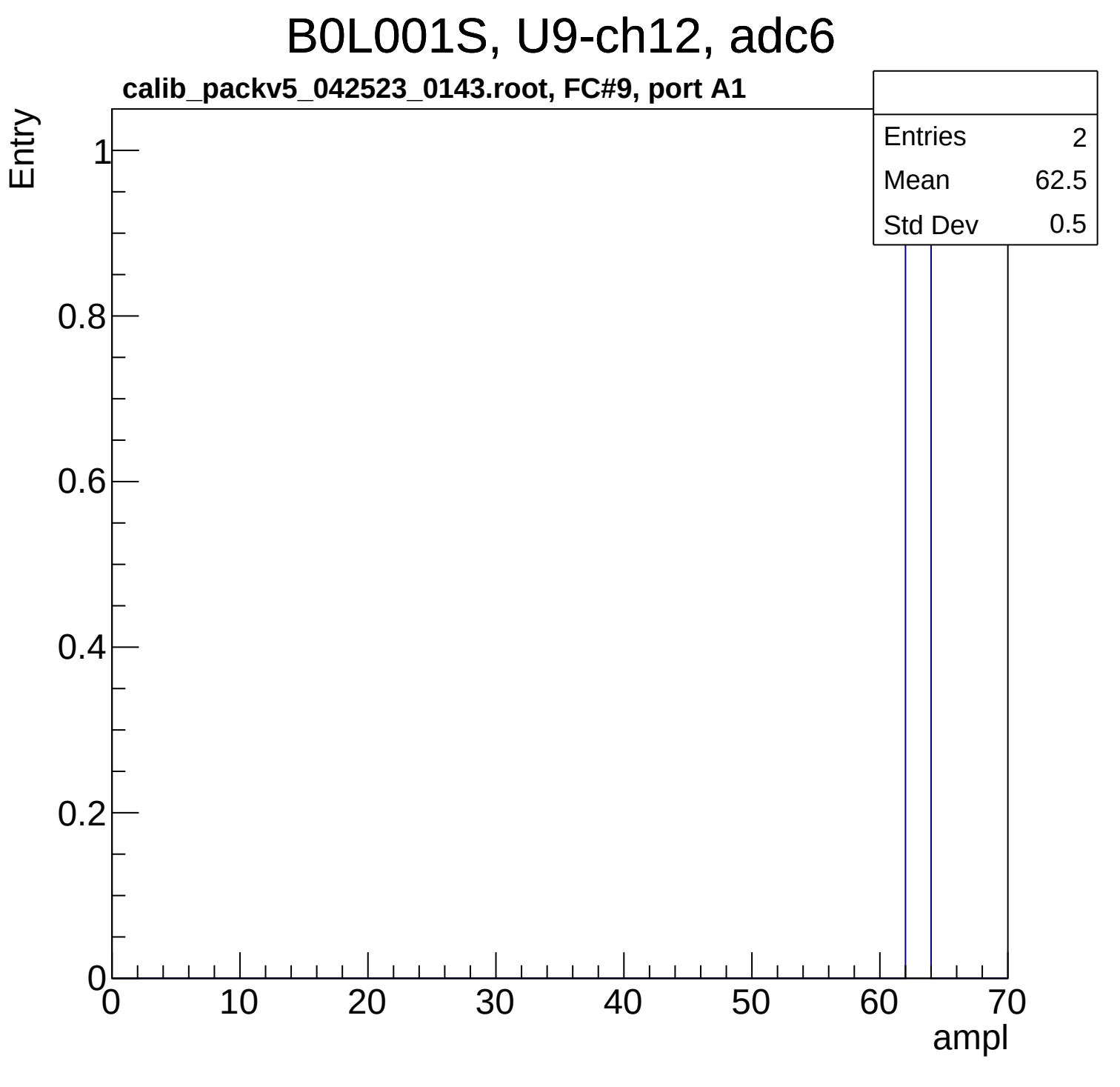
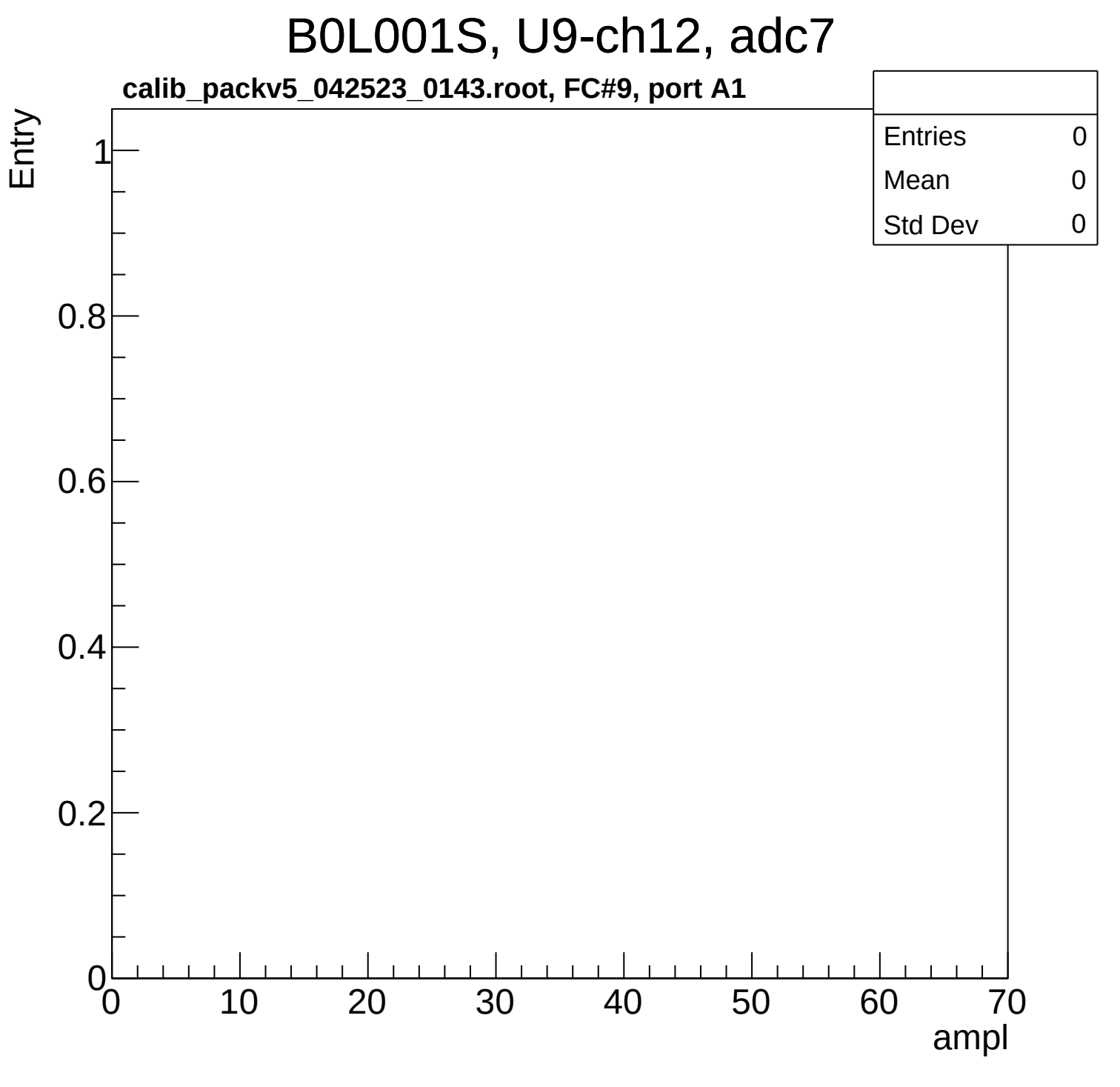

B0L001S, U9-ch12, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59.03
Std Dev 10.9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch12, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch12, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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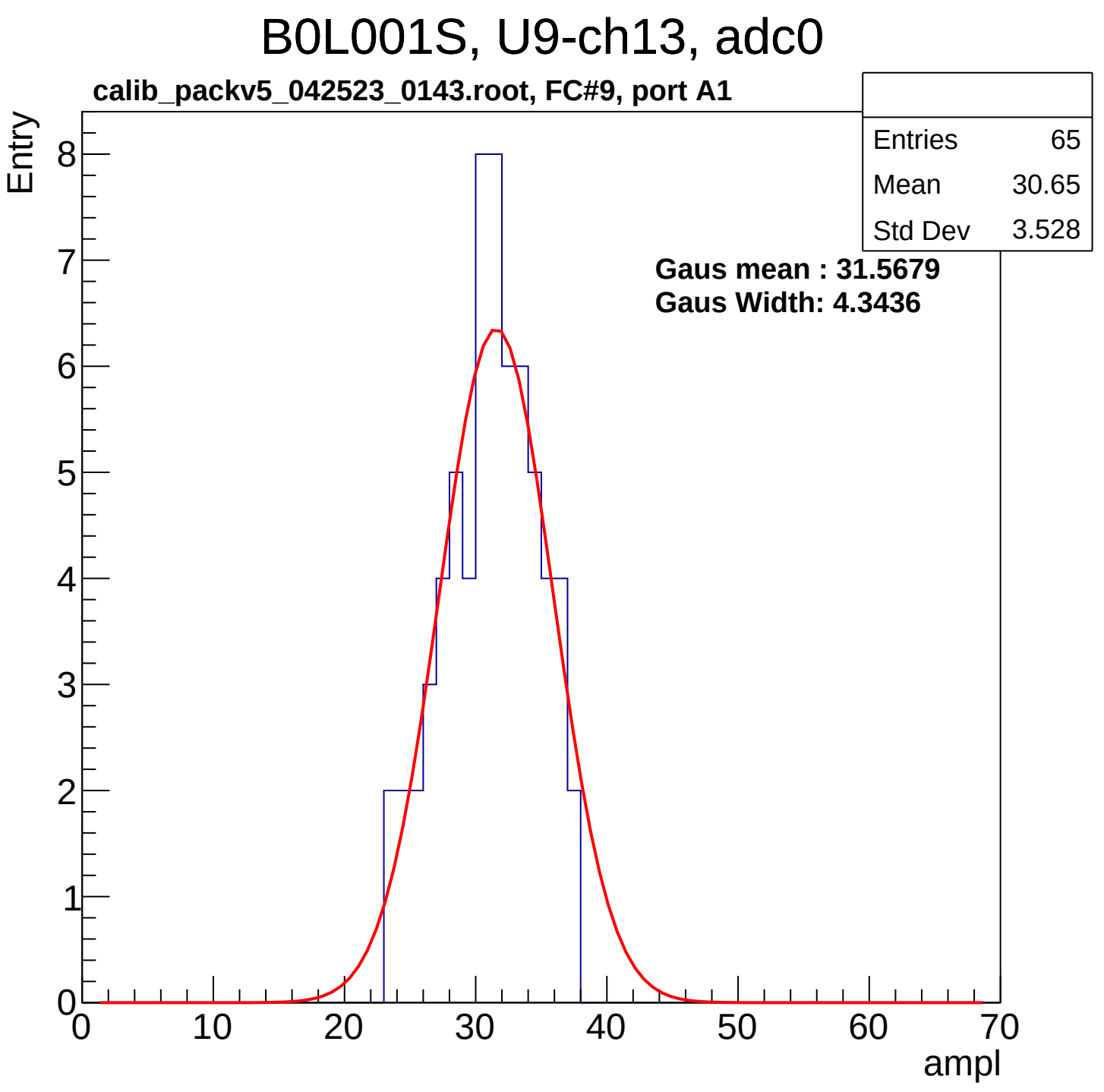
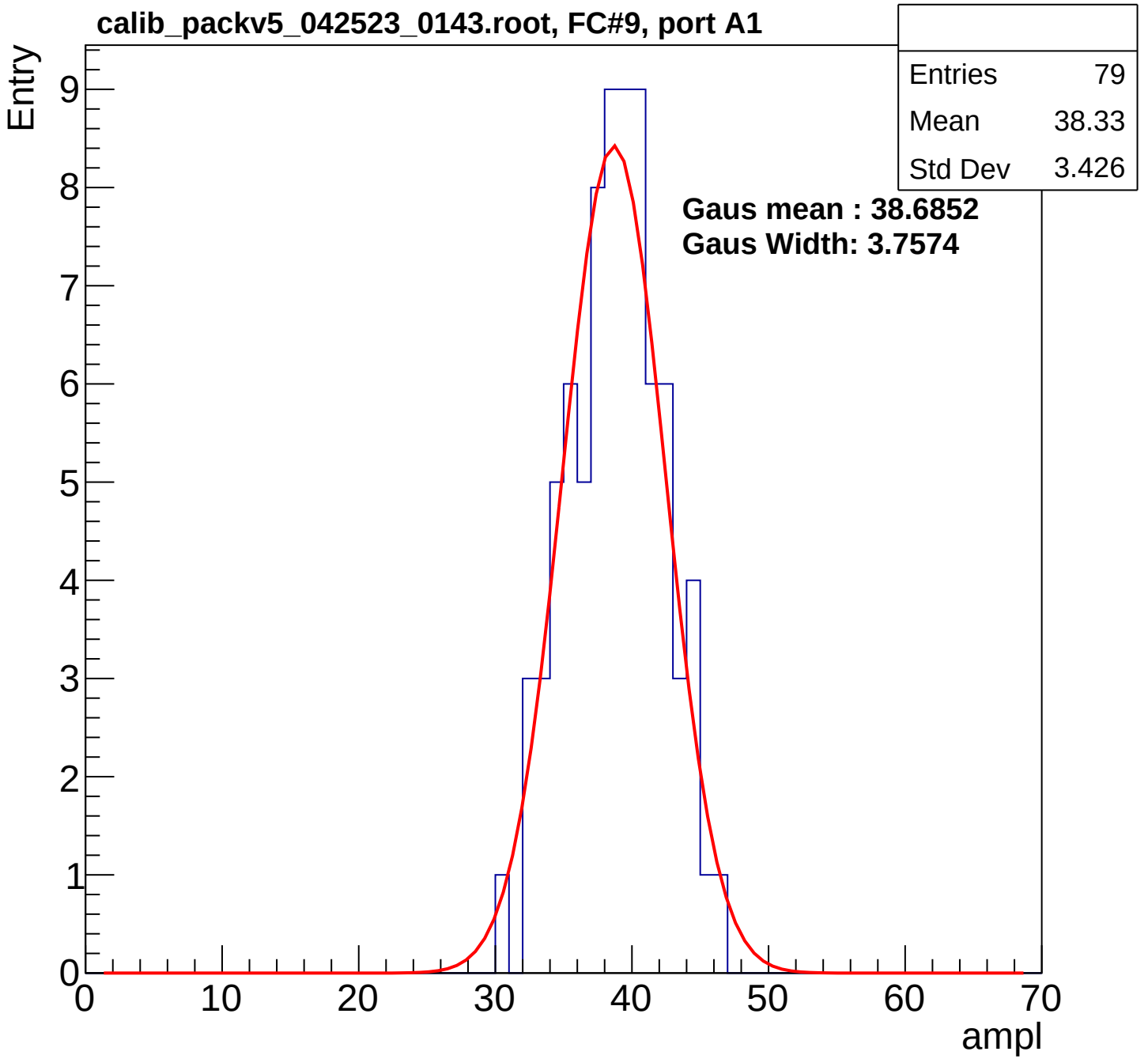
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch13, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.65
Std Dev 3.528
Gaus mean : 31.5679
Gaus Width: 4.3436
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch13, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 38.33
Std Dev 3.426
Gaus mean : 38.6852
Gaus Width: 3.7574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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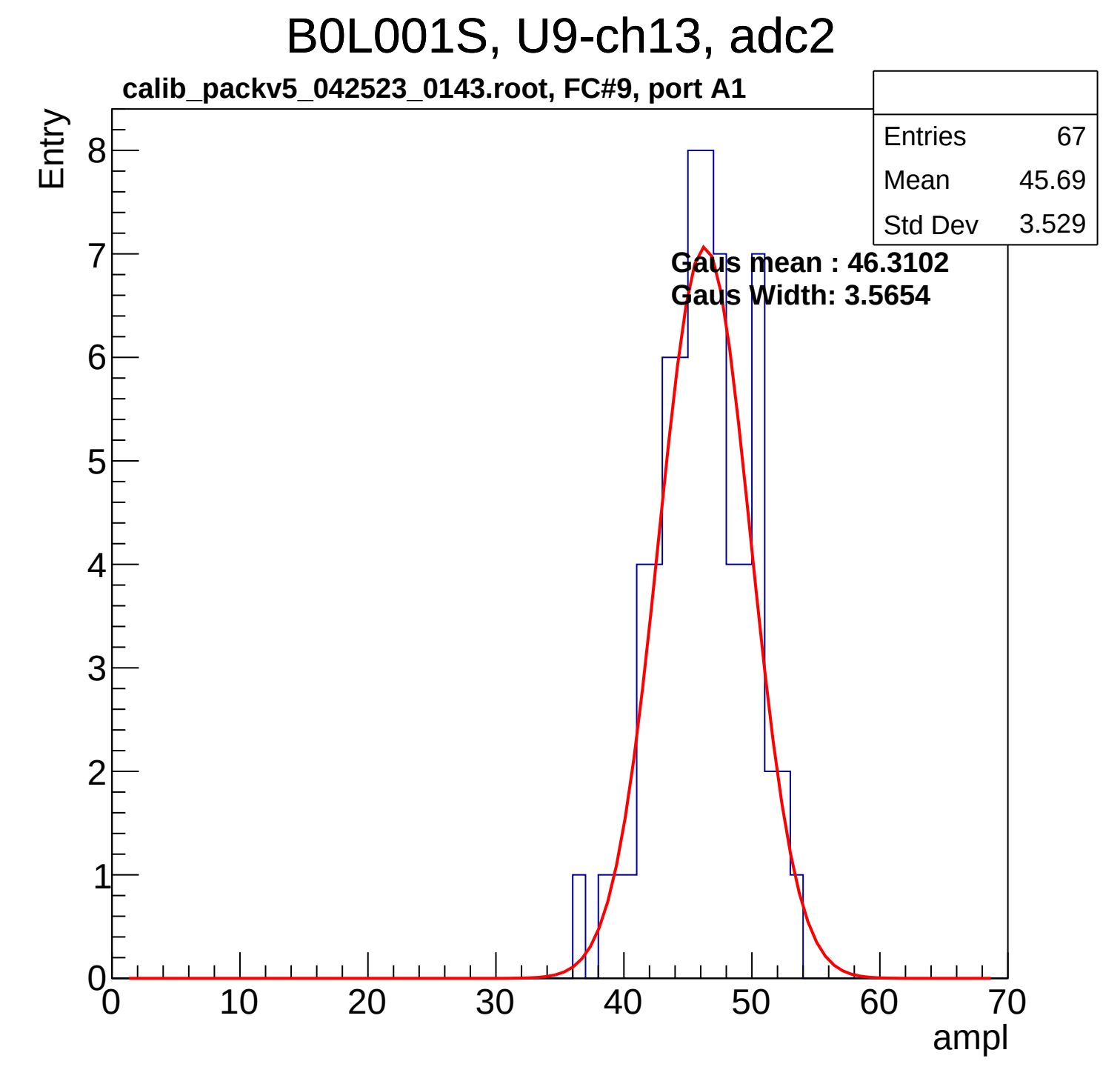
B0L001S, U9-ch13, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 45.69
Std Dev 3.529
Gaus mean : 46.3102
Gaus Width: 3.5654
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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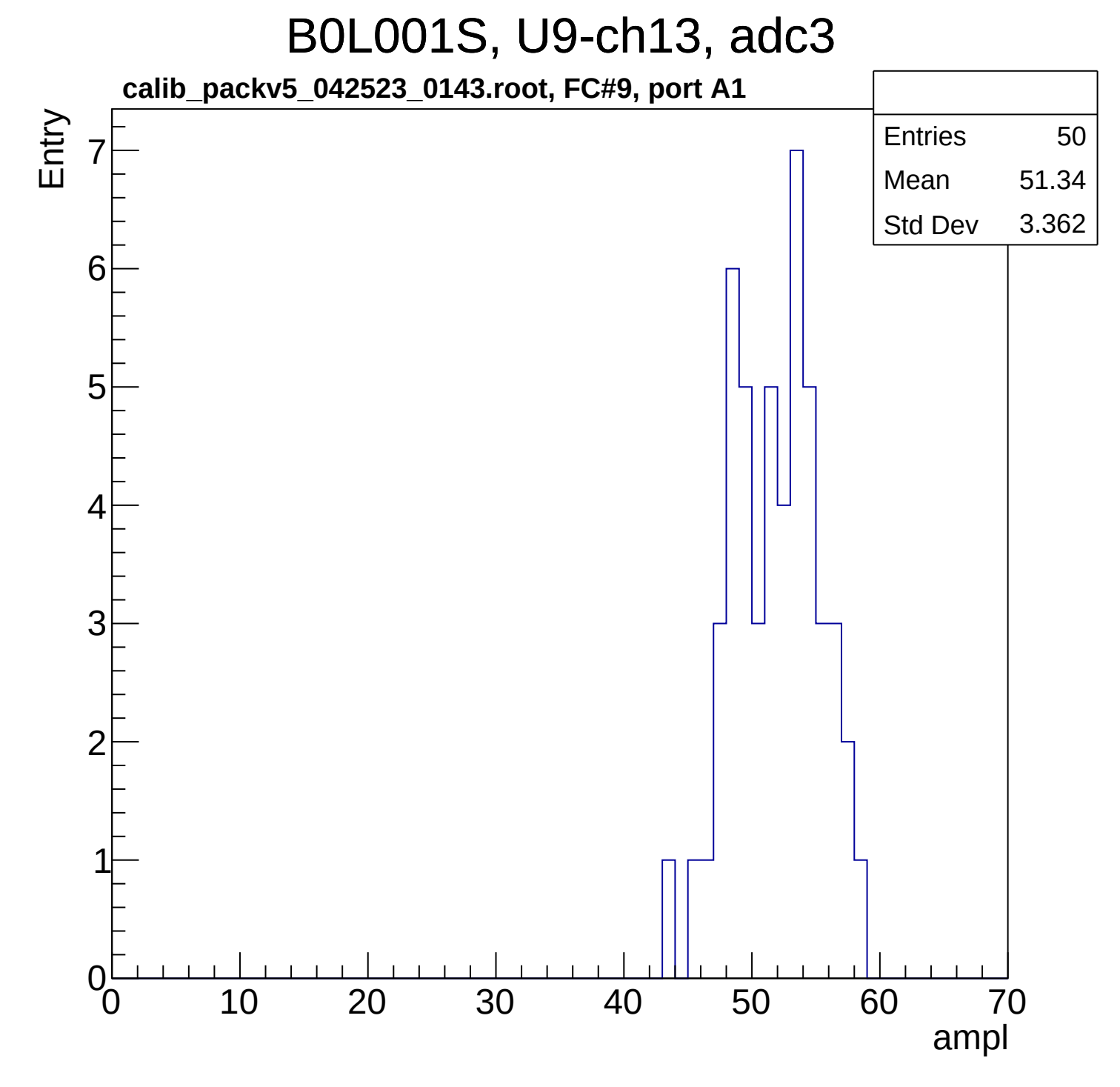
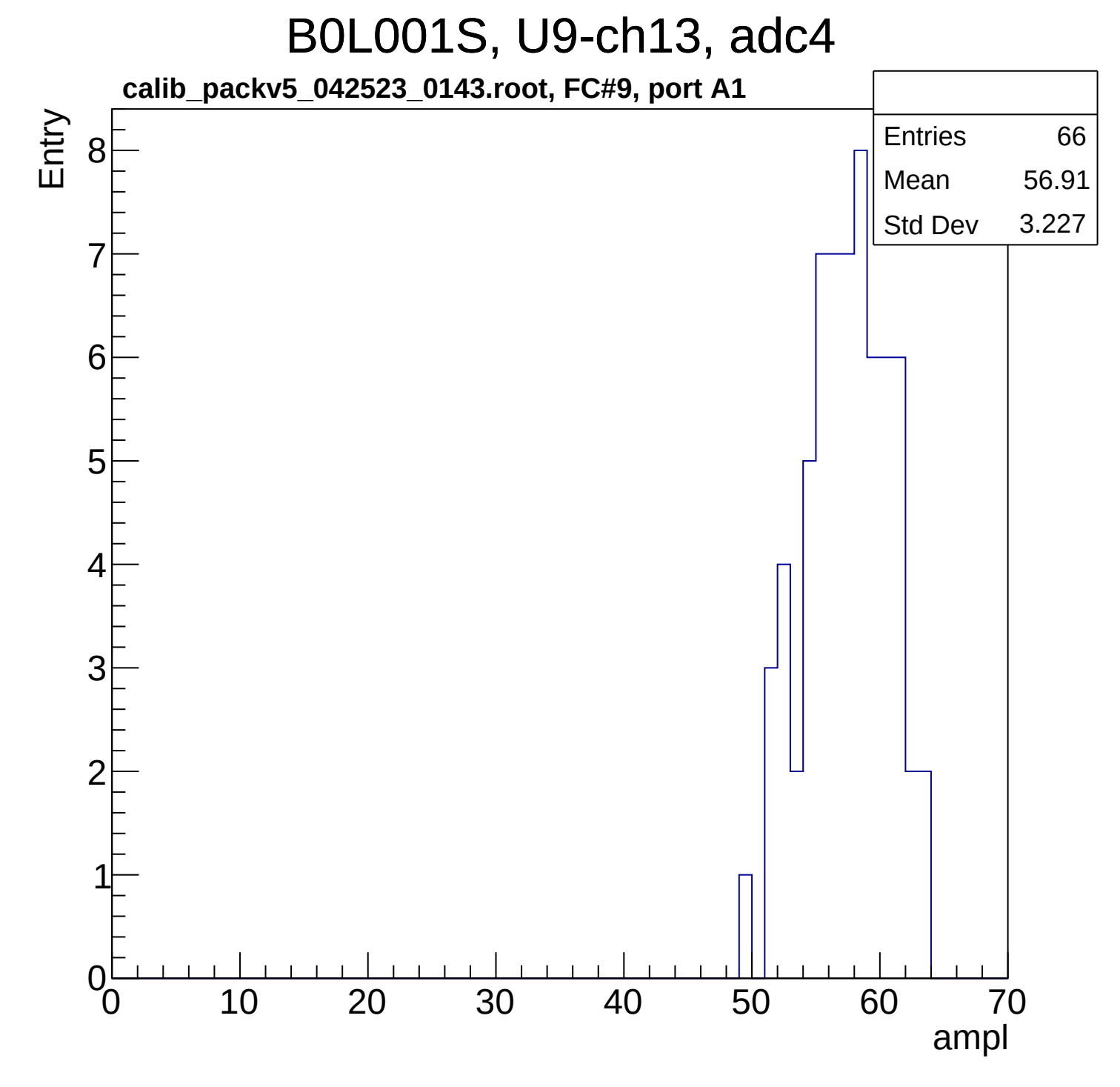

B0L001S, U9-ch13, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 51.34
Std Dev 3.362
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch13, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.91
Std Dev 3.227
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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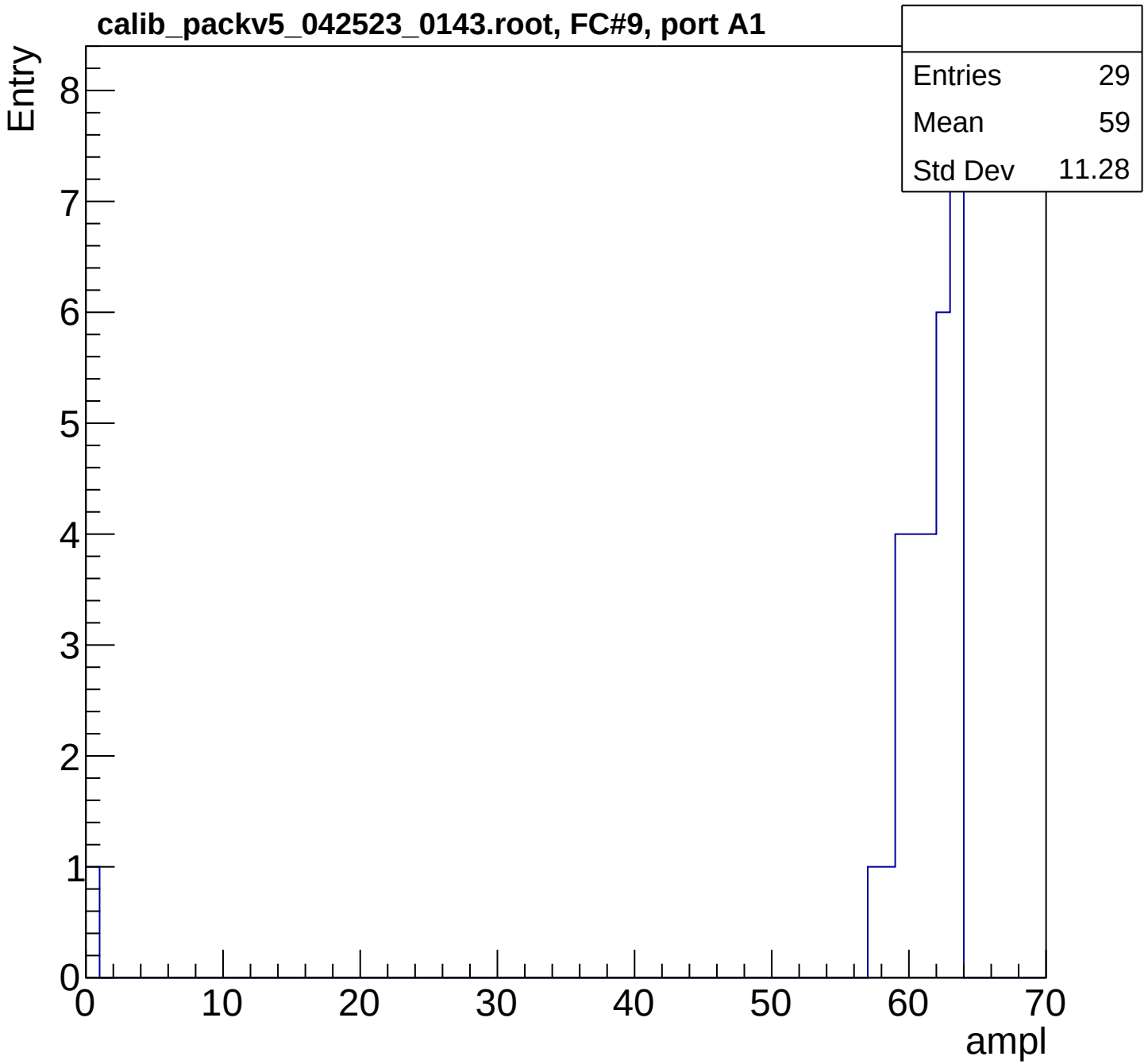
40
50
60
70


B0L001S, U9-ch13, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 59
Std Dev 11.28
Entry
0
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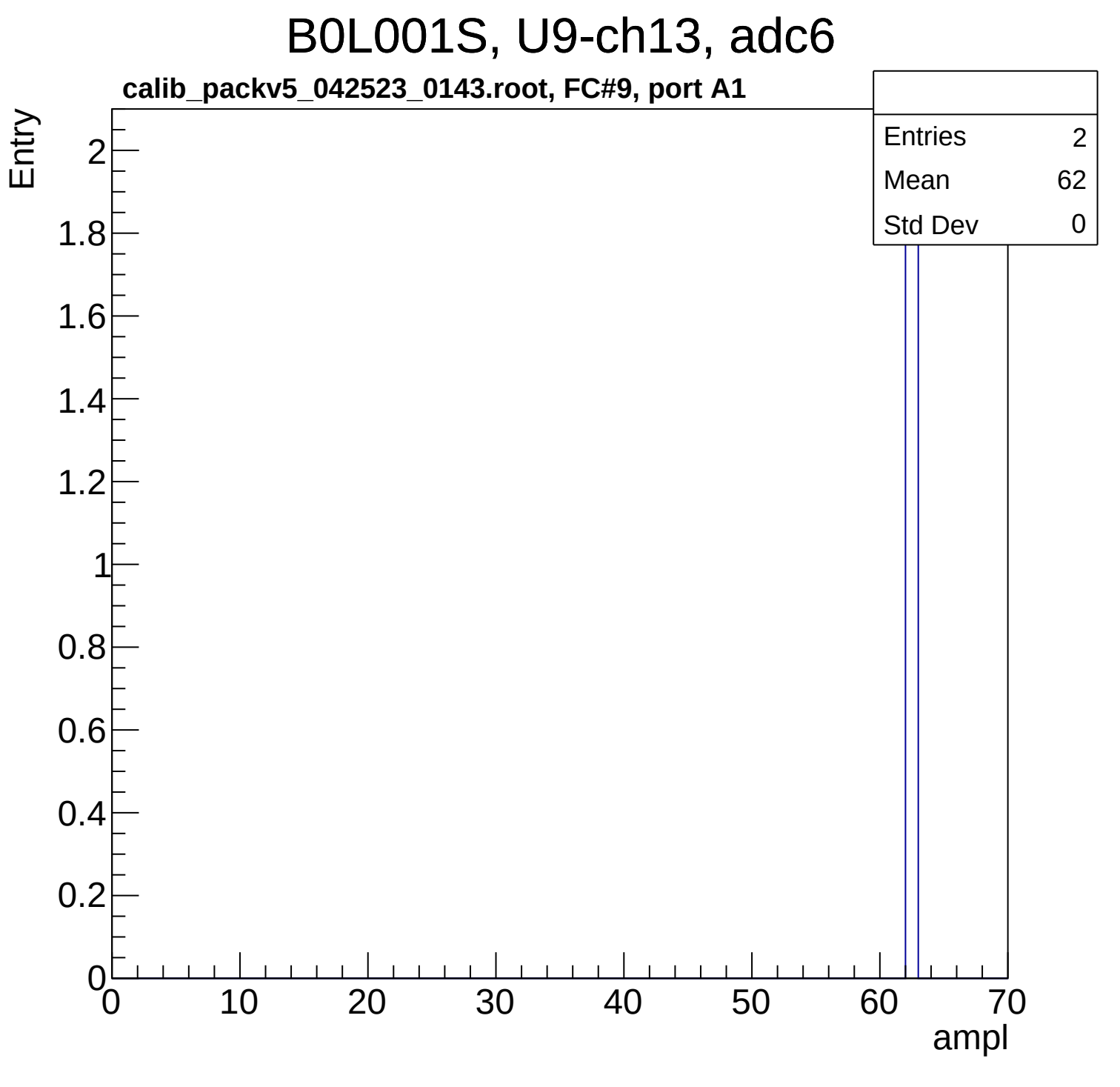
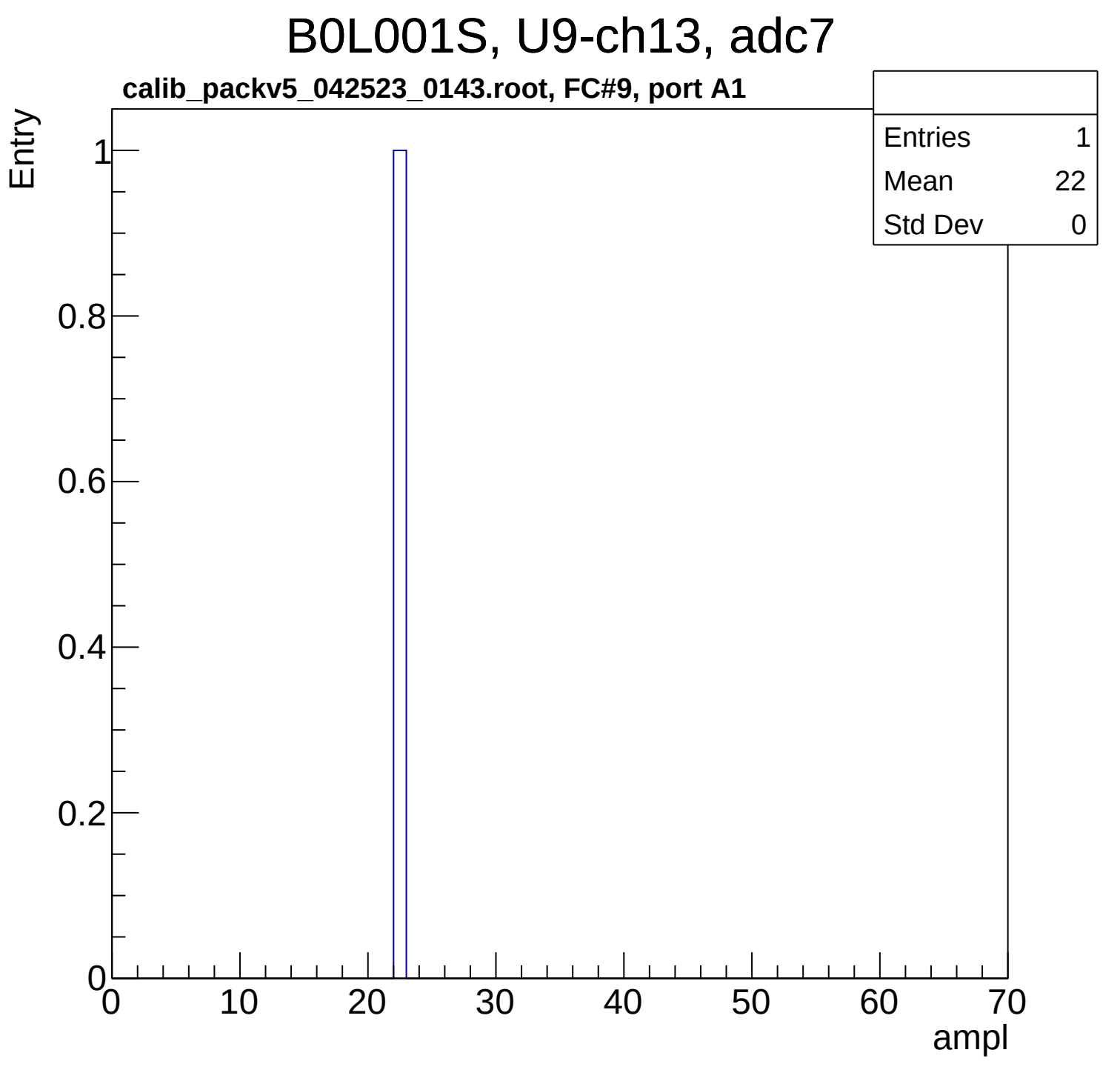
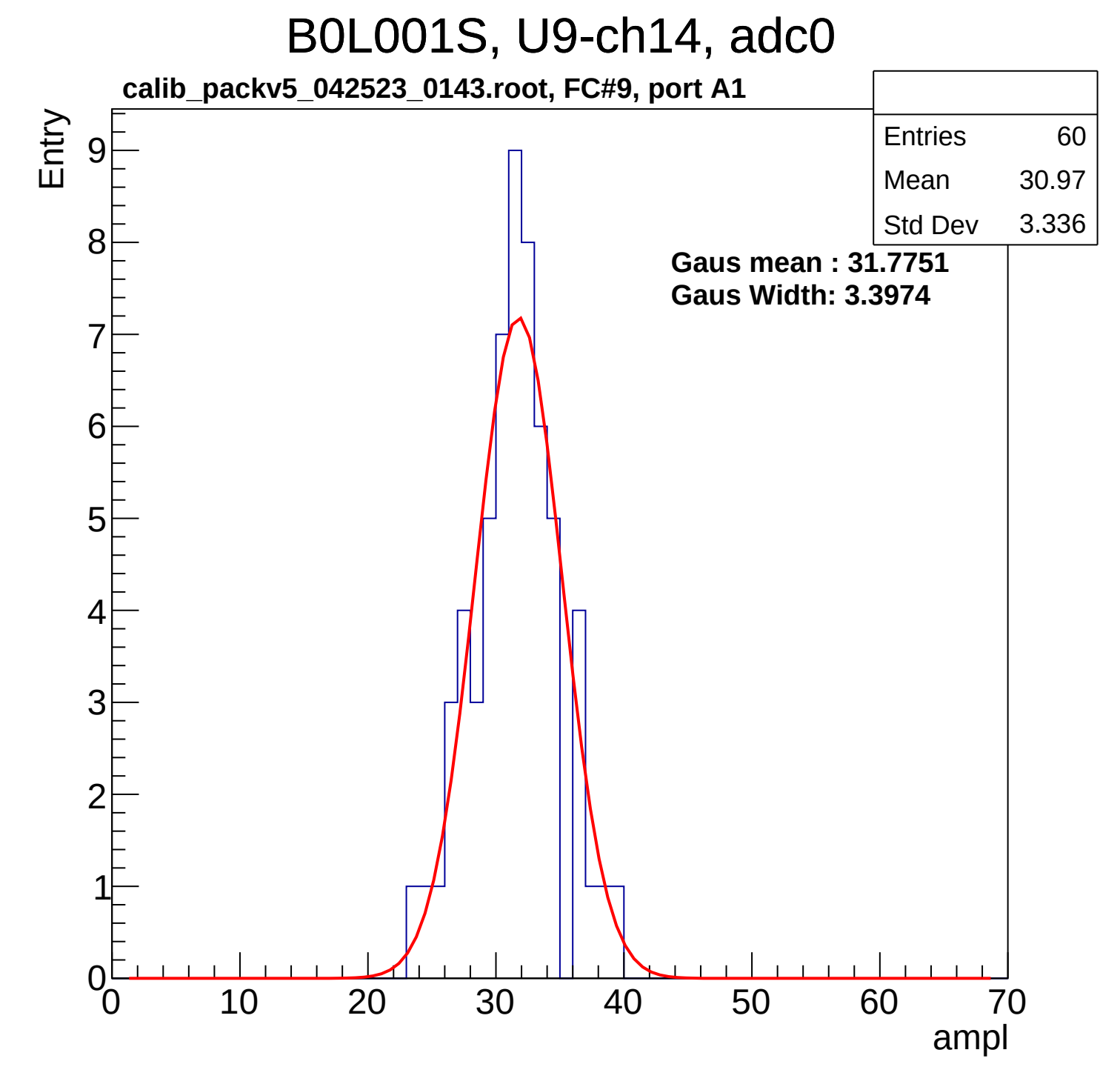
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch13, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch13, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch14, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.97
Std Dev 3.336
Gaus mean : 31.7751
Gaus Width: 3.3974
Entry
ampl
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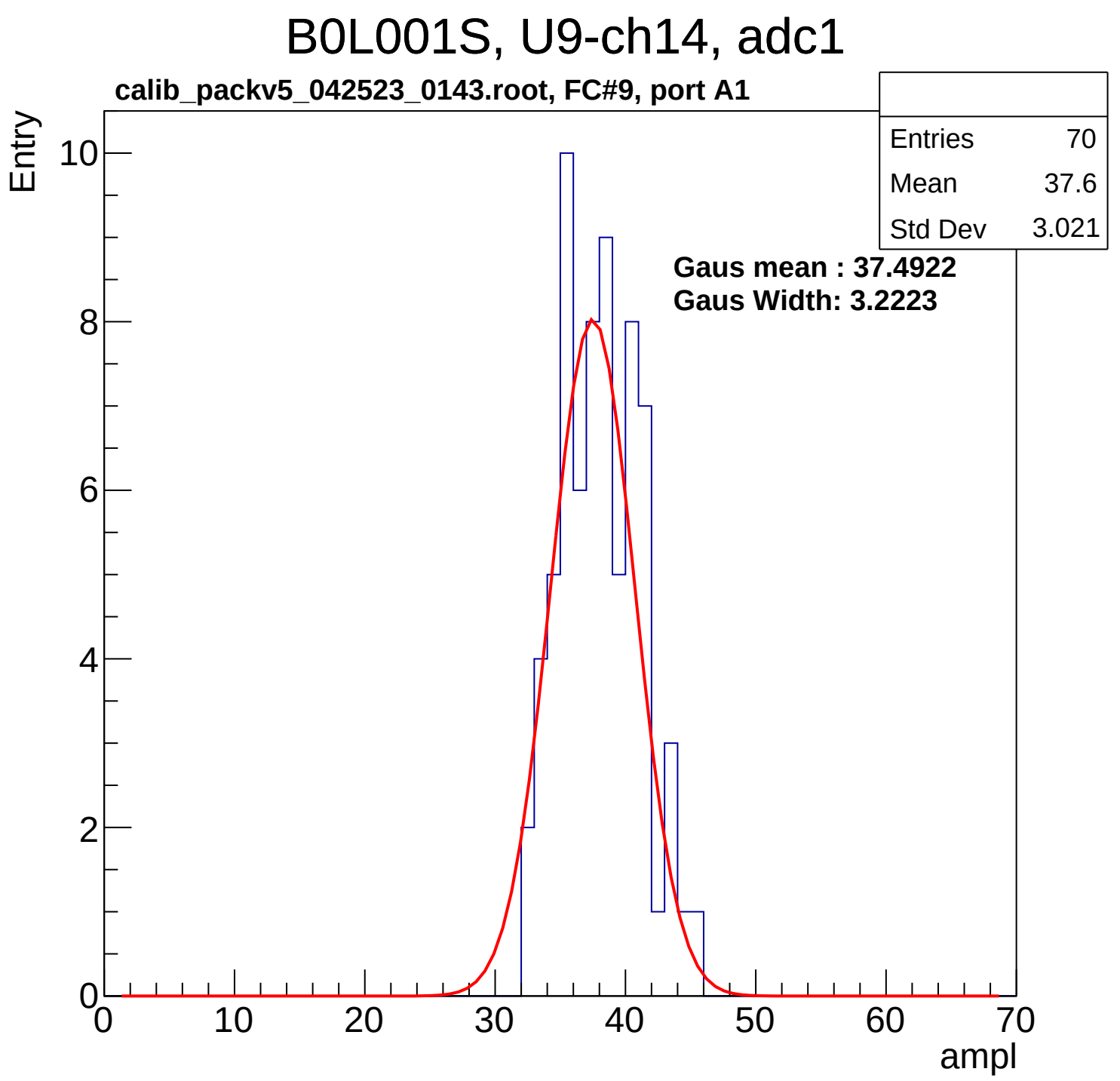
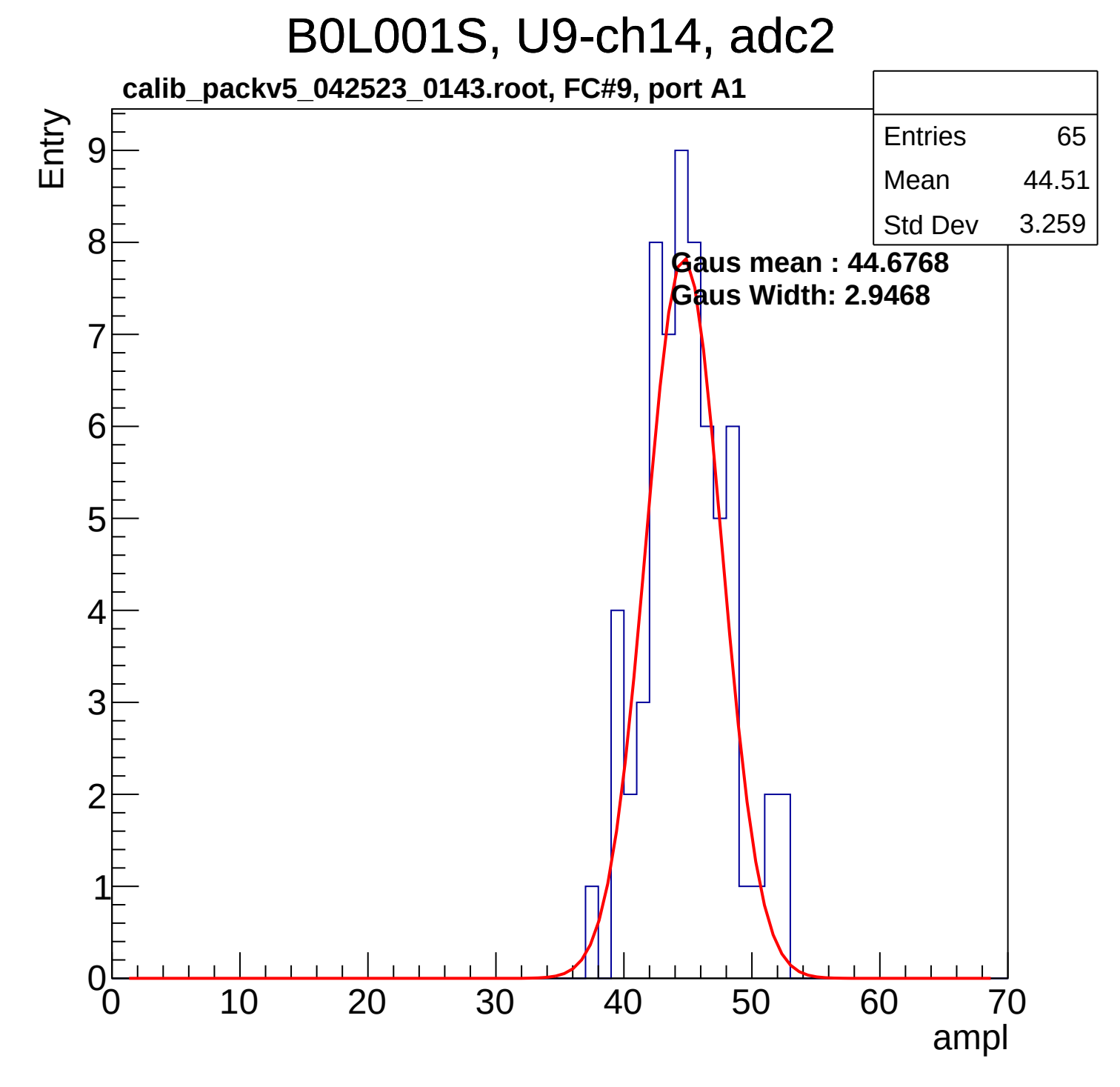
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch14, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.6
Std Dev 3.021
Gaus mean : 37.4922
Gaus Width: 3.2223
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch14, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.51
Std Dev 3.259
Gaus mean : 44.6768
Gaus Width: 2.9468
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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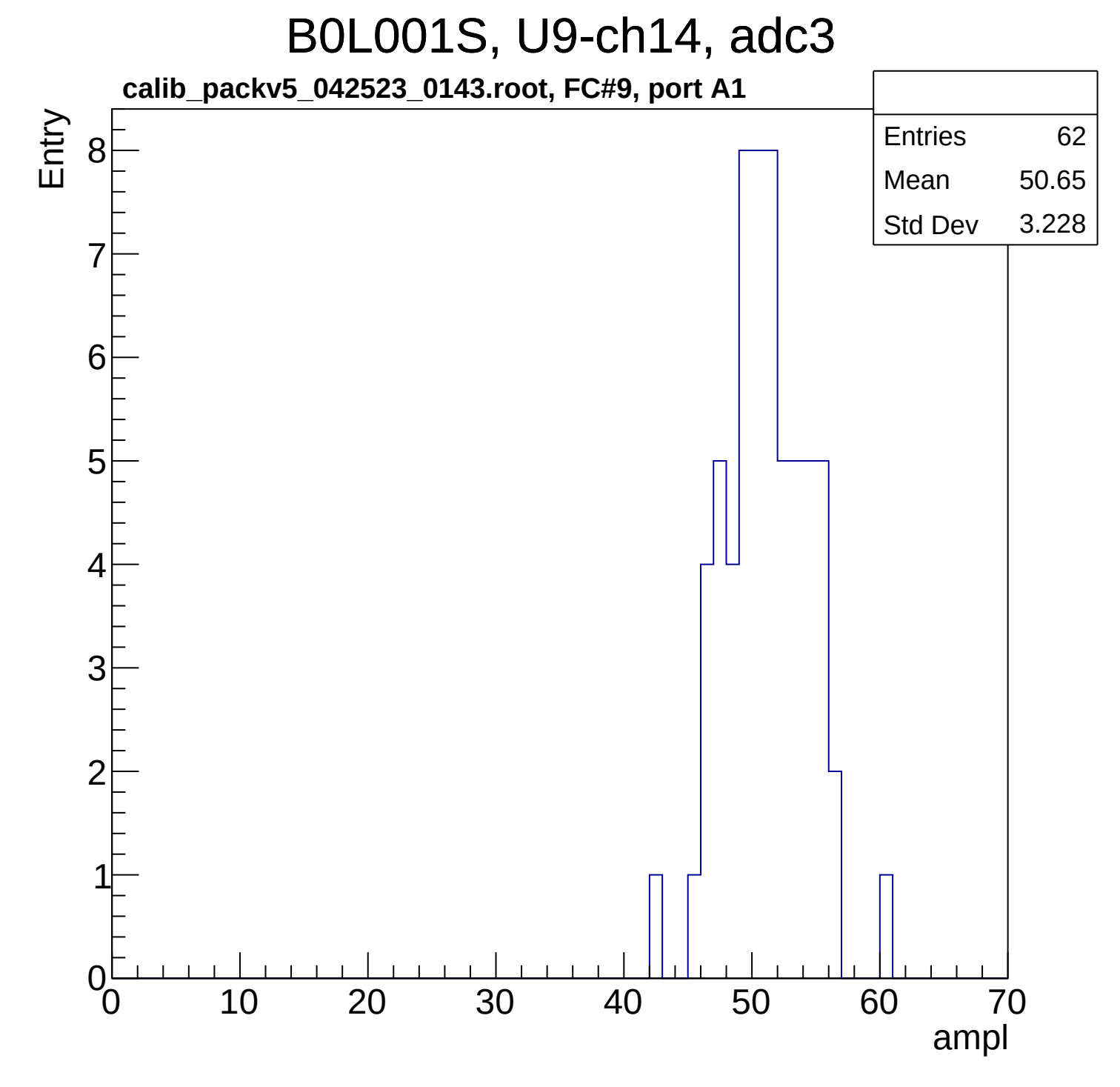
40
50
60
70


B0L001S, U9-ch14, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.65
Std Dev 3.228
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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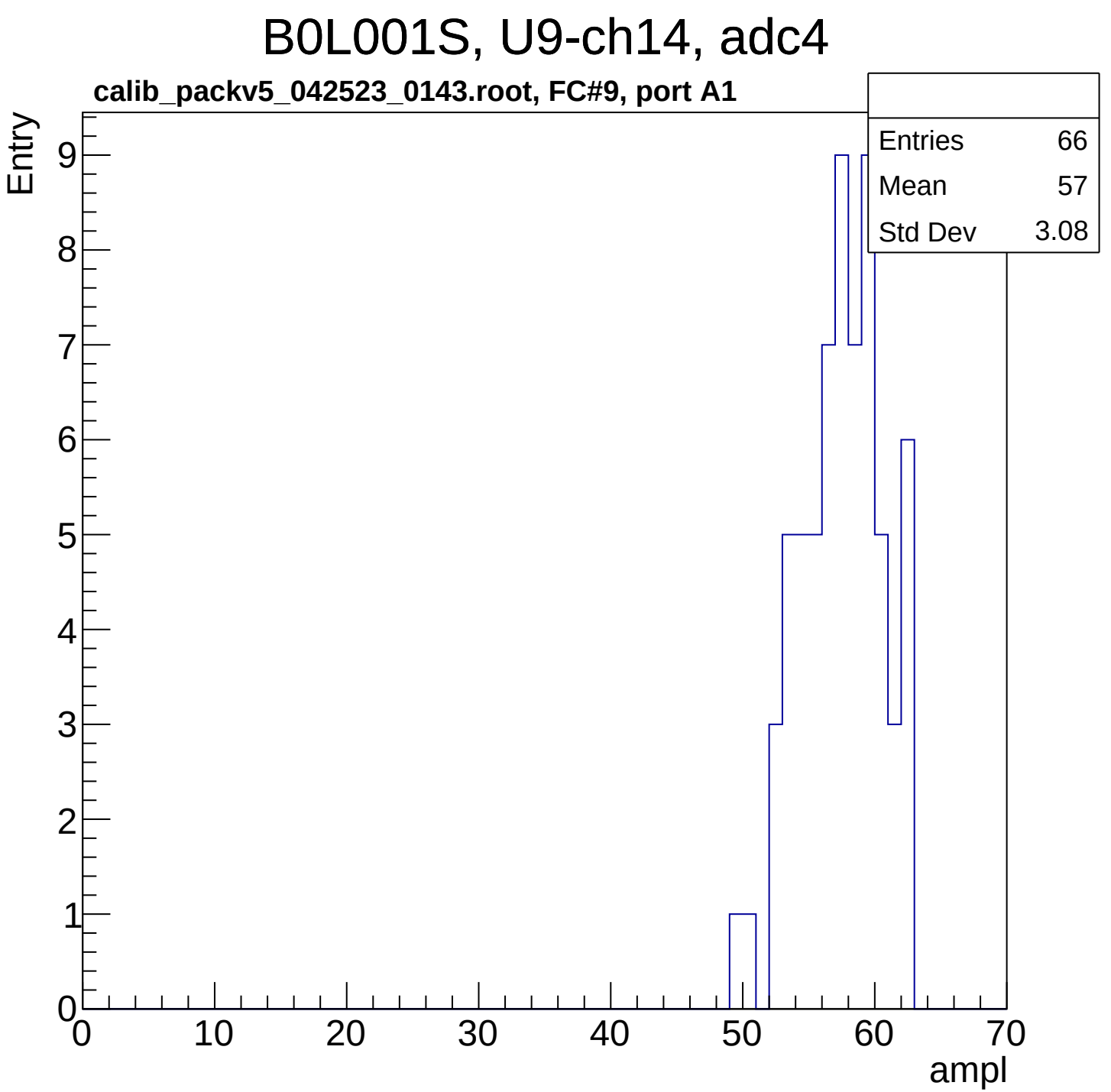
40
50
60
70


B0L001S, U9-ch14, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57
Std Dev 3.08
Entry
ampl
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3
4
5
6
7
8
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0
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20
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40
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60
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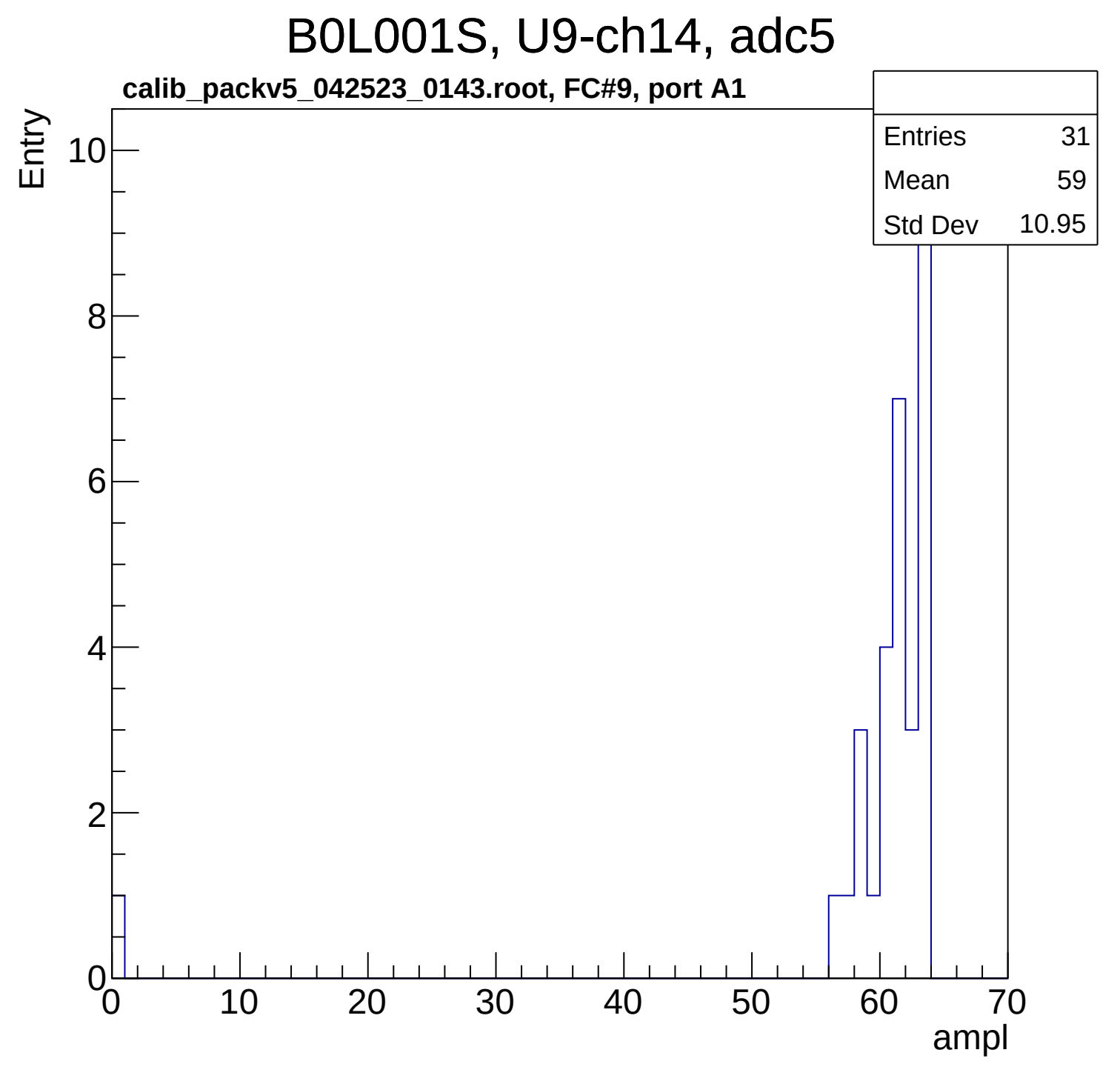
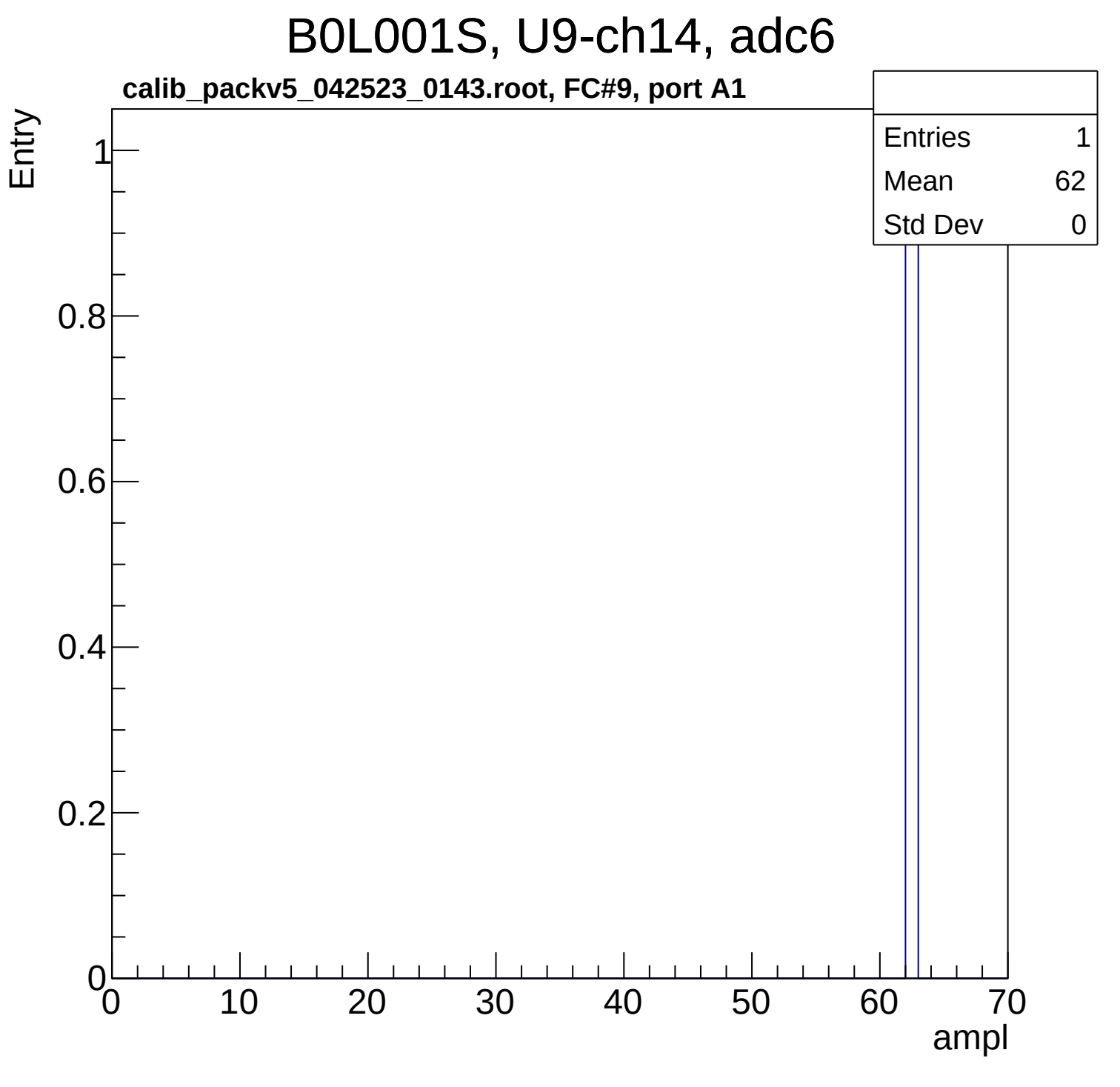

B0L001S, U9-ch14, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59
Std Dev 10.95
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch14, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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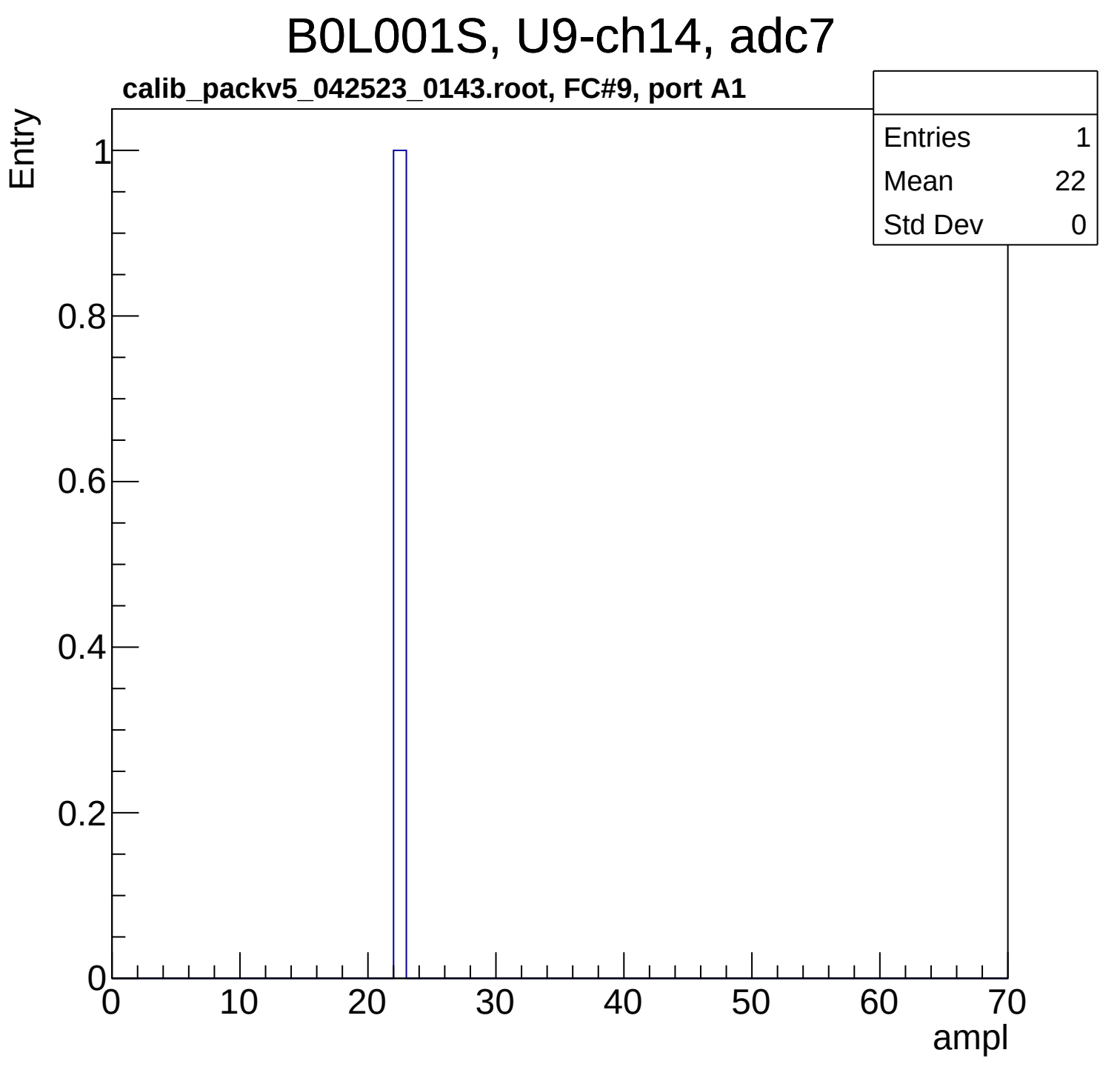
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch14, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
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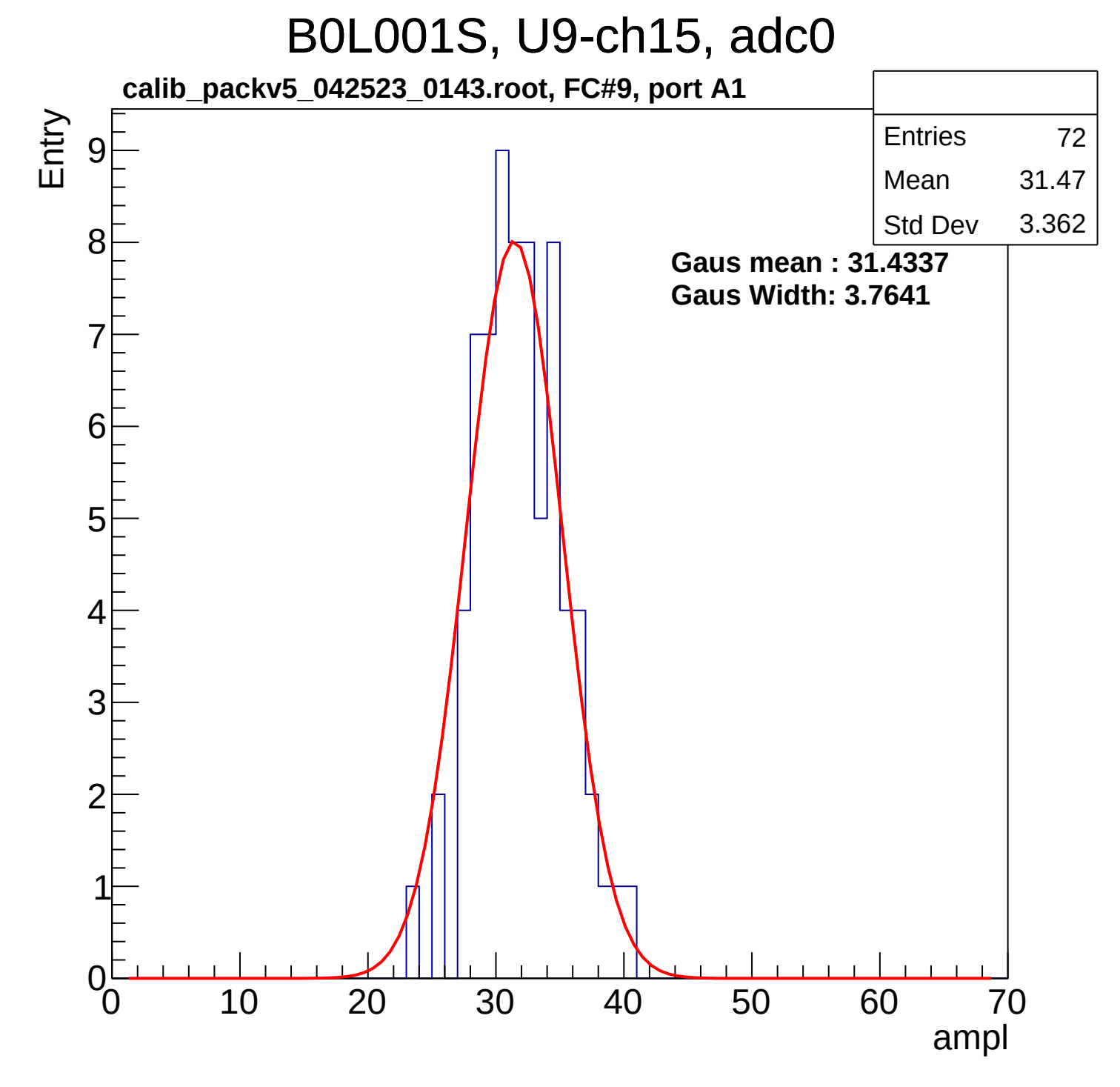
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch15, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 31.47
Std Dev 3.362
Gaus mean : 31.4337
Gaus Width: 3.7641
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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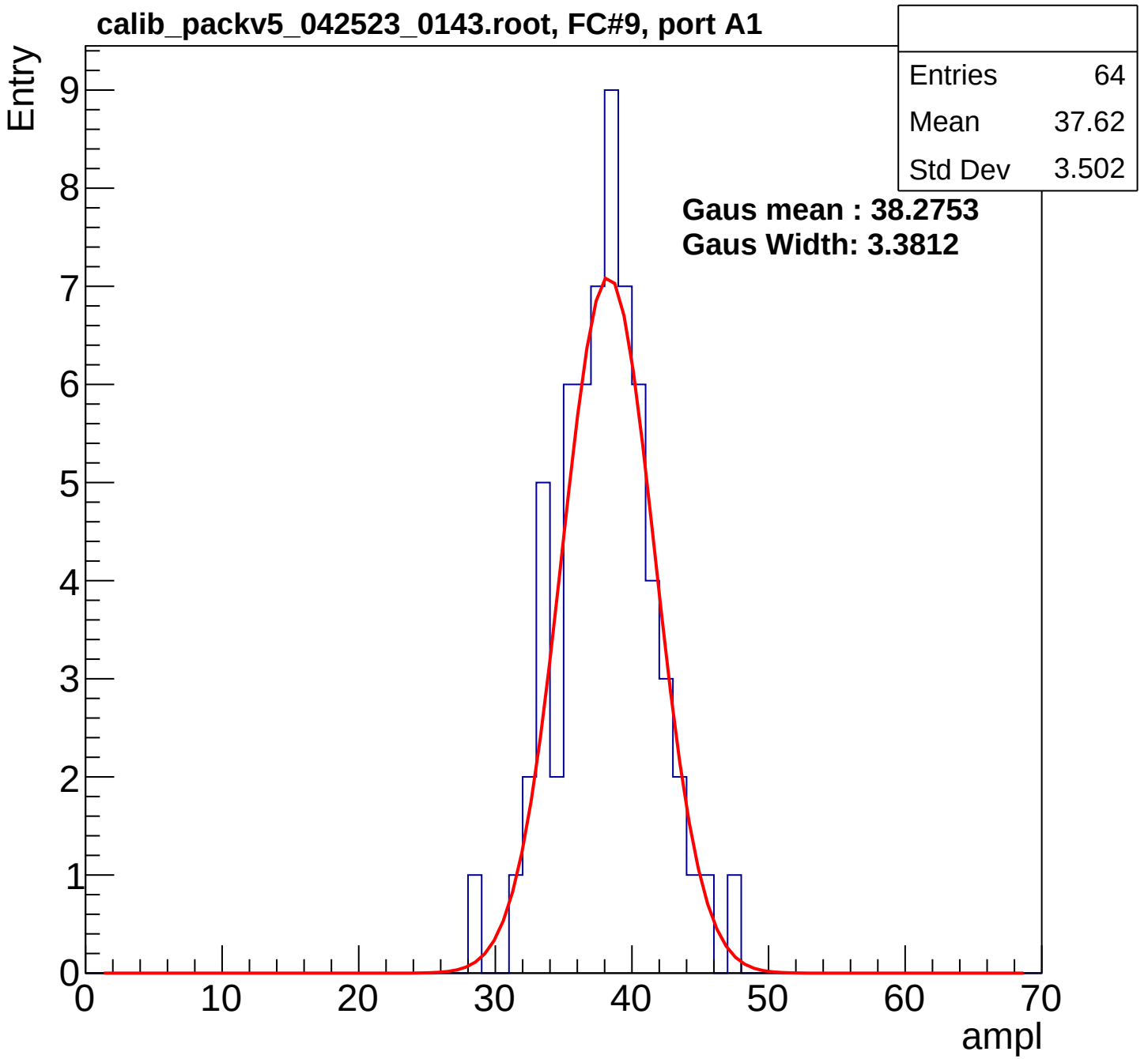
B0L001S, U9-ch15, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.62
Std Dev 3.502
Gaus mean : 38.2753
Gaus Width: 3.3812
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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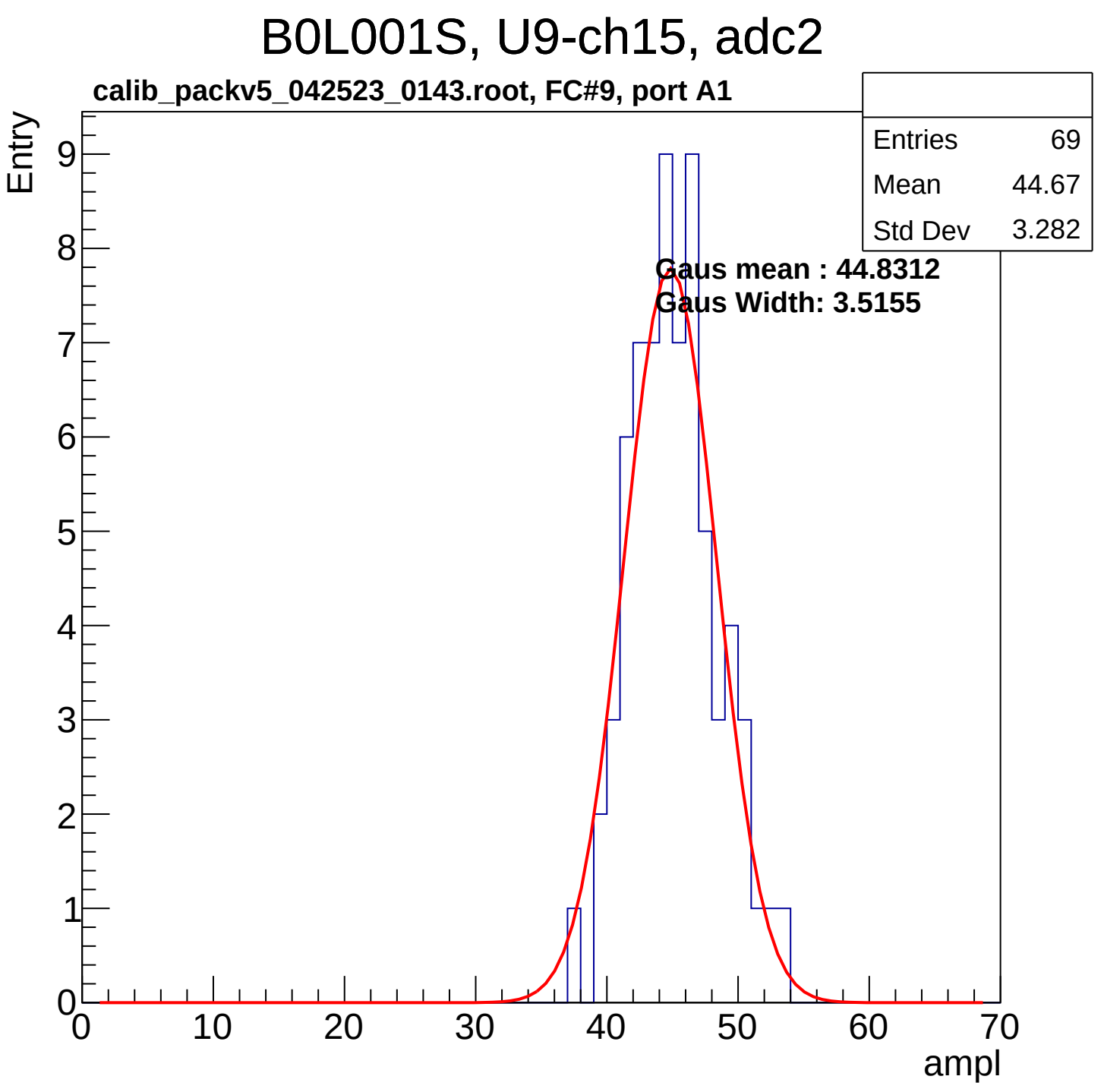
B0L001S, U9-ch15, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.67
Std Dev 3.282
Gaus mean : 44.8312
Gaus Width: 3.5155
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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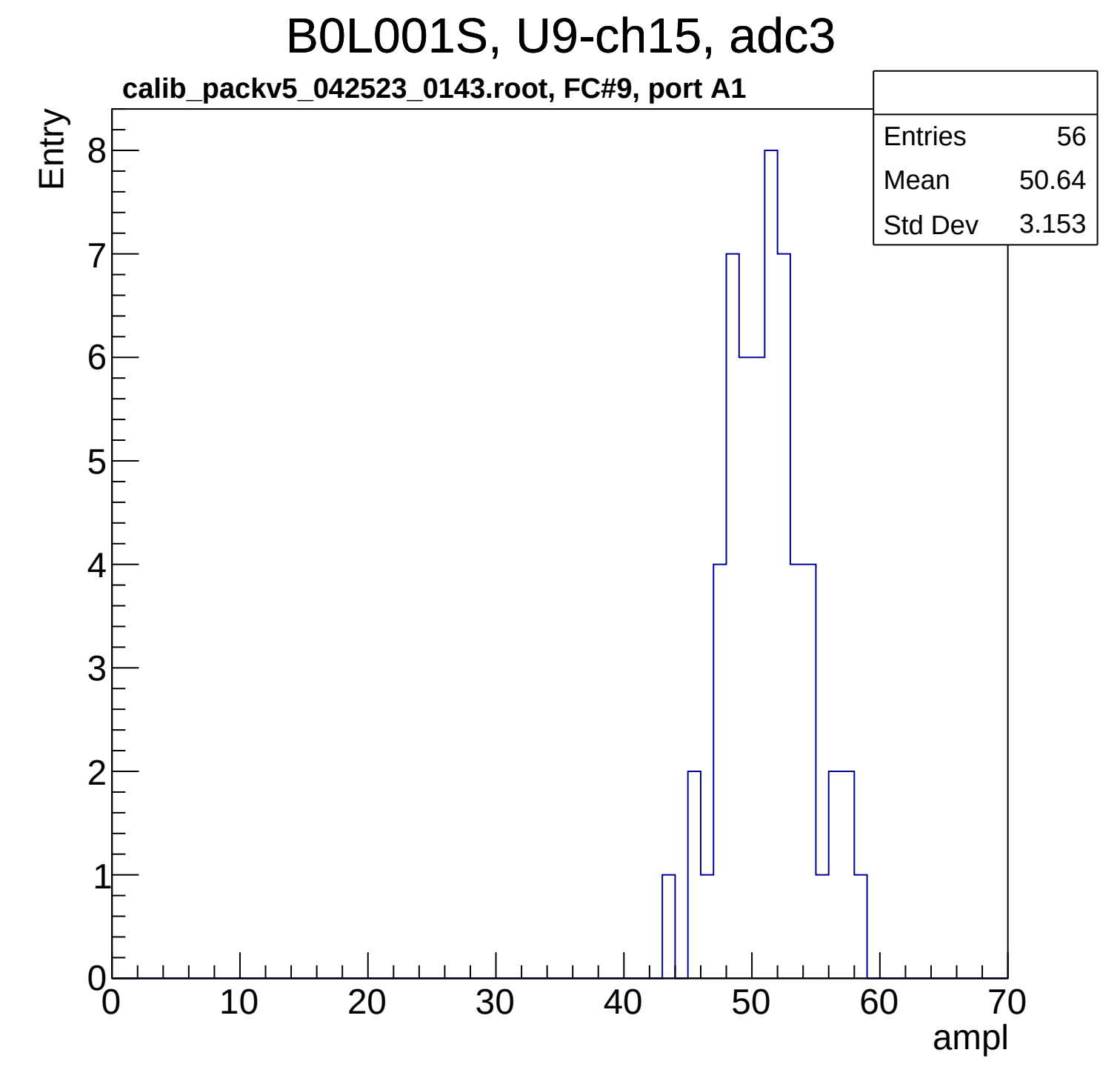
B0L001S, U9-ch15, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 50.64
Std Dev 3.153
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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50
60
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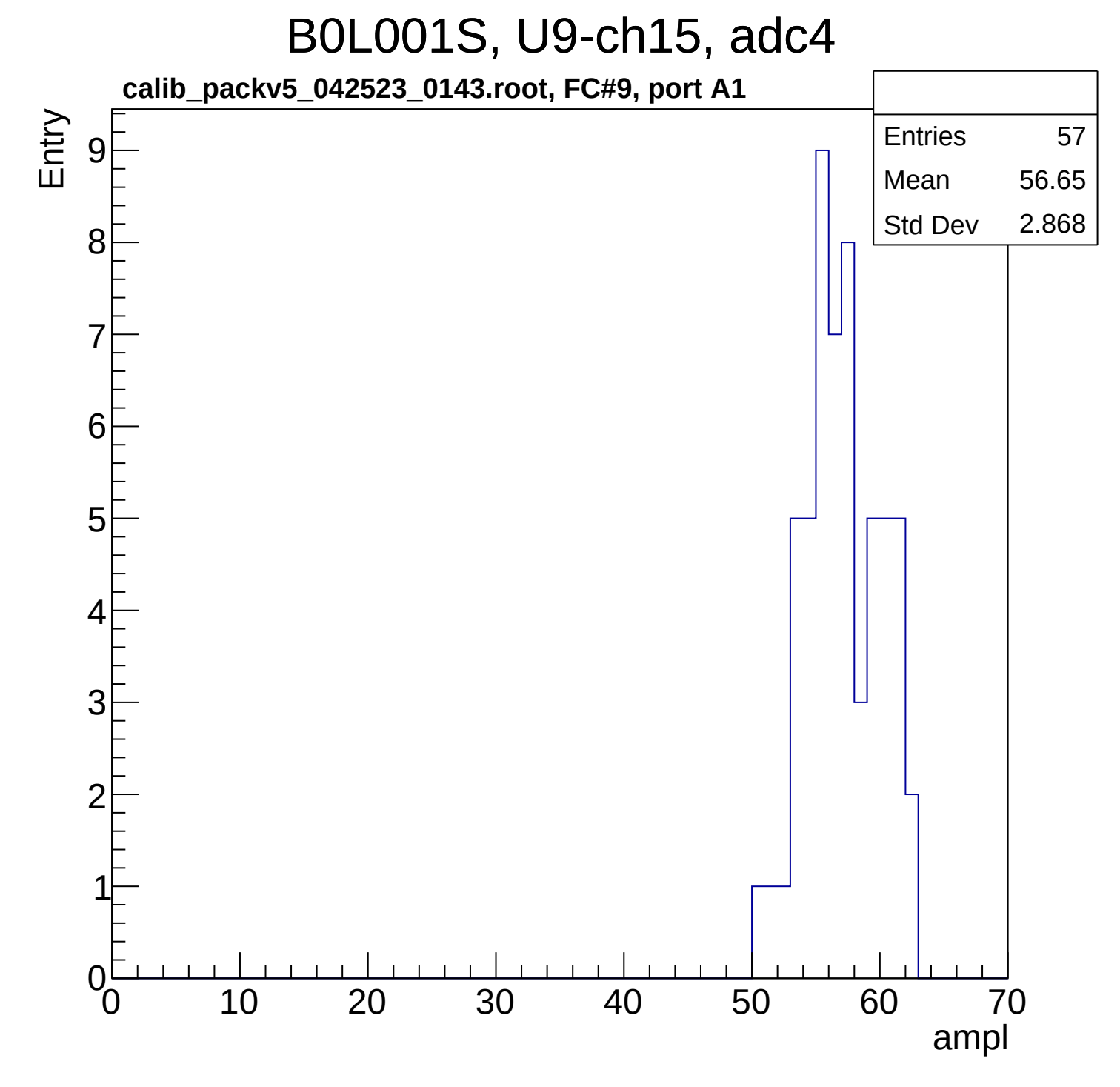
B0L001S, U9-ch15, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.65
Std Dev 2.868
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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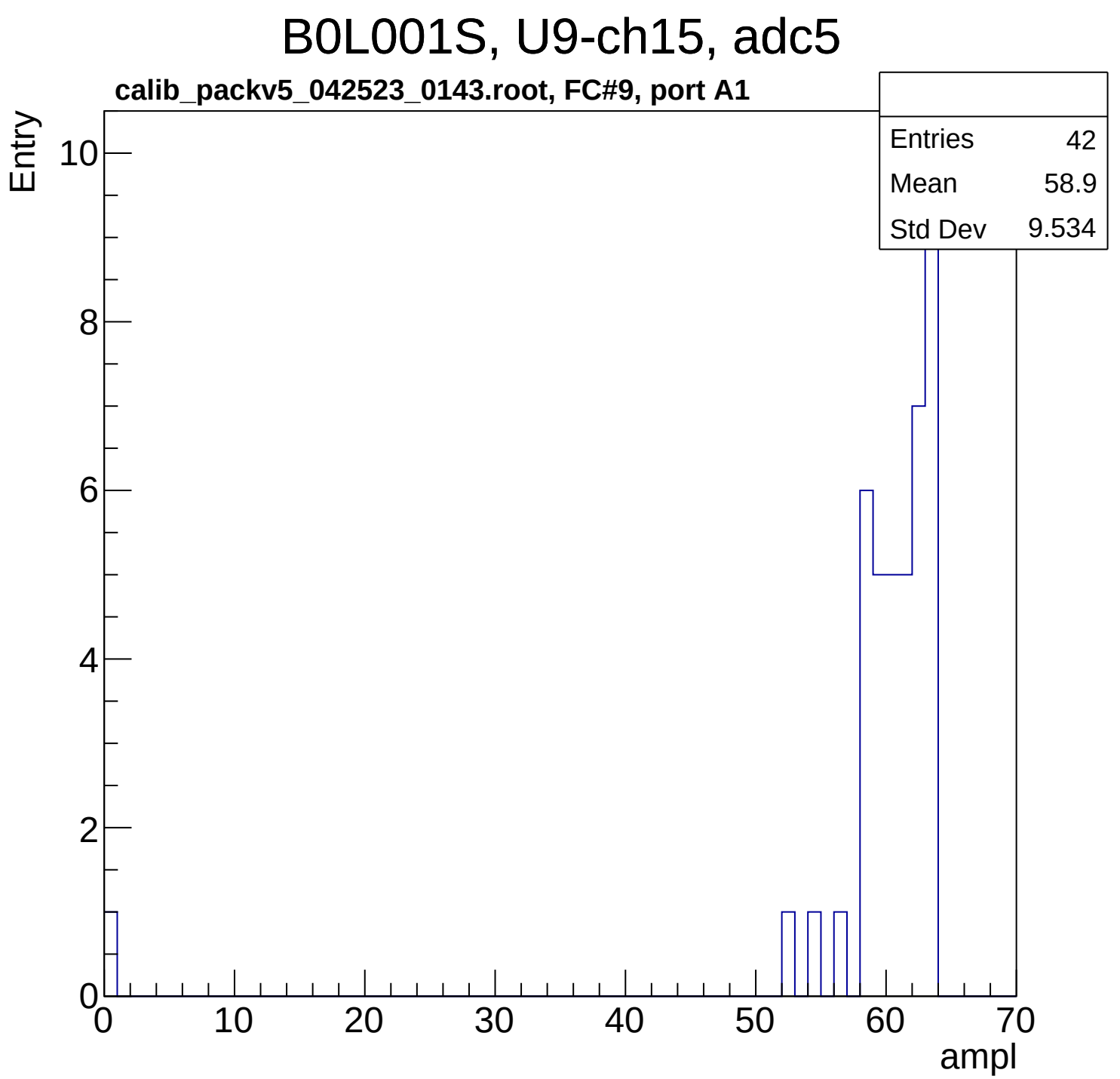
B0L001S, U9-ch15, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.9
Std Dev 9.534
Entry
0
2
4
6
8
10
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10
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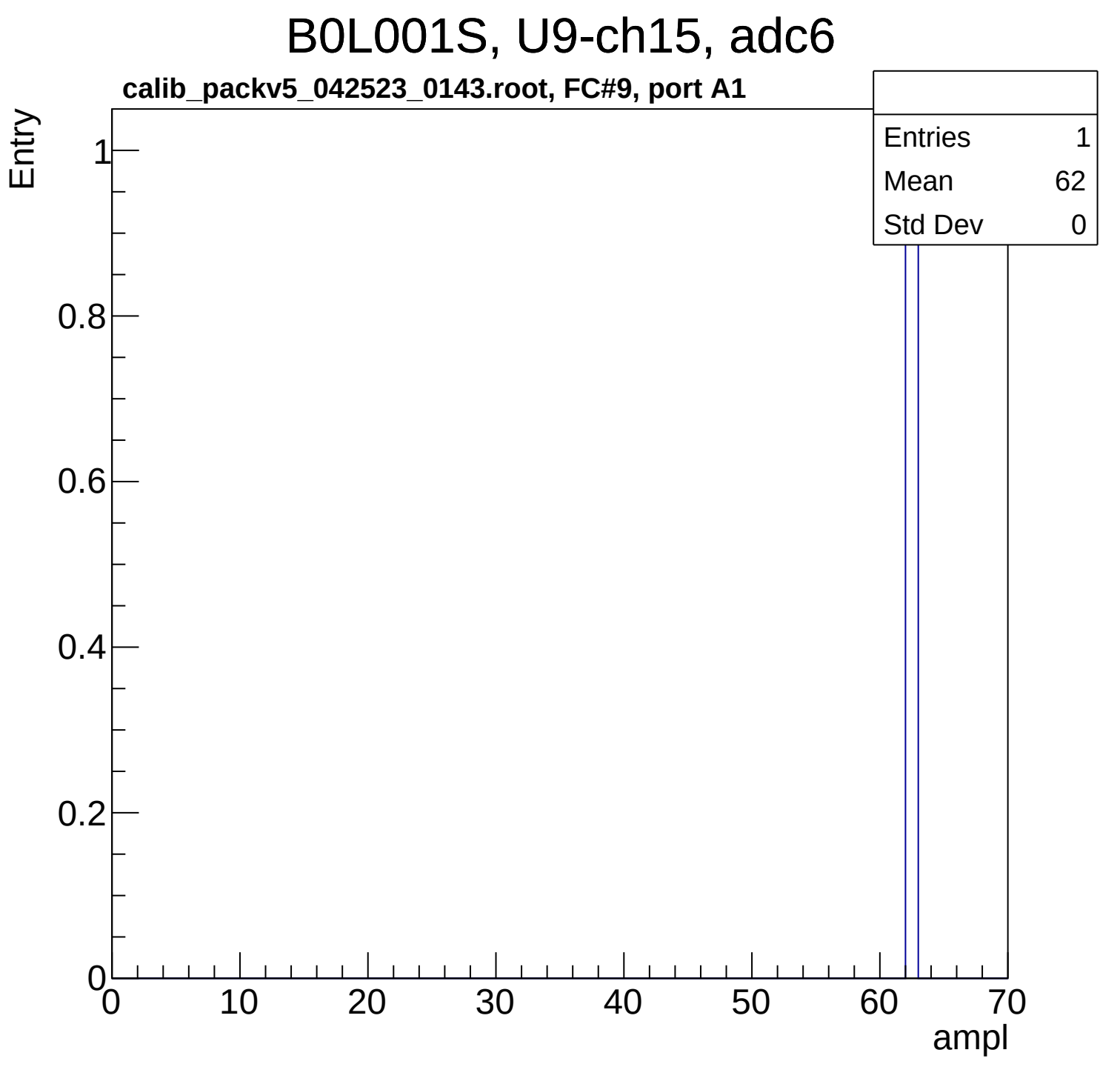
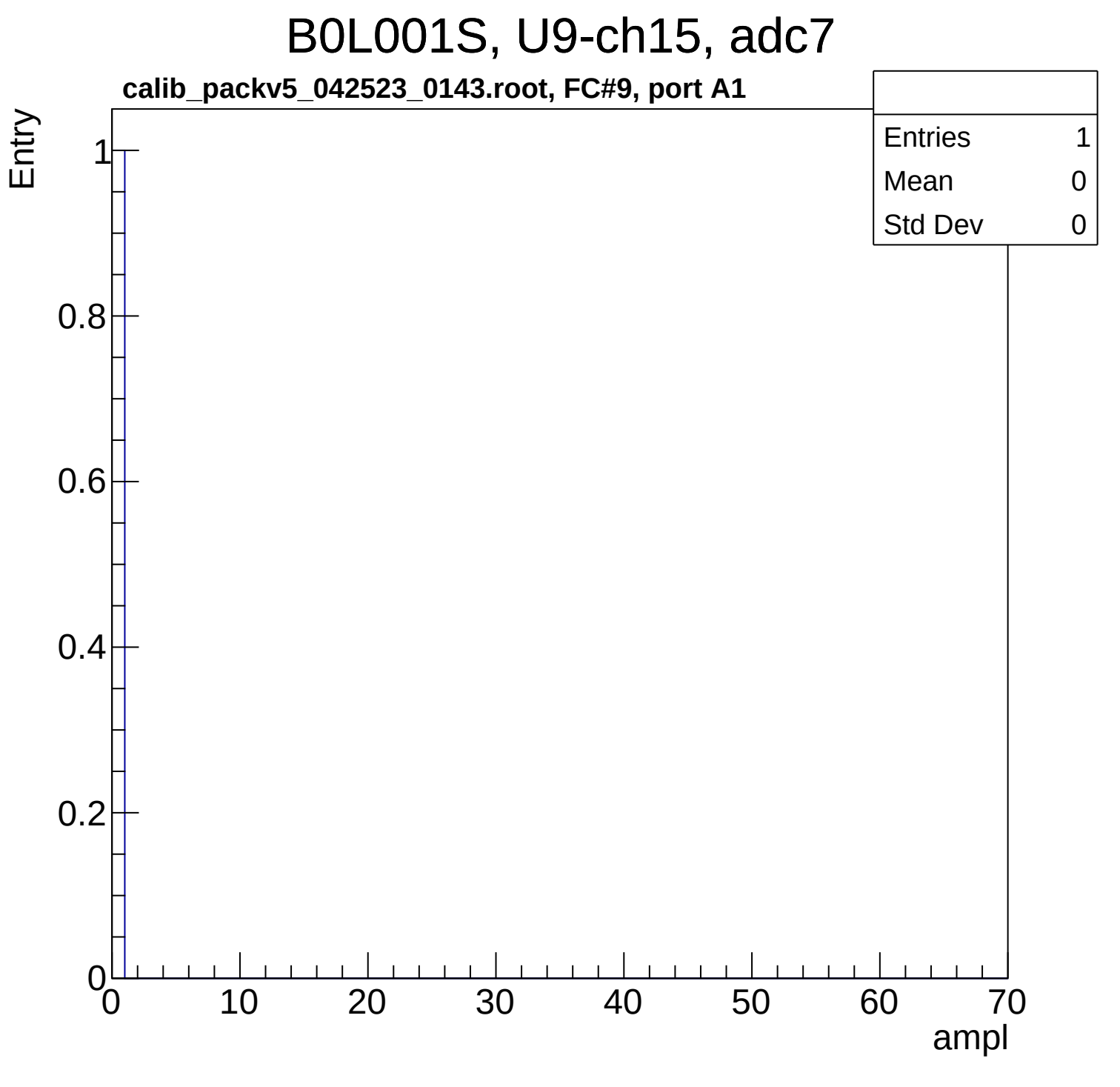
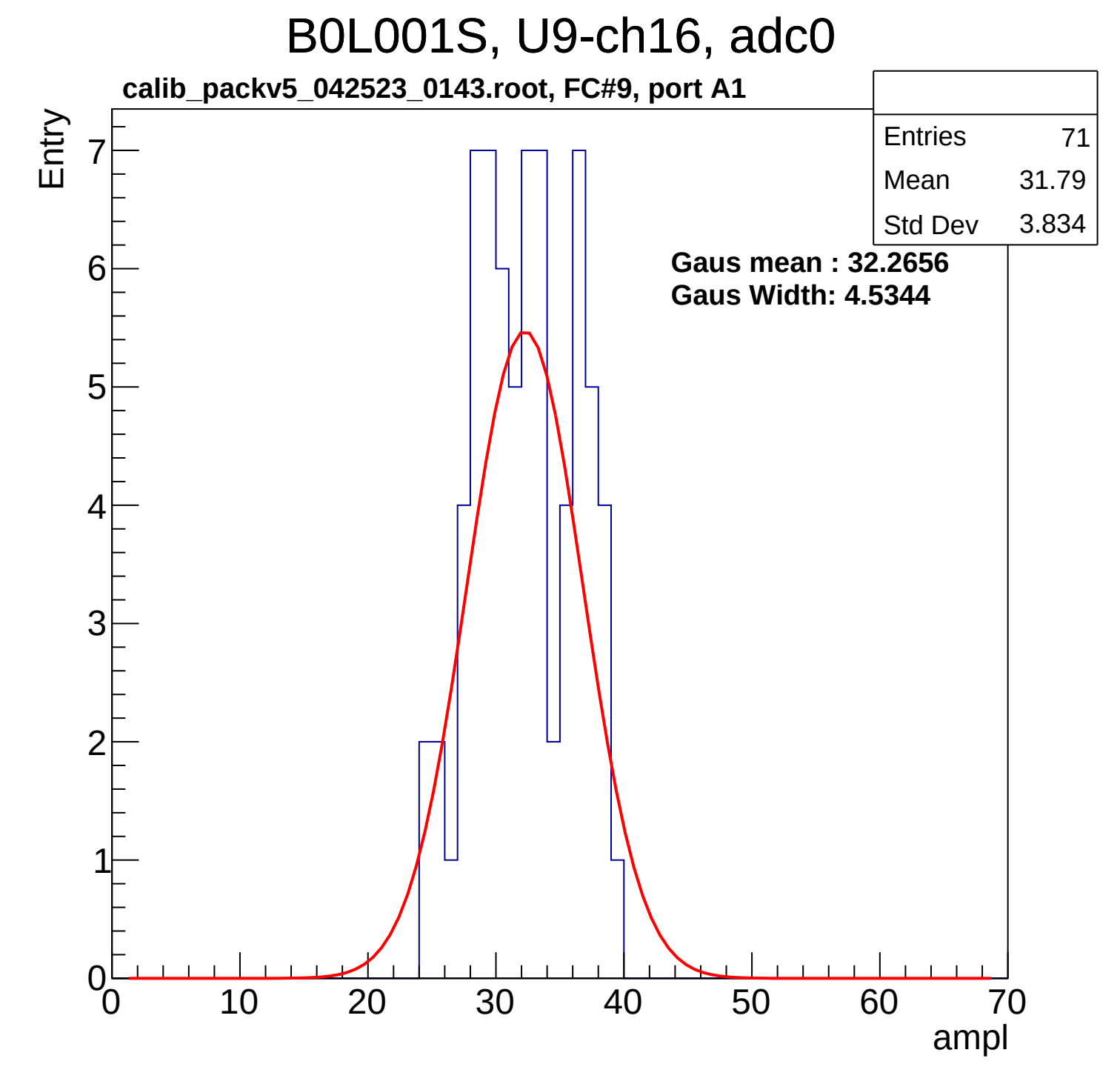
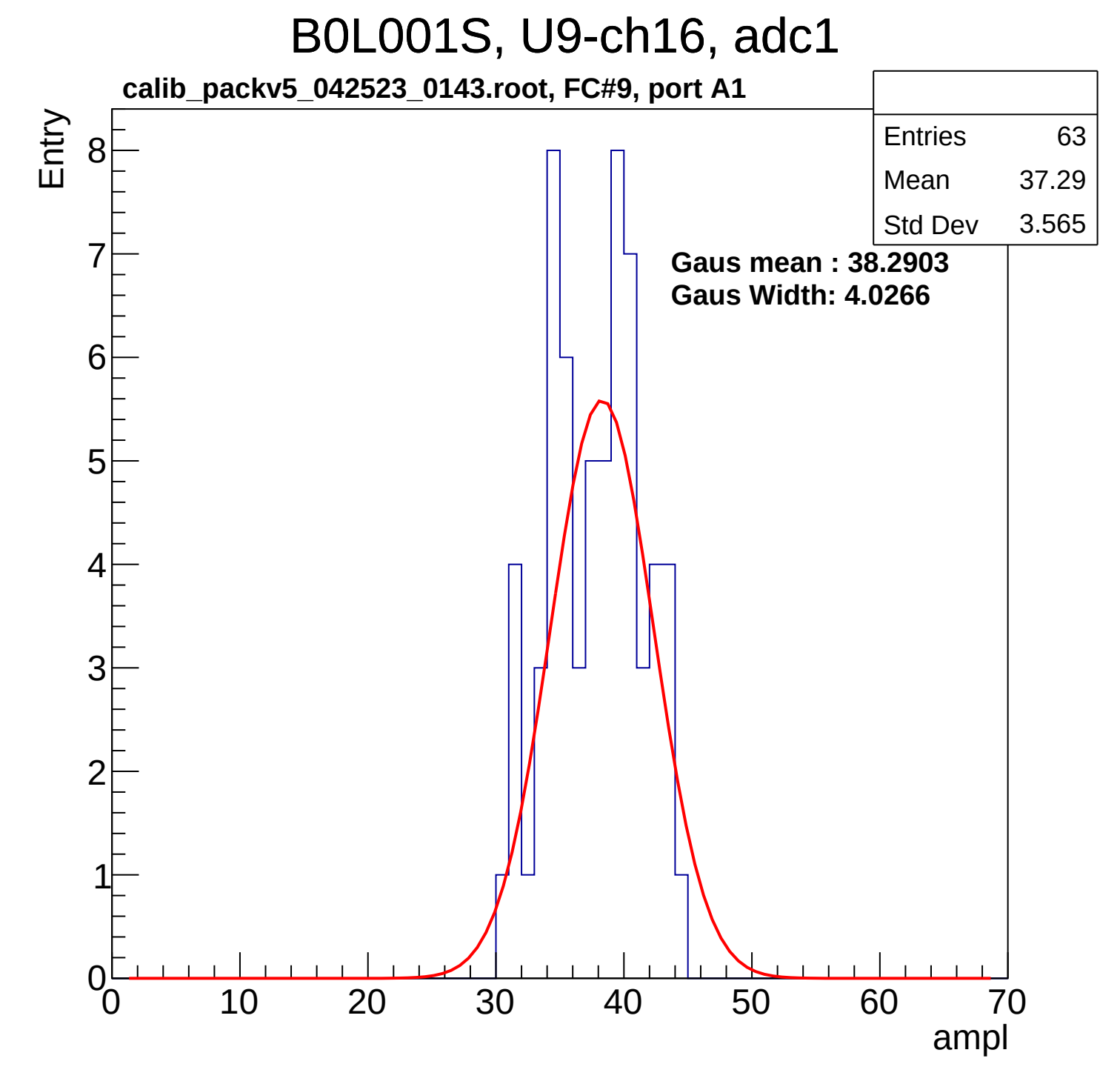
50
60
70
ampl


B0L001S, U9-ch15, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch15, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch16, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 31.79
Std Dev 3.834
Gaus mean : 32.2656
Gaus Width: 4.5344
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch16, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.29
Std Dev 3.565
Gaus mean : 38.2903
Gaus Width: 4.0266
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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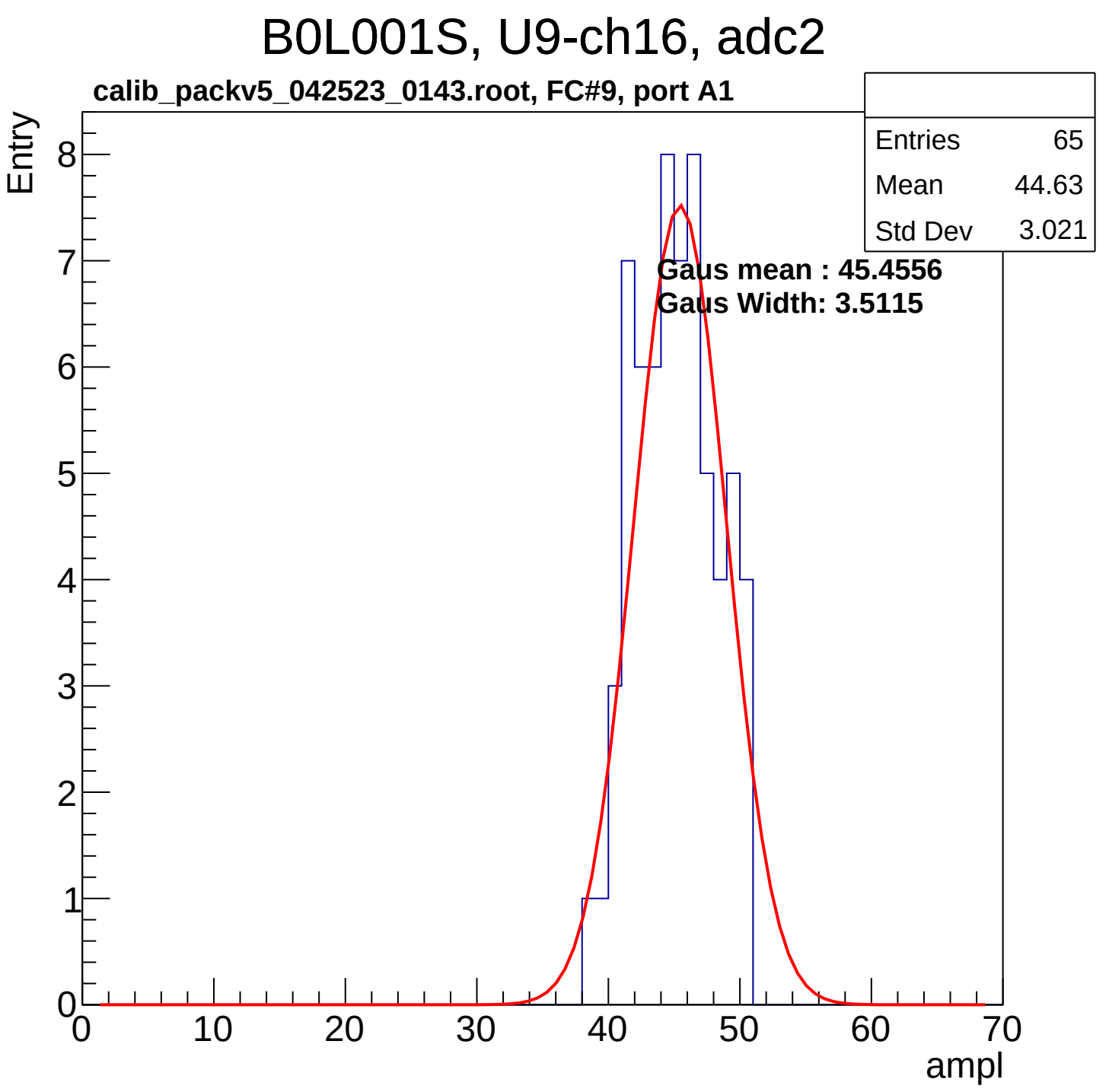
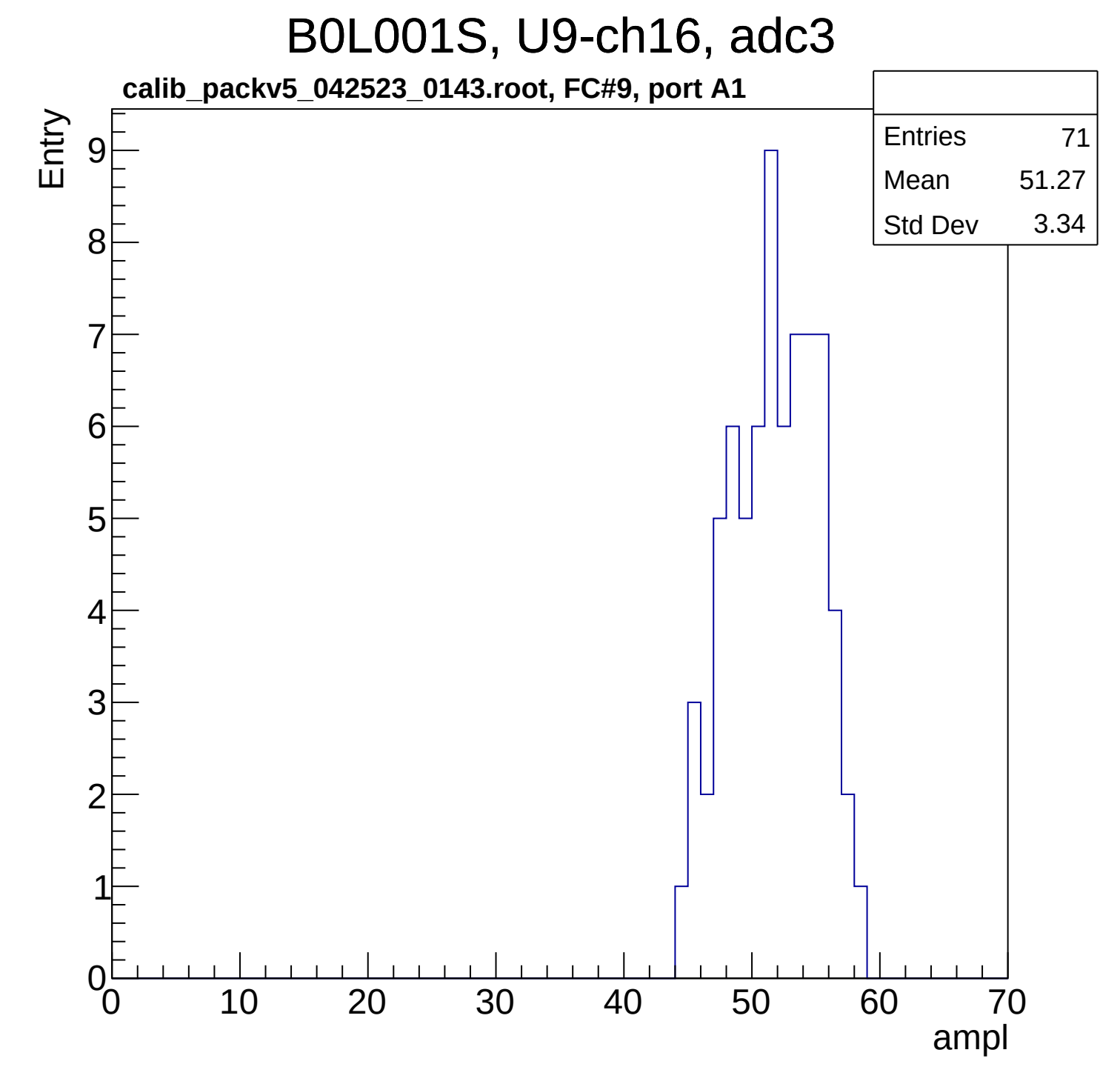
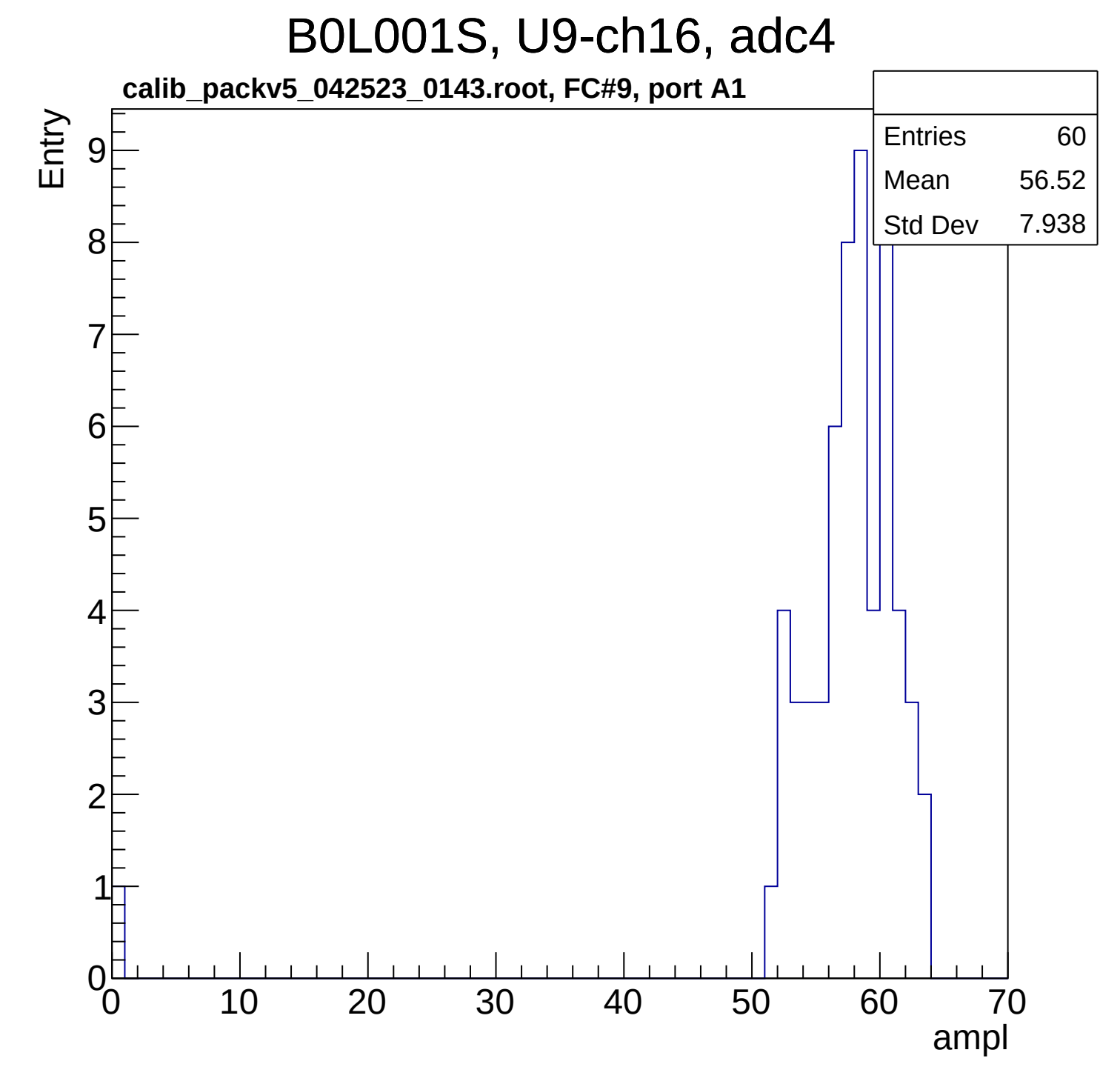
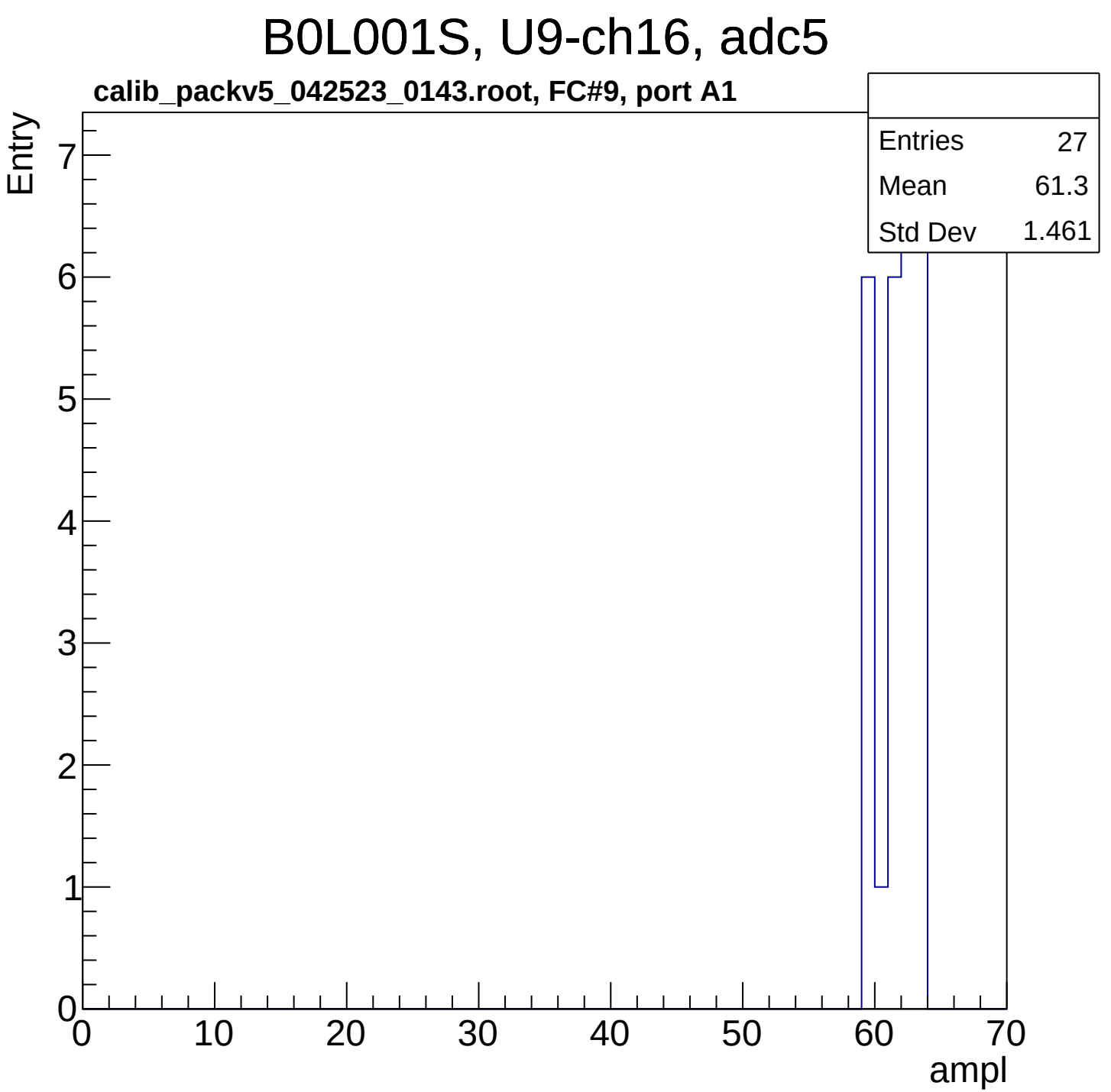
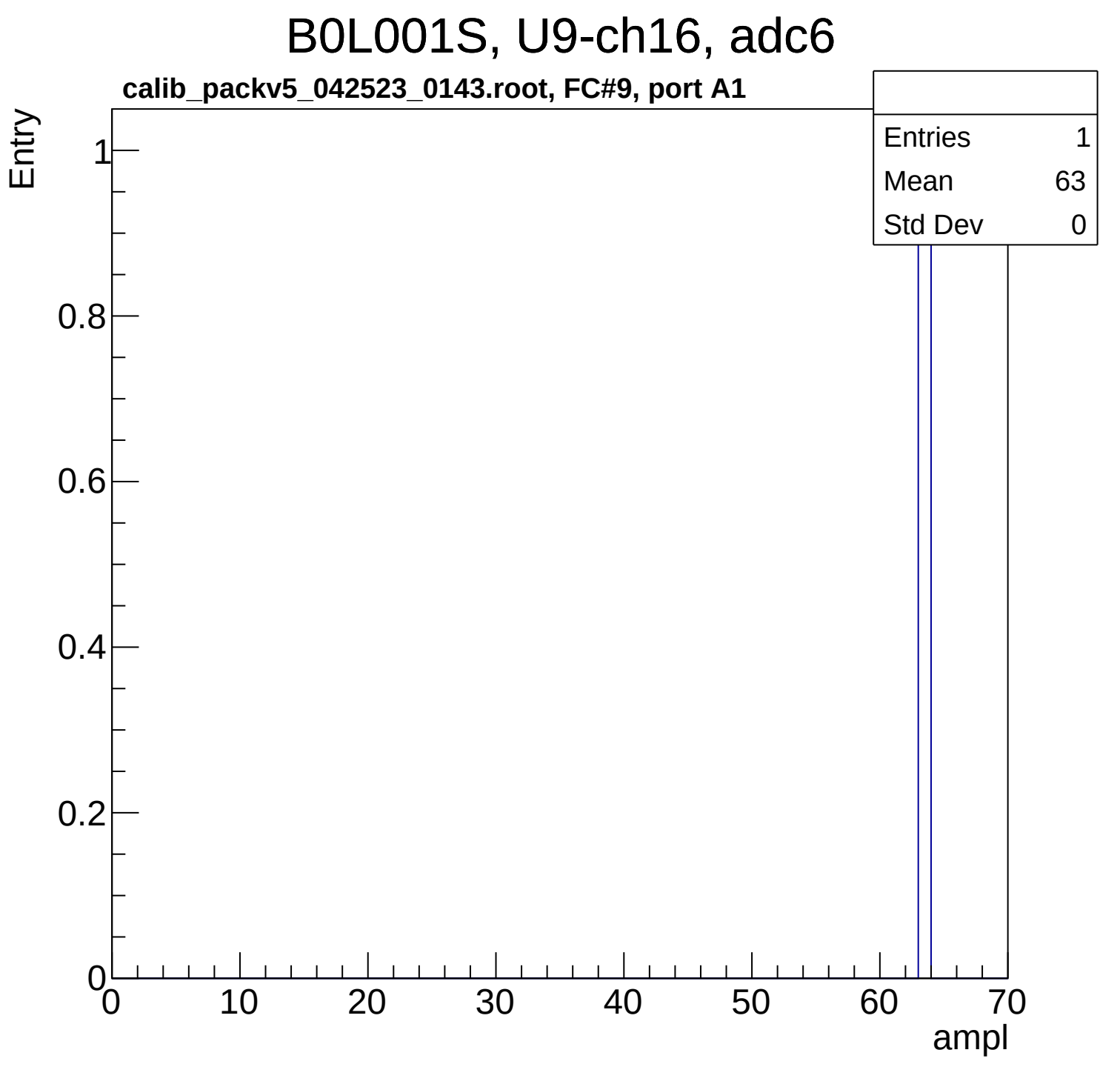
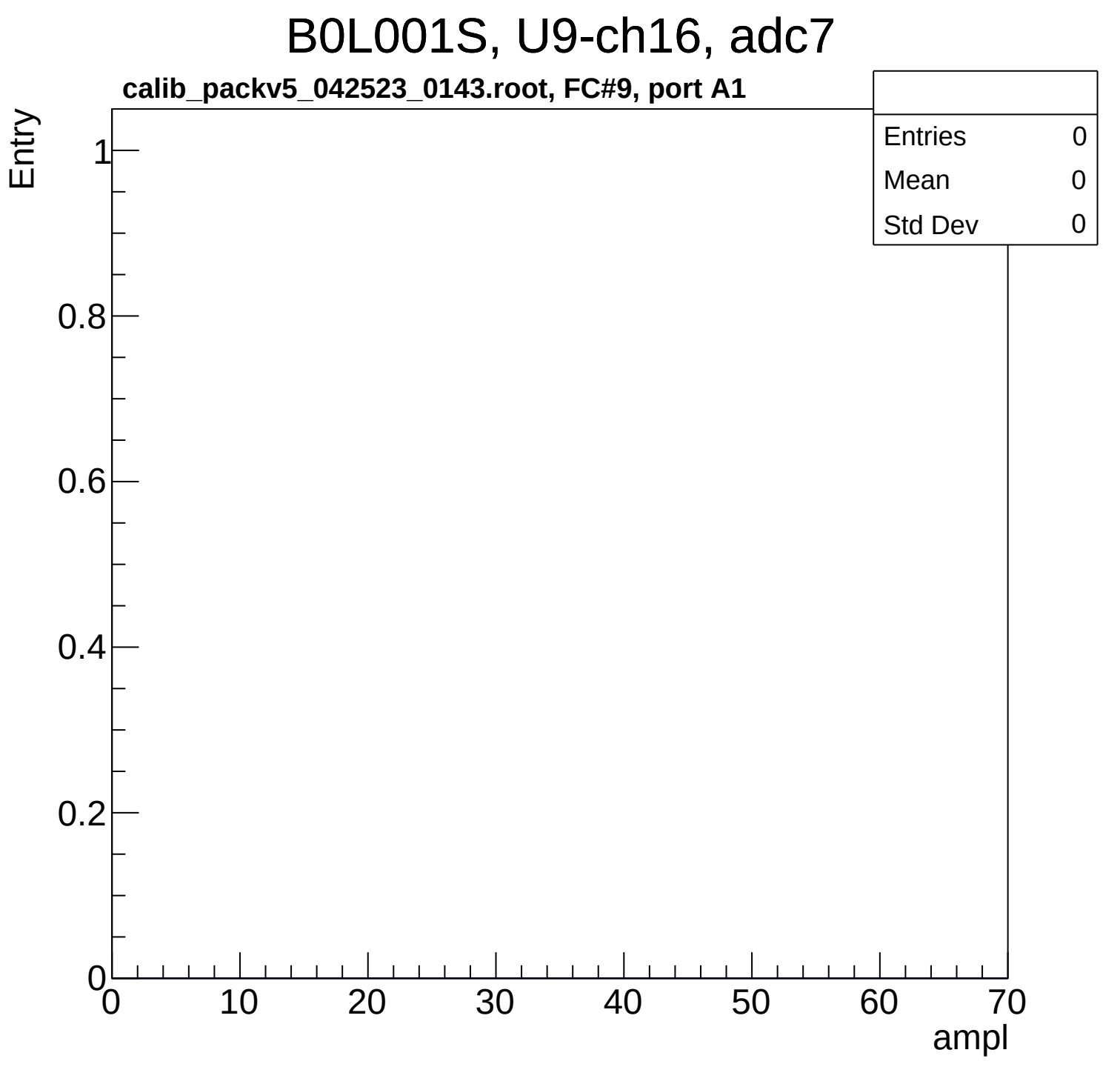

B0L001S, U9-ch16, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.63
Std Dev 3.021
Gaus mean : 45.4556
Gaus Width: 3.5115
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

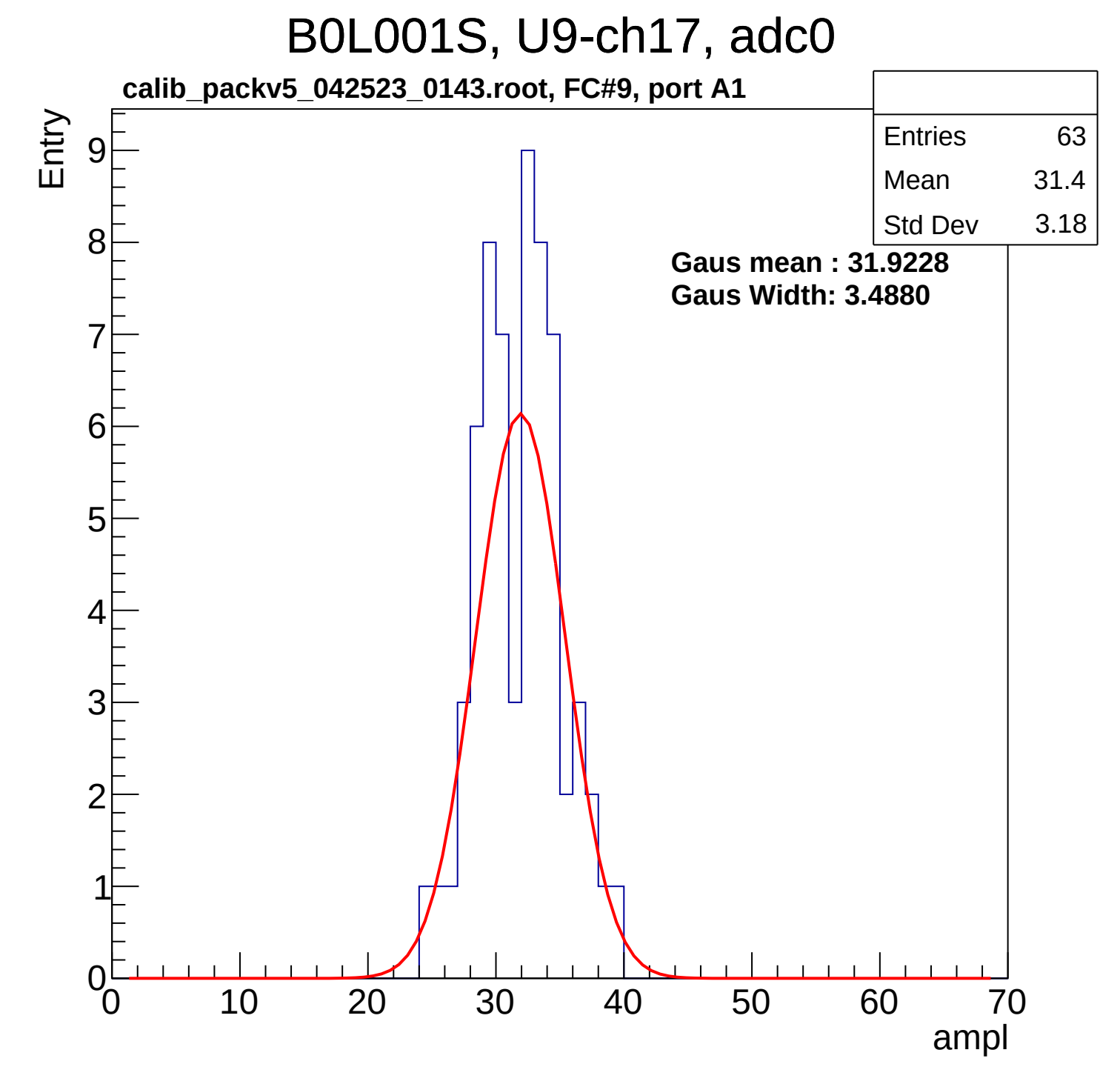

B0L001S, U9-ch16, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.27
Std Dev 3.34
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

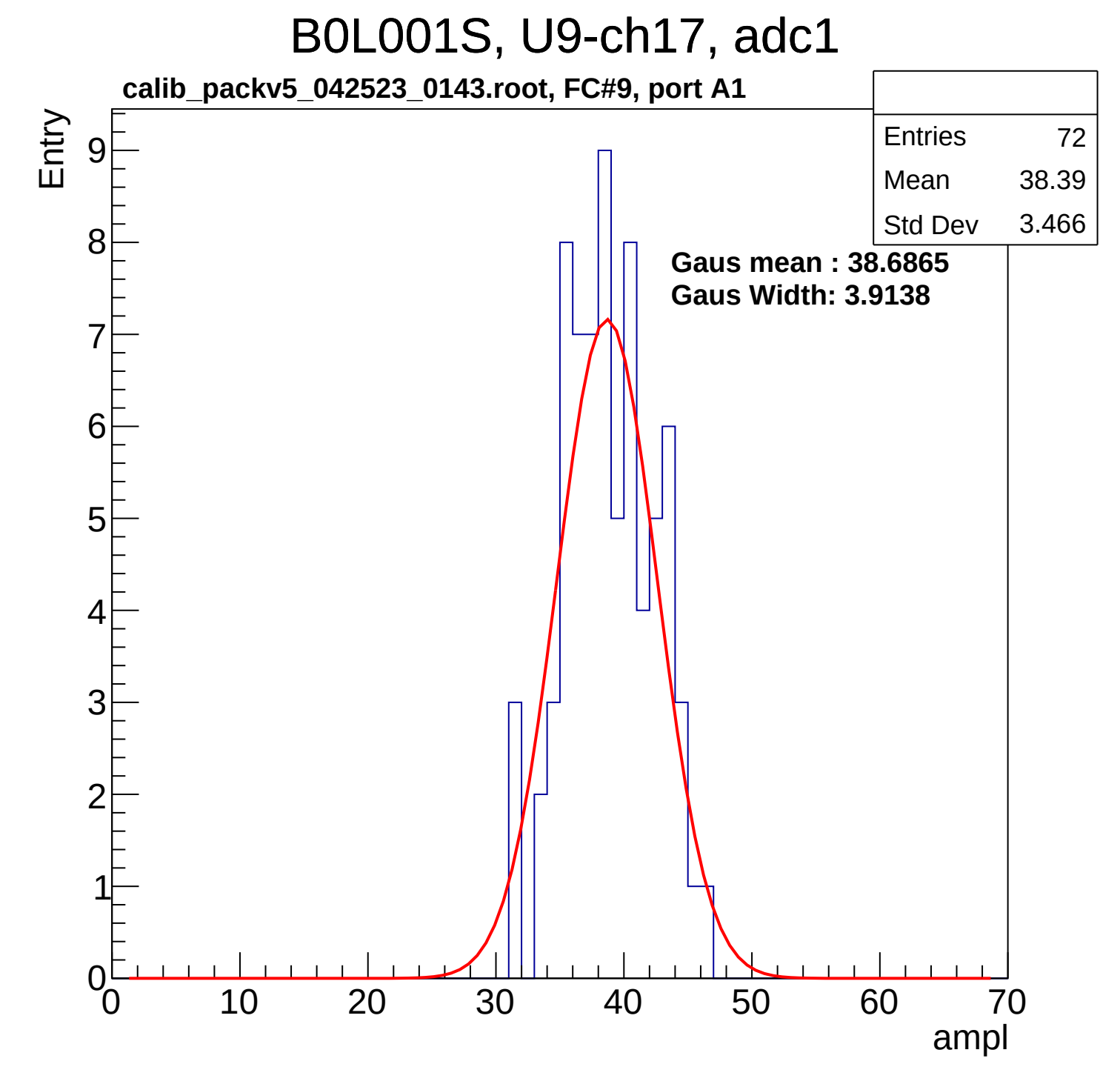

B0L001S, U9-ch16, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.52
Std Dev 7.938
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

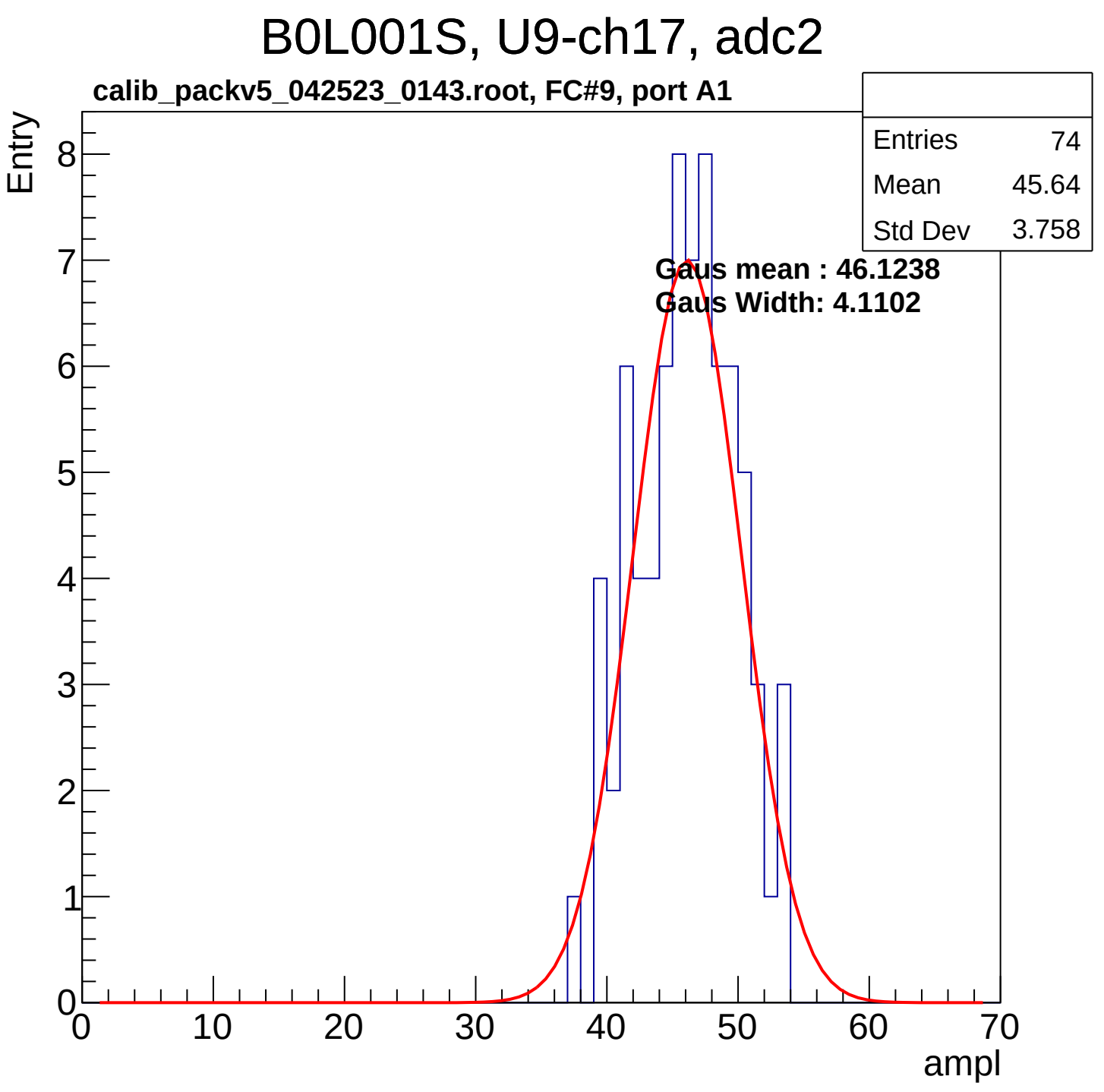

B0L001S, U9-ch16, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 61.3
Std Dev 1.461
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

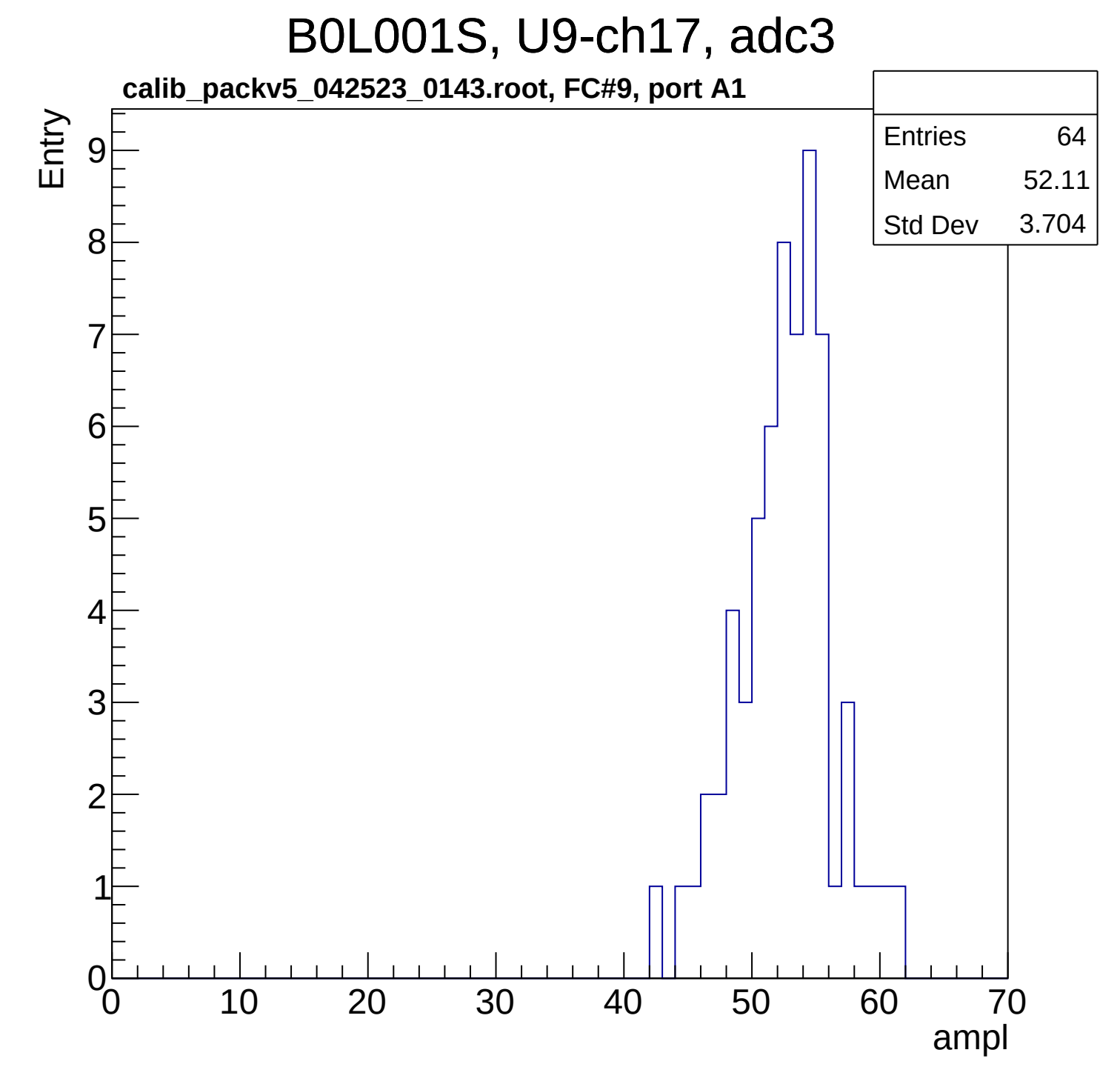

B0L001S, U9-ch16, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch16, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch17, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 31.4
Std Dev 3.18
Gaus mean : 31.9228
Gaus Width: 3.4880
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch17, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.39
Std Dev 3.466
Gaus mean : 38.6865
Gaus Width: 3.9138
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch17, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 45.64
Std Dev 3.758
Gaus mean : 46.1238
Gaus Width: 4.1102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch17, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.11
Std Dev 3.704
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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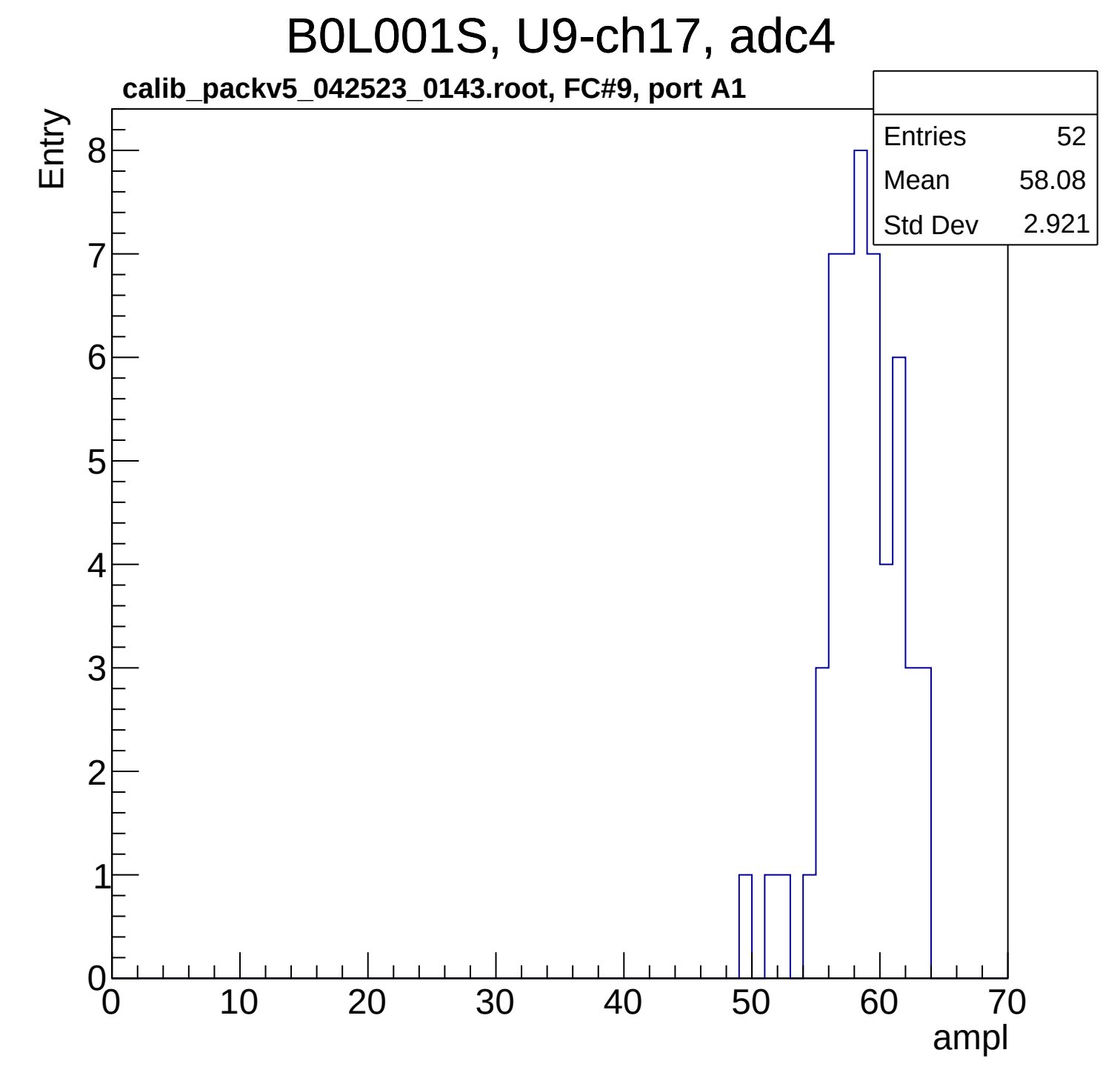
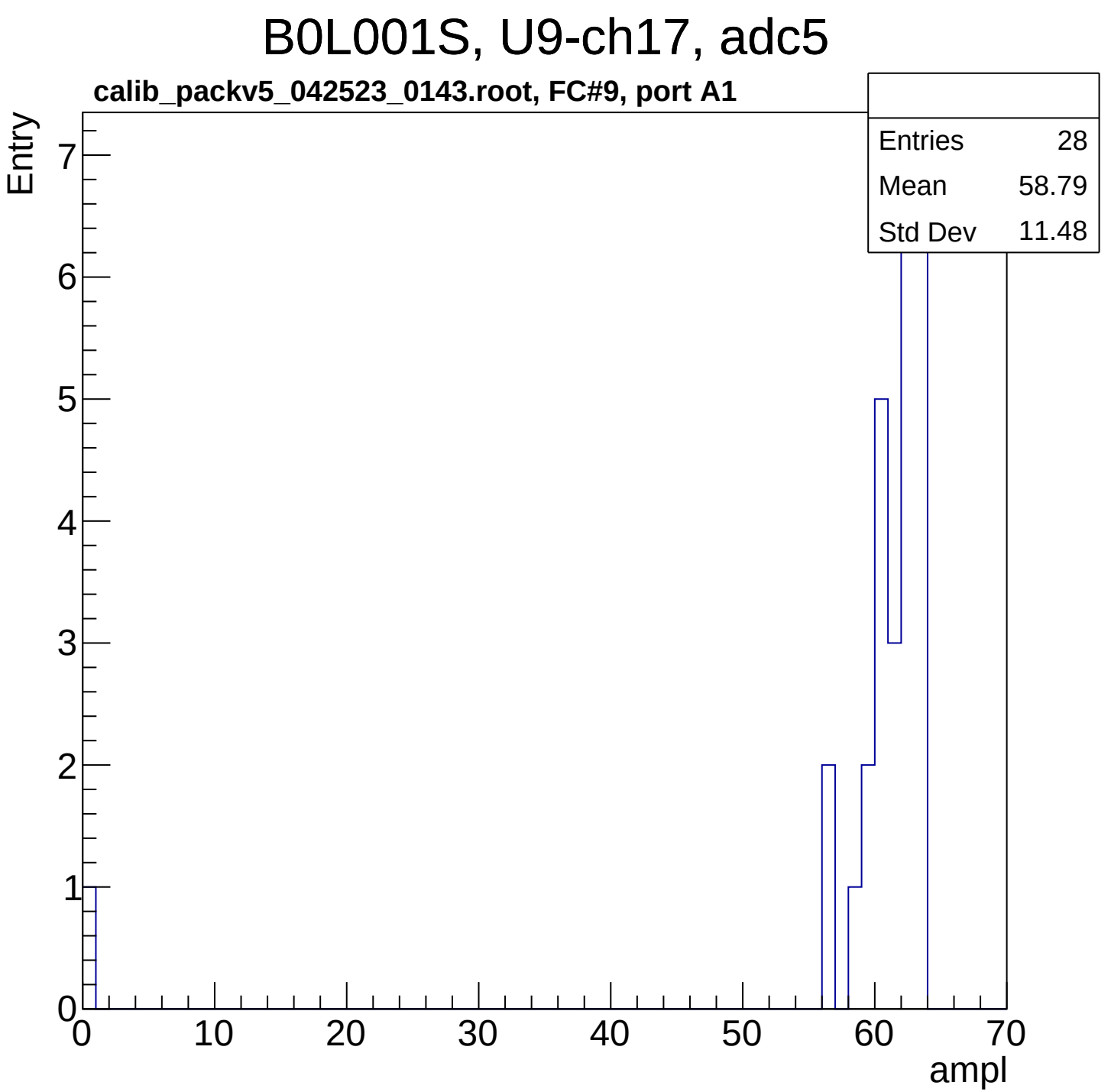

B0L001S, U9-ch17, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 58.08
Std Dev 2.921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch17, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.79
Std Dev 11.48
Entry
ampl
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1
2
3
4
5
6
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0
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40
50
60
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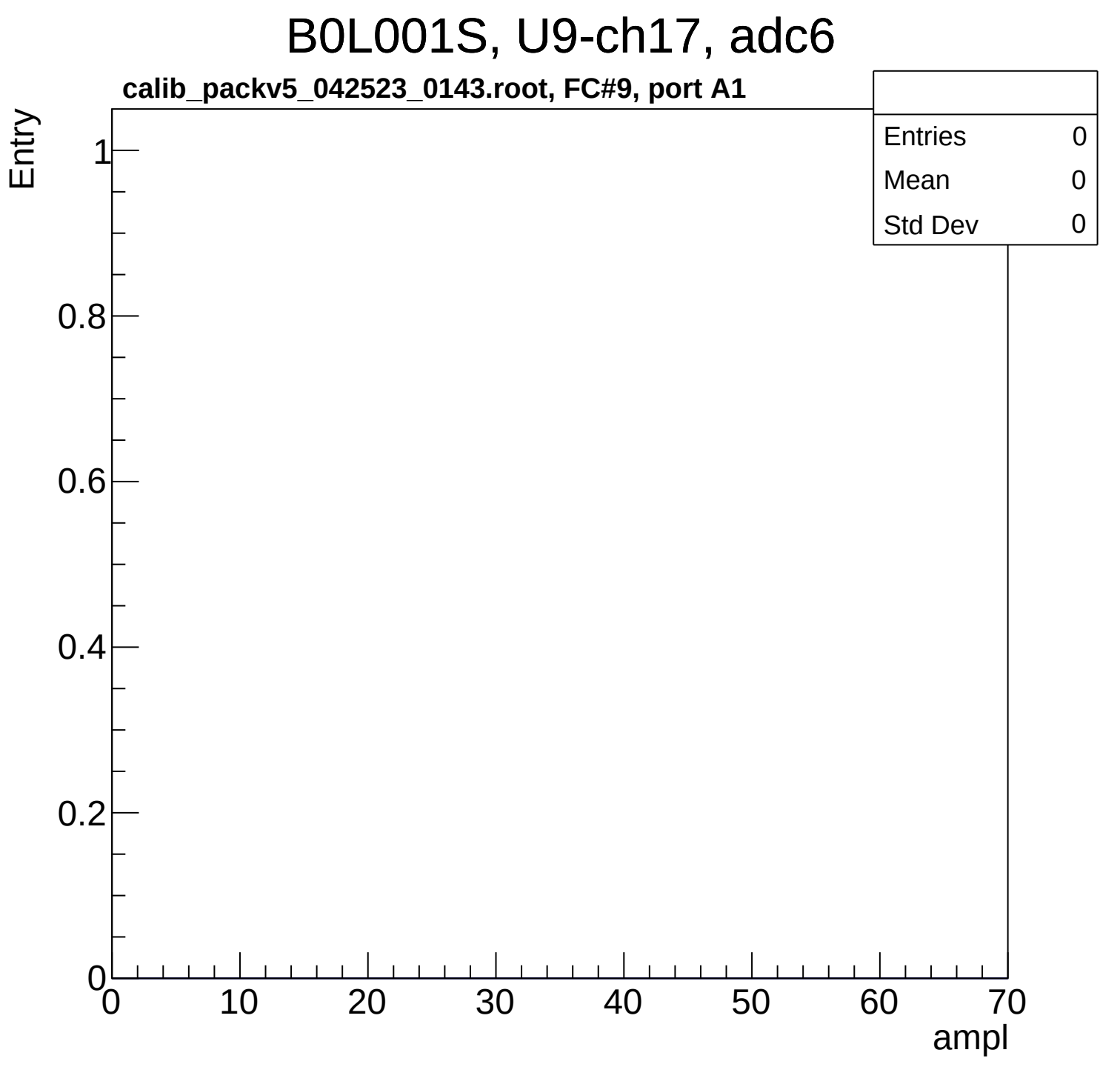
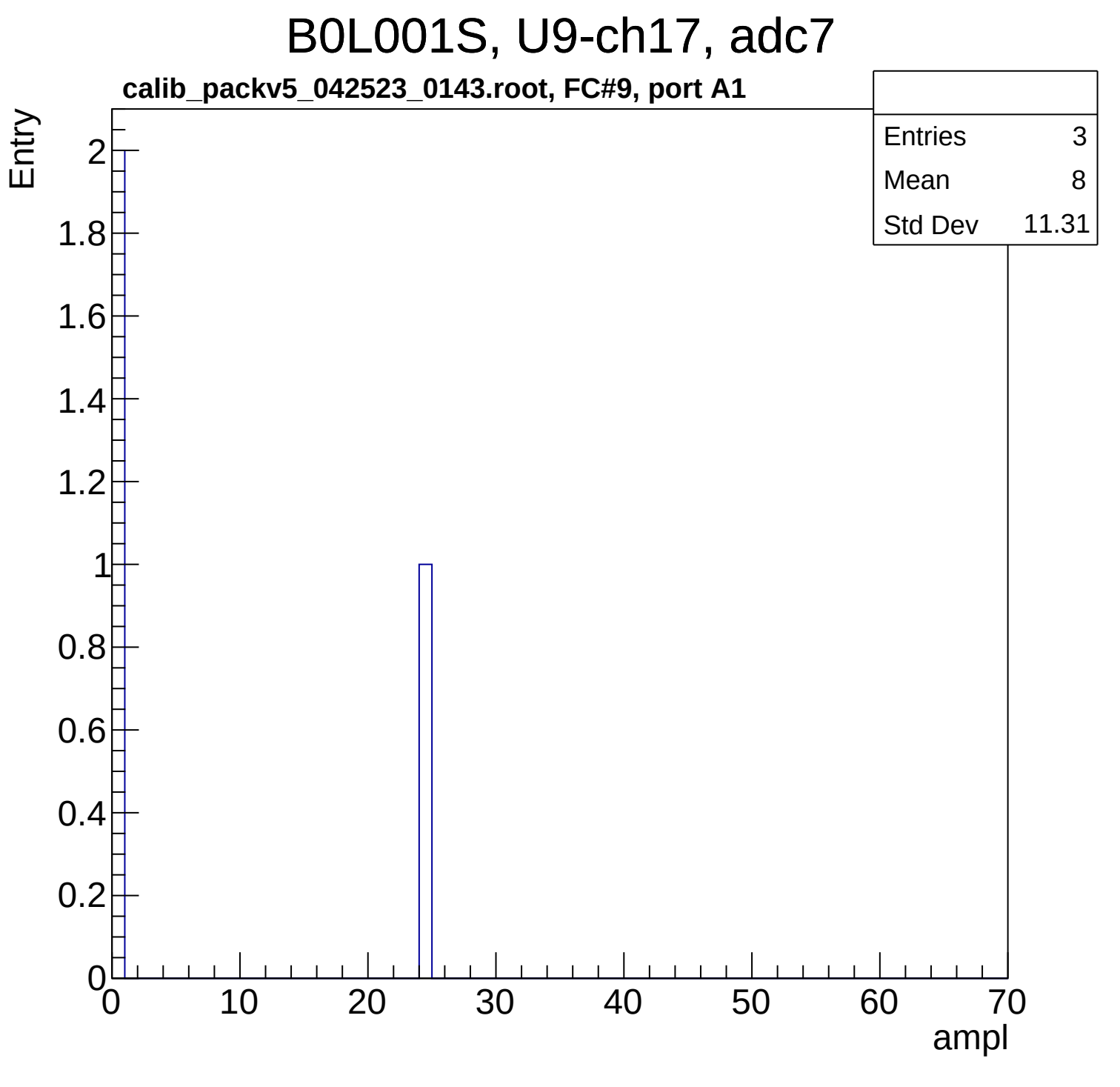

B0L001S, U9-ch17, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch17, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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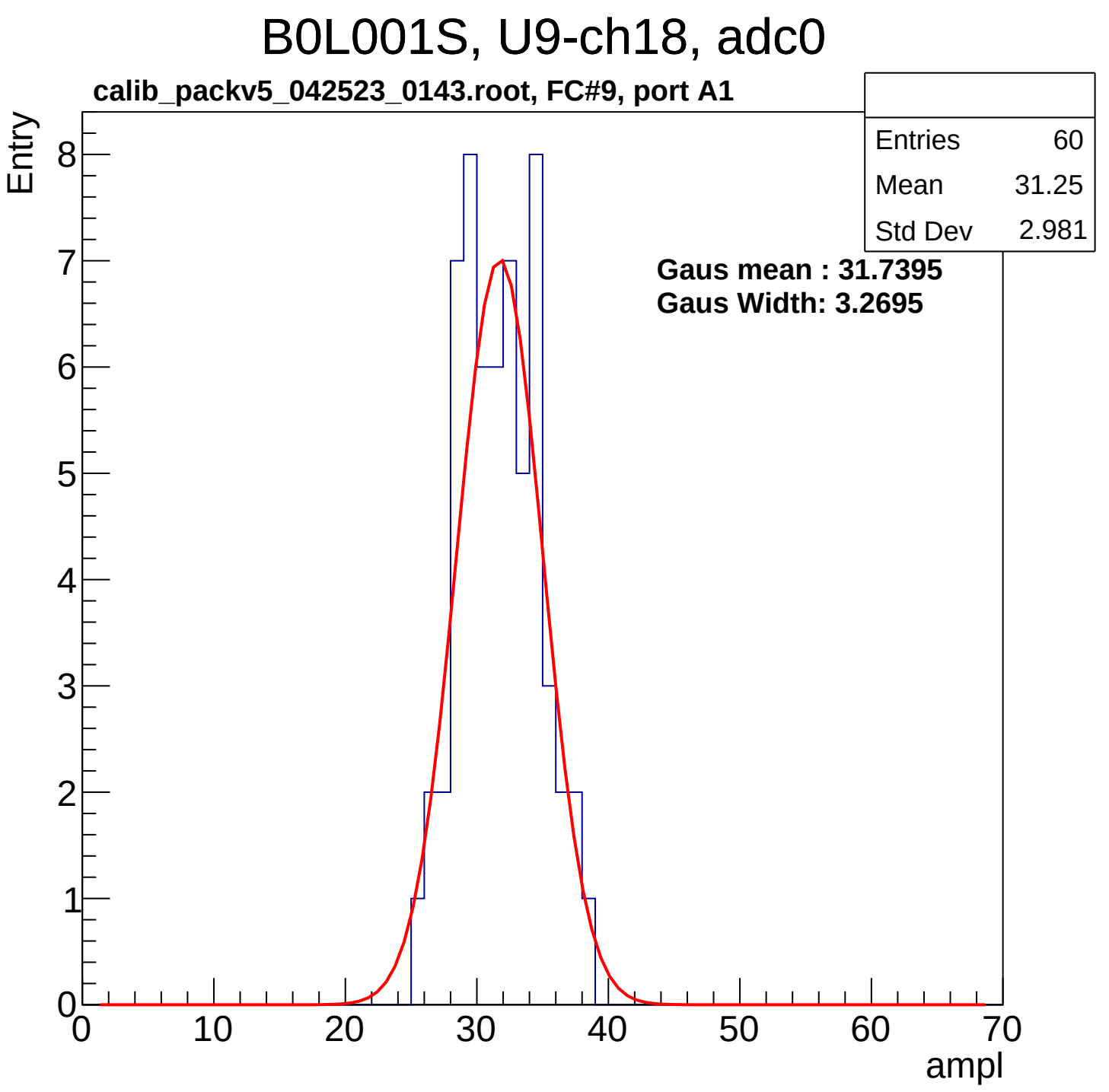
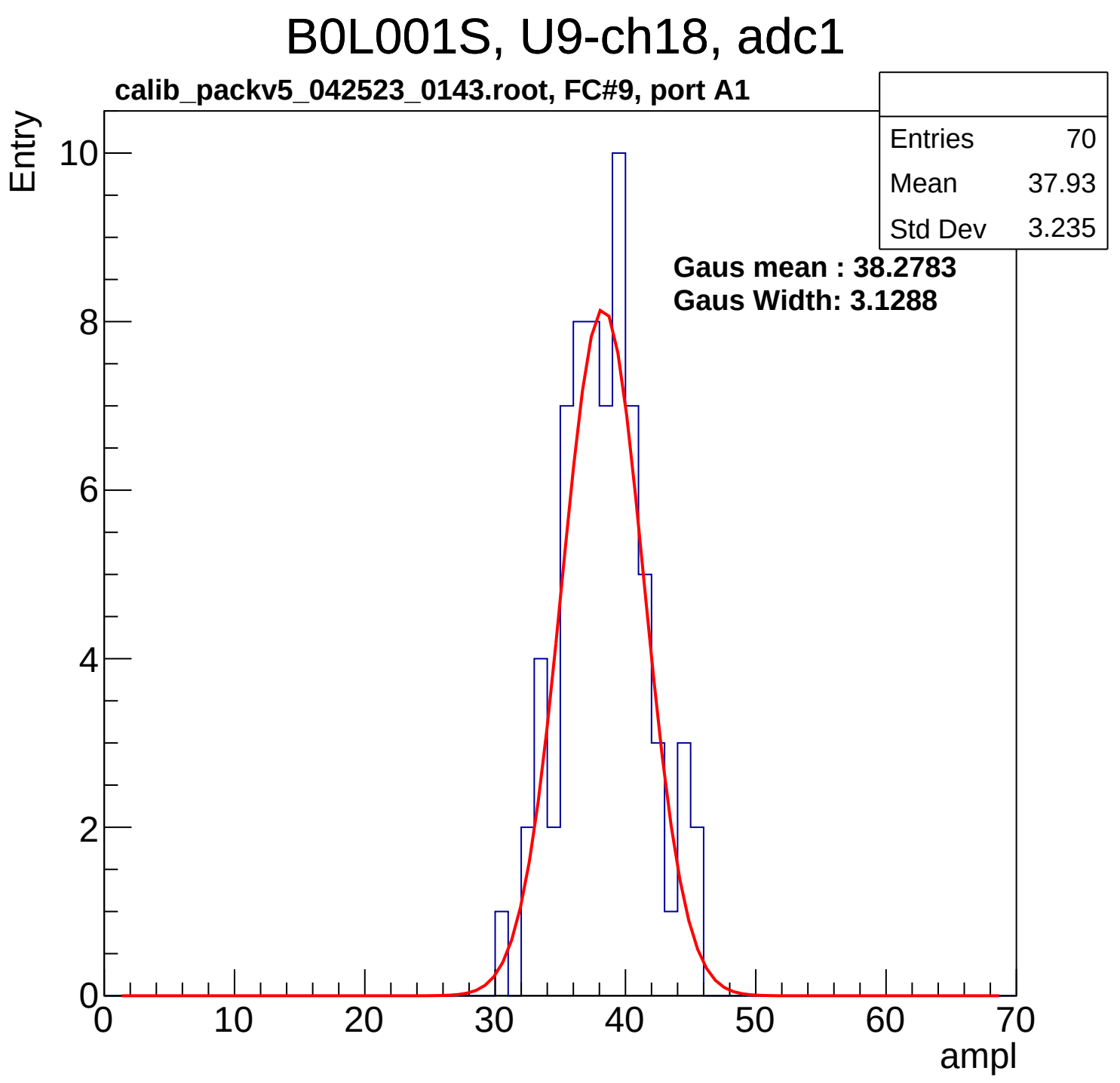
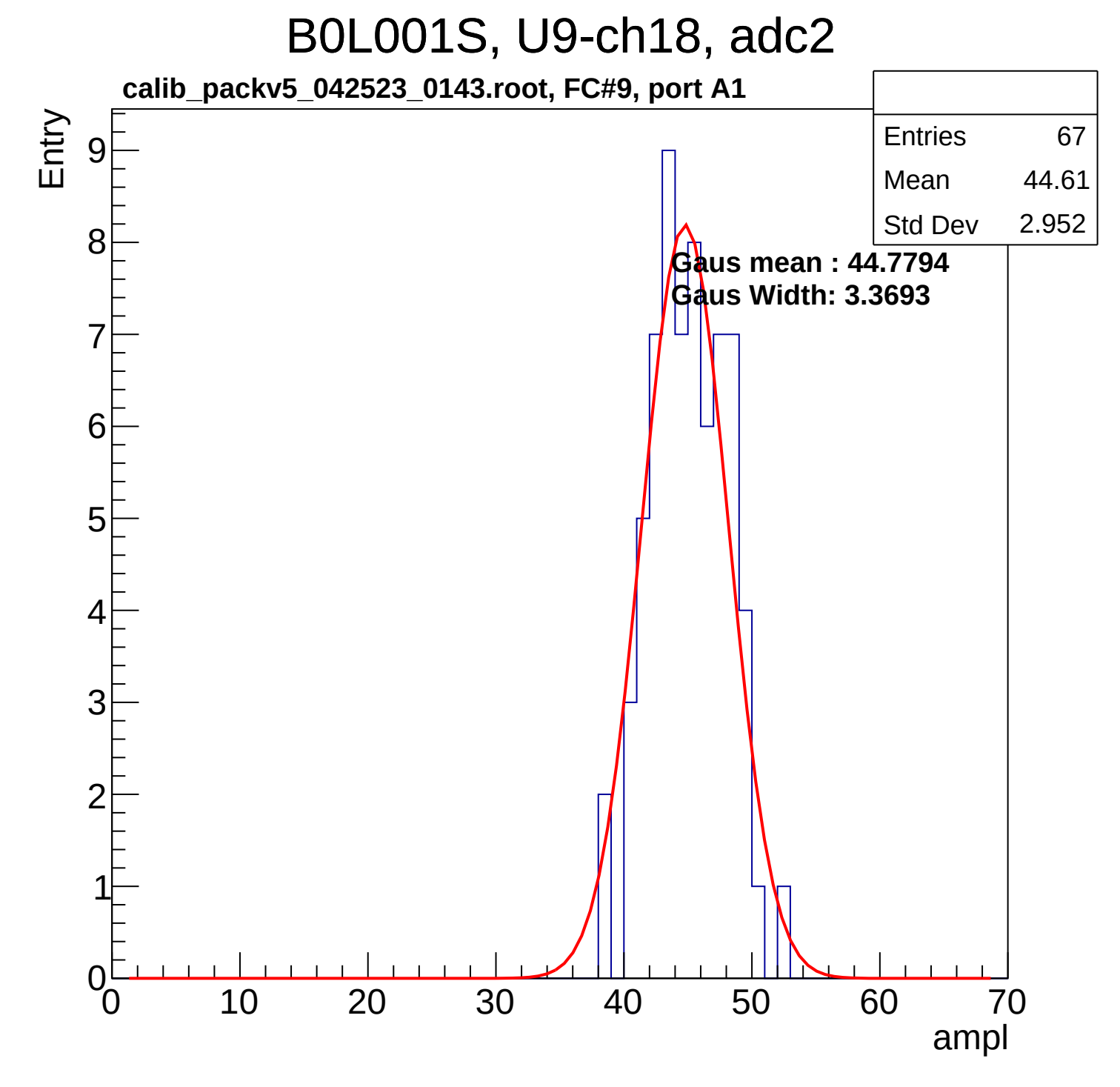
40
50
60
70


B0L001S, U9-ch18, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 31.25
Std Dev 2.981
Gaus mean : 31.7395
Gaus Width: 3.2695
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch18, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.93
Std Dev 3.235
Gaus mean : 38.2783
Gaus Width: 3.1288
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch18, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.61
Std Dev 2.952
Gaus mean : 44.7794
Gaus Width: 3.3693
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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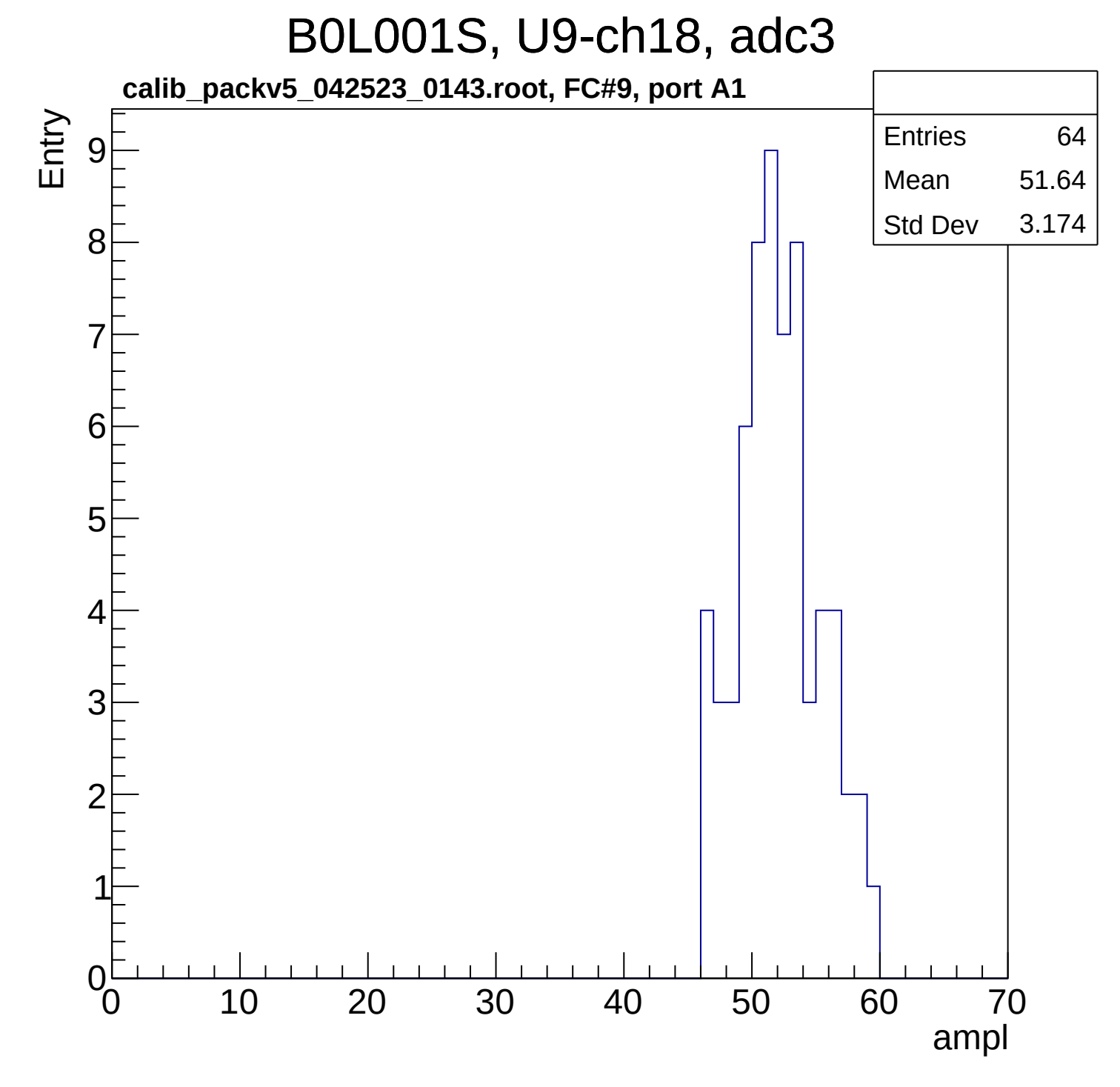
B0L001S, U9-ch18, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 51.64
Std Dev 3.174
Entry
ampl
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3
4
5
6
7
8
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0
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60
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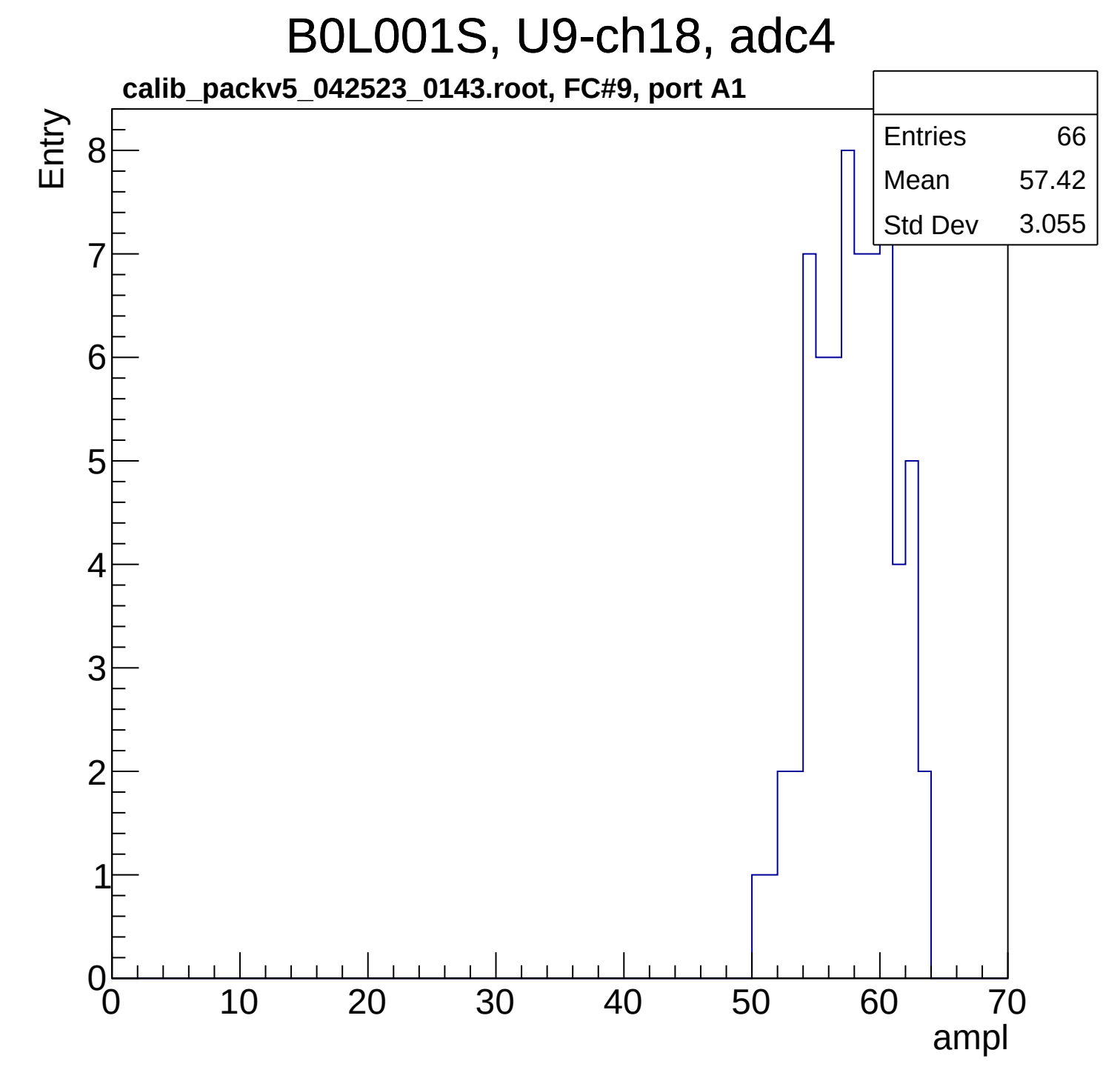
B0L001S, U9-ch18, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.42
Std Dev 3.055
Entry
ampl
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3
4
5
6
7
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50
60
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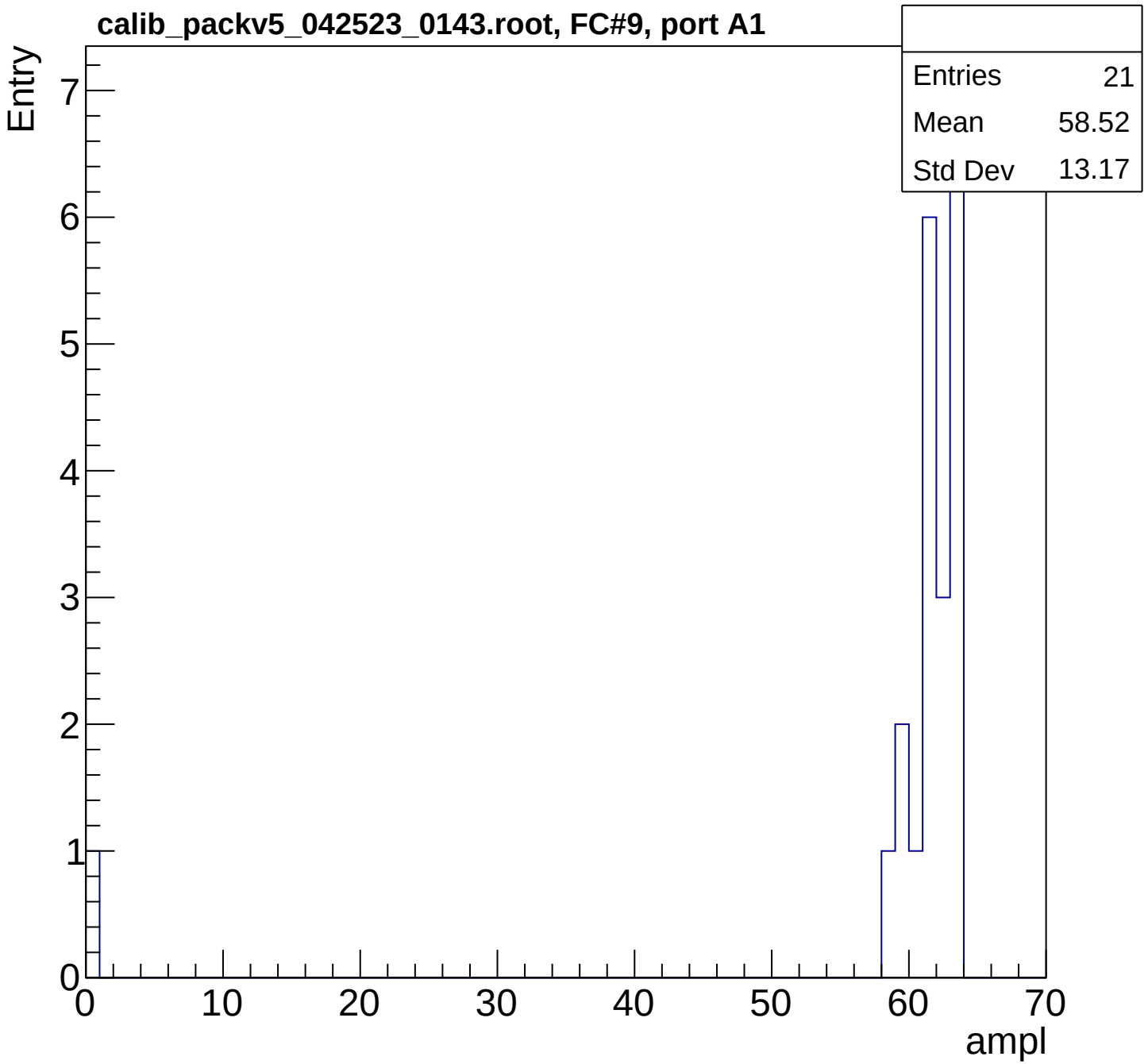
B0L001S, U9-ch18, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 21
Mean 58.52
Std Dev 13.17
Entry
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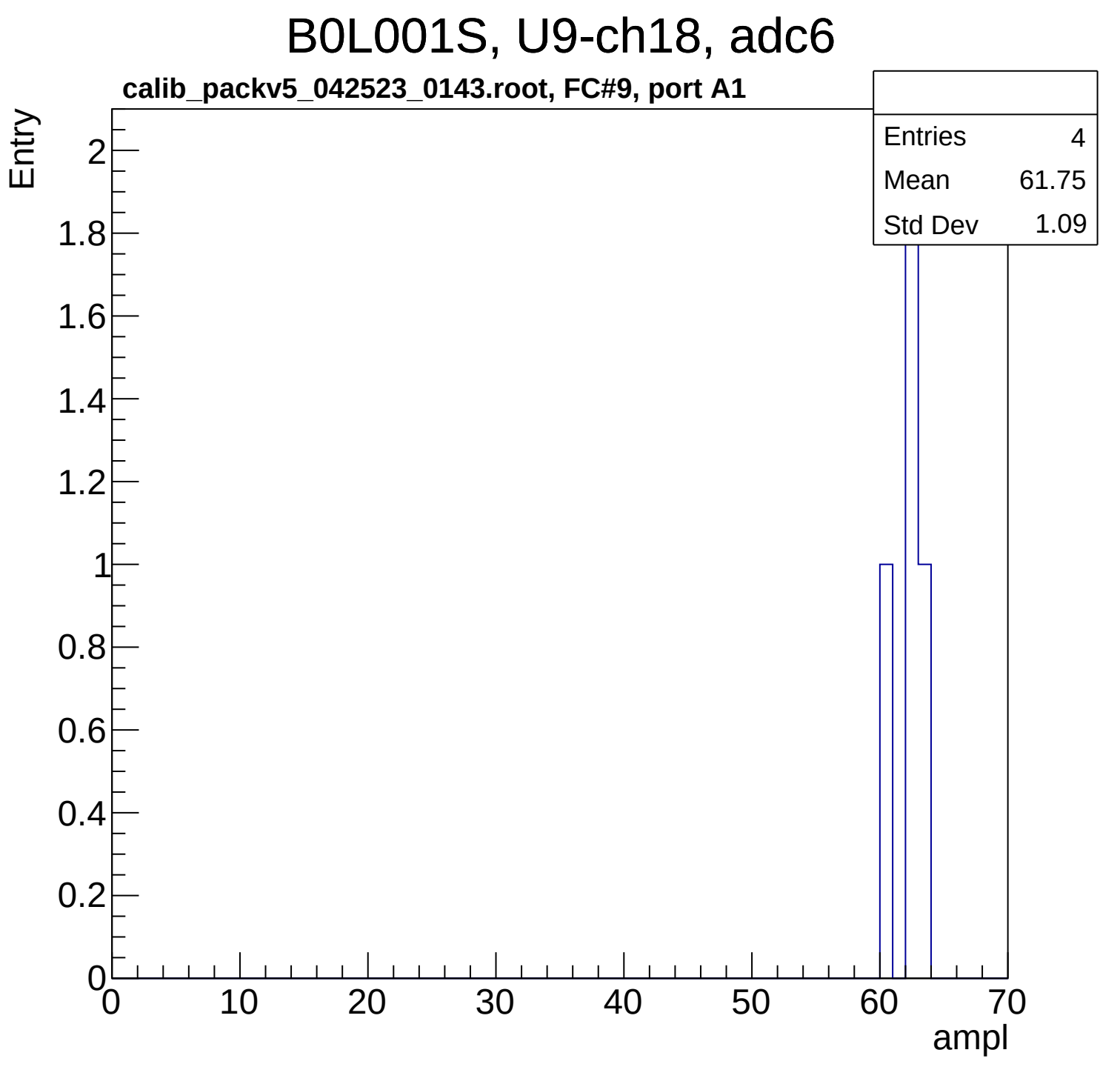
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch18, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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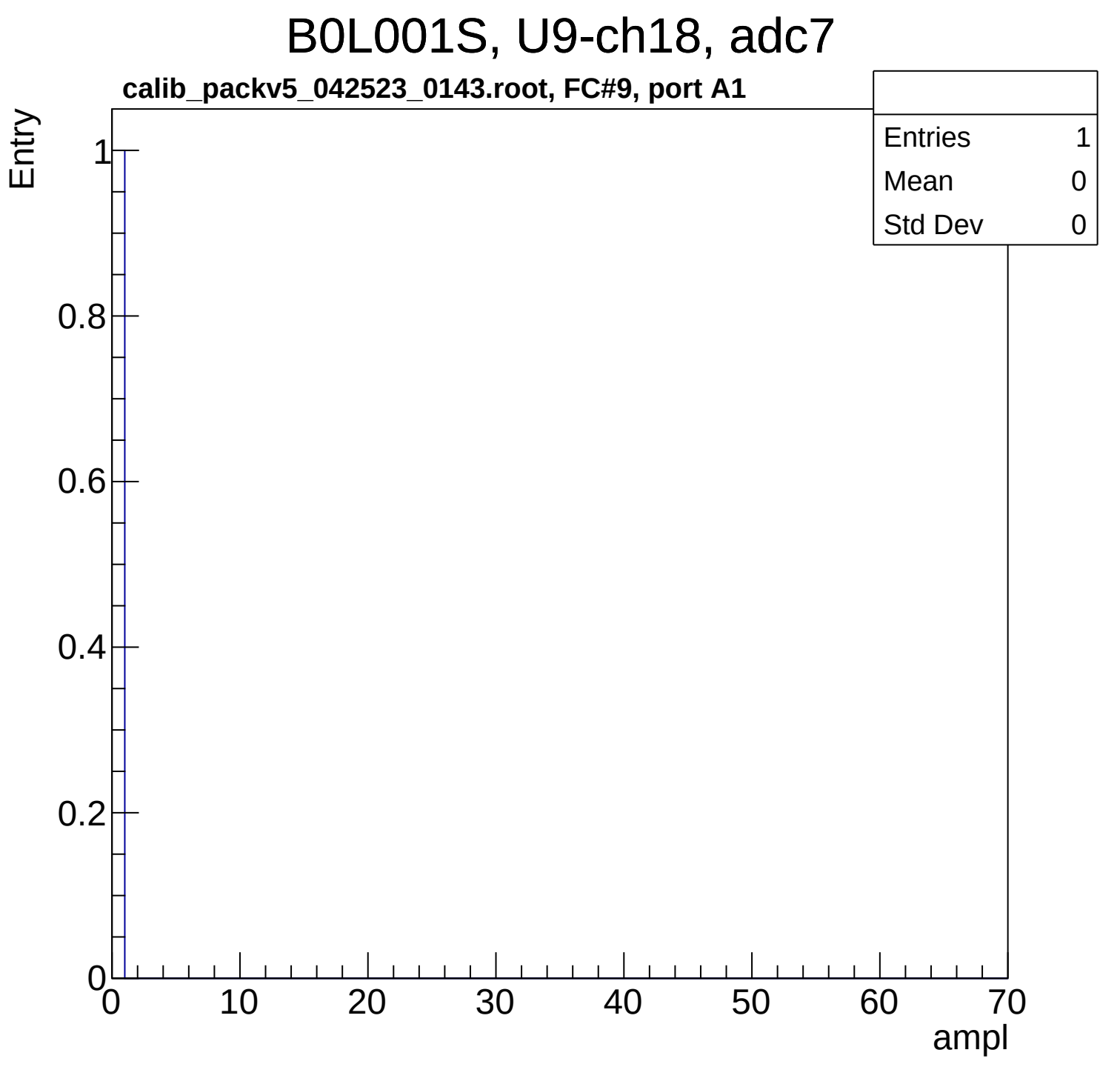
40
50
60
70


B0L001S, U9-ch18, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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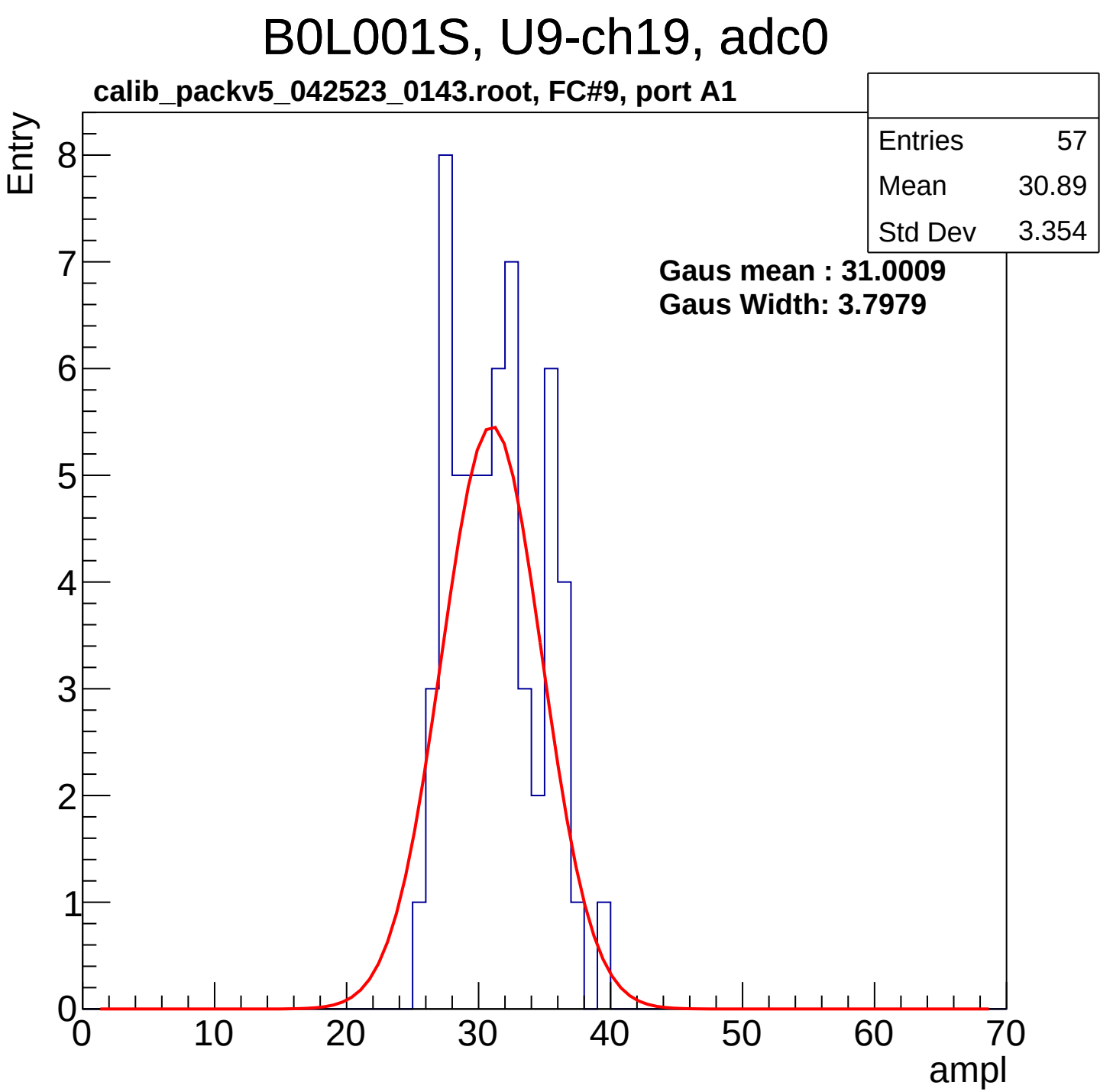
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch19, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 30.89
Std Dev 3.354
Gaus mean : 31.0009
Gaus Width: 3.7979
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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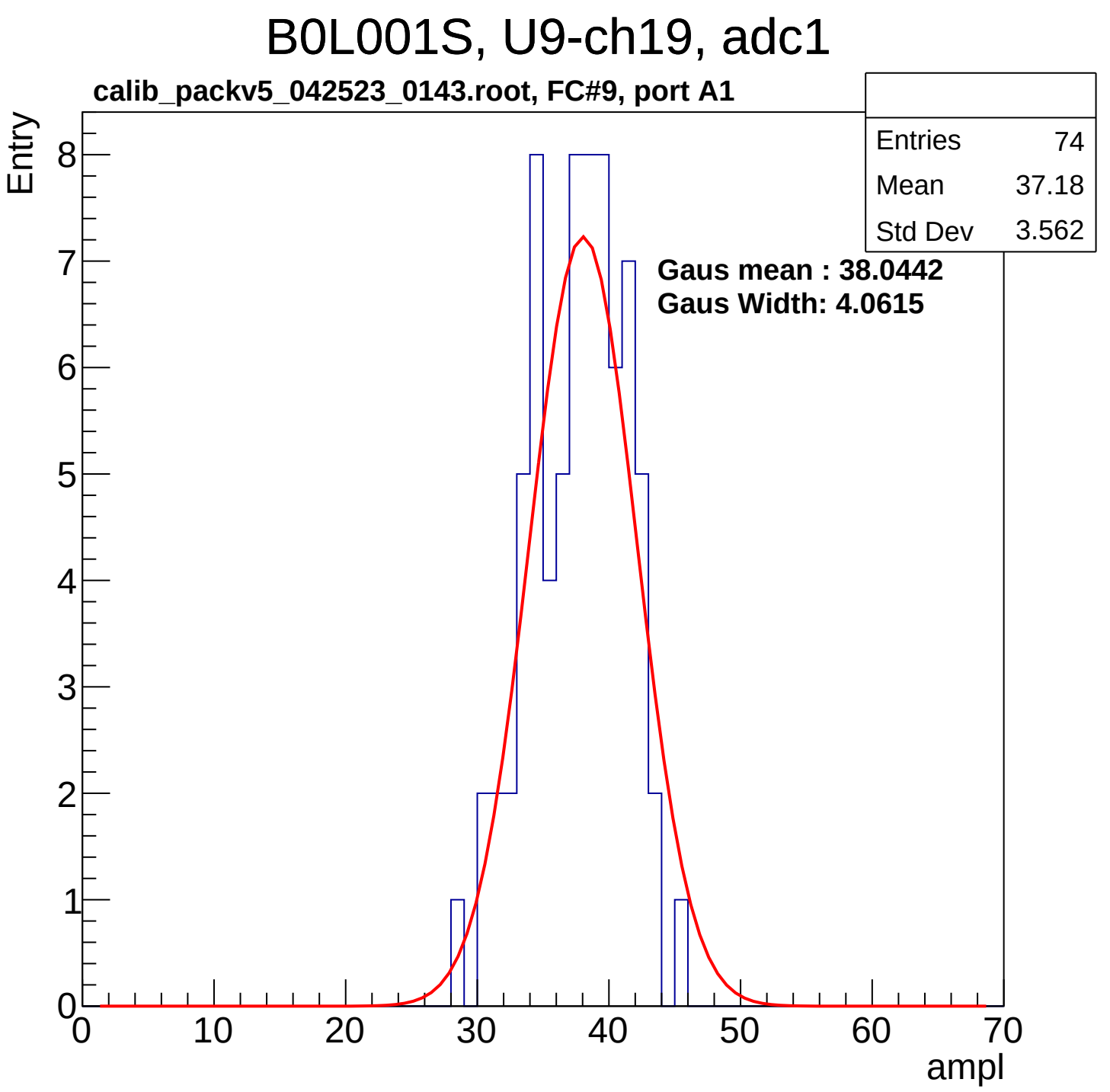
B0L001S, U9-ch19, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.18
Std Dev 3.562
Gaus mean : 38.0442
Gaus Width: 4.0615
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
50
60
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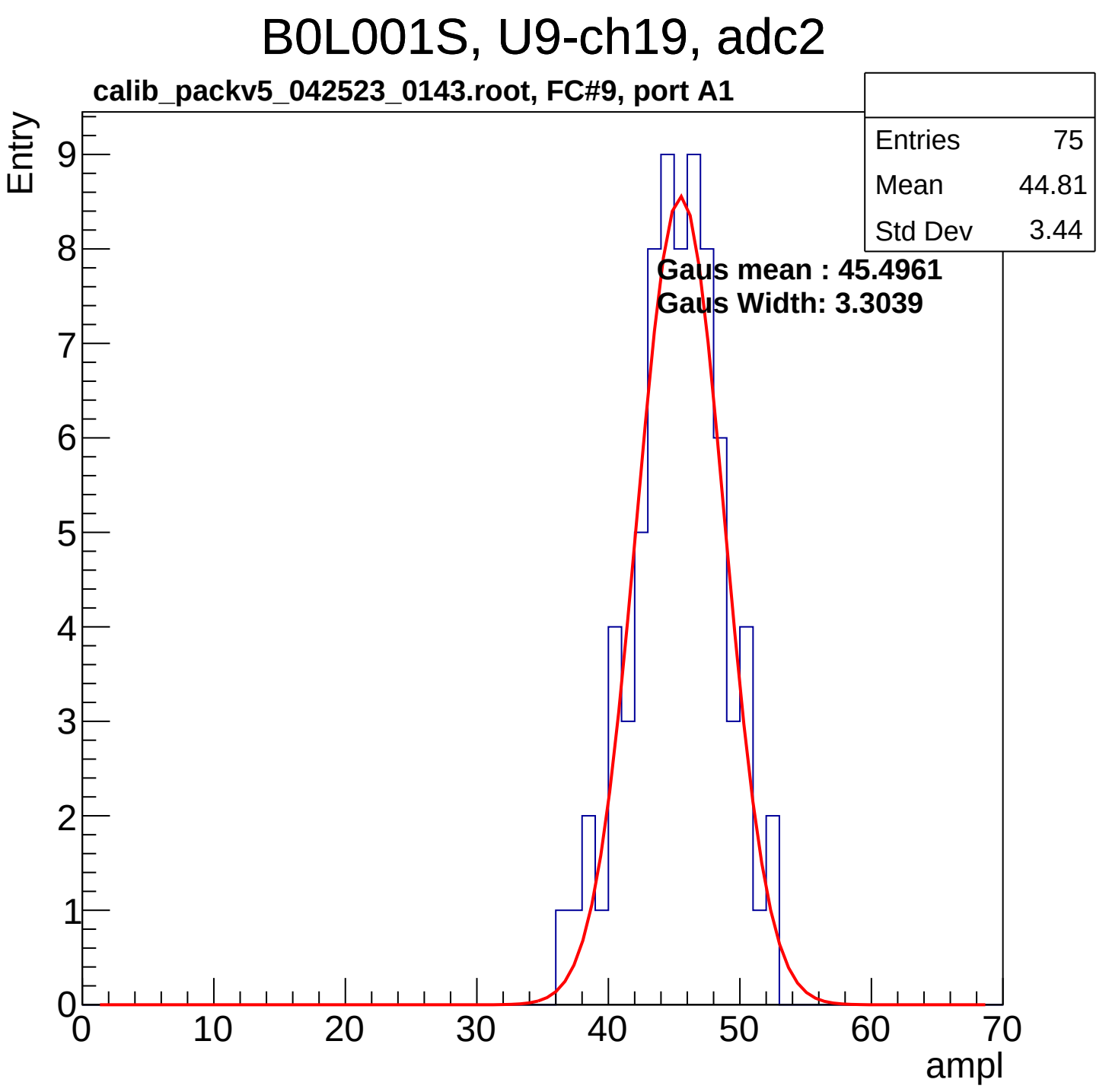
B0L001S, U9-ch19, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 44.81
Std Dev 3.44
Gaus mean : 45.4961
Gaus Width: 3.3039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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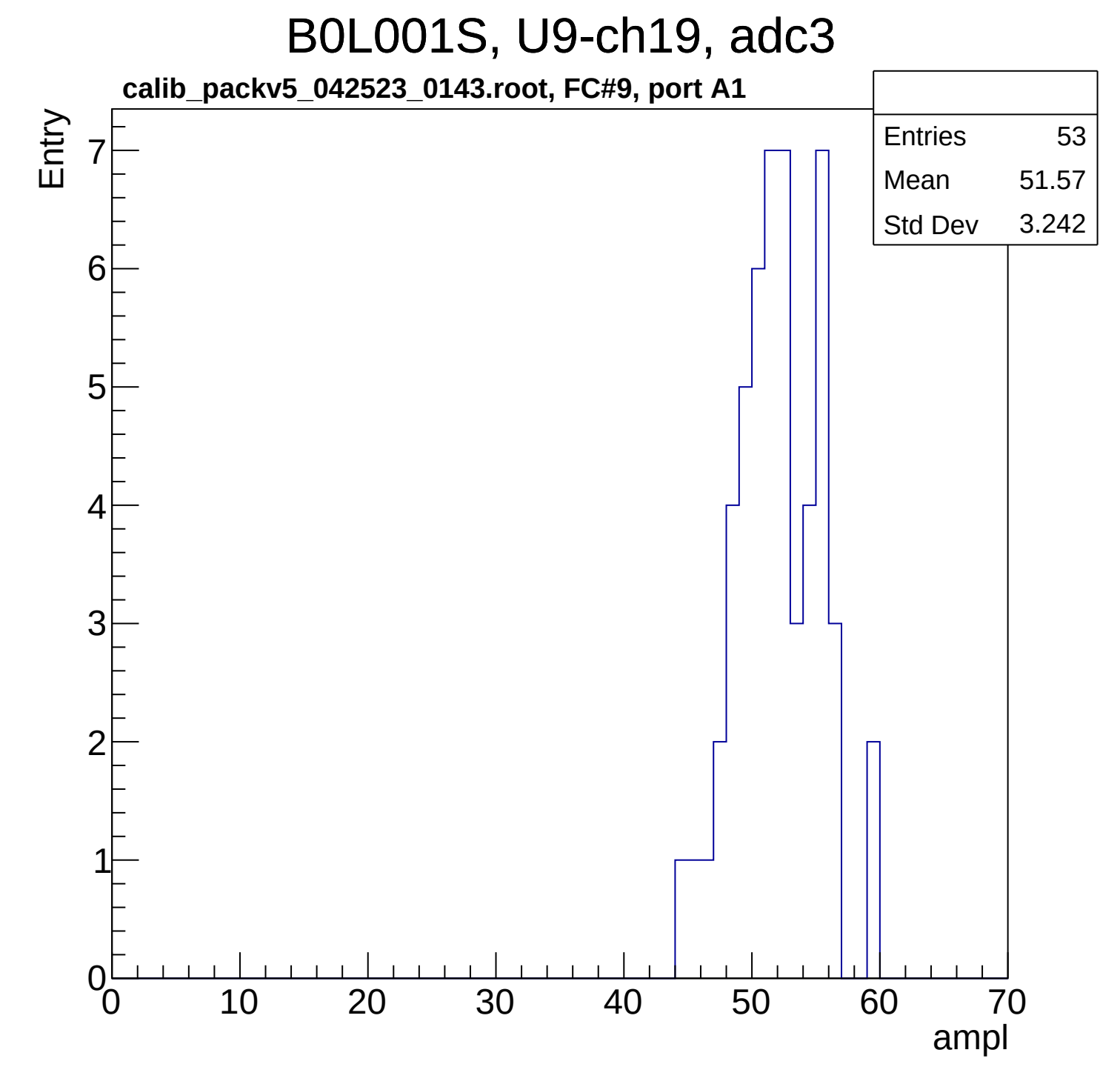
40
50
60
70


B0L001S, U9-ch19, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 51.57
Std Dev 3.242
Entry
ampl
0
1
2
3
4
5
6
7
0
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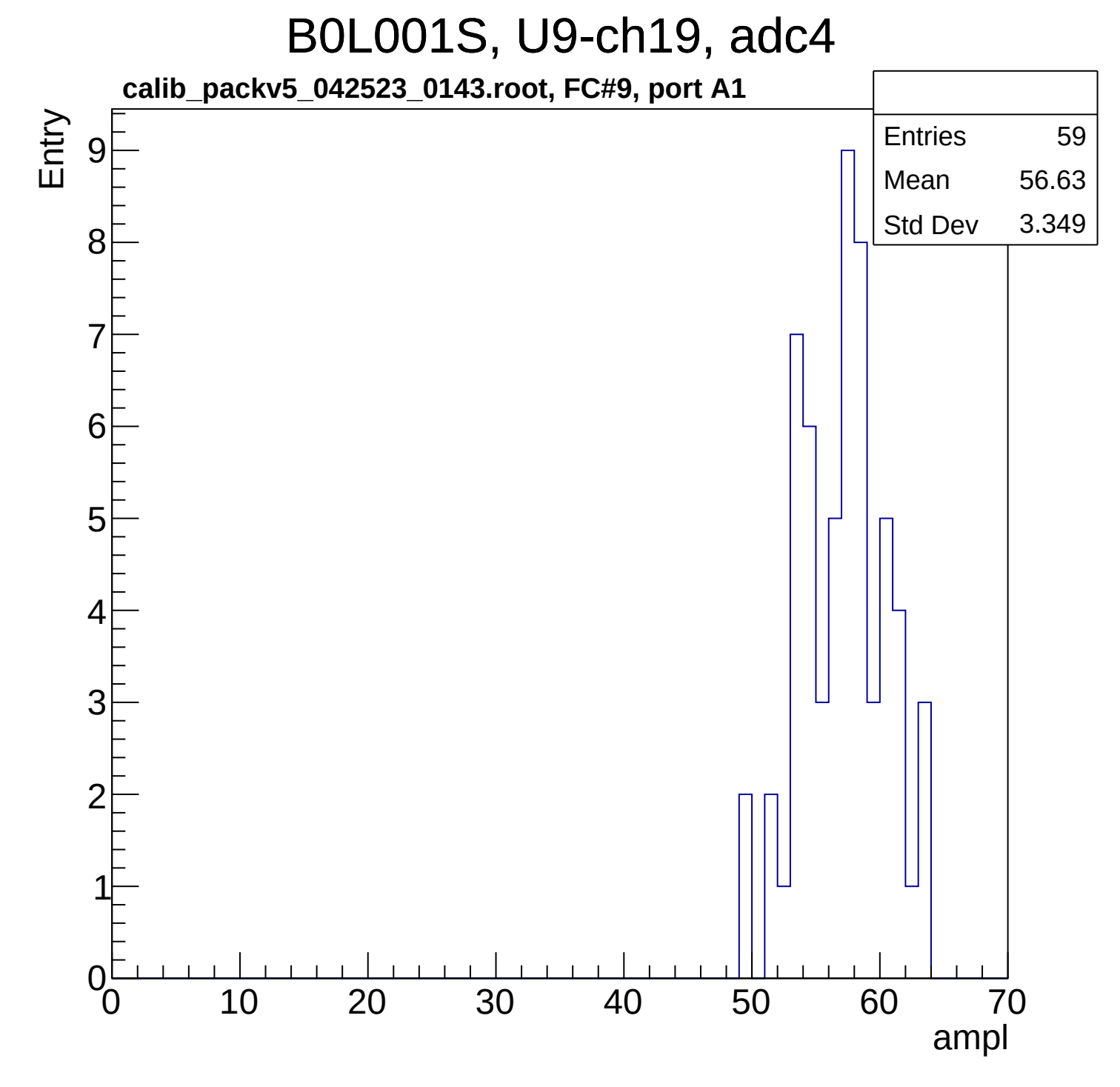
40
50
60
70


B0L001S, U9-ch19, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.63
Std Dev 3.349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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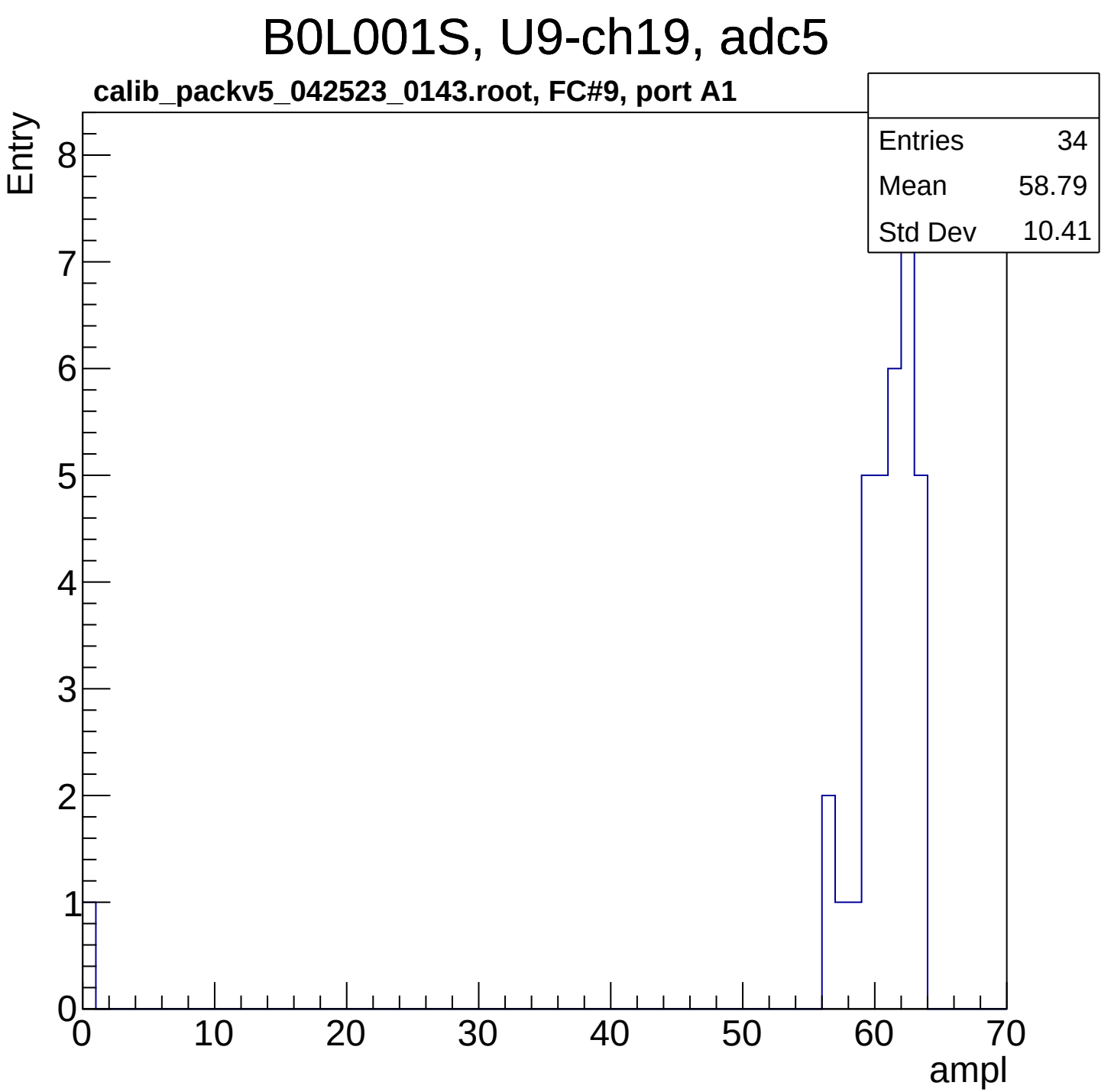
B0L001S, U9-ch19, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.79
Std Dev 10.41
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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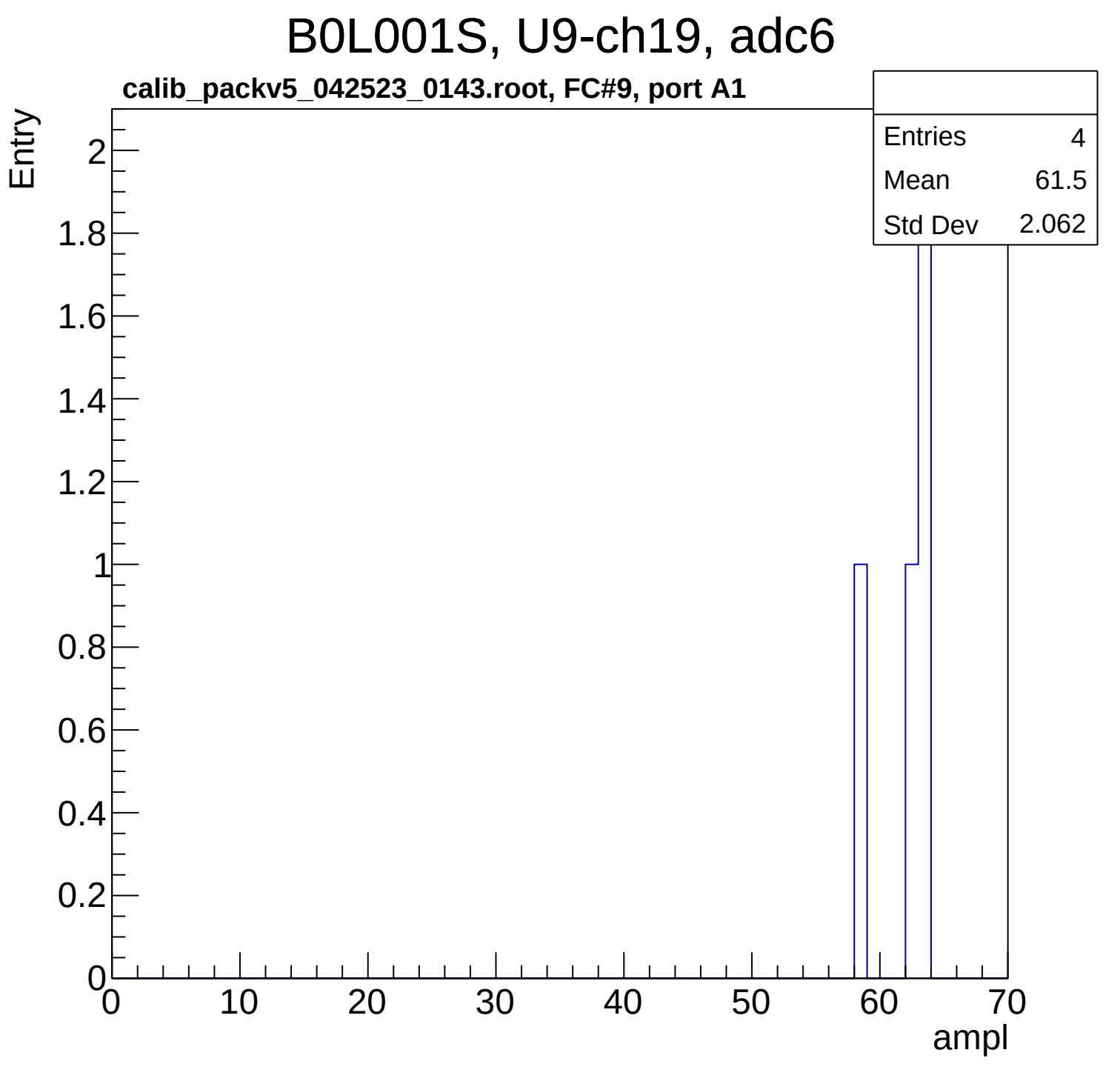
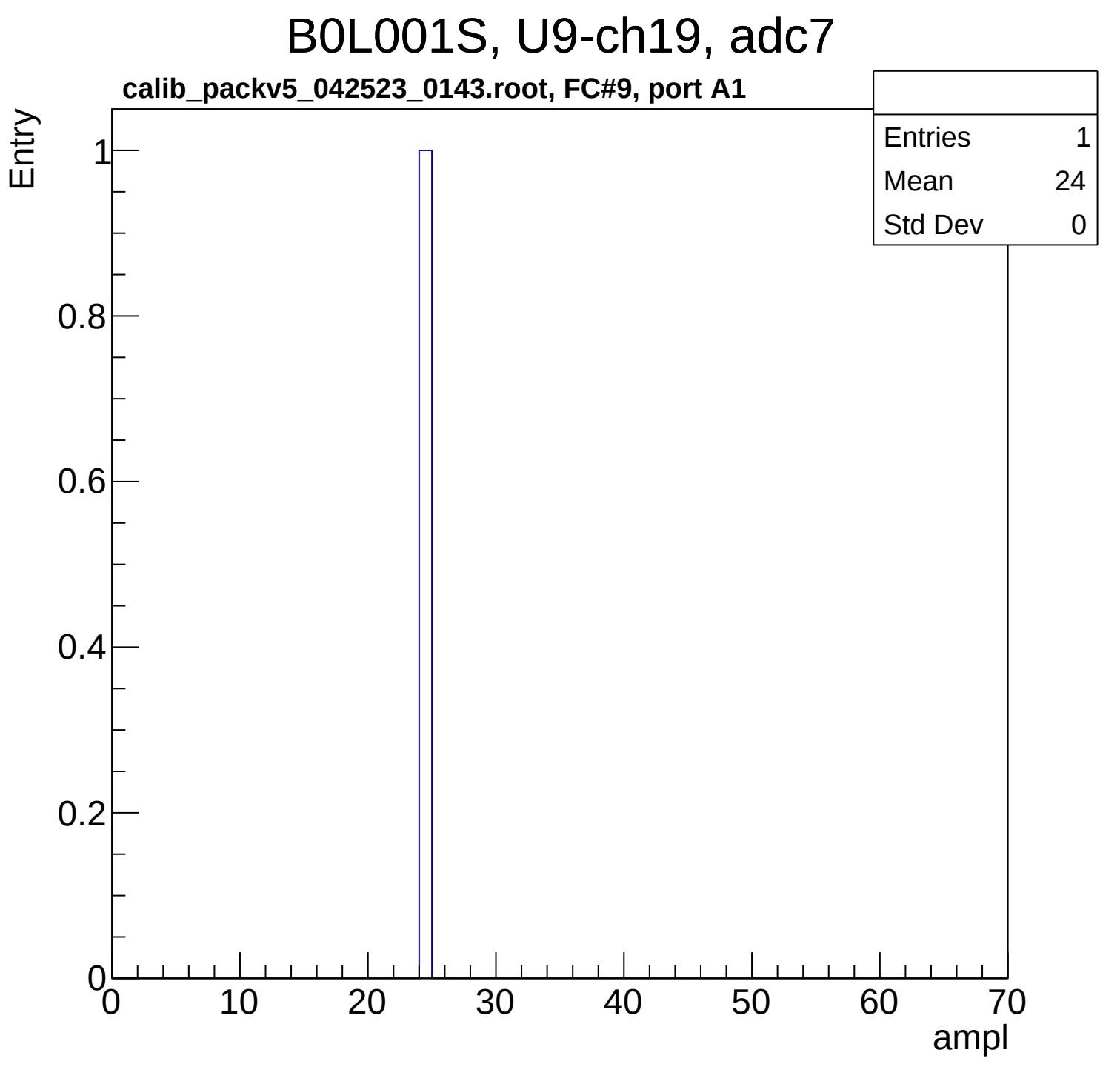
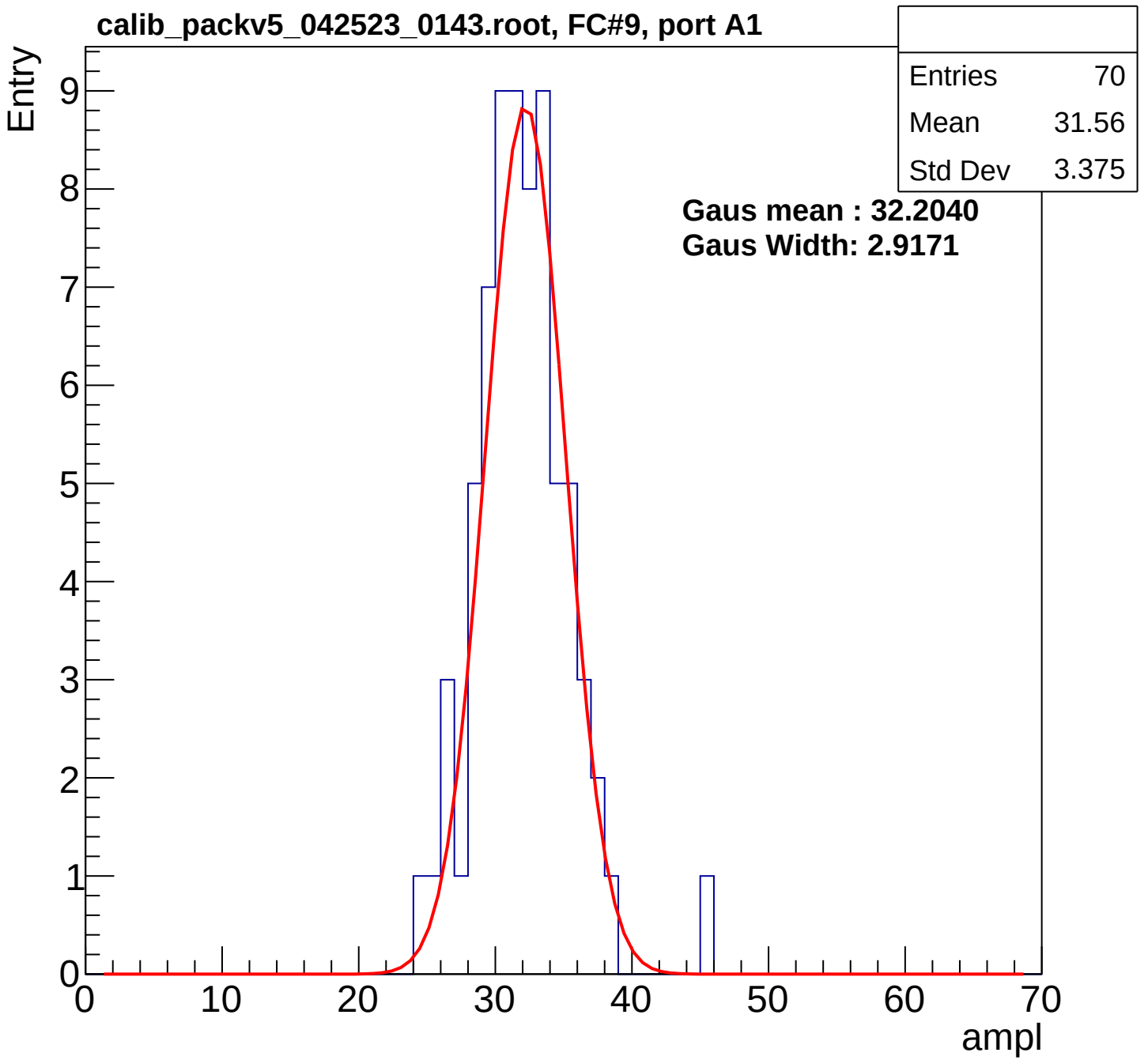
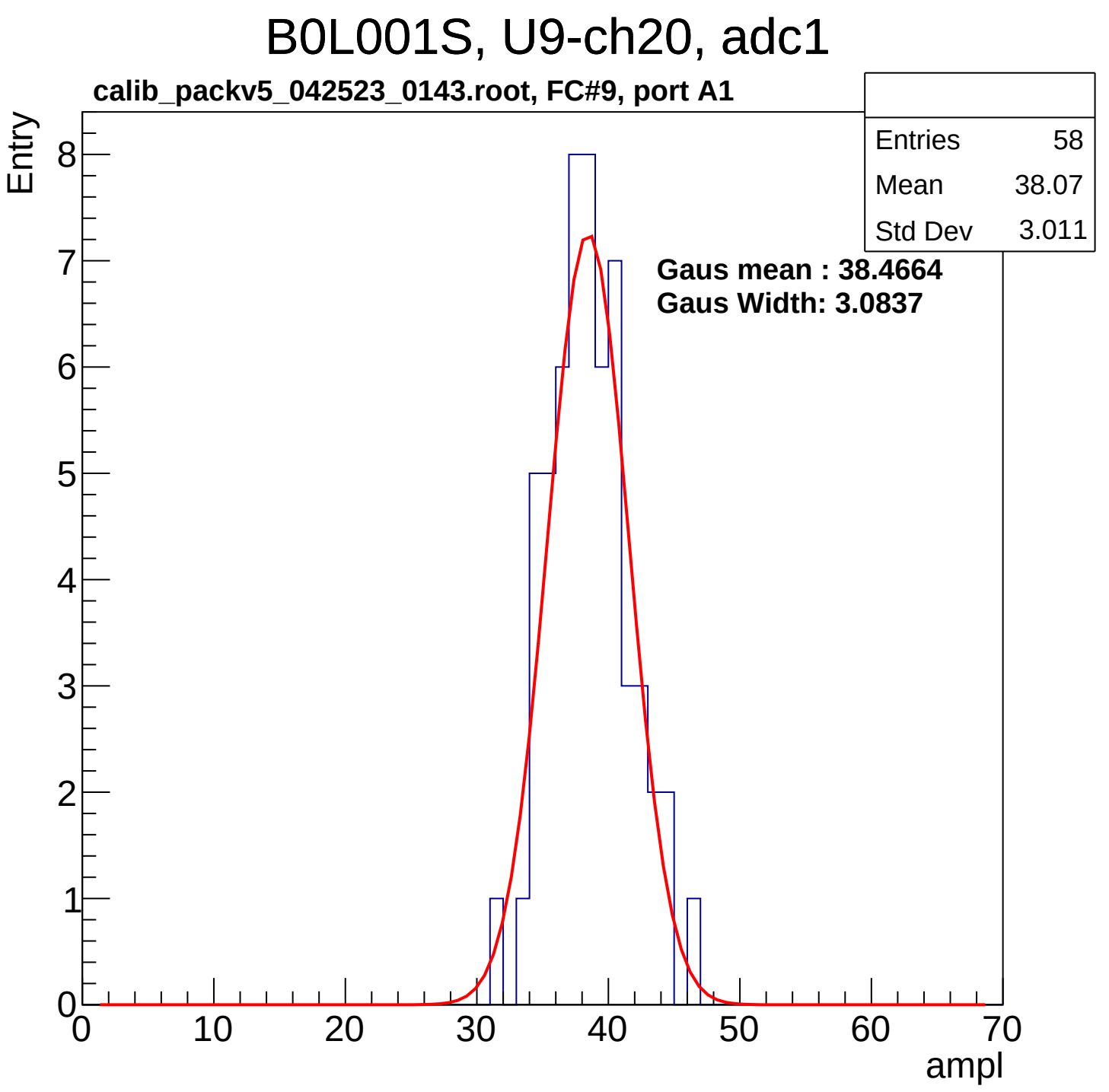
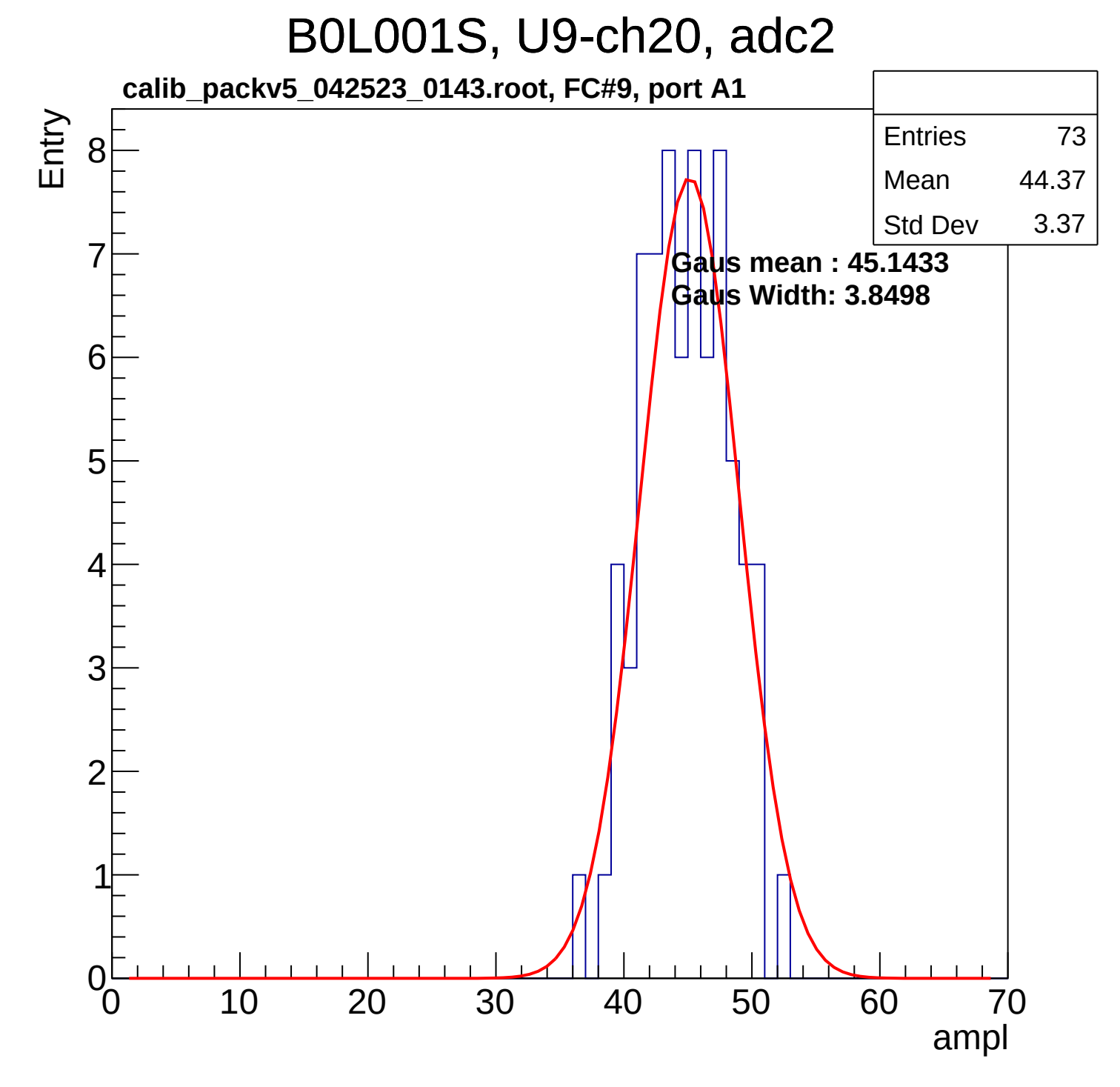

B0L001S, U9-ch19, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.5
Std Dev 2.062
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch19, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch20, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 31.56
Std Dev 3.375
Gaus mean : 32.2040
Gaus Width: 2.9171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch20, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 38.07
Std Dev 3.011
Gaus mean : 38.4664
Gaus Width: 3.0837
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch20, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 44.37
Std Dev 3.37
Gaus mean : 45.1433
Gaus Width: 3.8498
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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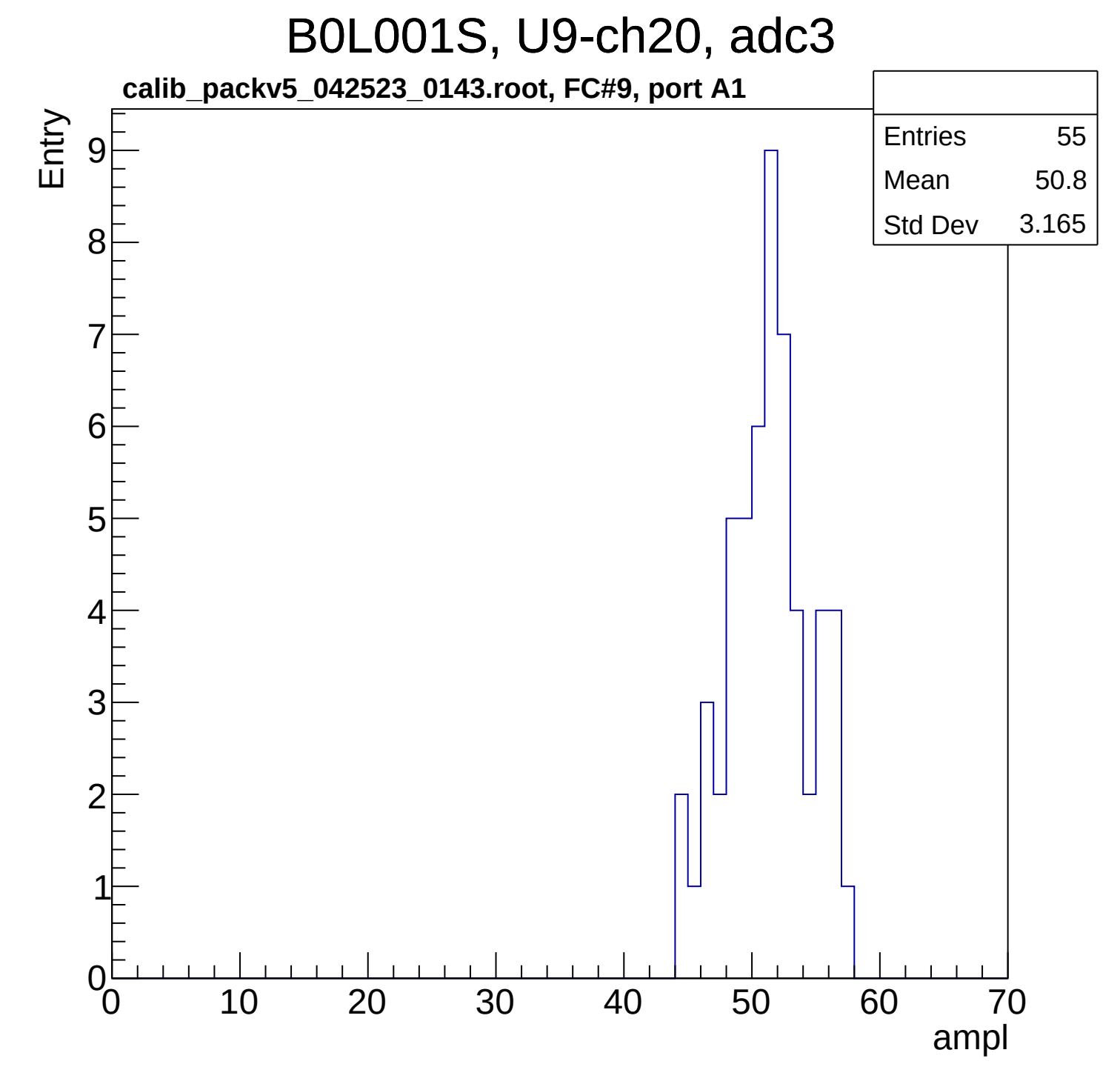
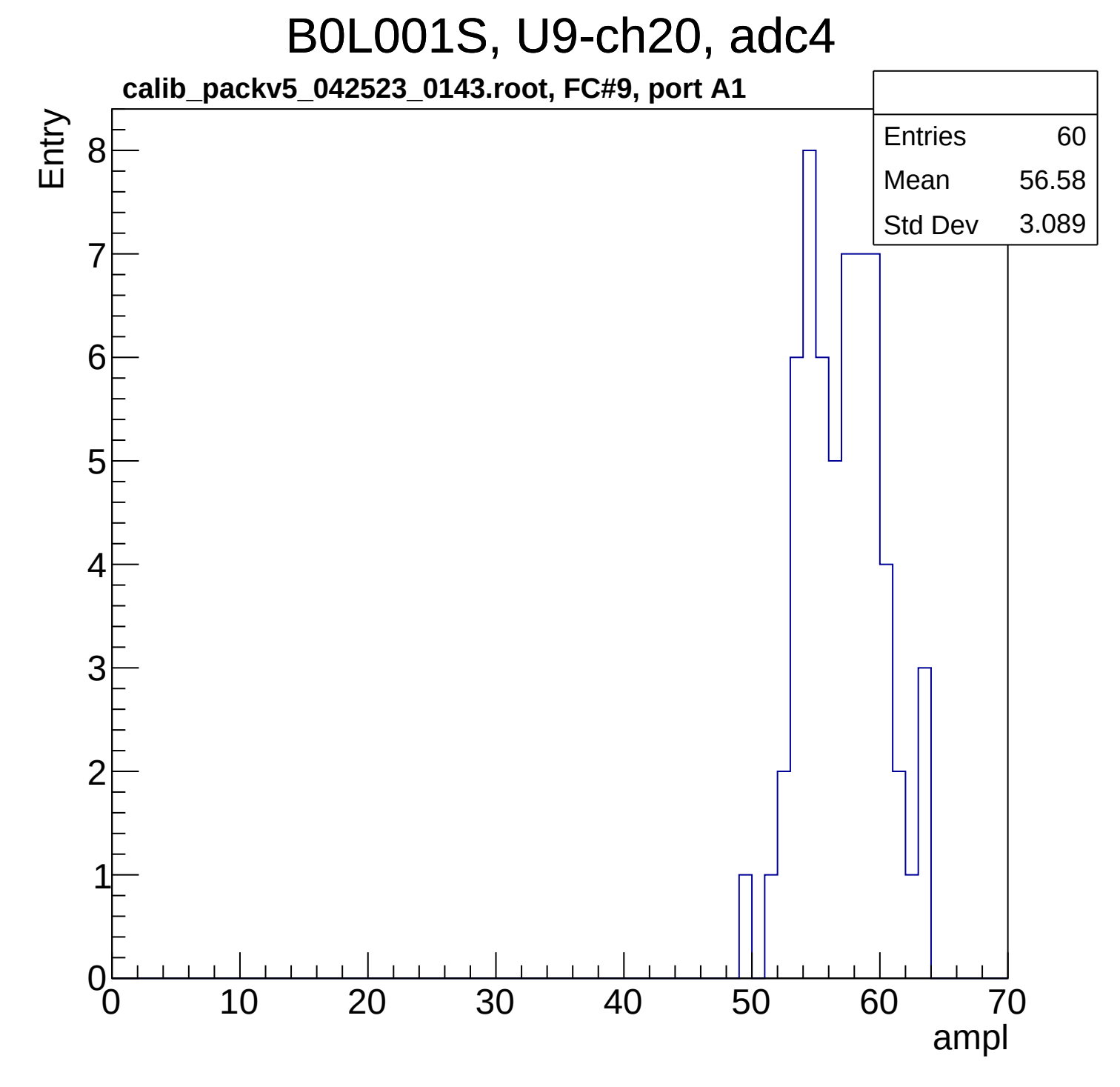

B0L001S, U9-ch20, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 50.8
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch20, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.58
Std Dev 3.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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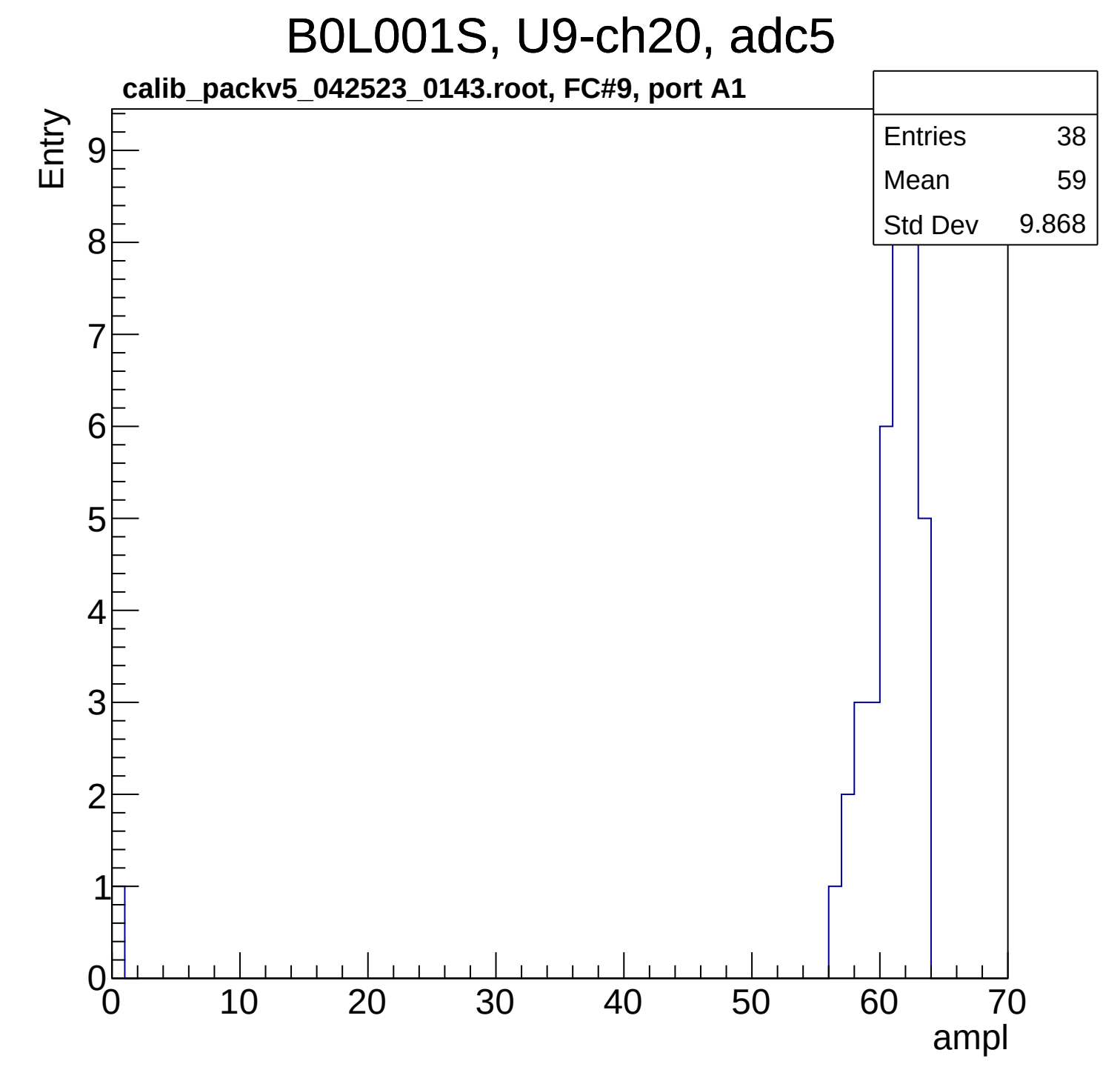
B0L001S, U9-ch20, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 59
Std Dev 9.868
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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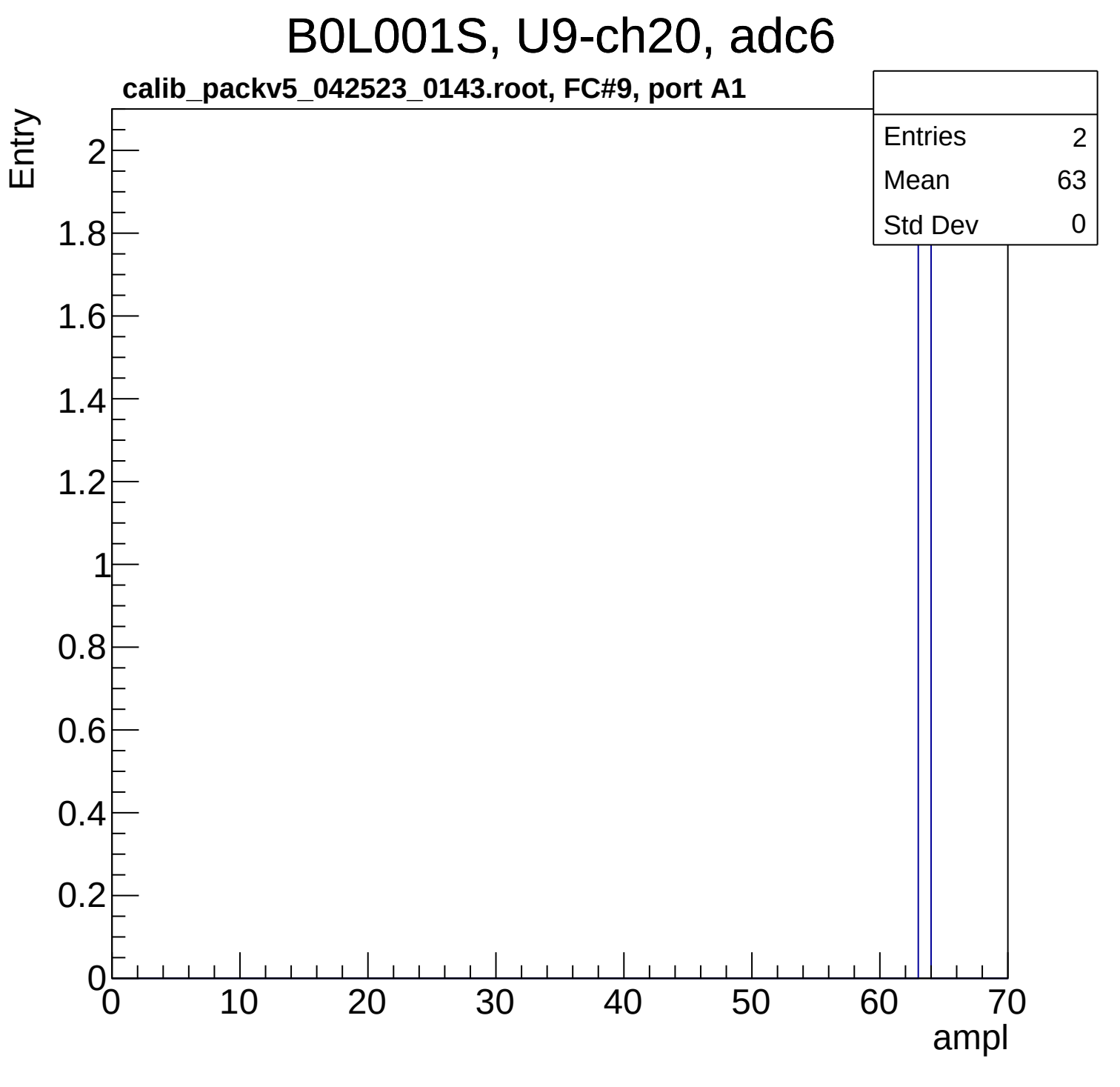
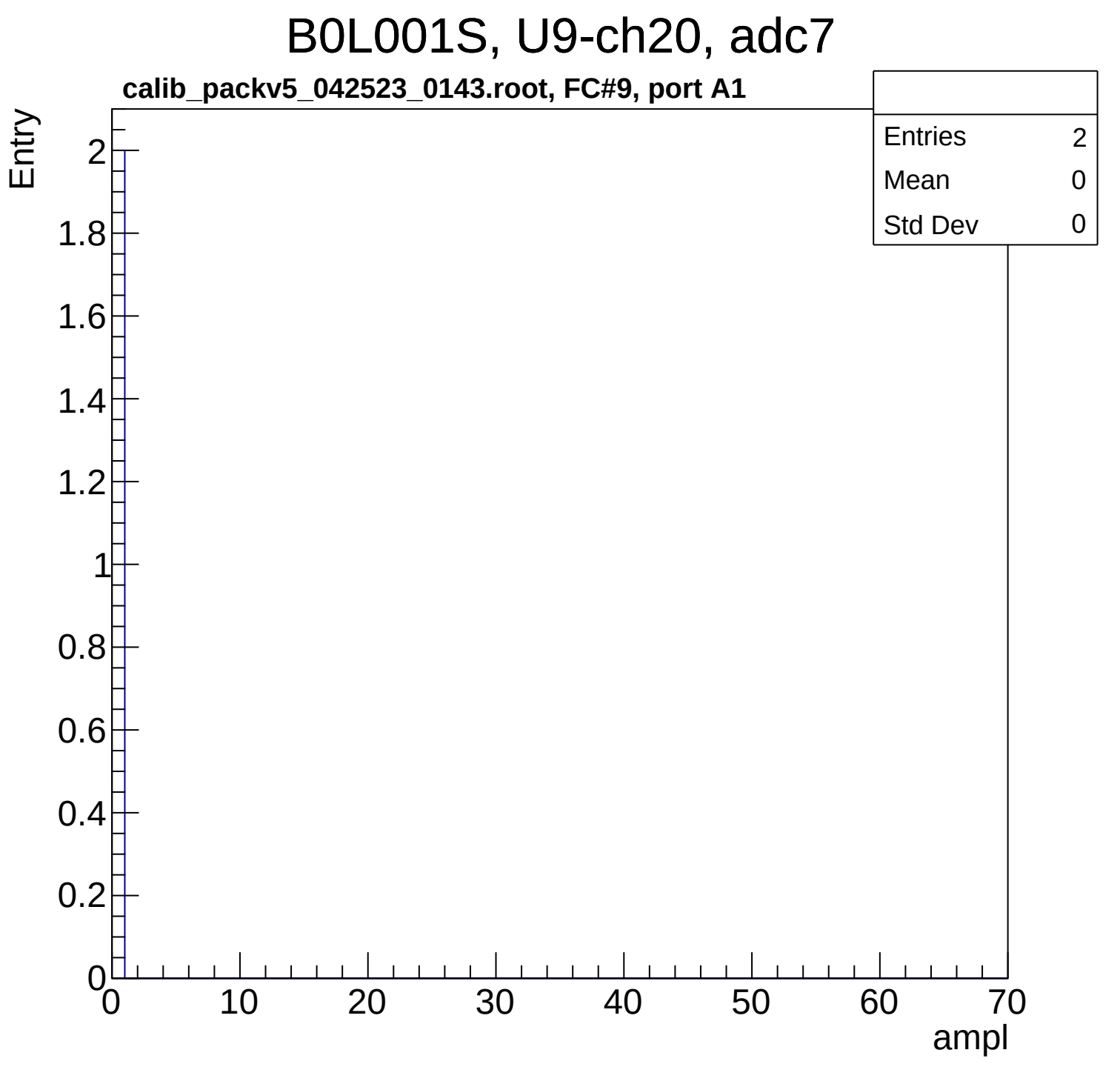

B0L001S, U9-ch20, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch20, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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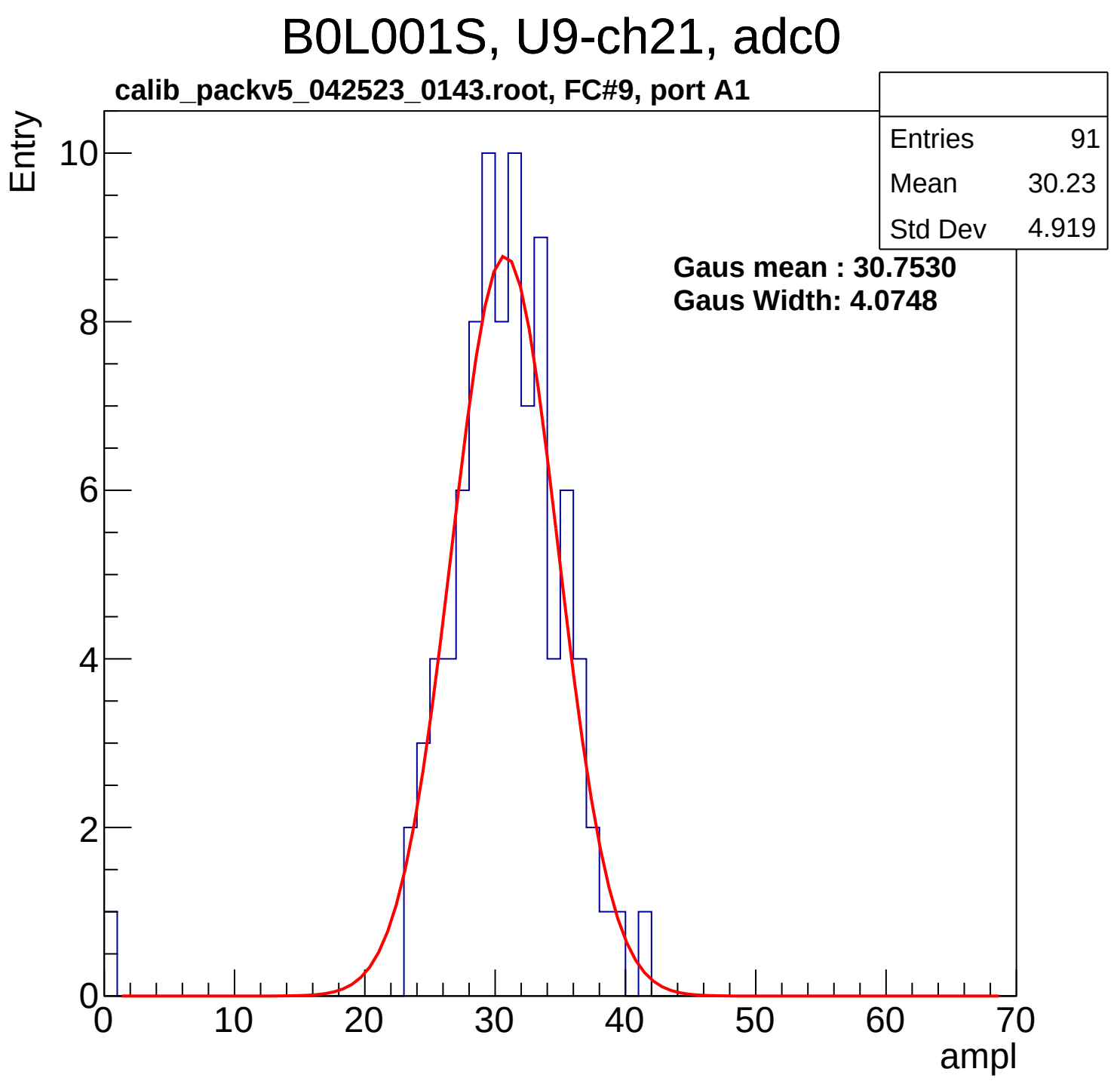
B0L001S, U9-ch21, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 91
Mean 30.23
Std Dev 4.919
Gaus mean : 30.7530
Gaus Width: 4.0748
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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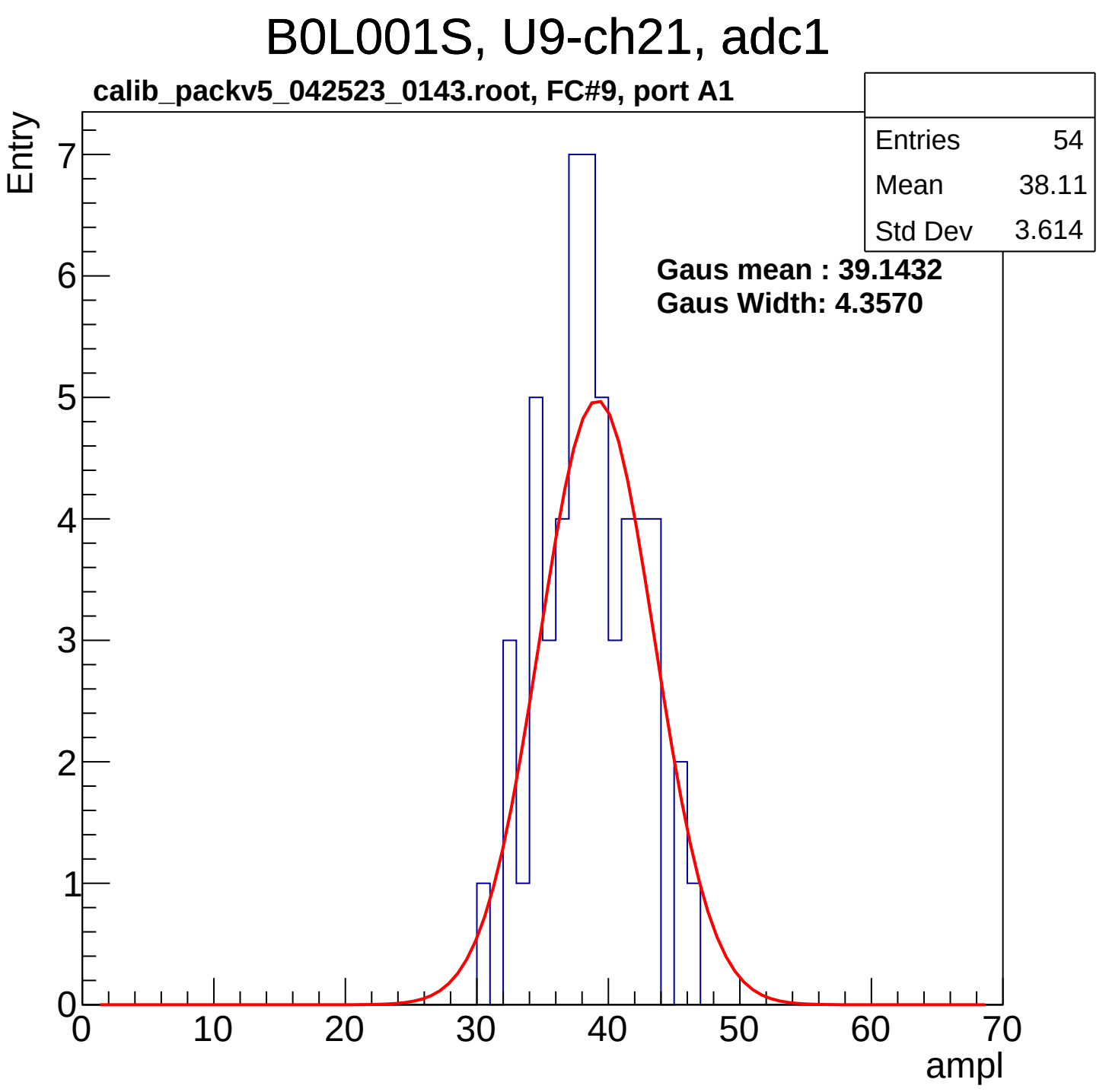
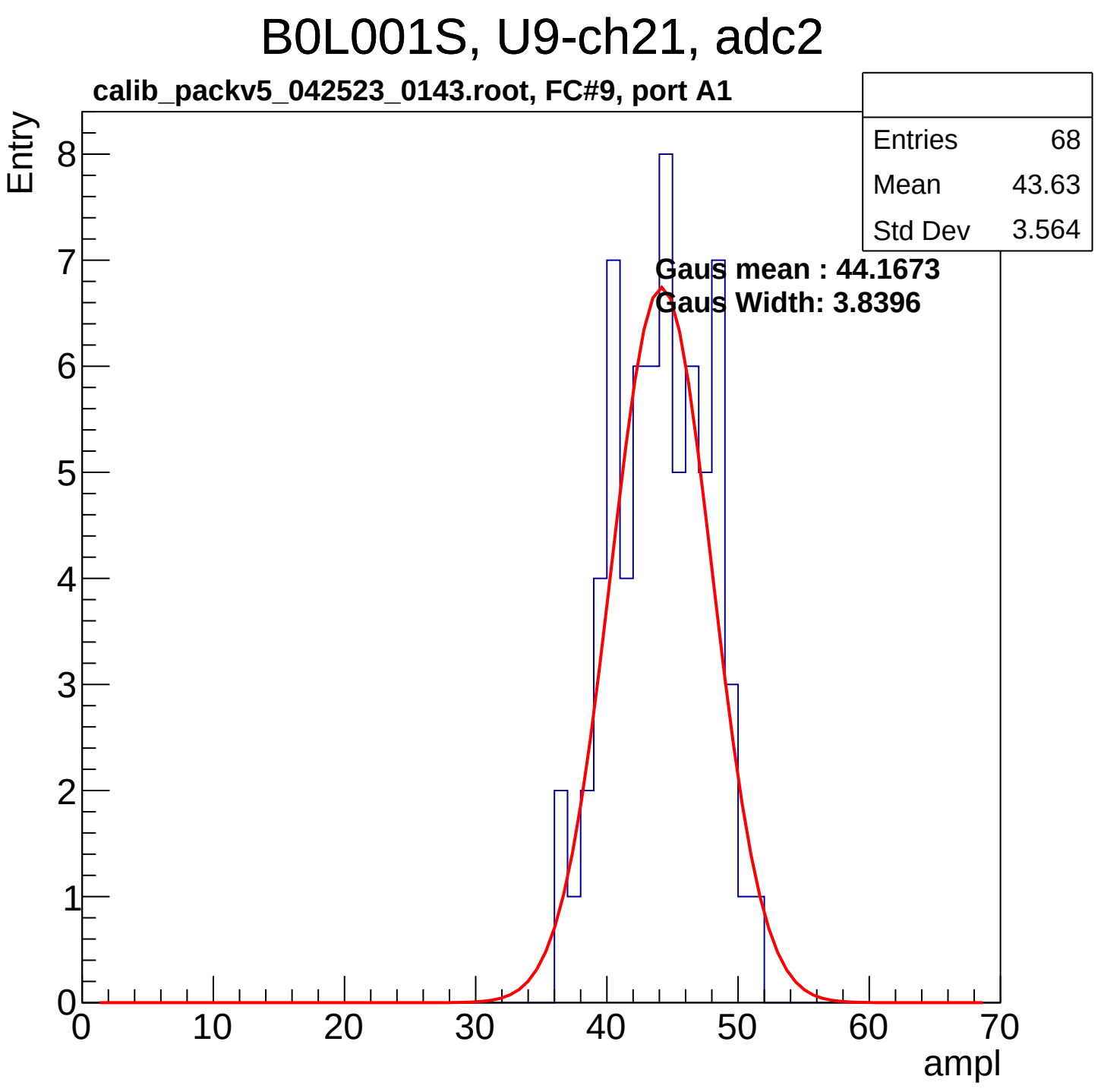
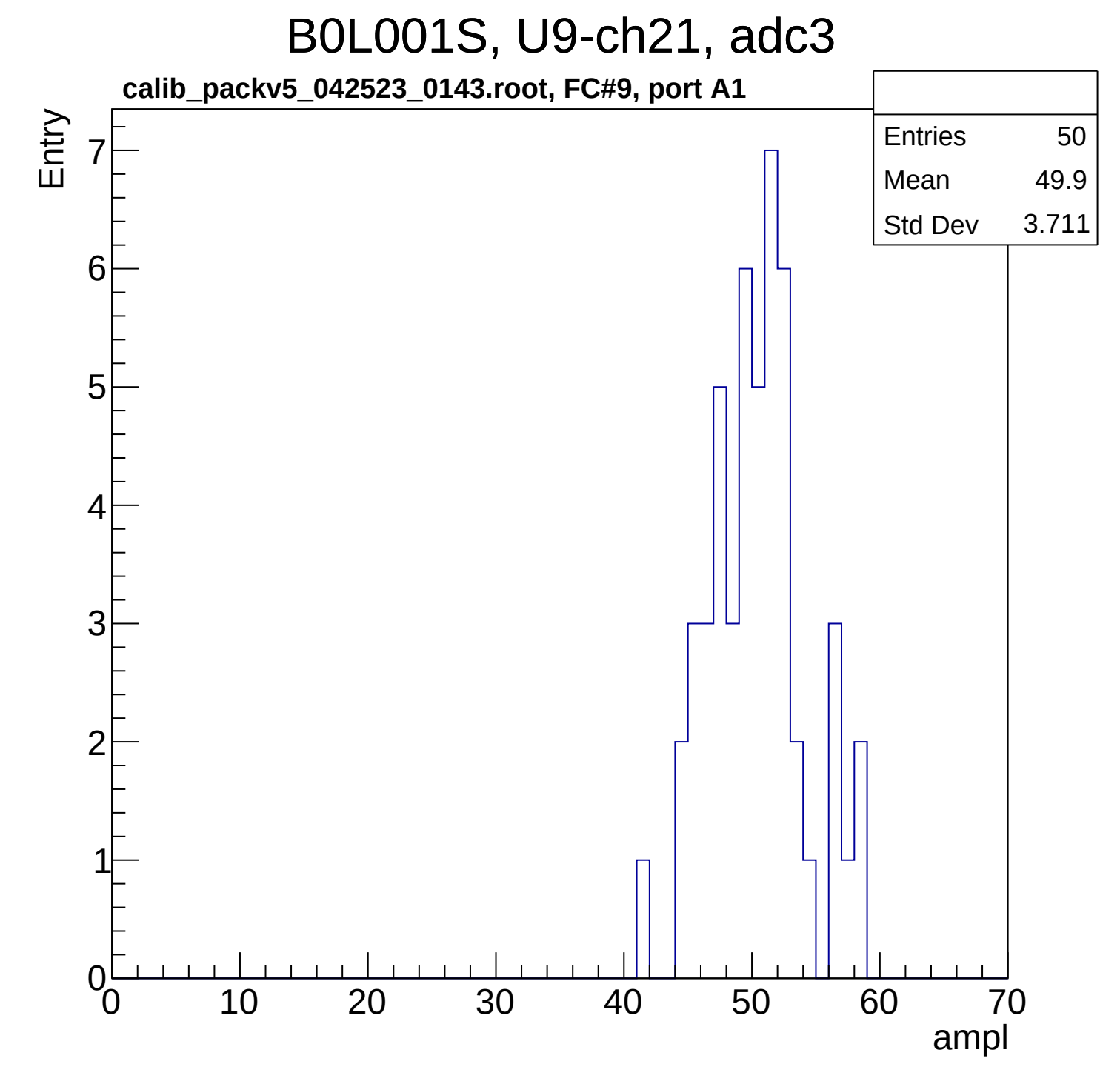
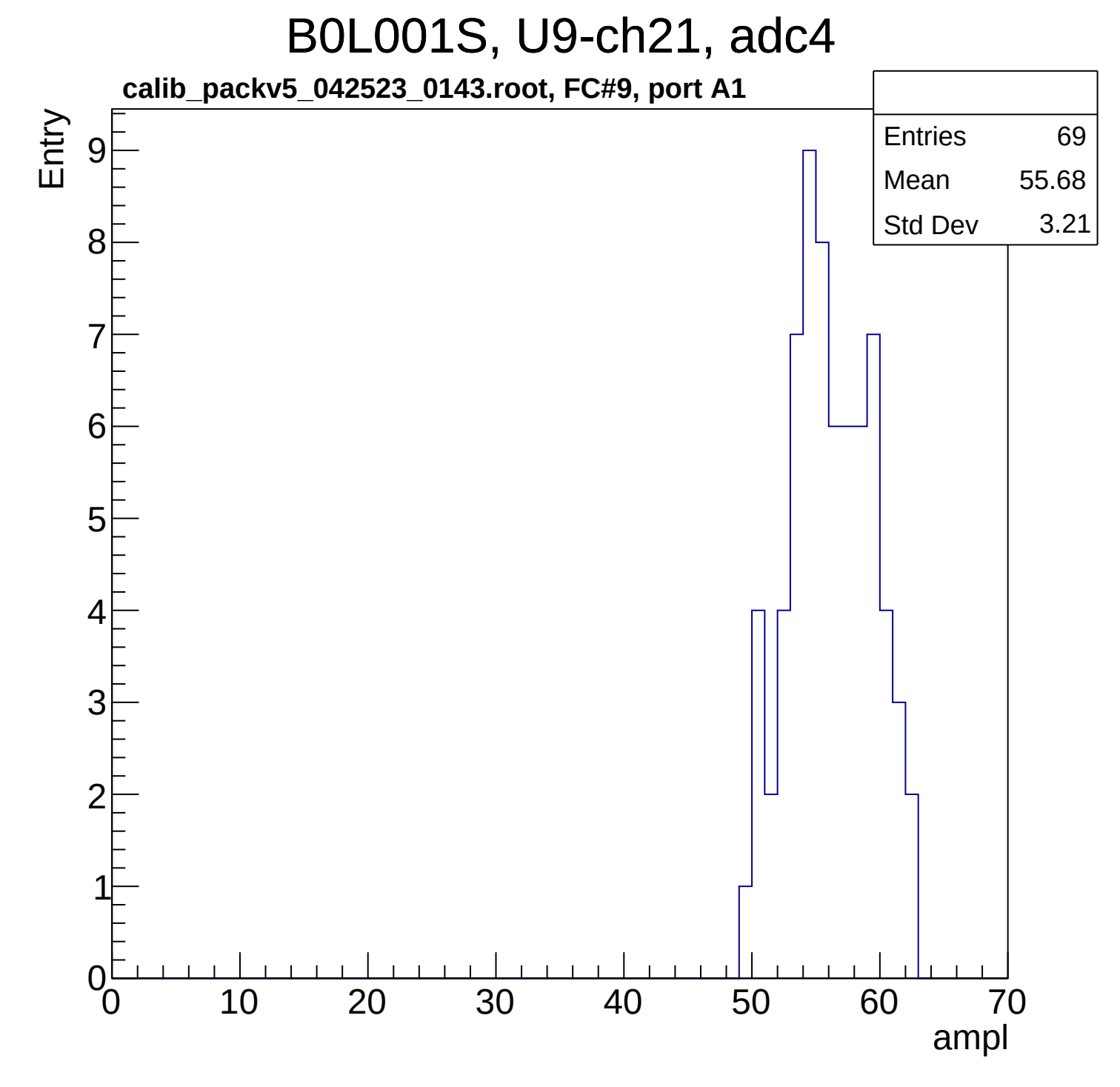

B0L001S, U9-ch21, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 38.11
Std Dev 3.614
Gaus mean : 39.1432
Gaus Width: 4.3570
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch21, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.63
Std Dev 3.564
Gaus mean : 44.1673
Gaus Width: 3.8396
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch21, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 49.9
Std Dev 3.711
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch21, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 55.68
Std Dev 3.21
Entry
ampl
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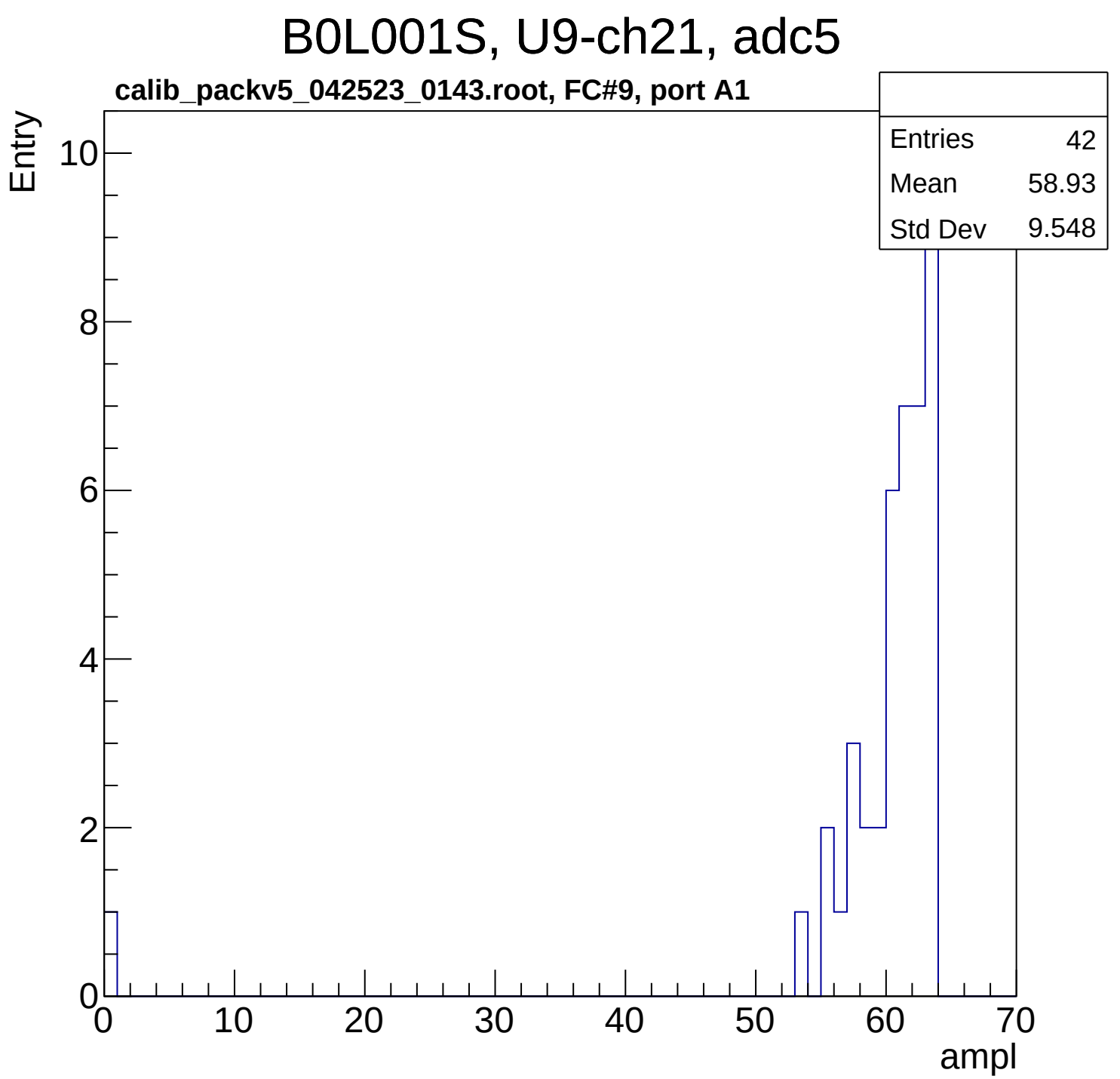
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch21, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.93
Std Dev 9.548
Entry
0
2
4
6
8
10
0
10
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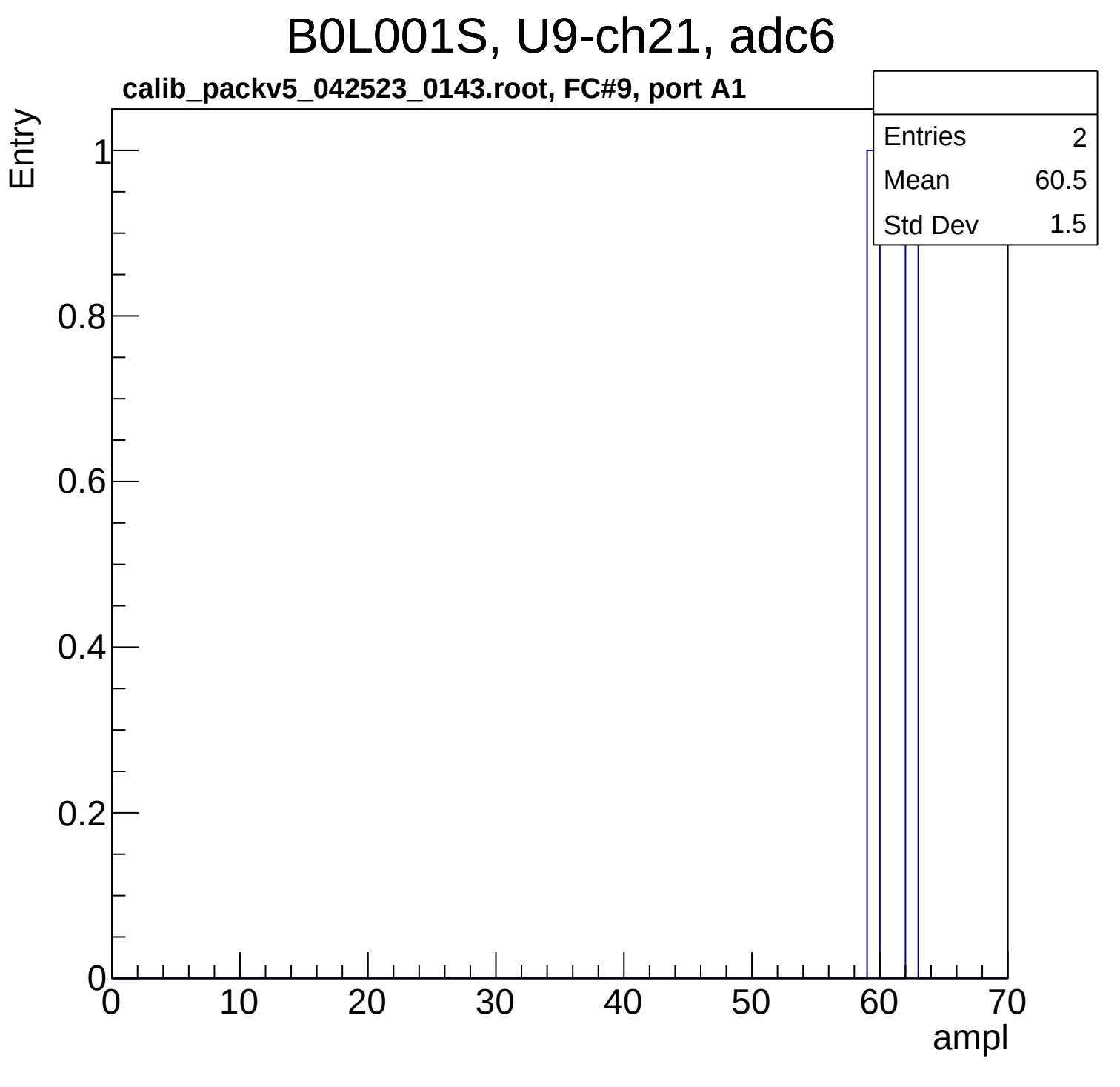
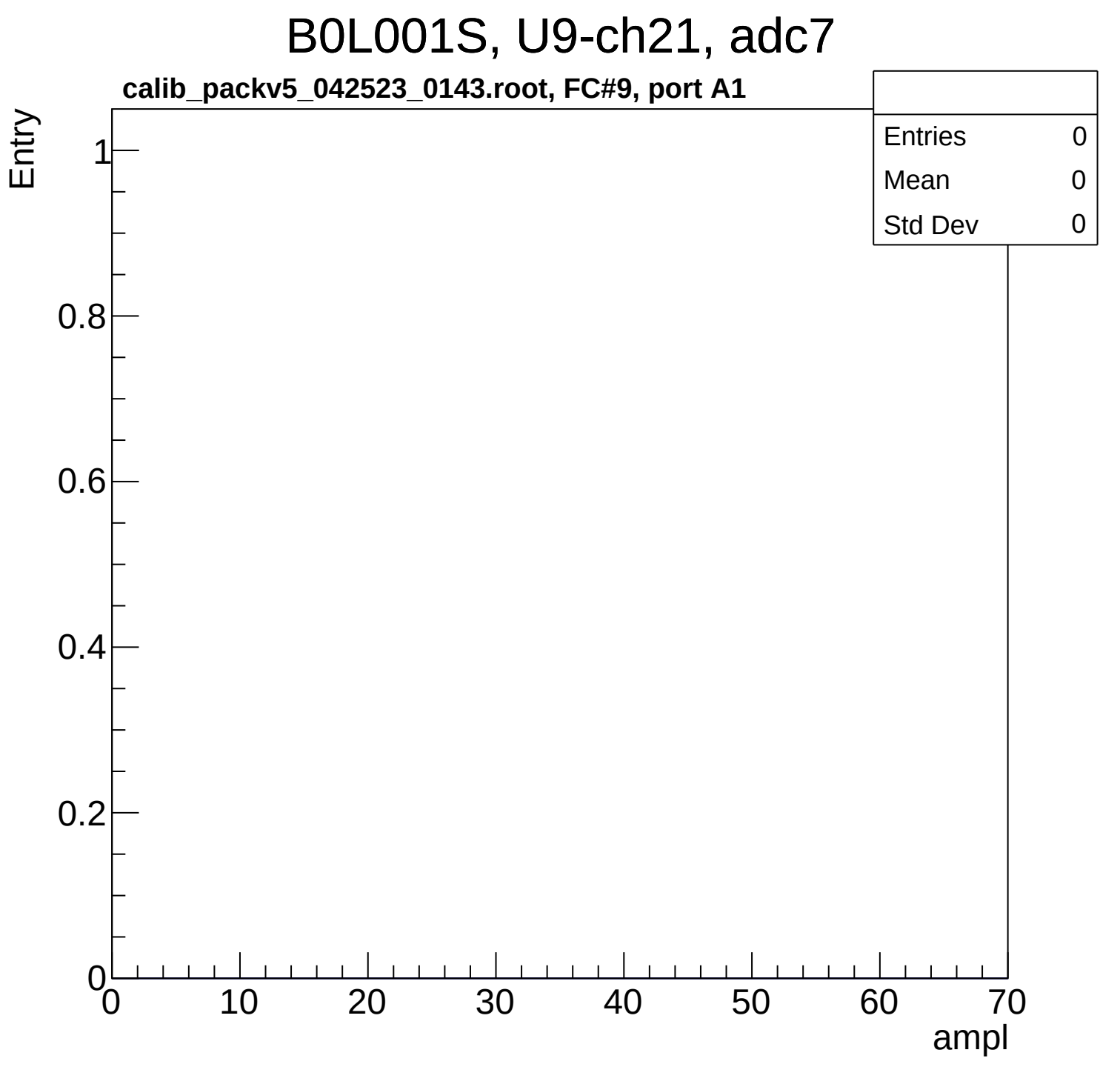
30
40
50
60
70
ampl


B0L001S, U9-ch21, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 60.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch21, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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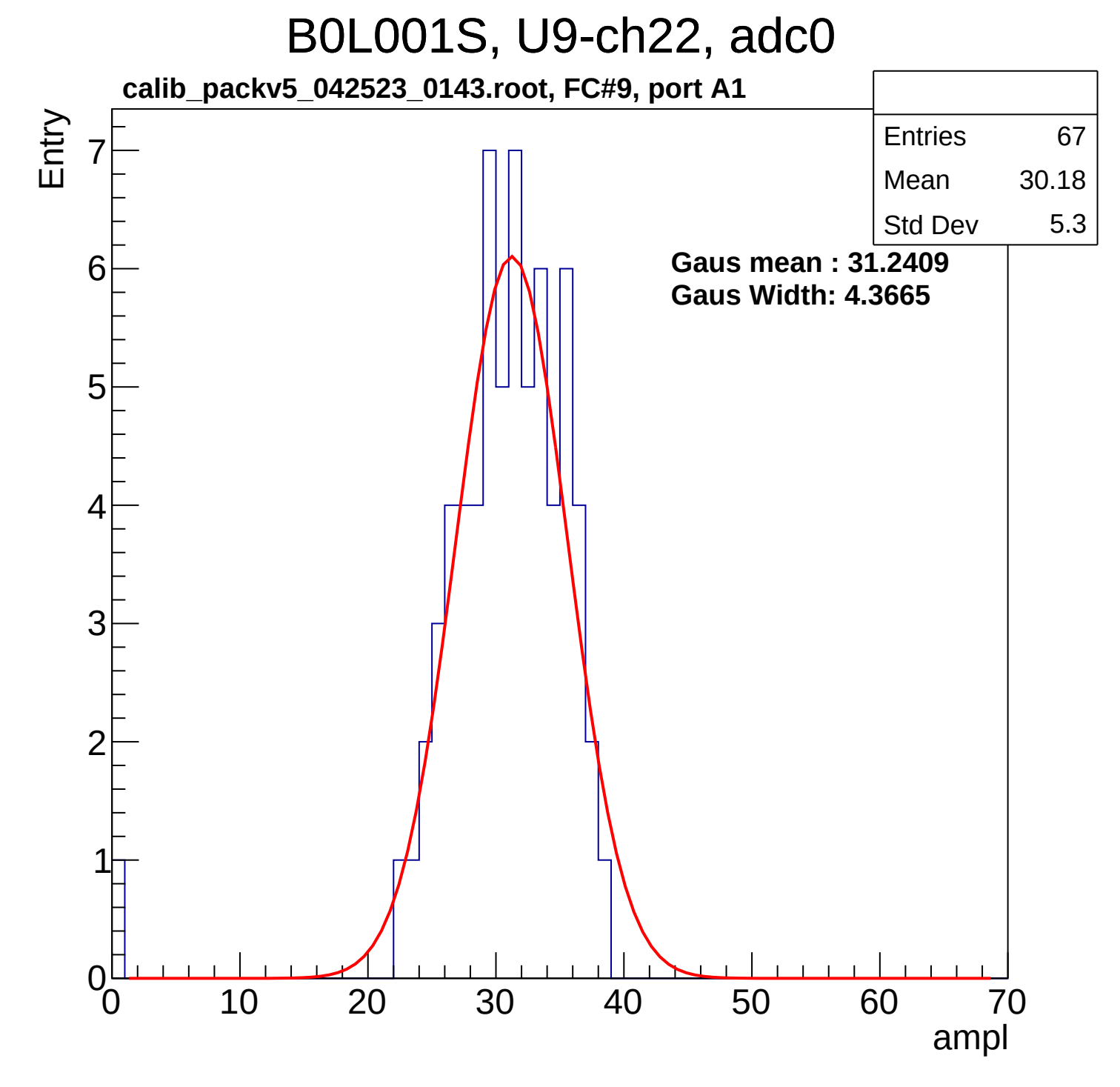
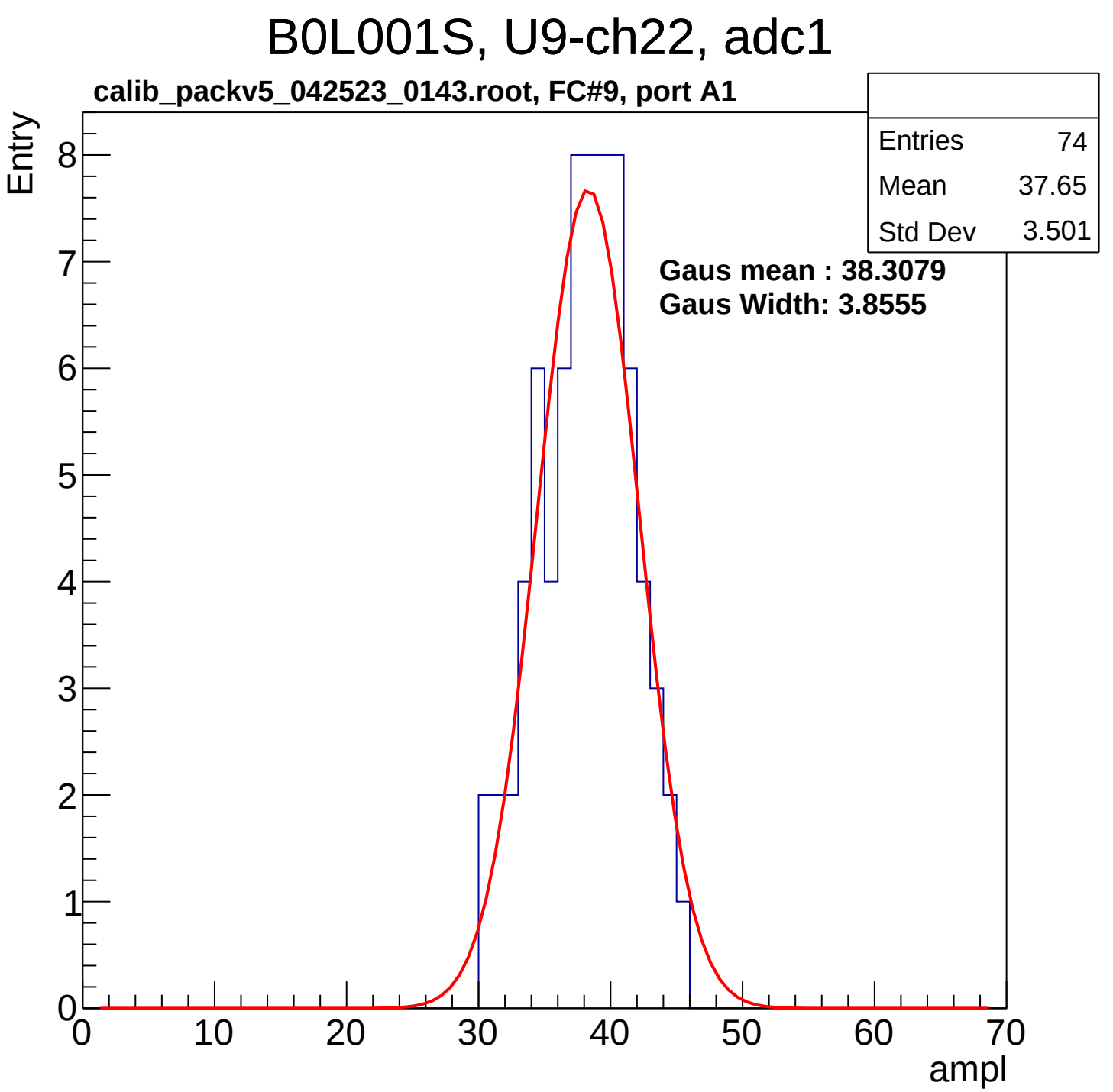
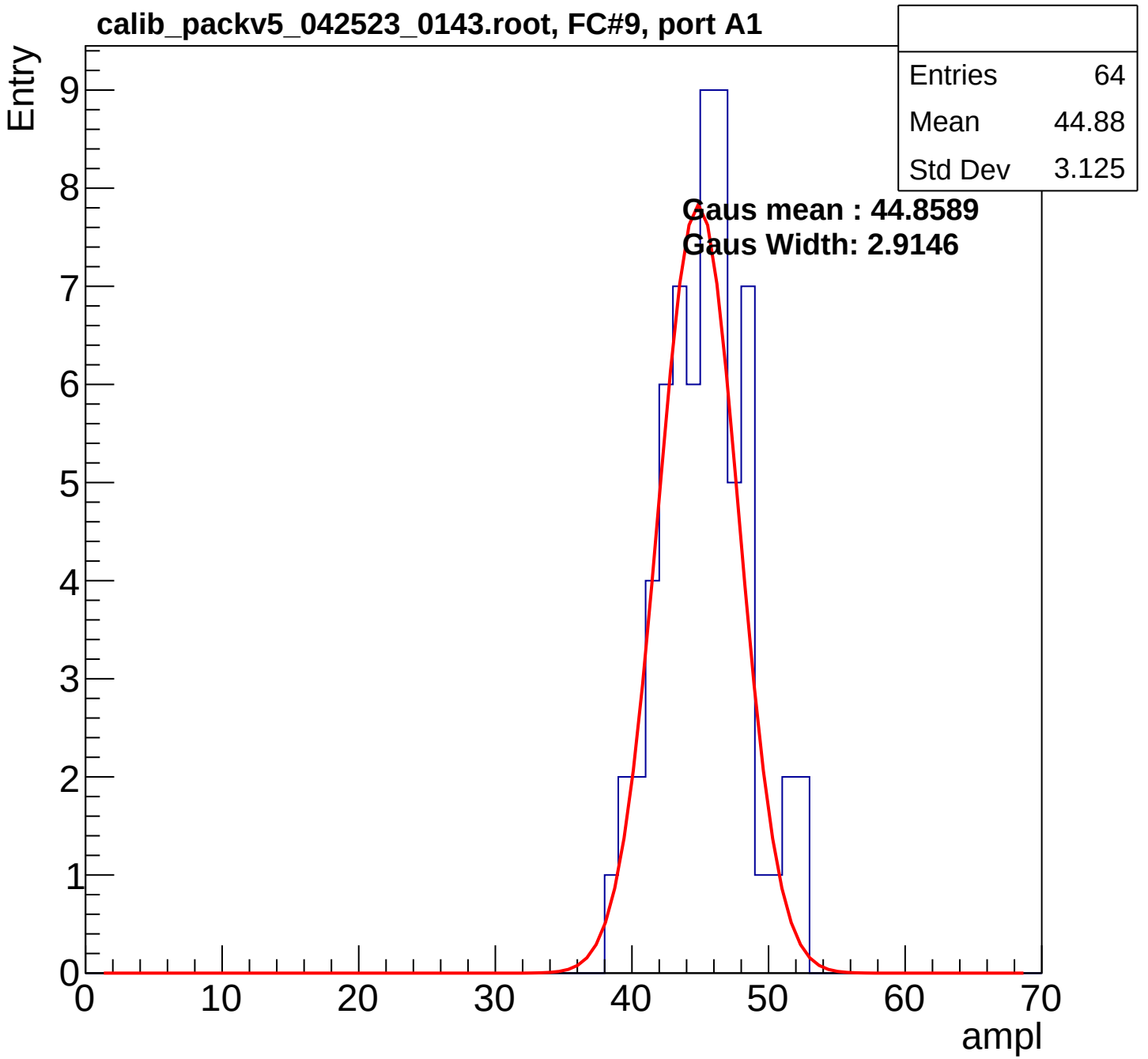
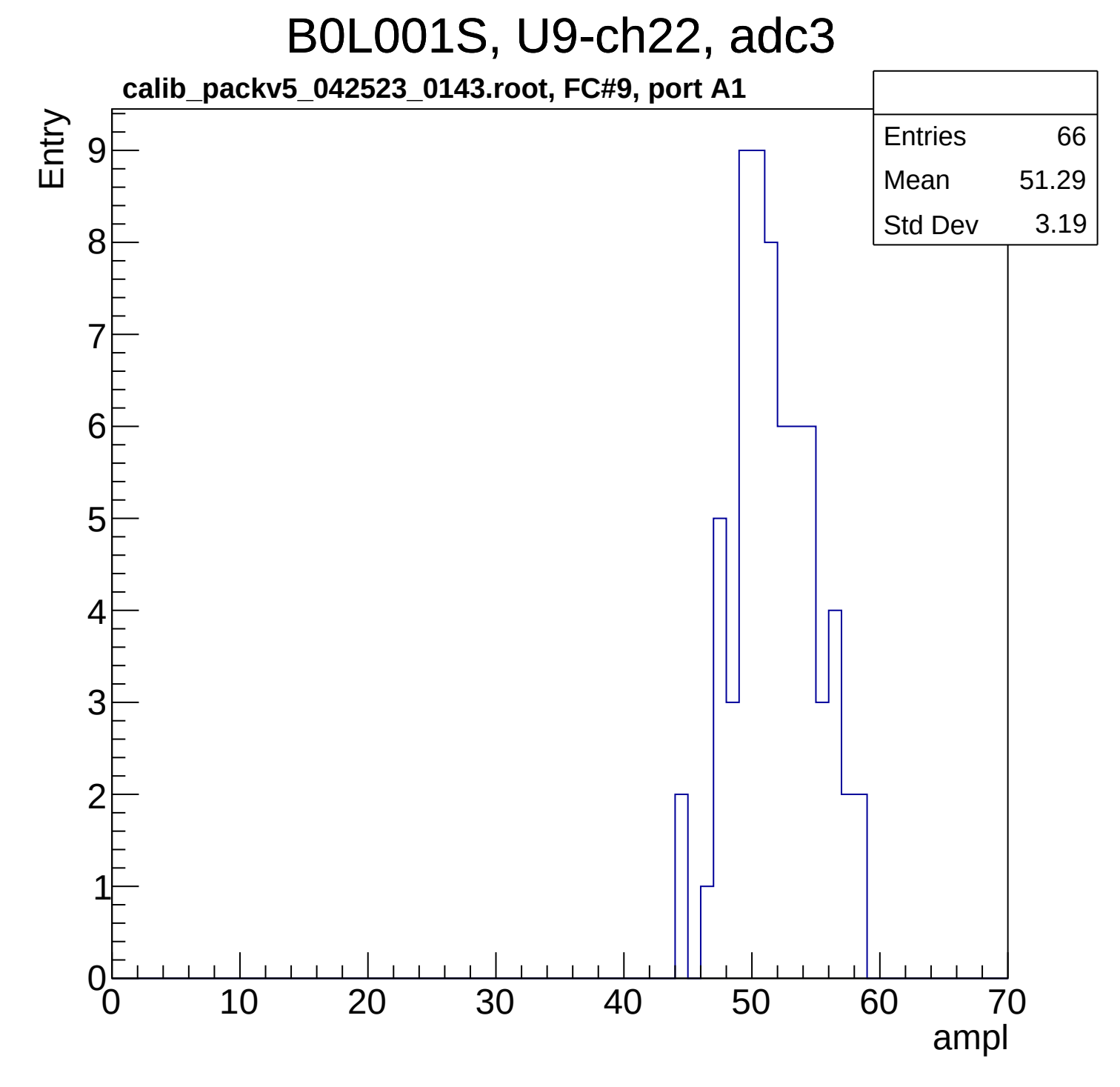
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch22, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.18
Std Dev 5.3
Gaus mean : 31.2409
Gaus Width: 4.3665
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch22, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.65
Std Dev 3.501
Gaus mean : 38.3079
Gaus Width: 3.8555
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch22, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 44.88
Std Dev 3.125
Gaus mean : 44.8589
Gaus Width: 2.9146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch22, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.29
Std Dev 3.19
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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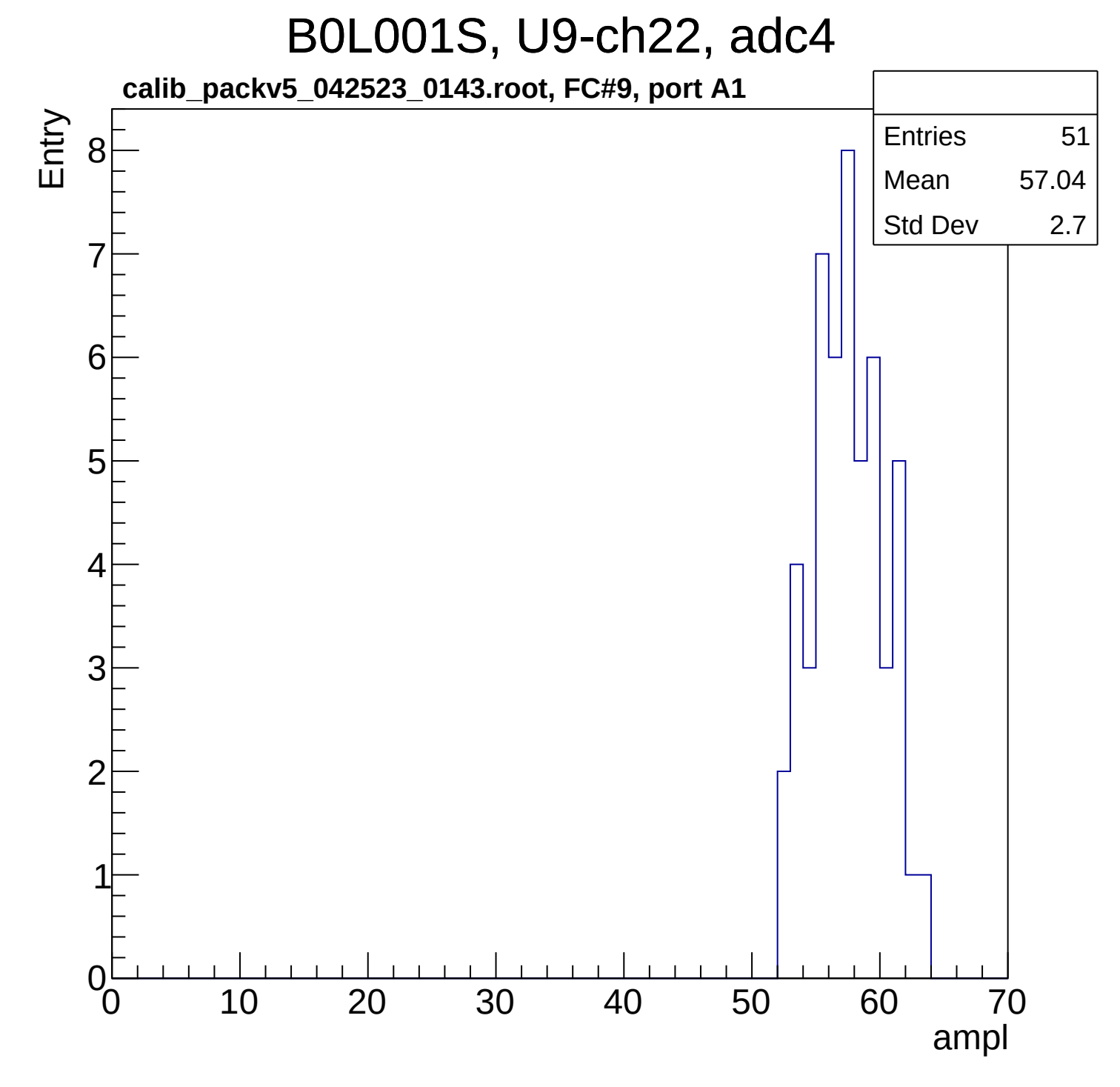
40
50
60
70


B0L001S, U9-ch22, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 57.04
Std Dev 2.7
Entry
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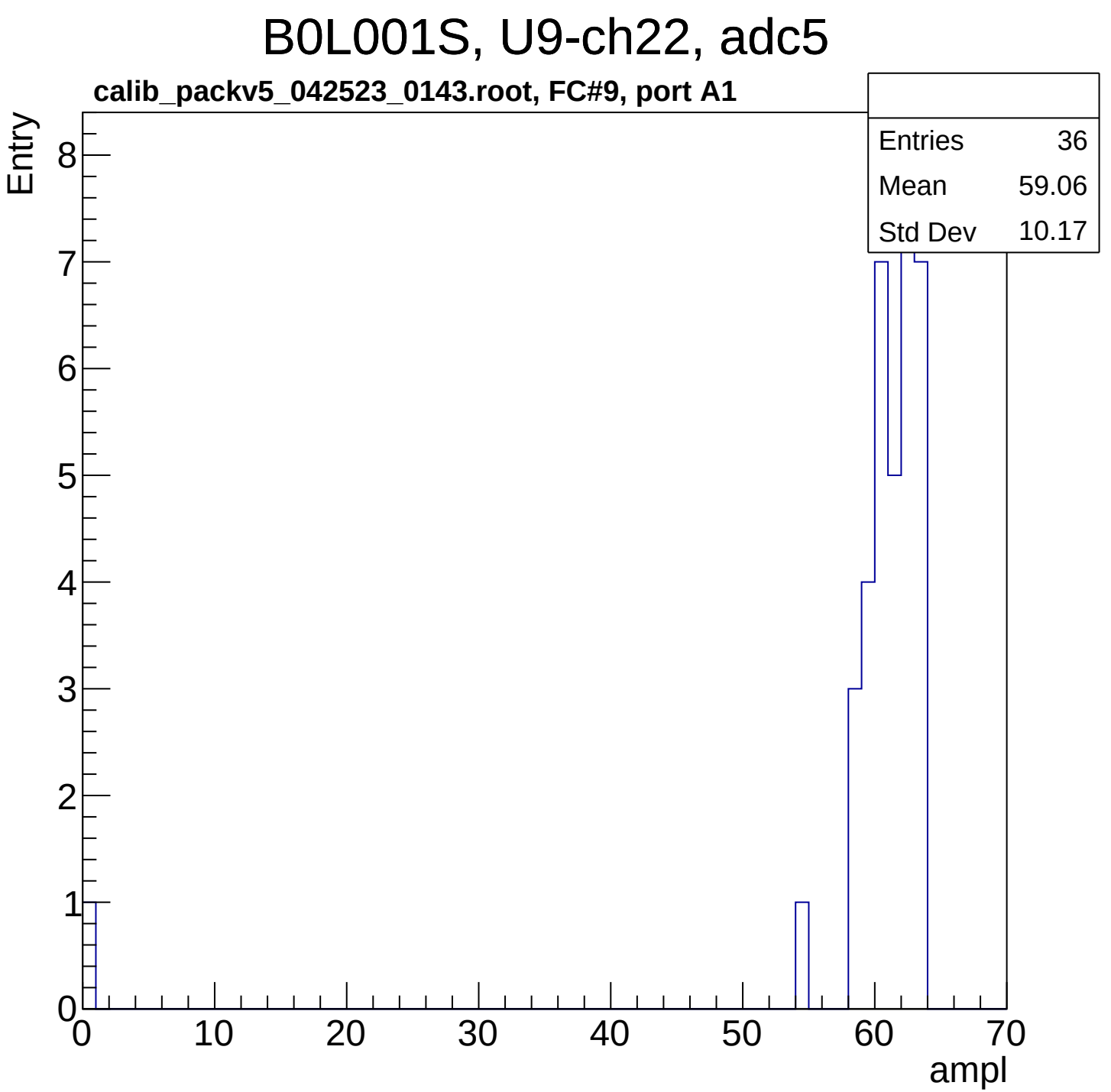
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch22, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.06
Std Dev 10.17
Entry
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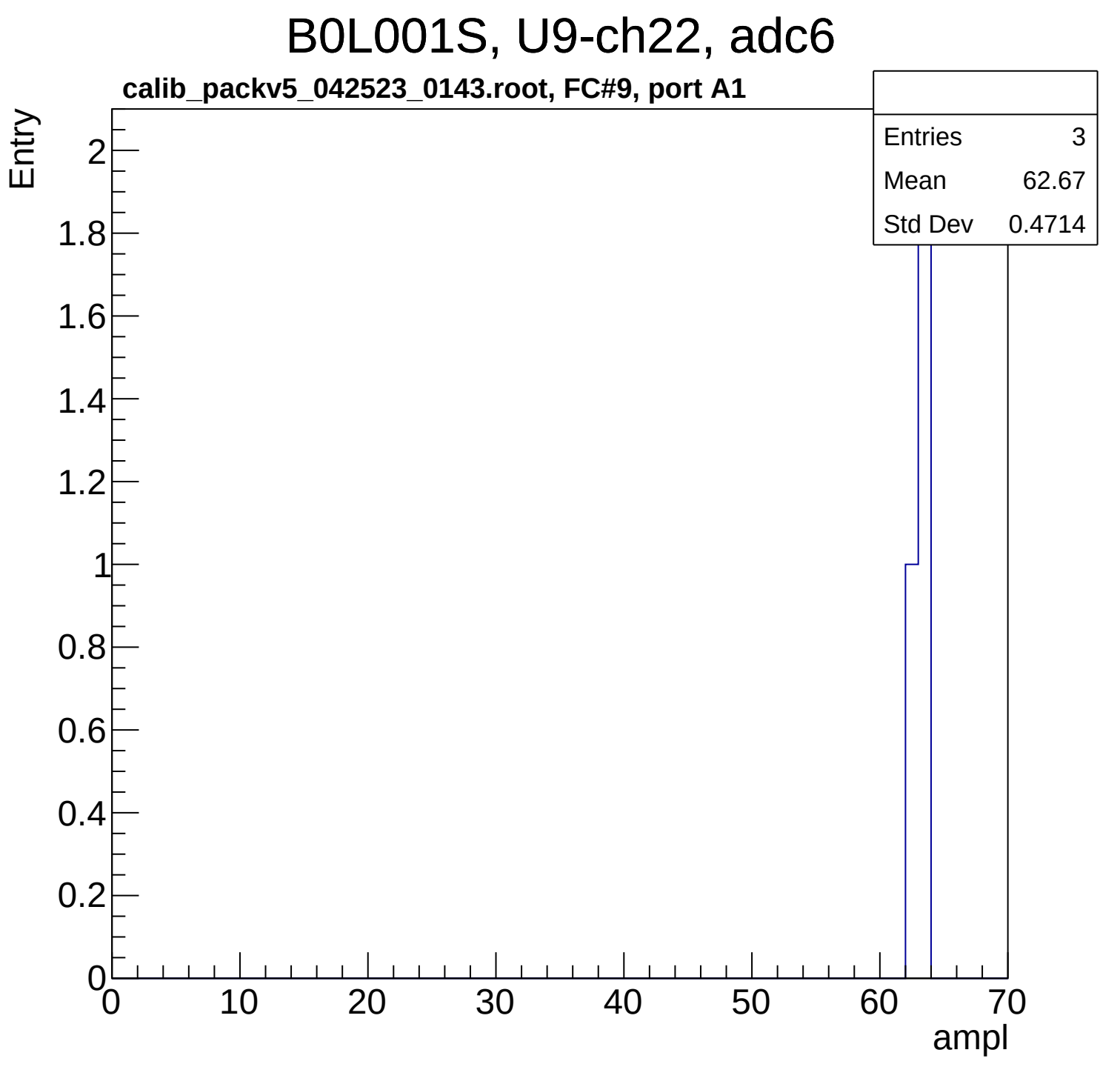
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch22, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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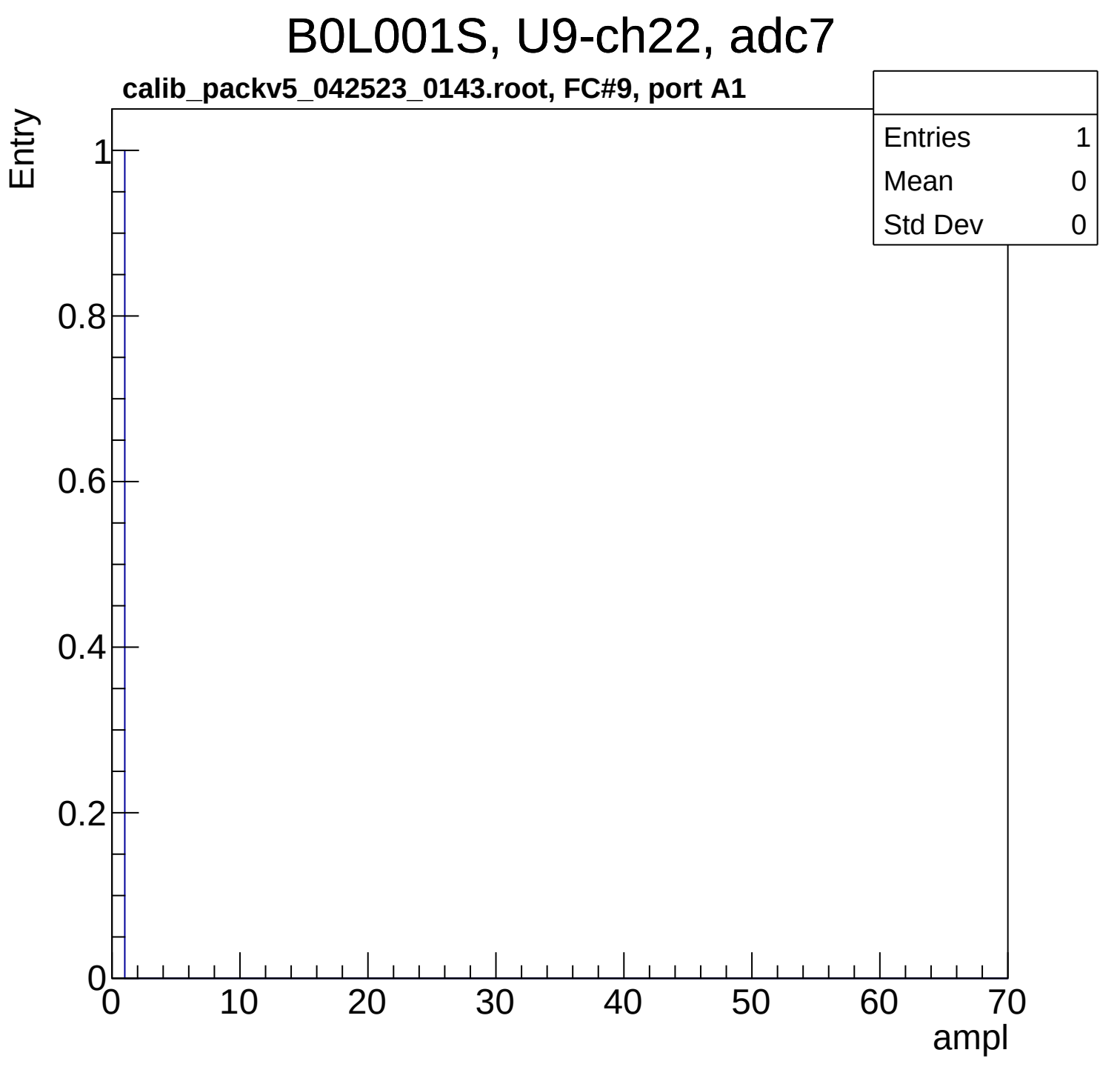
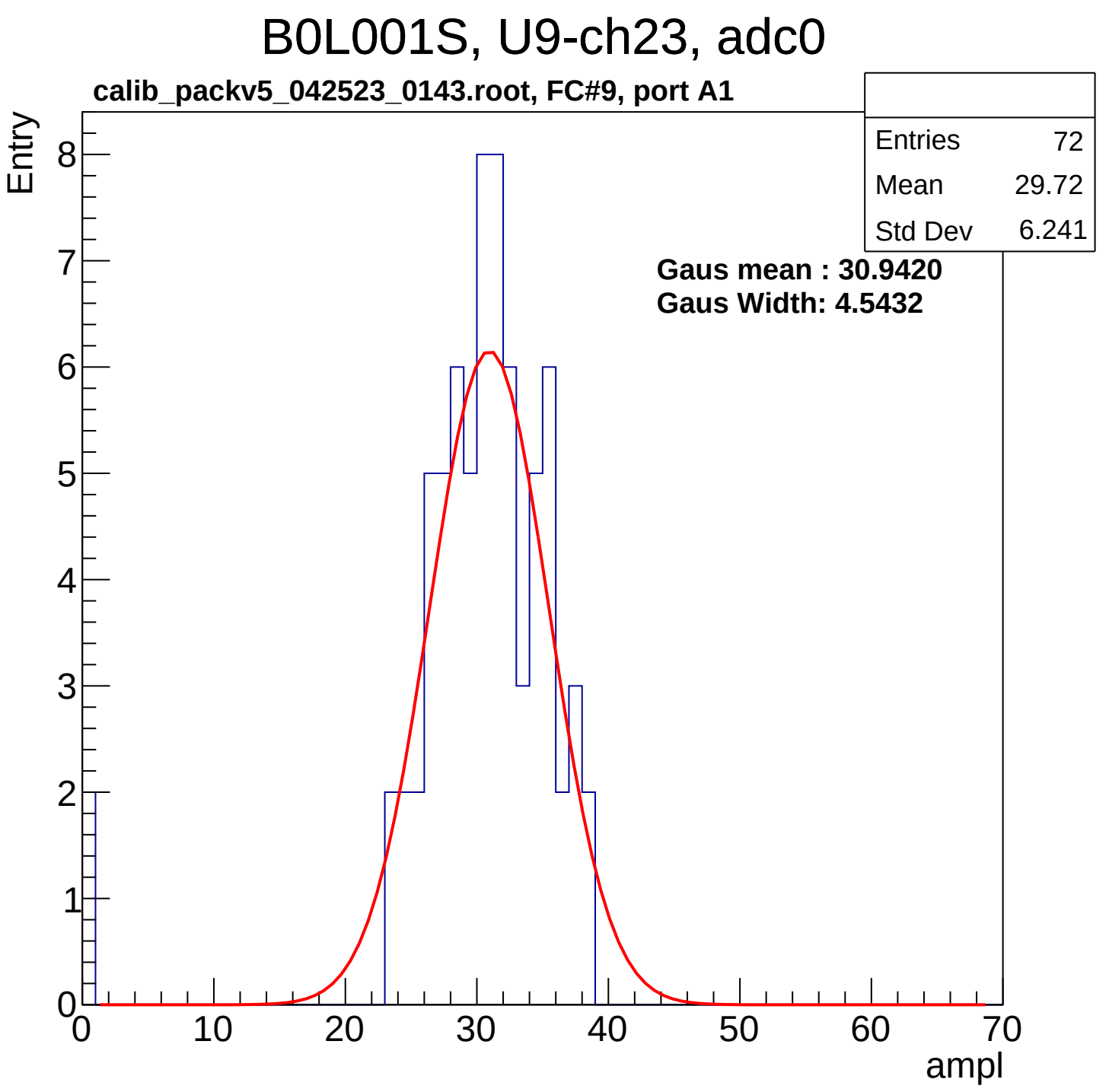
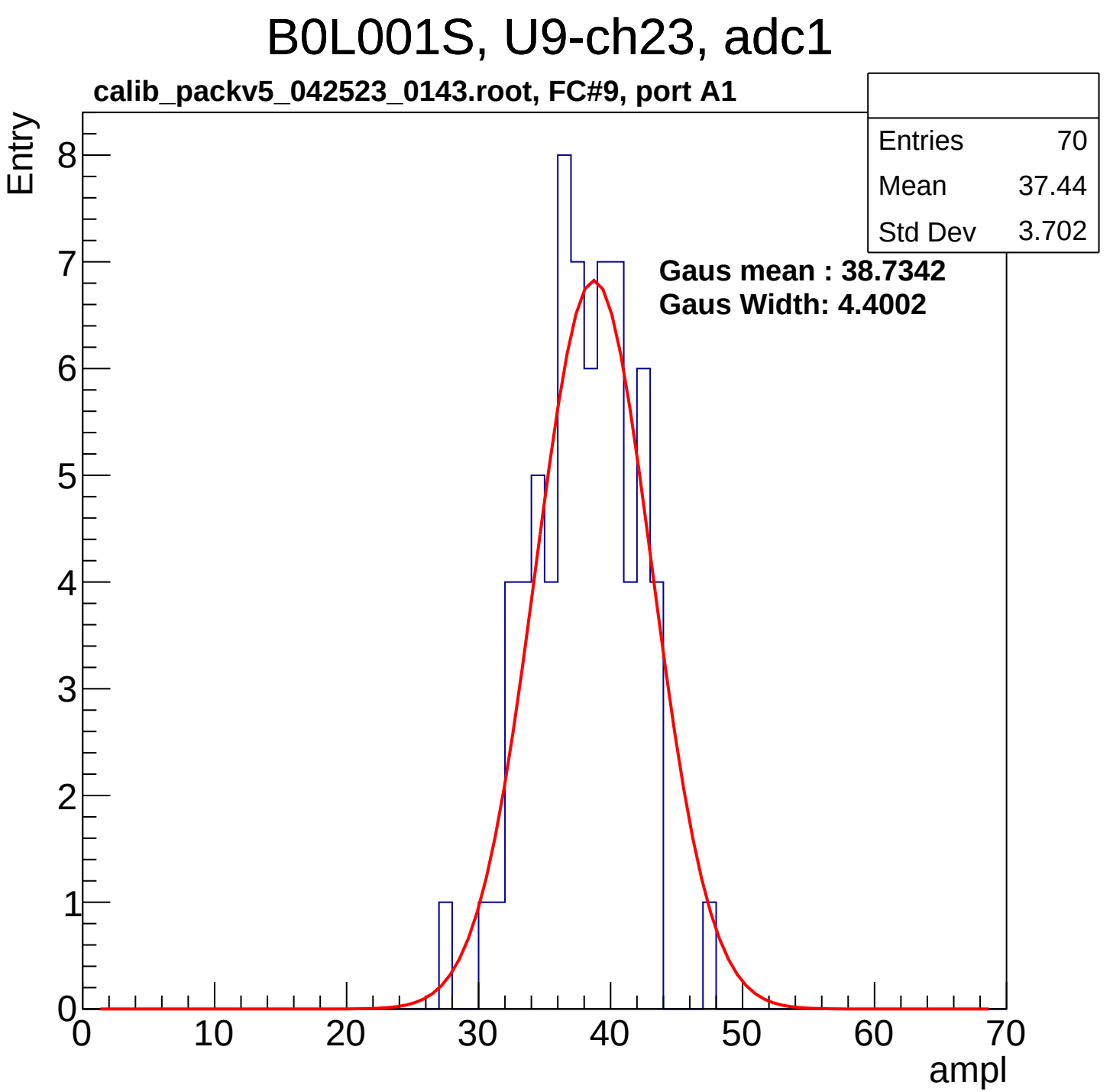

B0L001S, U9-ch22, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch23, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.72
Std Dev 6.241
Gaus mean : 30.9420
Gaus Width: 4.5432
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch23, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.44
Std Dev 3.702
Gaus mean : 38.7342
Gaus Width: 4.4002
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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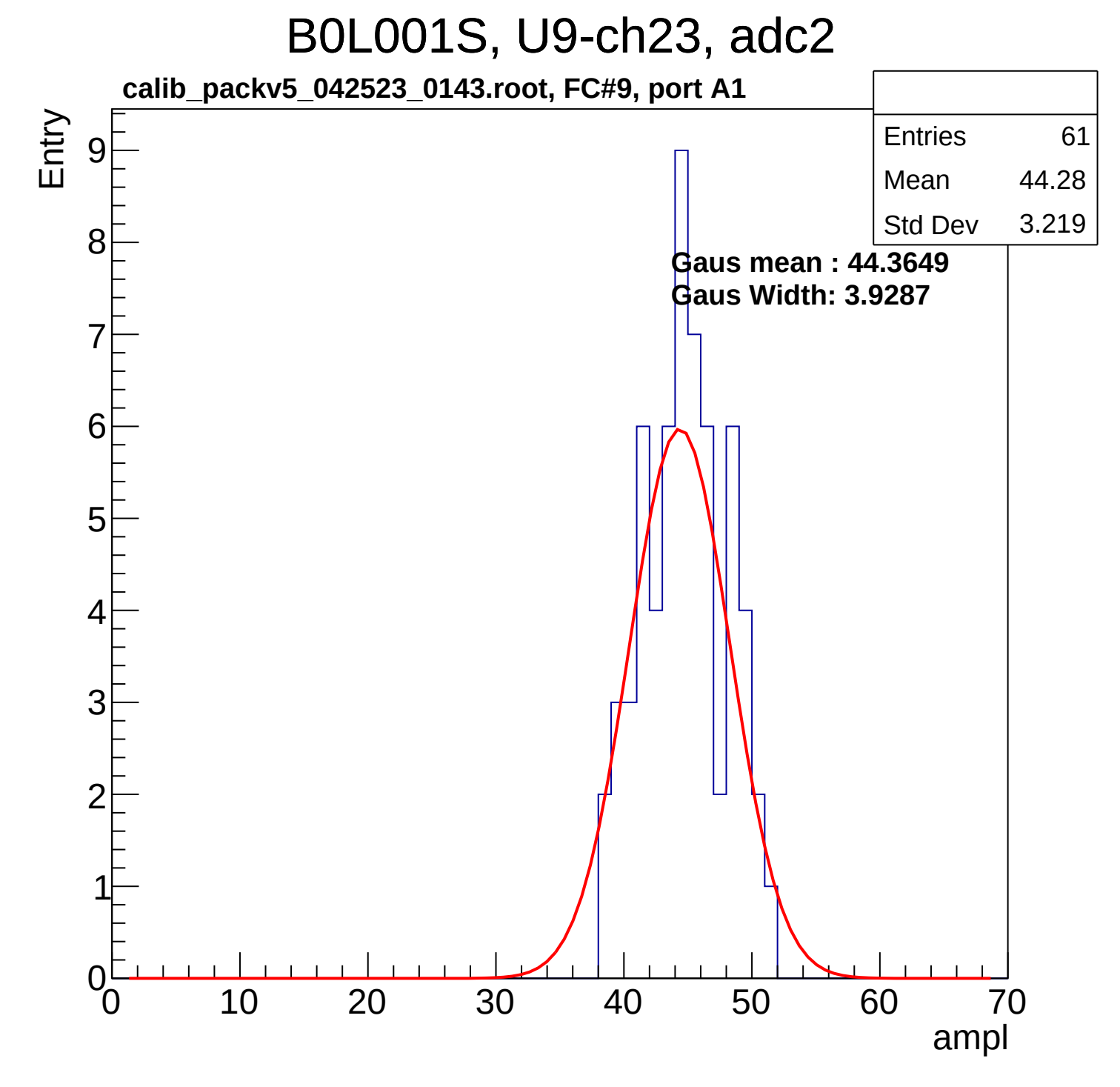
B0L001S, U9-ch23, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.28
Std Dev 3.219
Gaus mean : 44.3649
Gaus Width: 3.9287
Entry
ampl
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3
4
5
6
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8
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0
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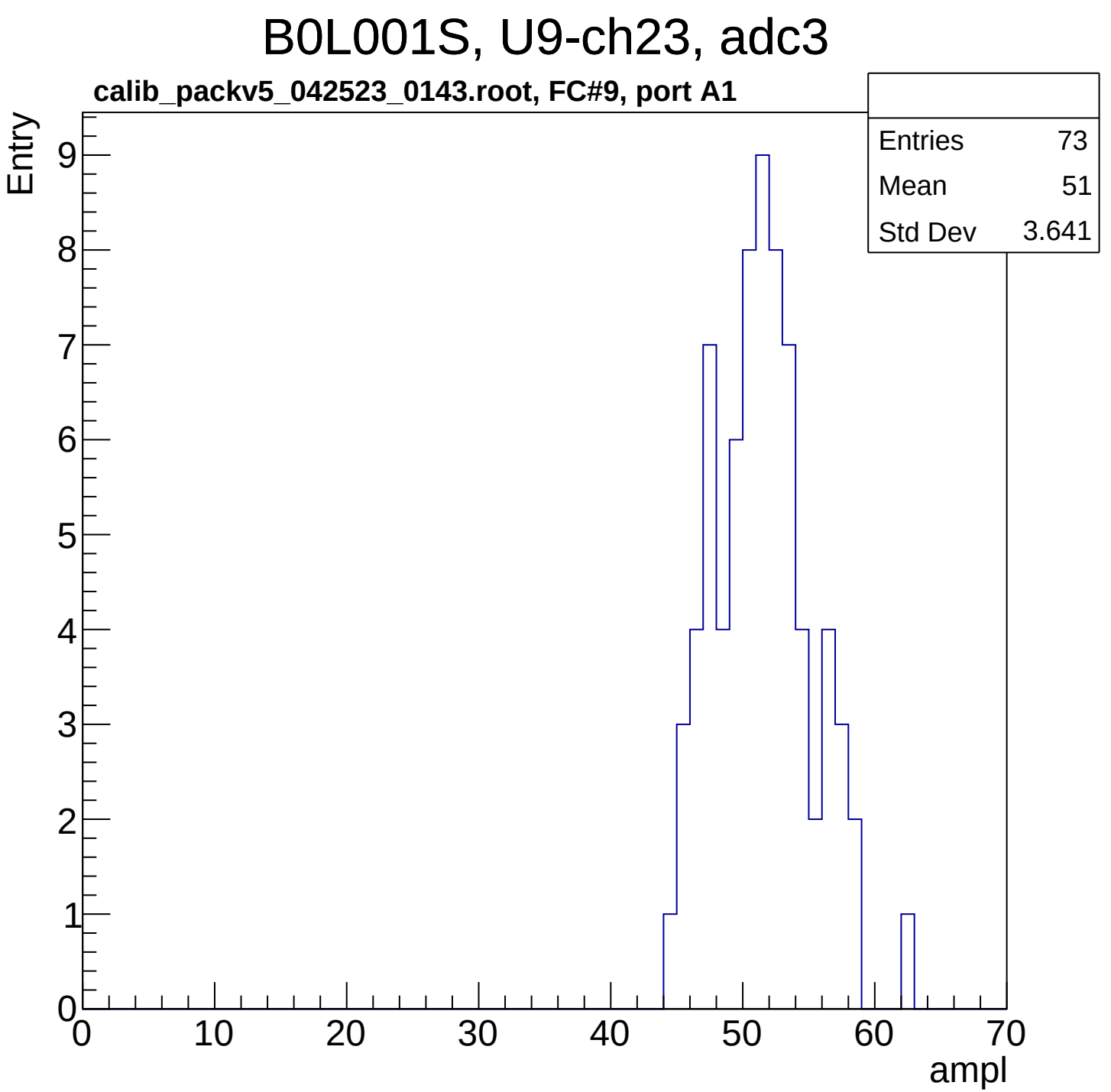
B0L001S, U9-ch23, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 51
Std Dev 3.641
Entry
ampl
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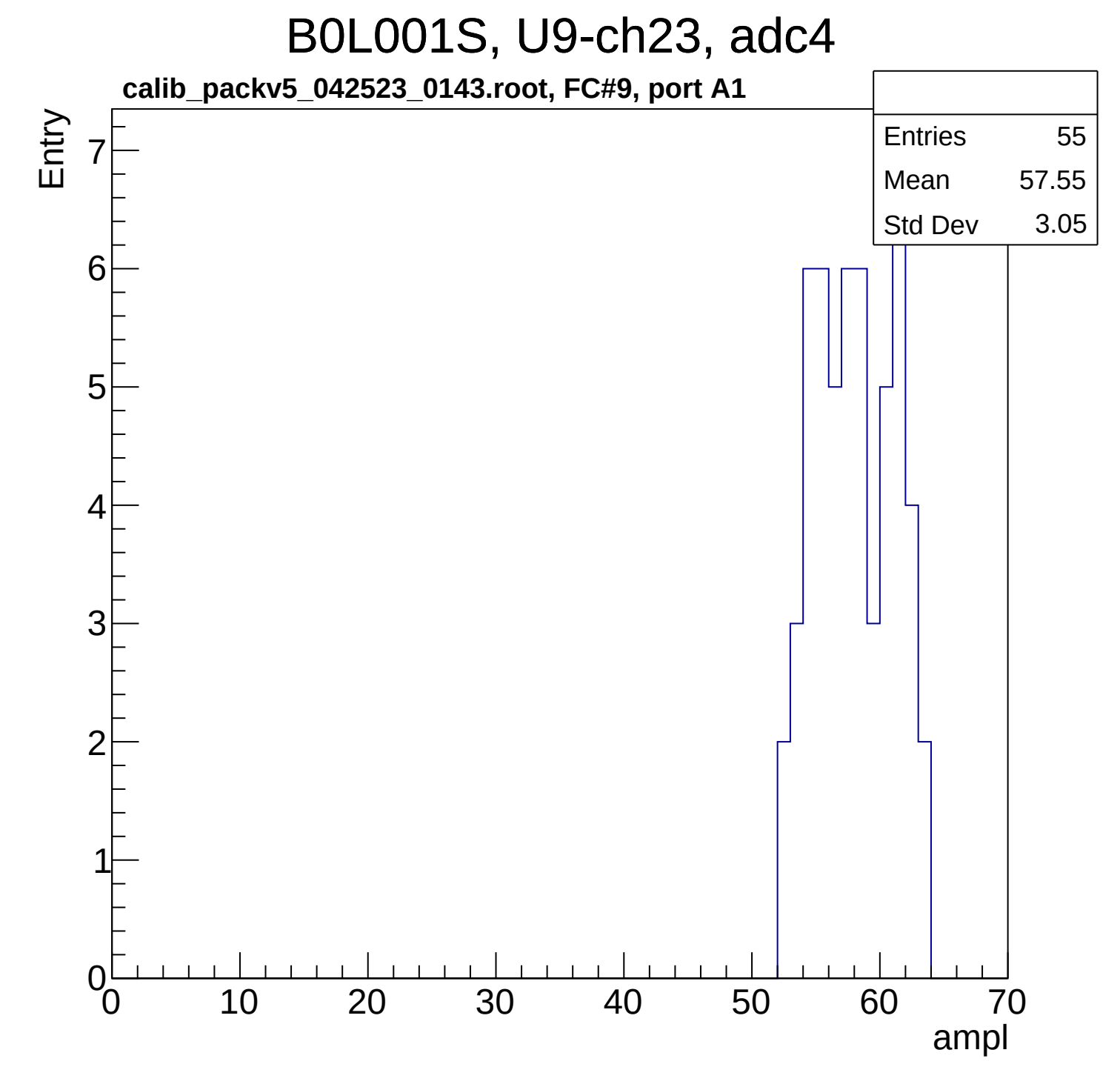
B0L001S, U9-ch23, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 57.55
Std Dev 3.05
Entry
ampl
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0
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40
50
60
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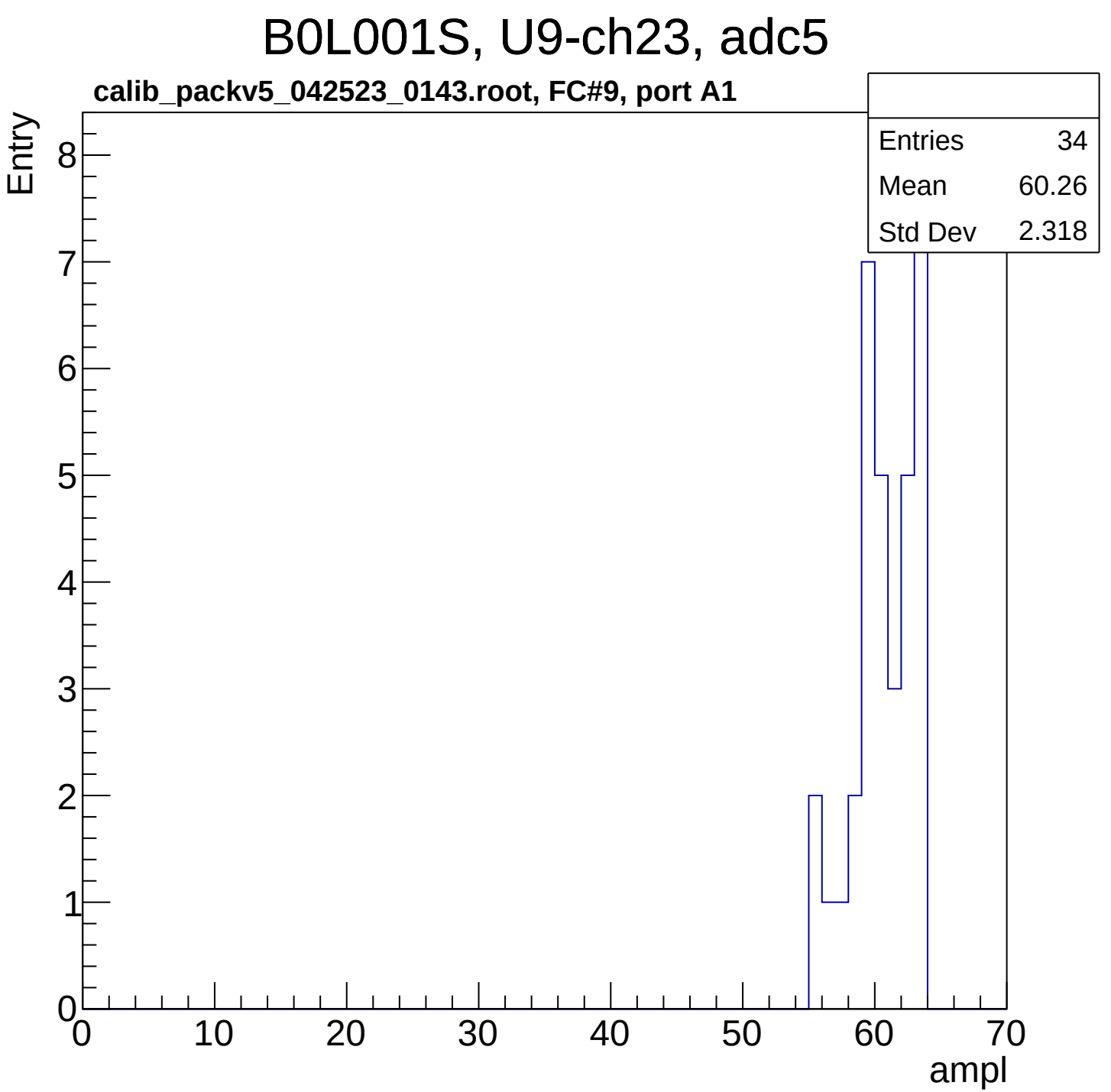
B0L001S, U9-ch23, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 60.26
Std Dev 2.318
Entry
ampl
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3
4
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0
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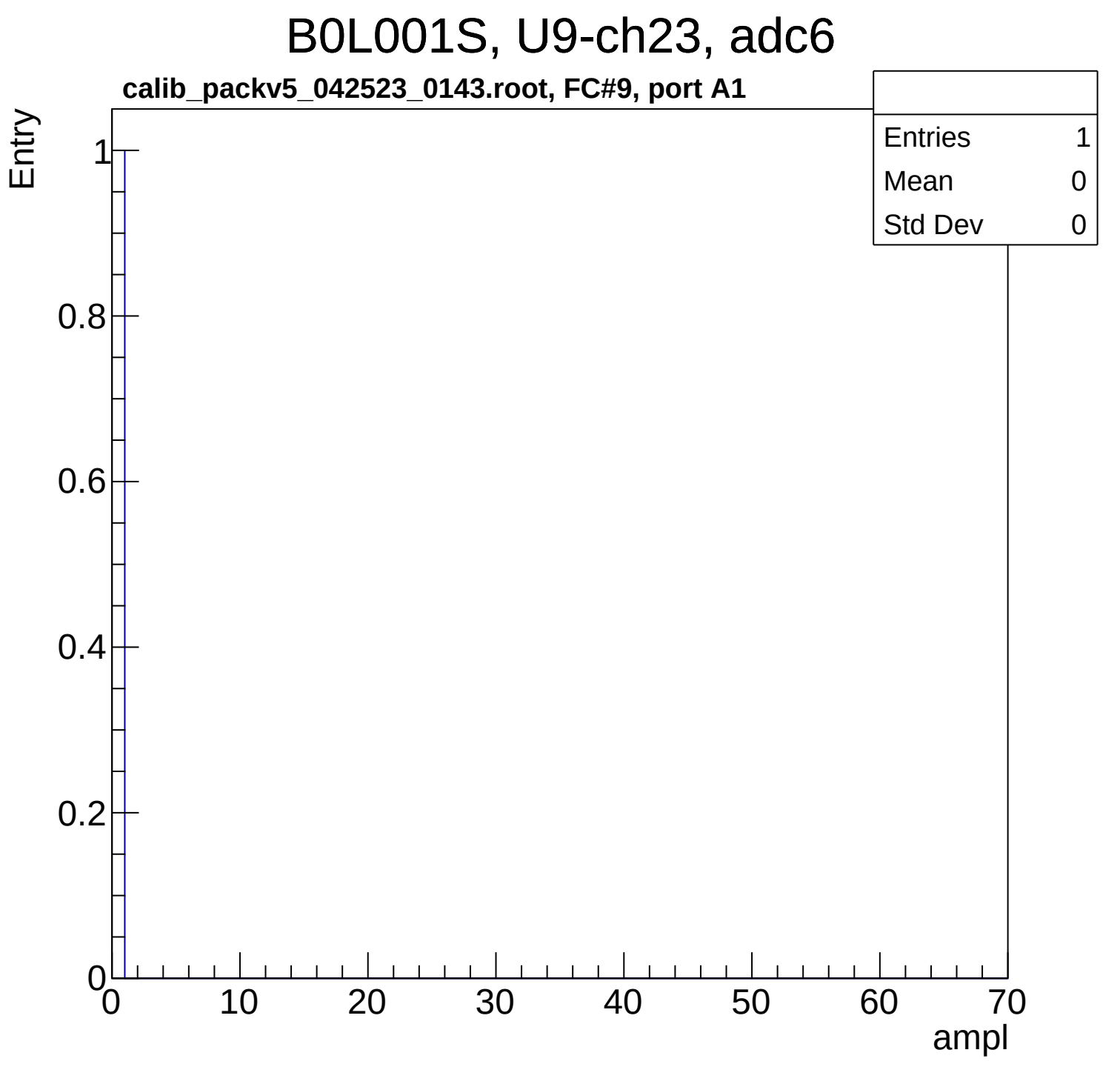
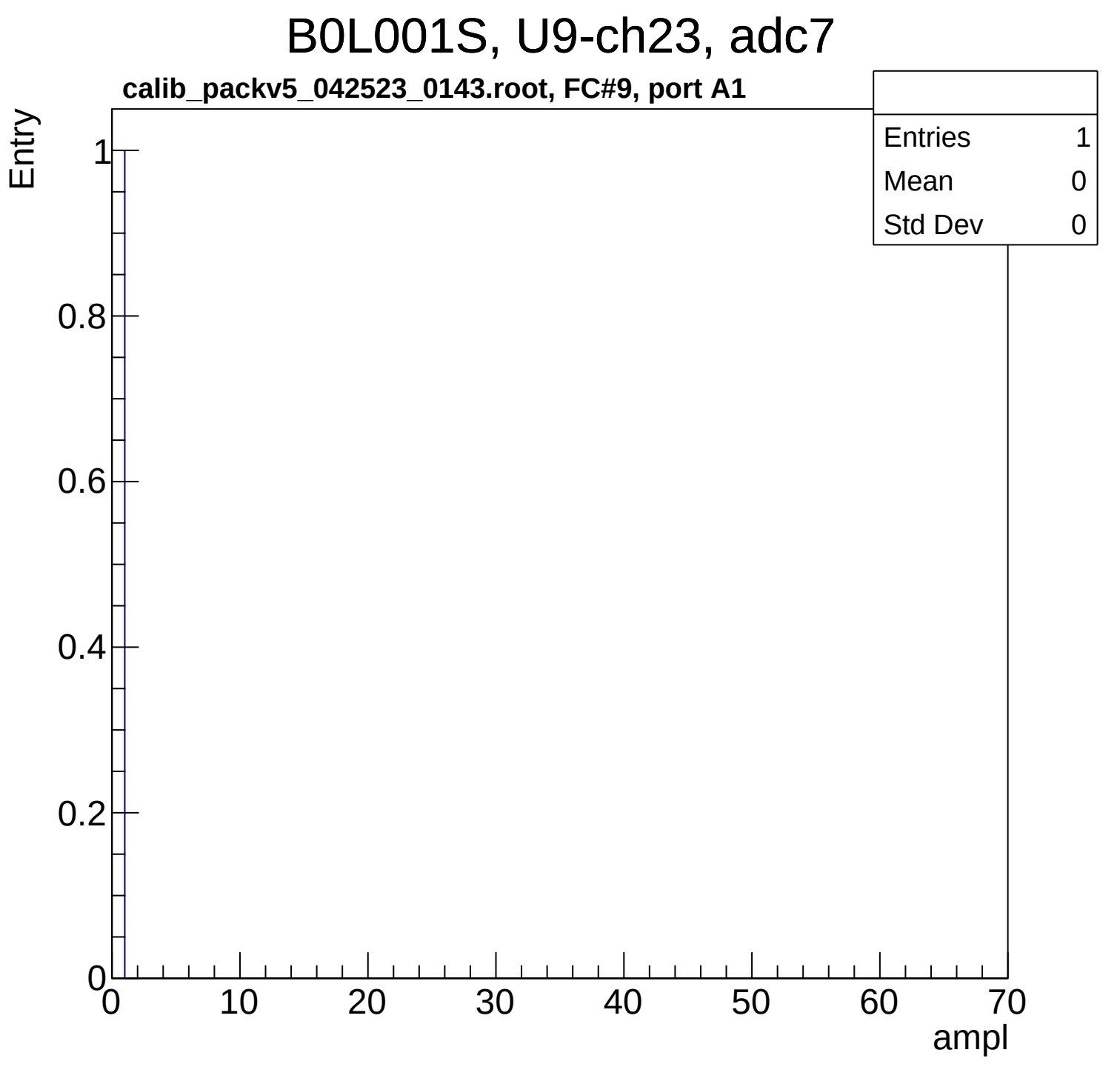
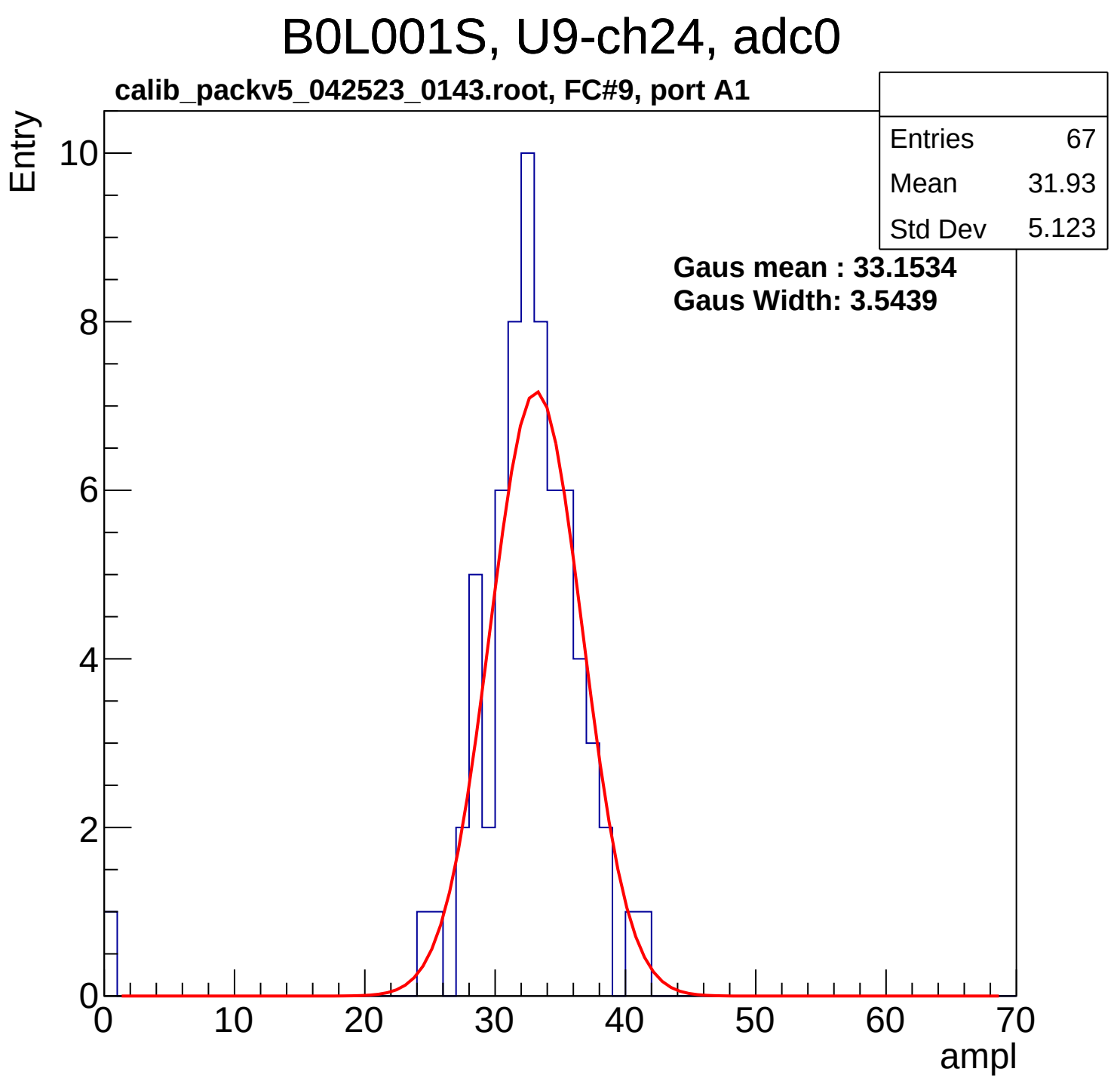

B0L001S, U9-ch23, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch23, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch24, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.93
Std Dev 5.123
Gaus mean : 33.1534
Gaus Width: 3.5439
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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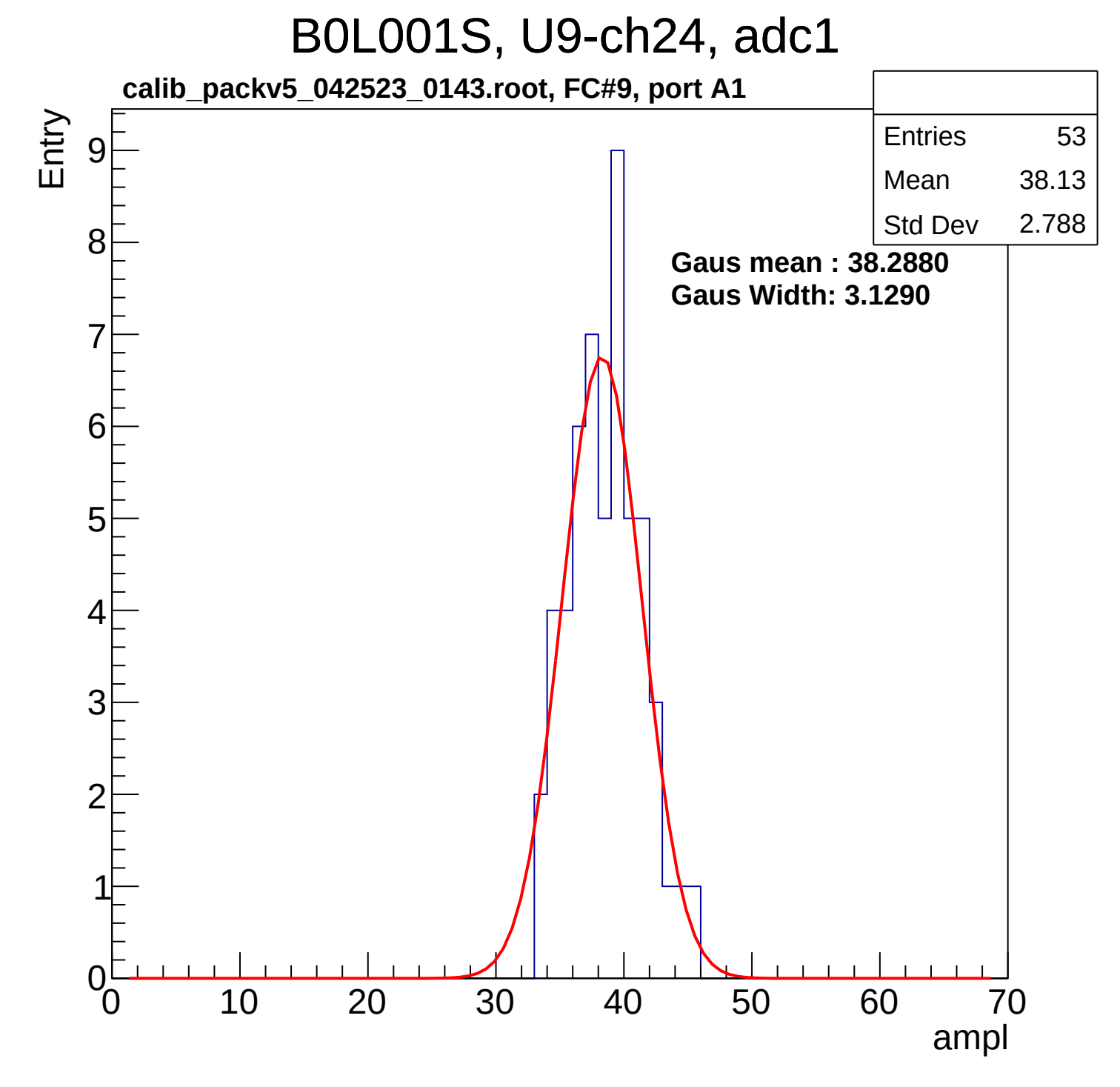
B0L001S, U9-ch24, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 38.13
Std Dev 2.788
Gaus mean : 38.2880
Gaus Width: 3.1290
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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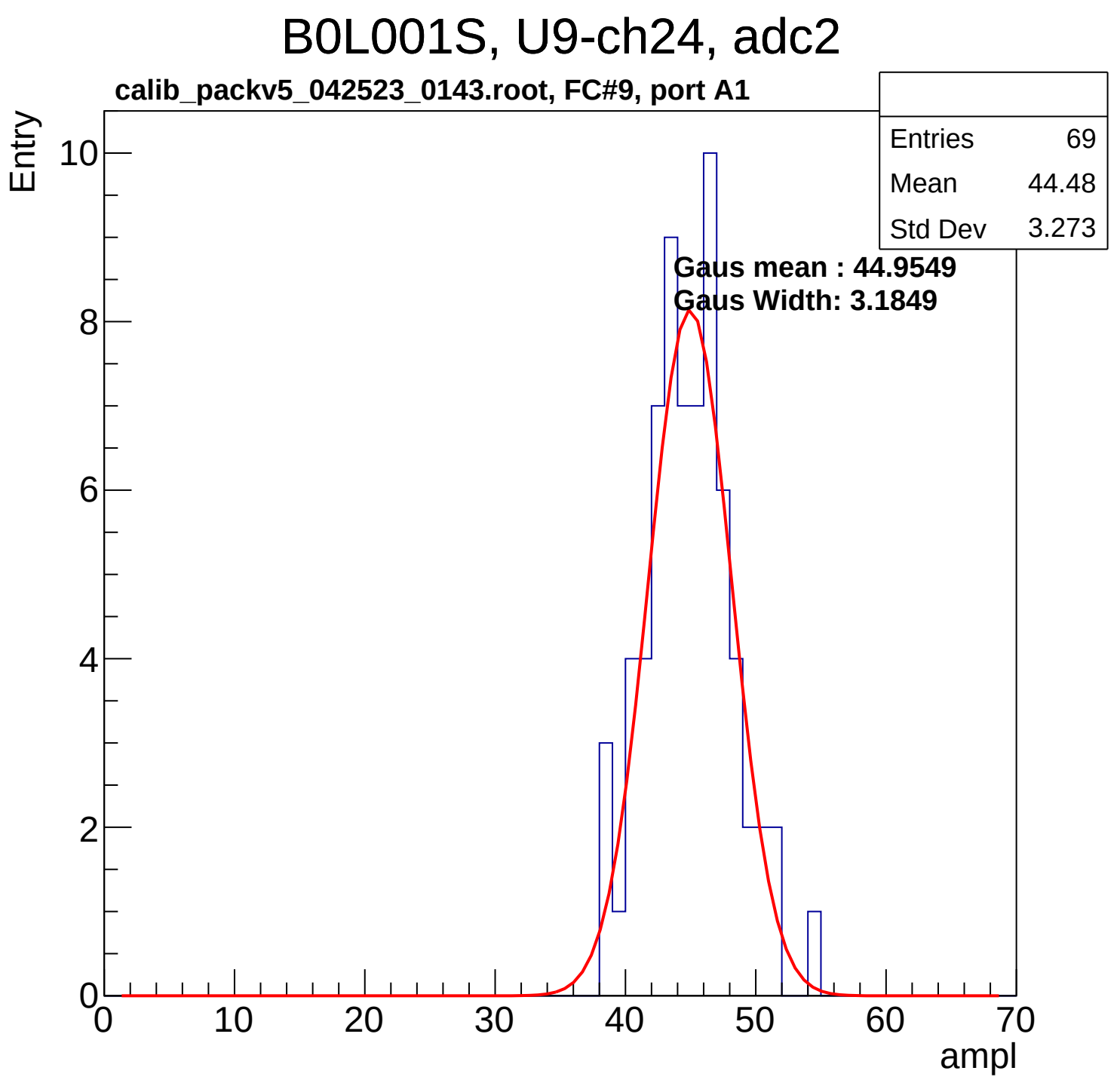
B0L001S, U9-ch24, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.48
Std Dev 3.273
Gaus mean : 44.9549
Gaus Width: 3.1849
Entry
ampl
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4
6
8
10
0
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20
30
40
50
60
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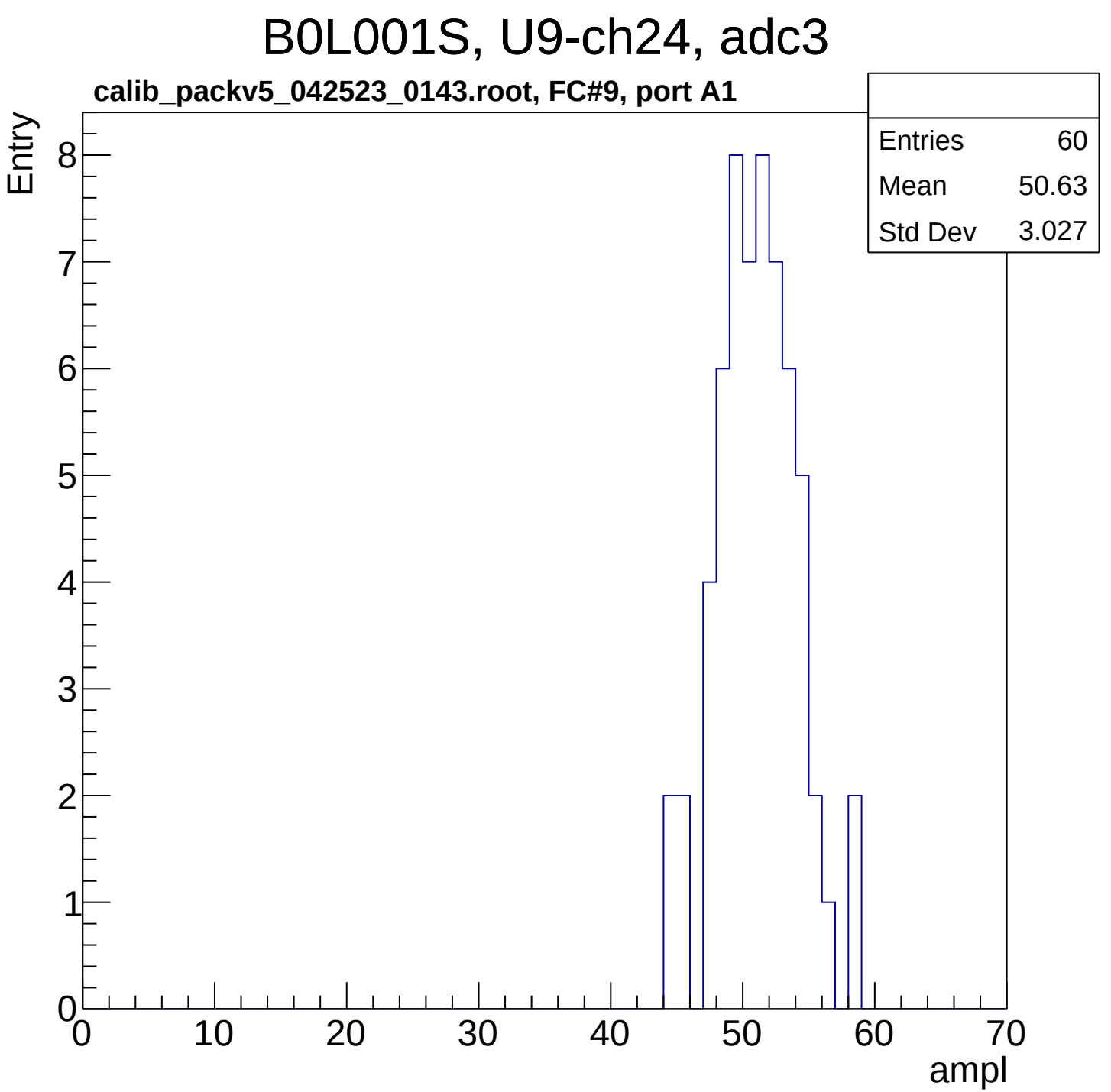
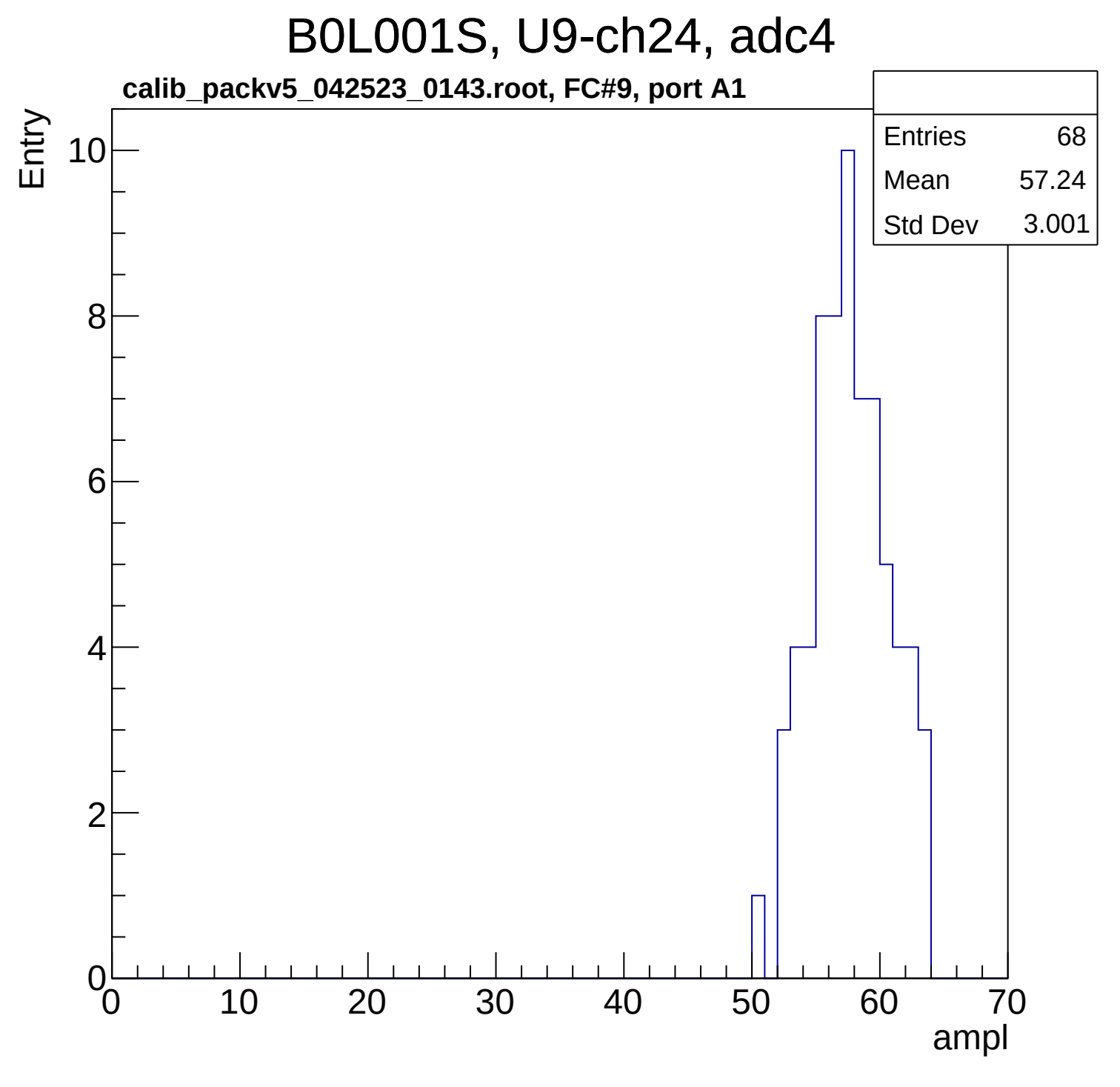

B0L001S, U9-ch24, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.63
Std Dev 3.027
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch24, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 57.24
Std Dev 3.001
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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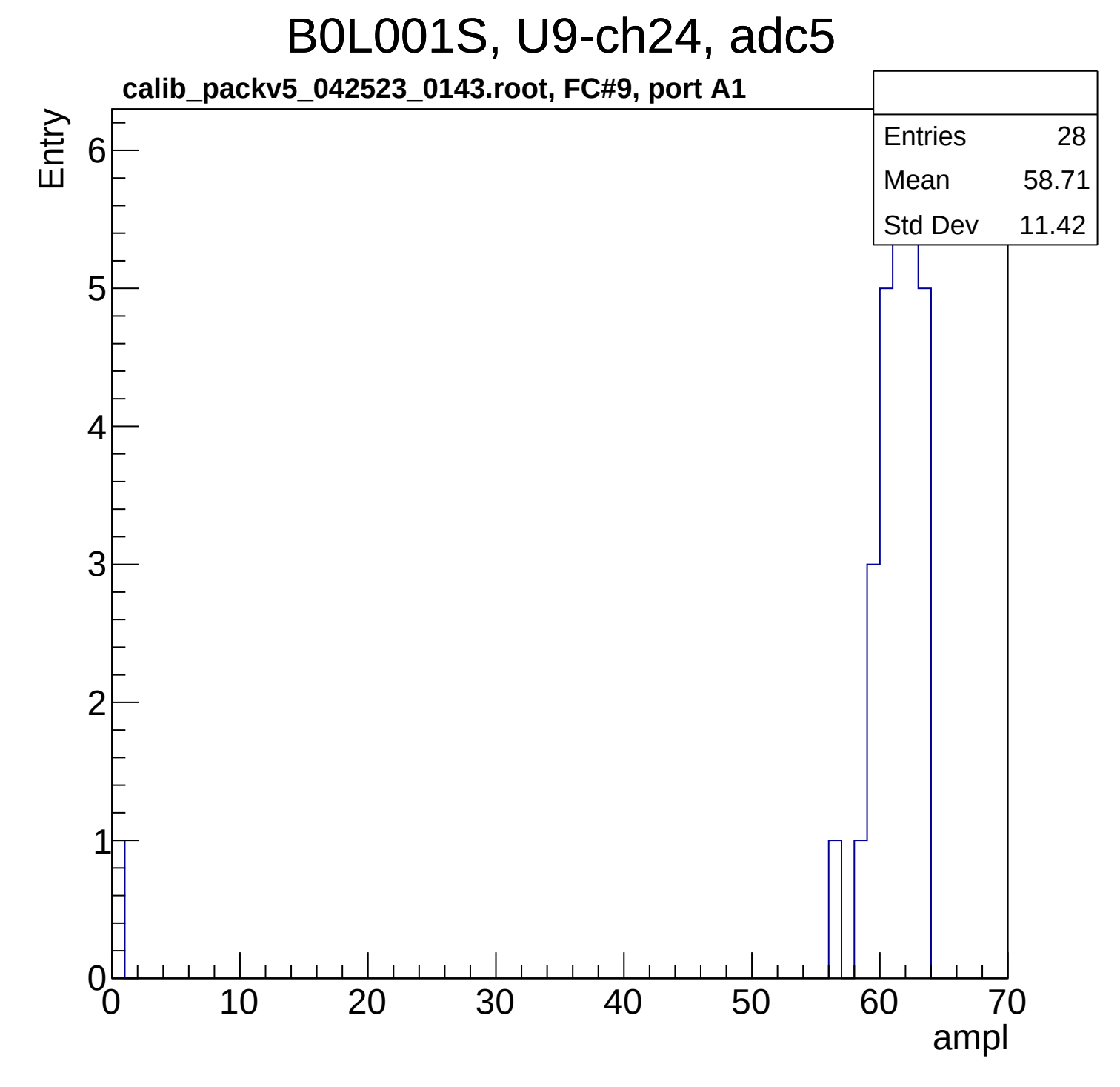
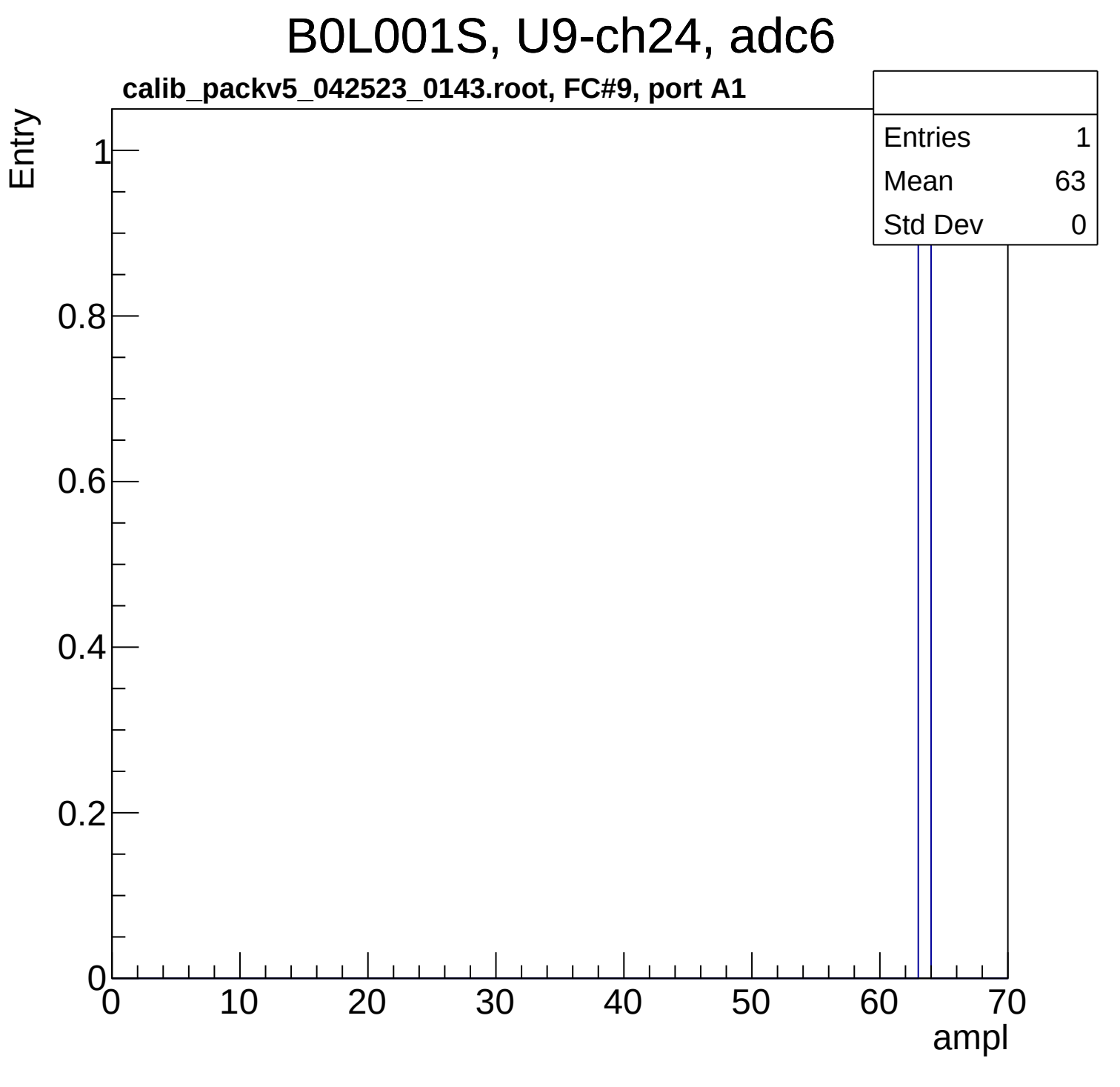
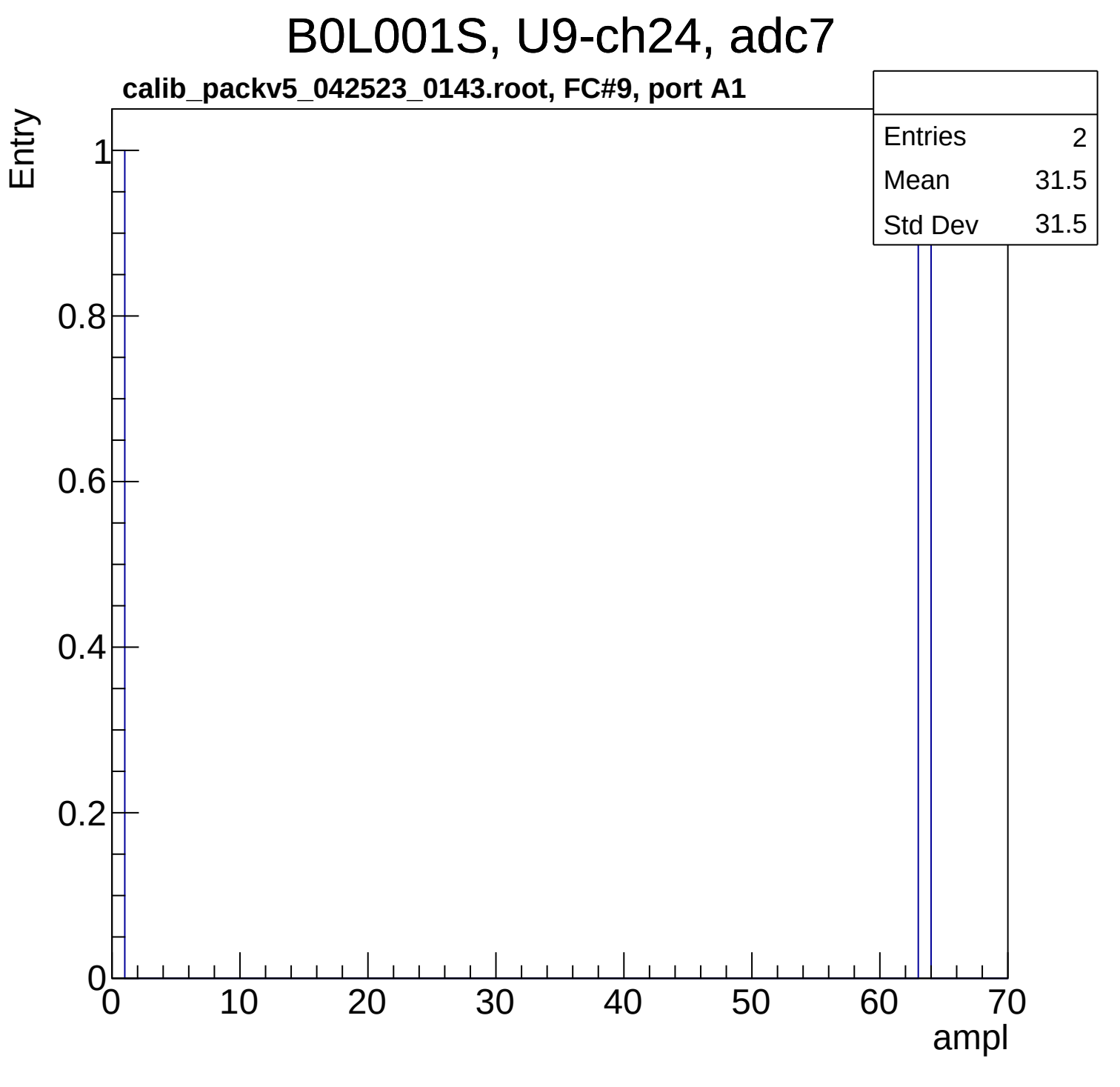

B0L001S, U9-ch24, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.71
Std Dev 11.42
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch24, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch24, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
0
10
20
30
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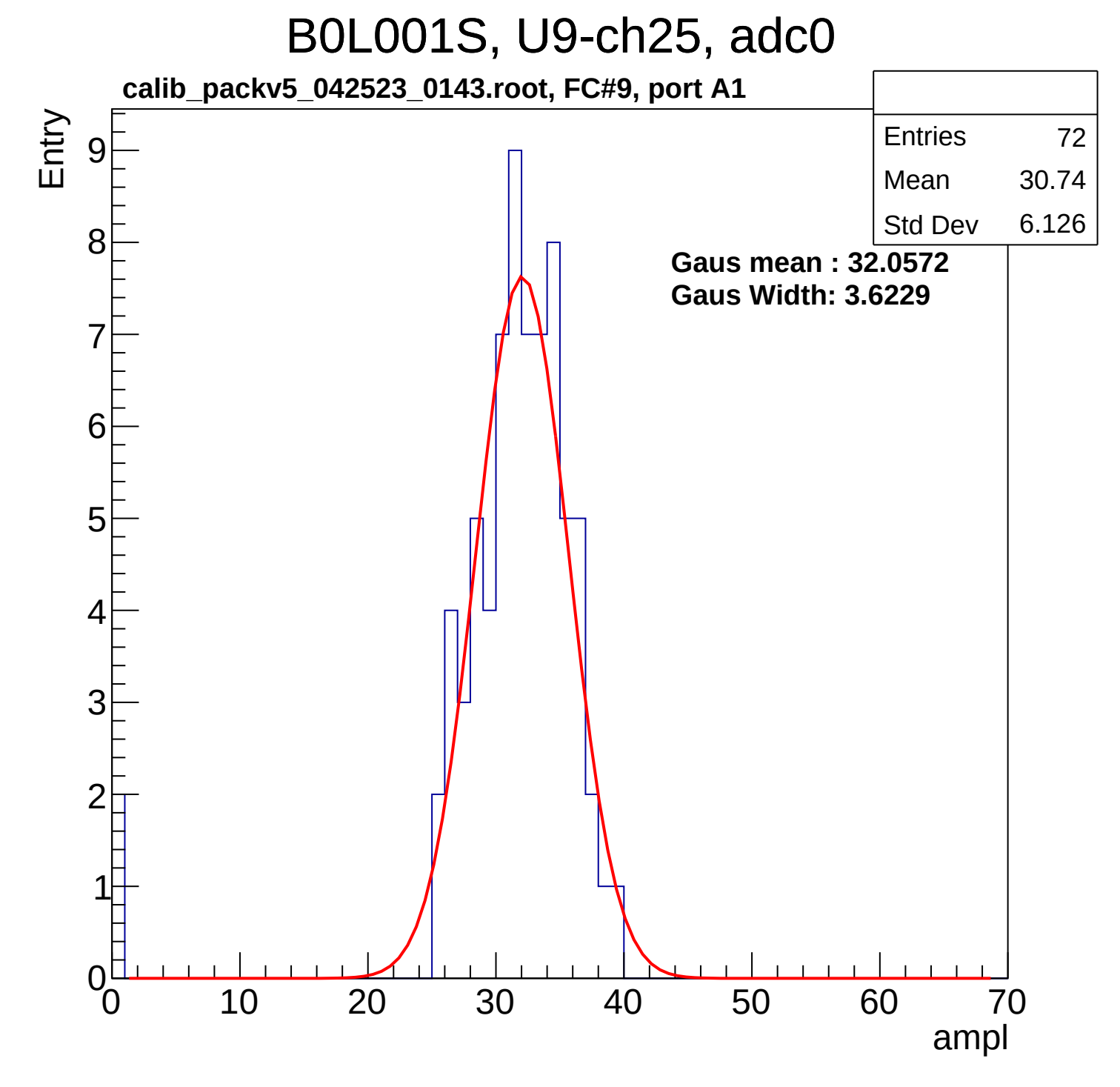
50
60
70
ampl


B0L001S, U9-ch25, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.74
Std Dev 6.126
Gaus mean : 32.0572
Gaus Width: 3.6229
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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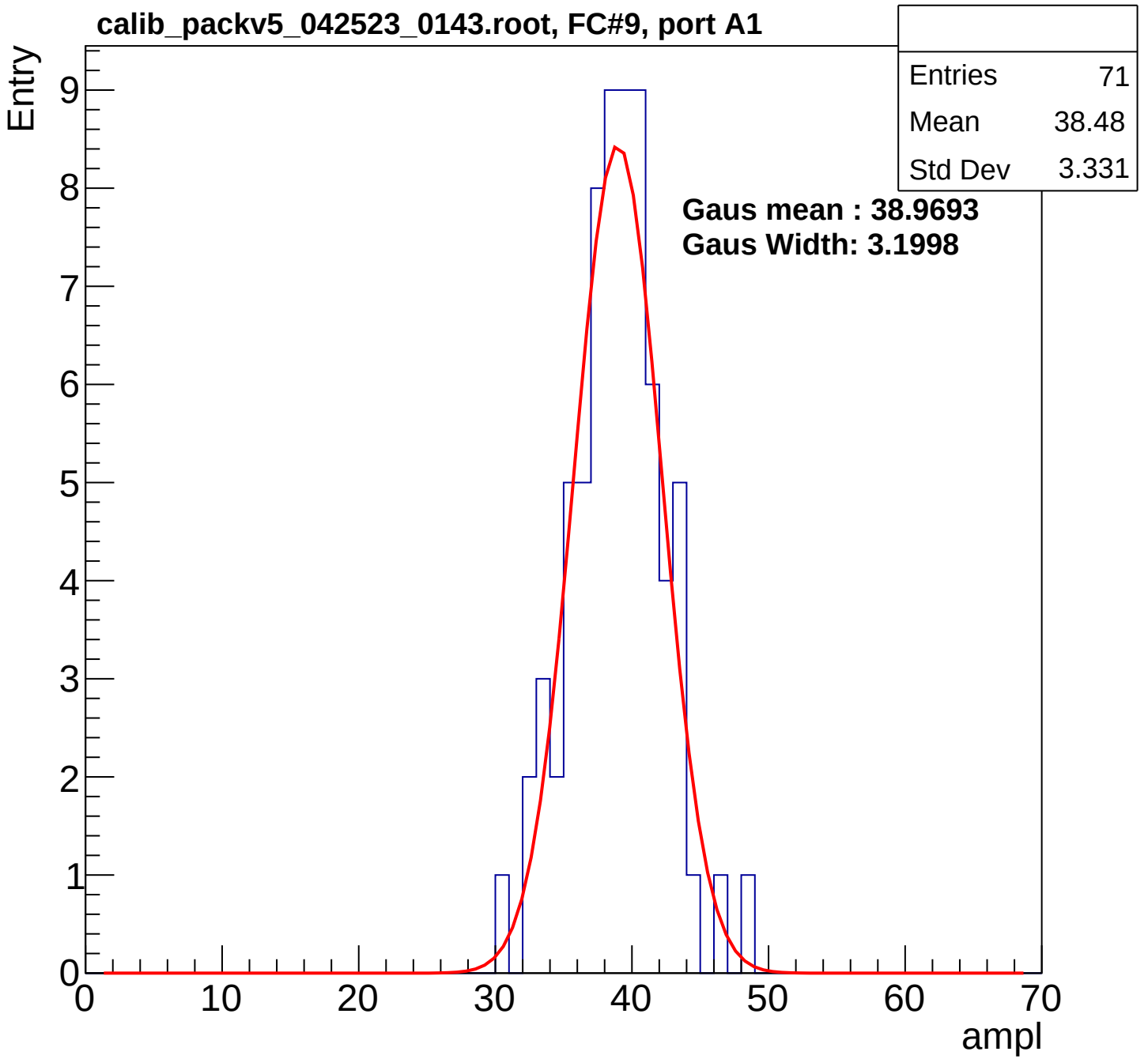
B0L001S, U9-ch25, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 38.48
Std Dev 3.331
Gaus mean : 38.9693
Gaus Width: 3.1998
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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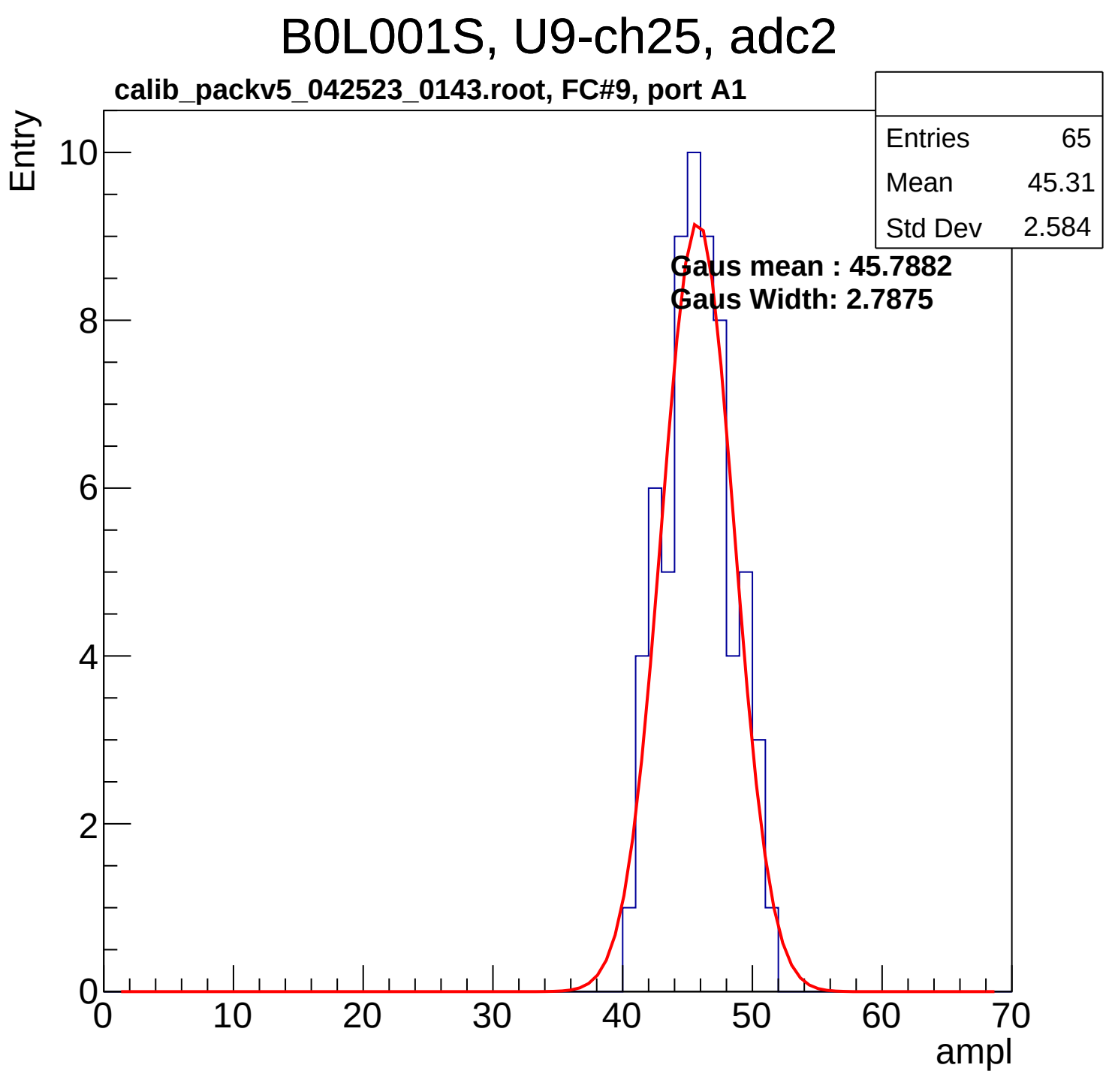
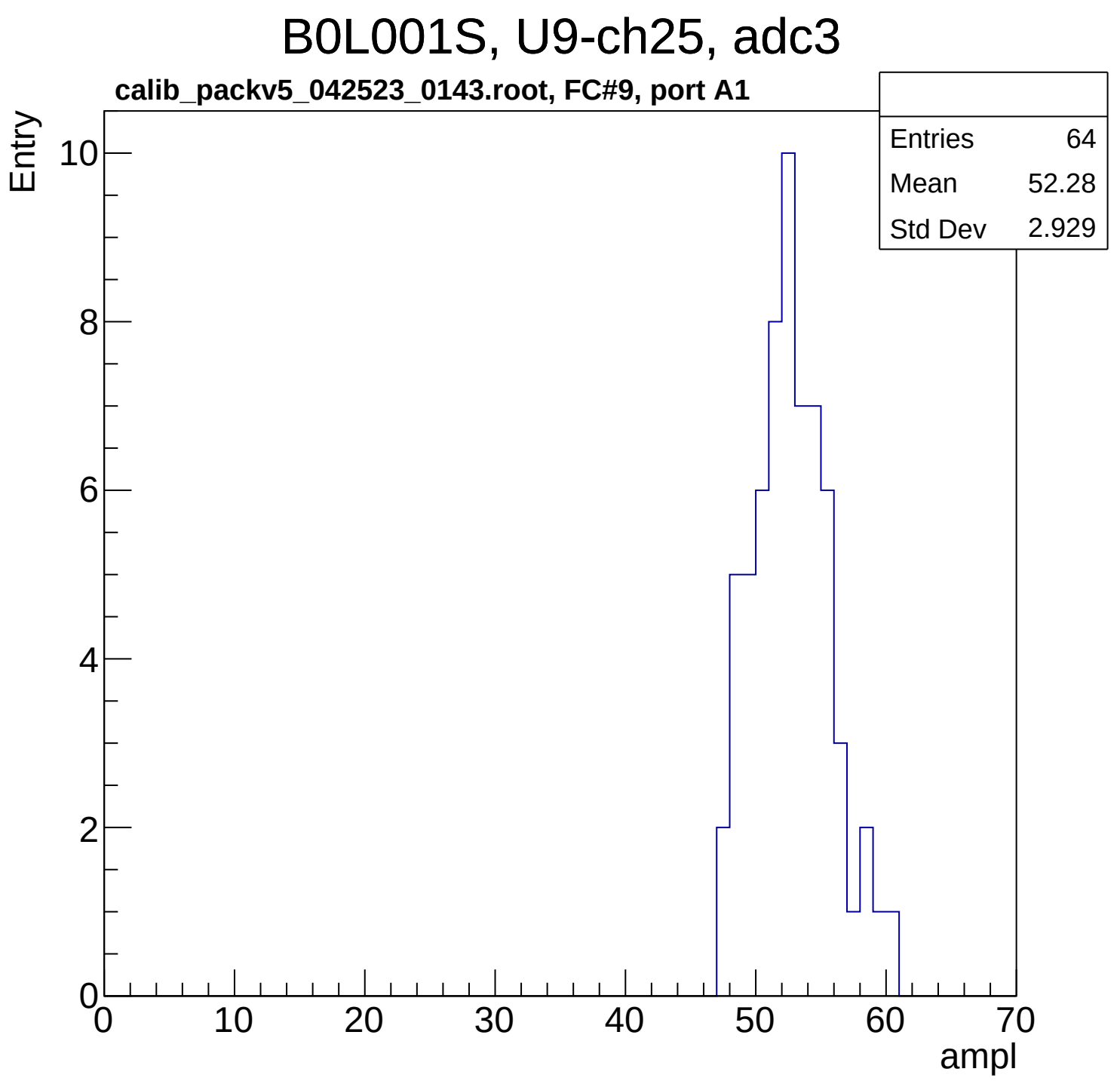
30
40
50
60
70


B0L001S, U9-ch25, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 45.31
Std Dev 2.584
Gaus mean : 45.7882
Gaus Width: 2.7875
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch25, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.28
Std Dev 2.929
Entry
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2
4
6
8
10
0
10
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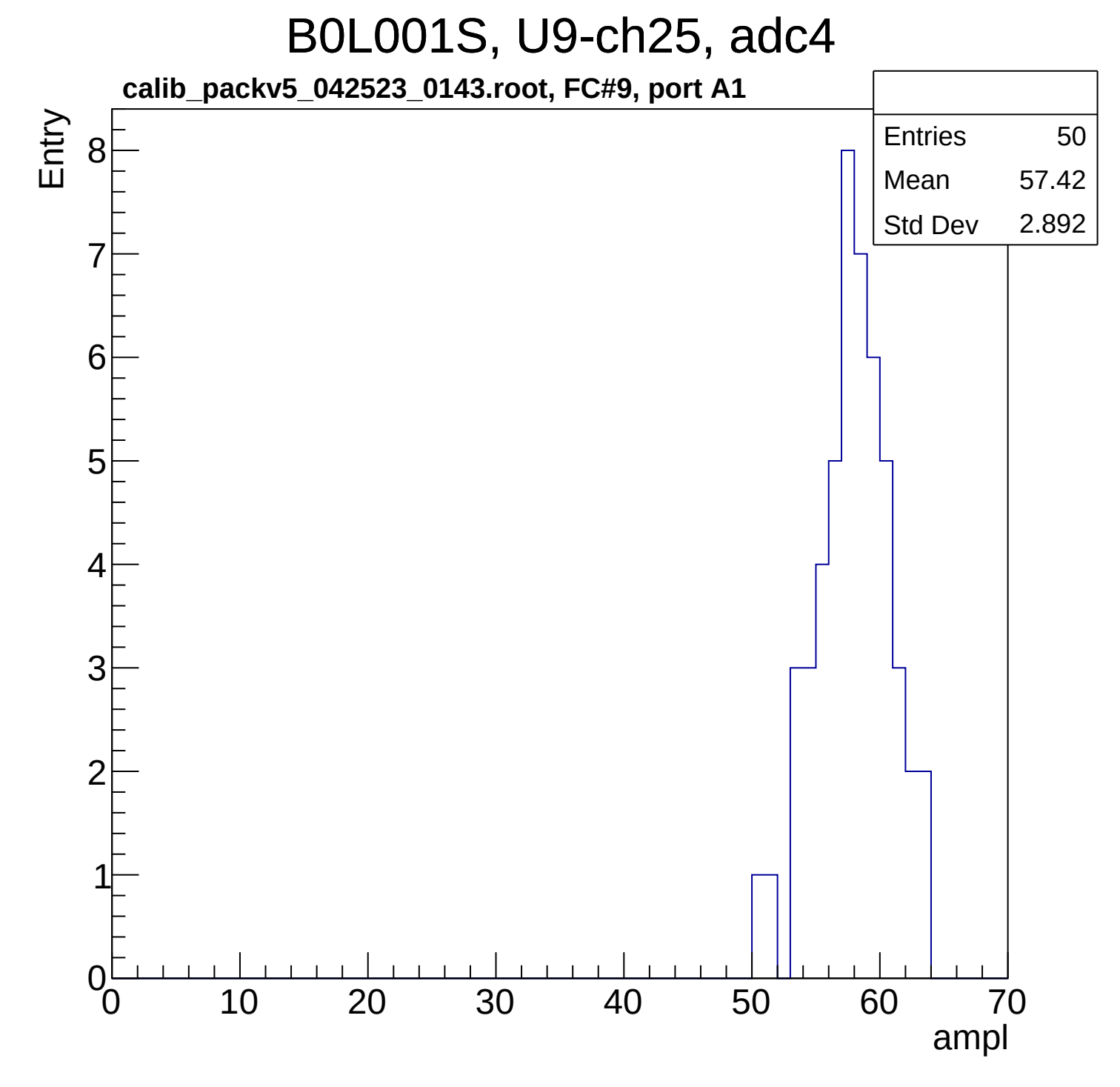
60
70
ampl


B0L001S, U9-ch25, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.42
Std Dev 2.892
Entry
ampl
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2
3
4
5
6
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0
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60
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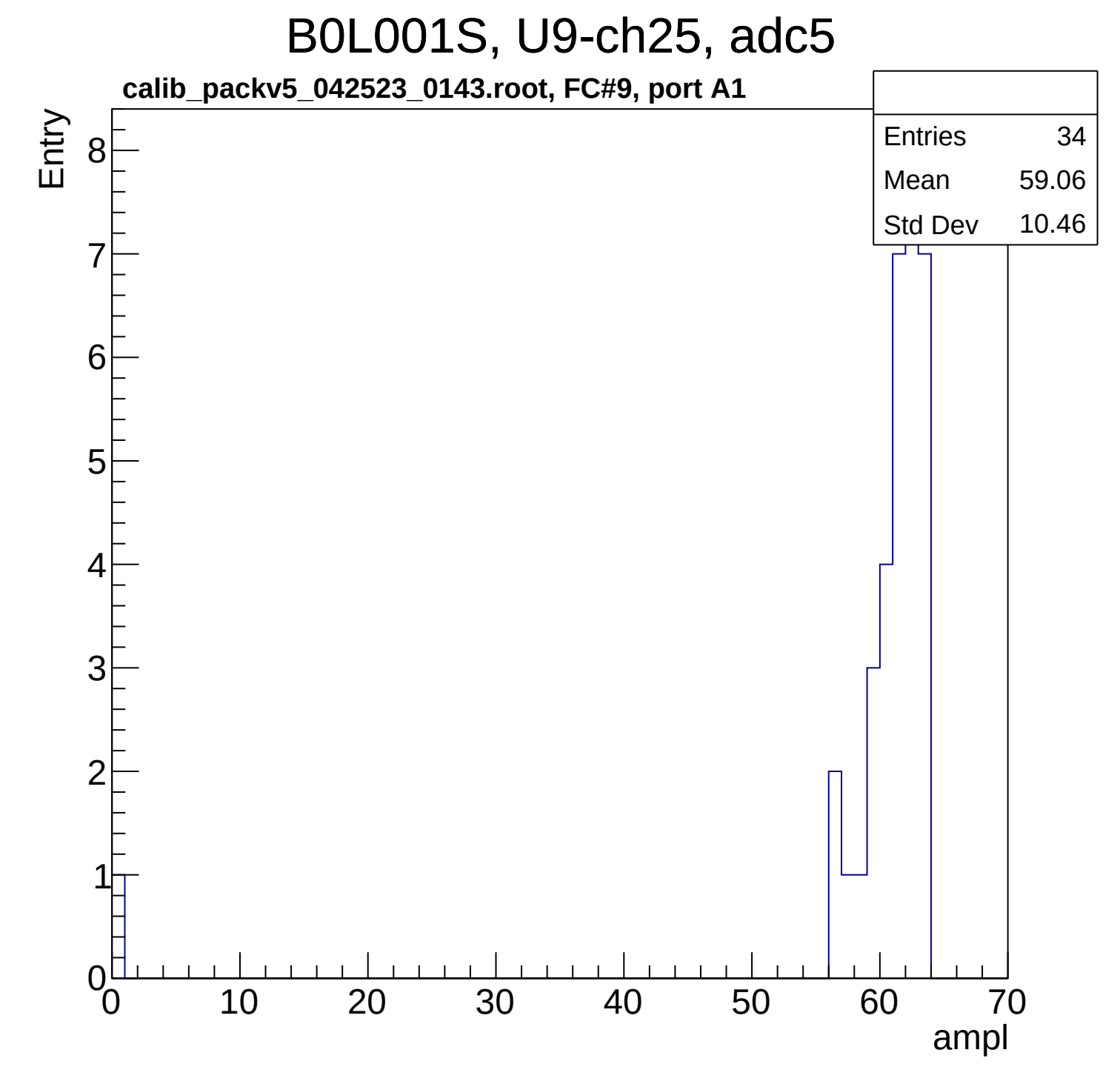
B0L001S, U9-ch25, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 59.06
Std Dev 10.46
Entry
ampl
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5
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0
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60
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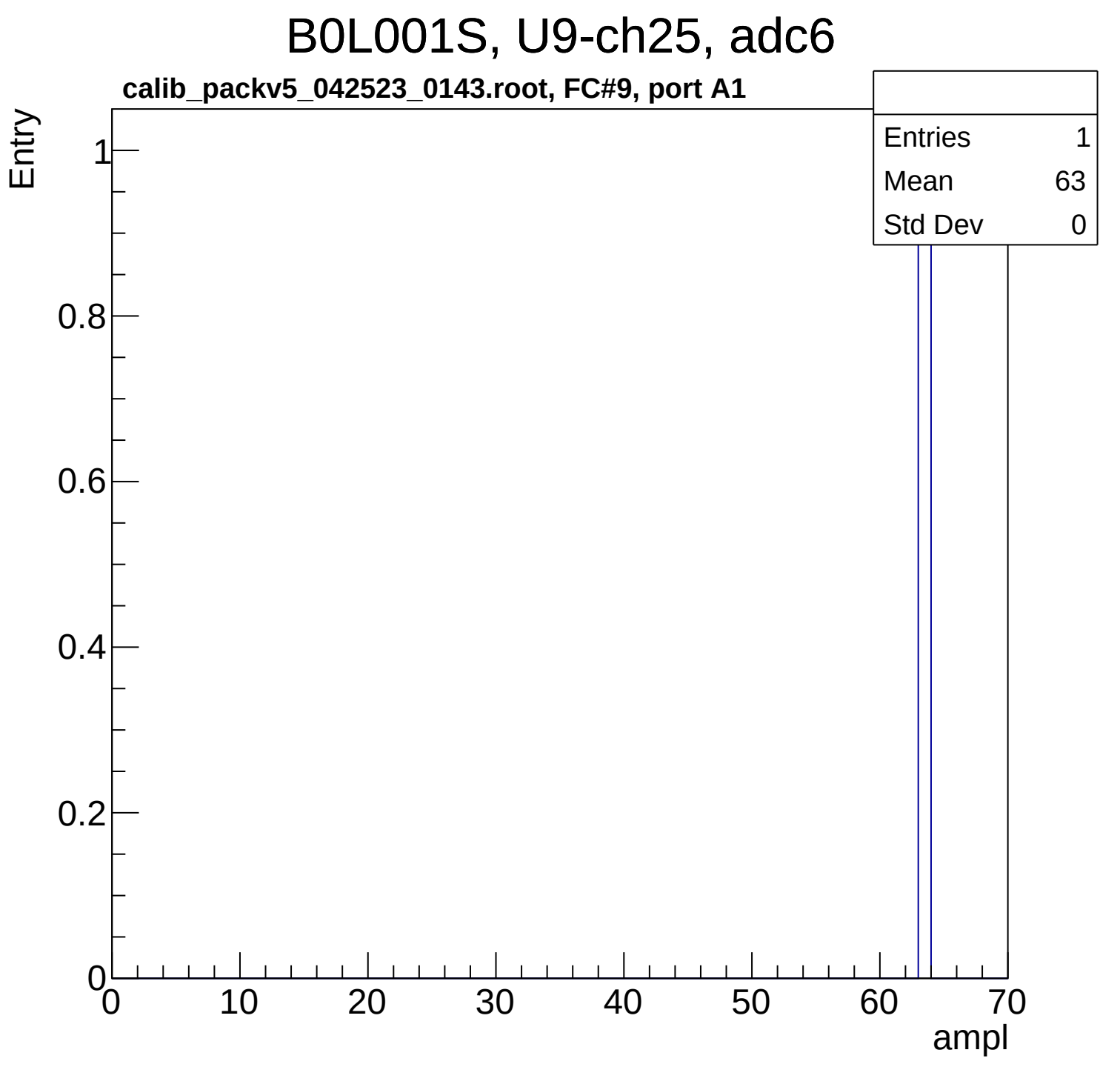
B0L001S, U9-ch25, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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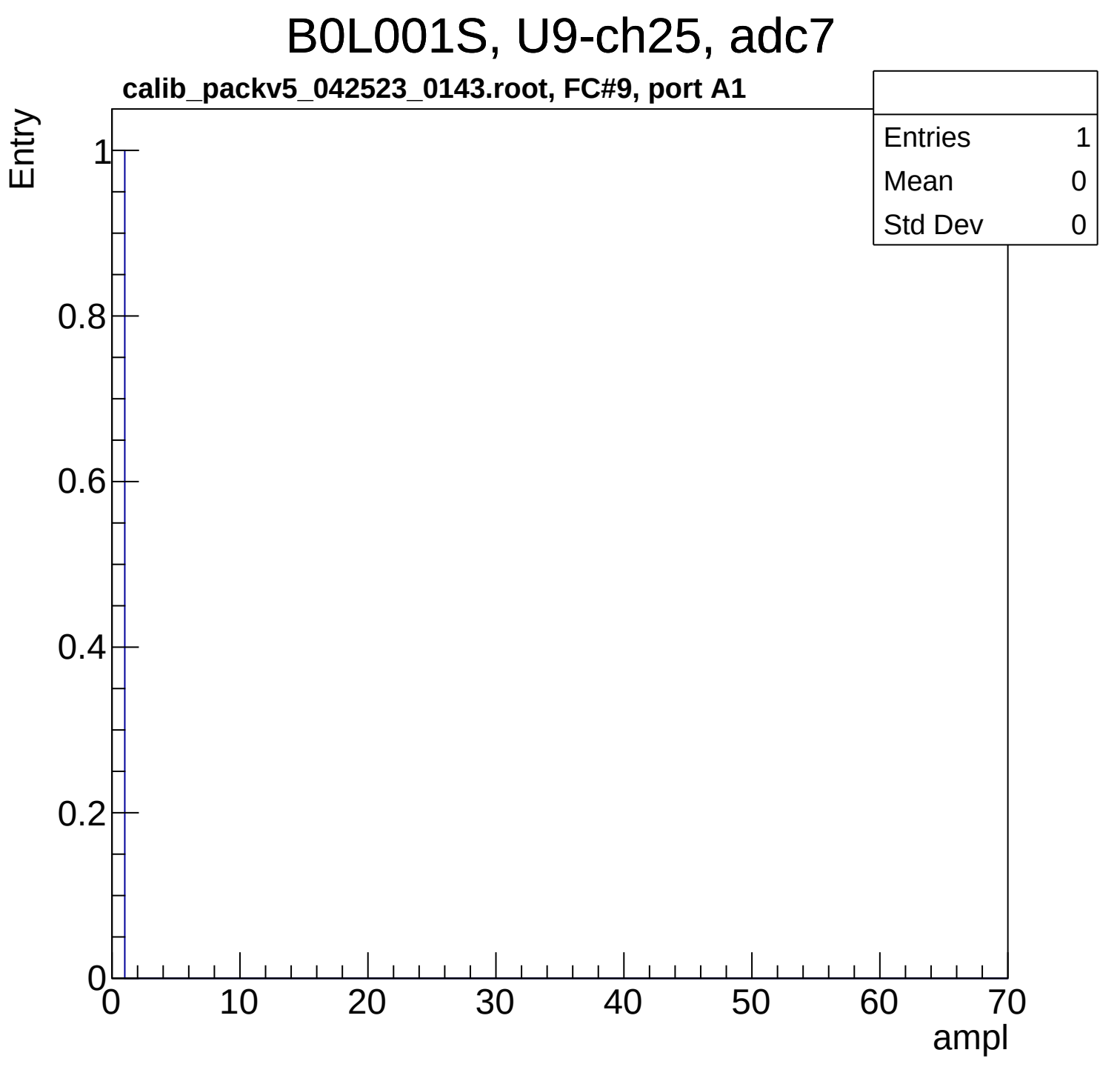
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch25, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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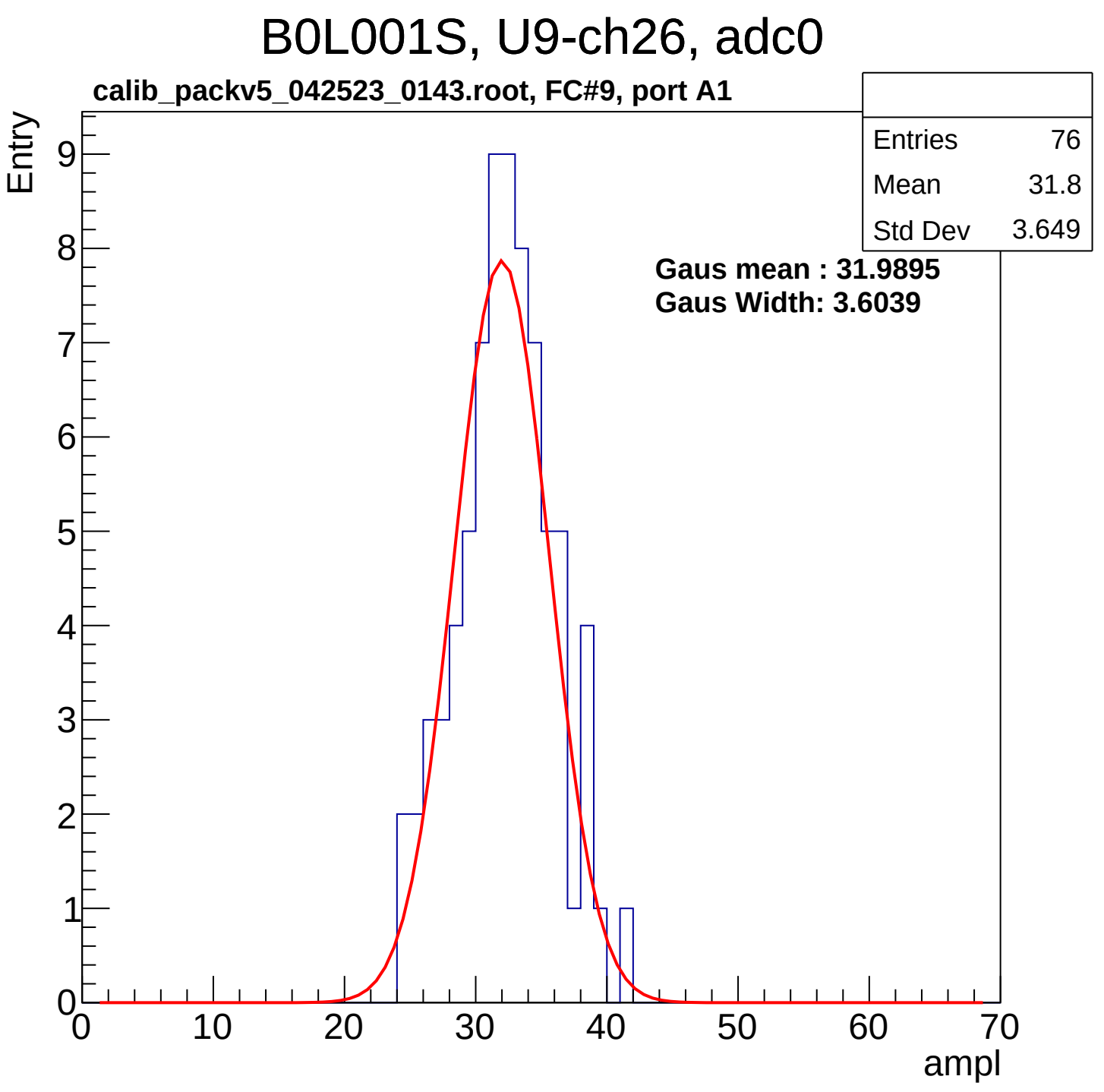
50
60
70
ampl


B0L001S, U9-ch26, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 31.8
Std Dev 3.649
Gaus mean : 31.9895
Gaus Width: 3.6039
Entry
ampl
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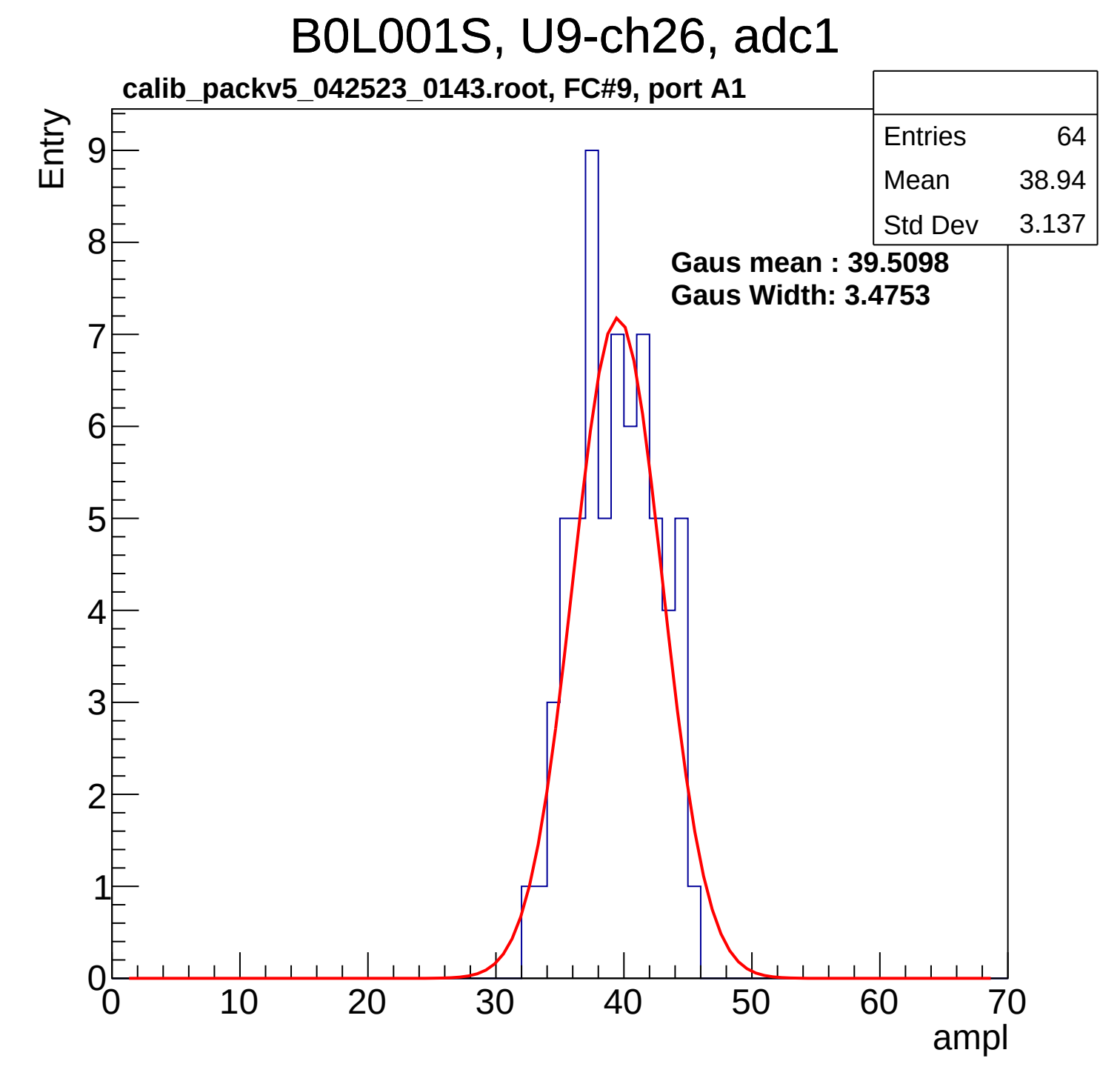
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch26, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.94
Std Dev 3.137
Gaus mean : 39.5098
Gaus Width: 3.4753
Entry
ampl
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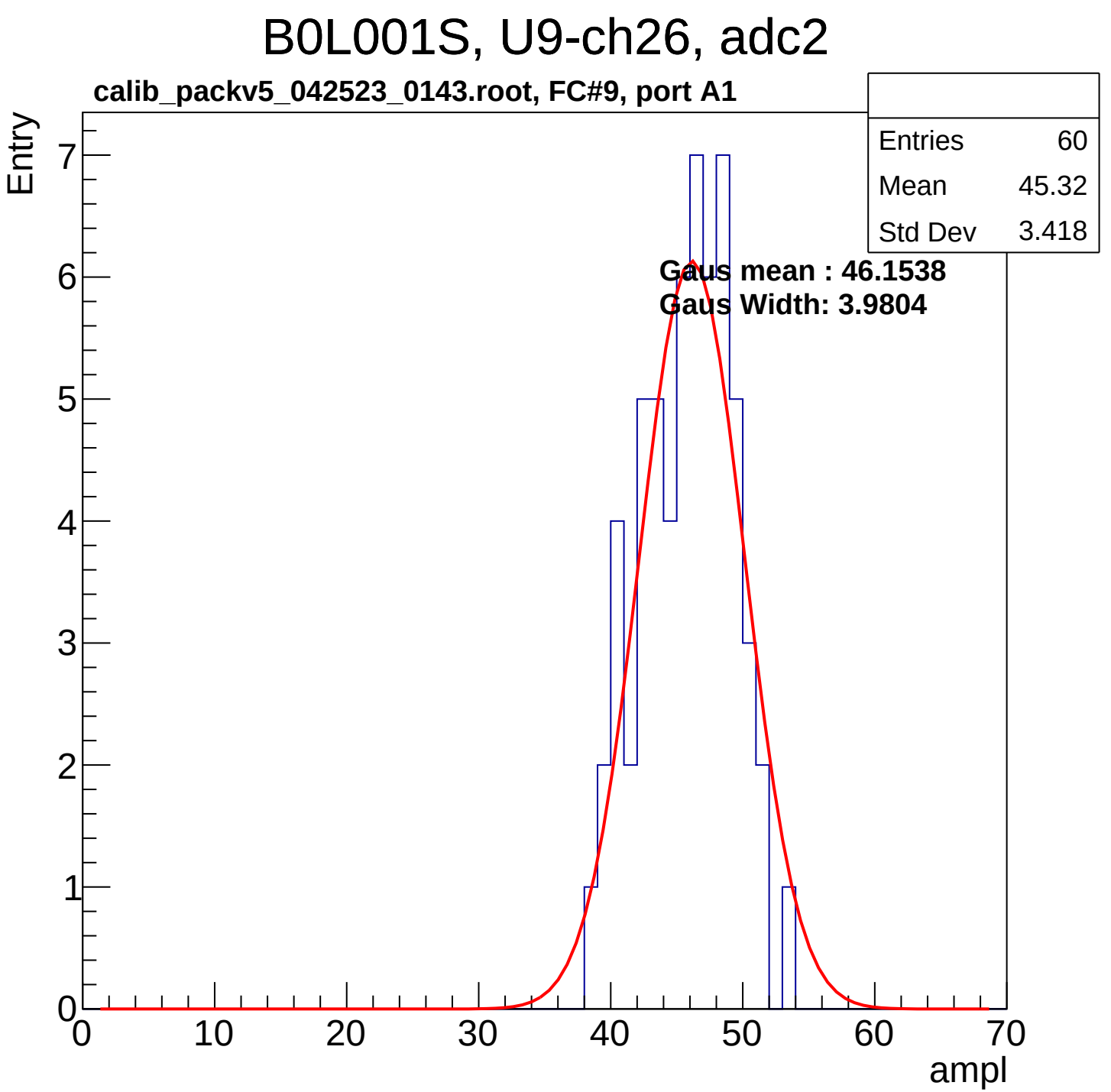
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U9-ch26, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 45.32
Std Dev 3.418
Gaus mean : 46.1538
Gaus Width: 3.9804
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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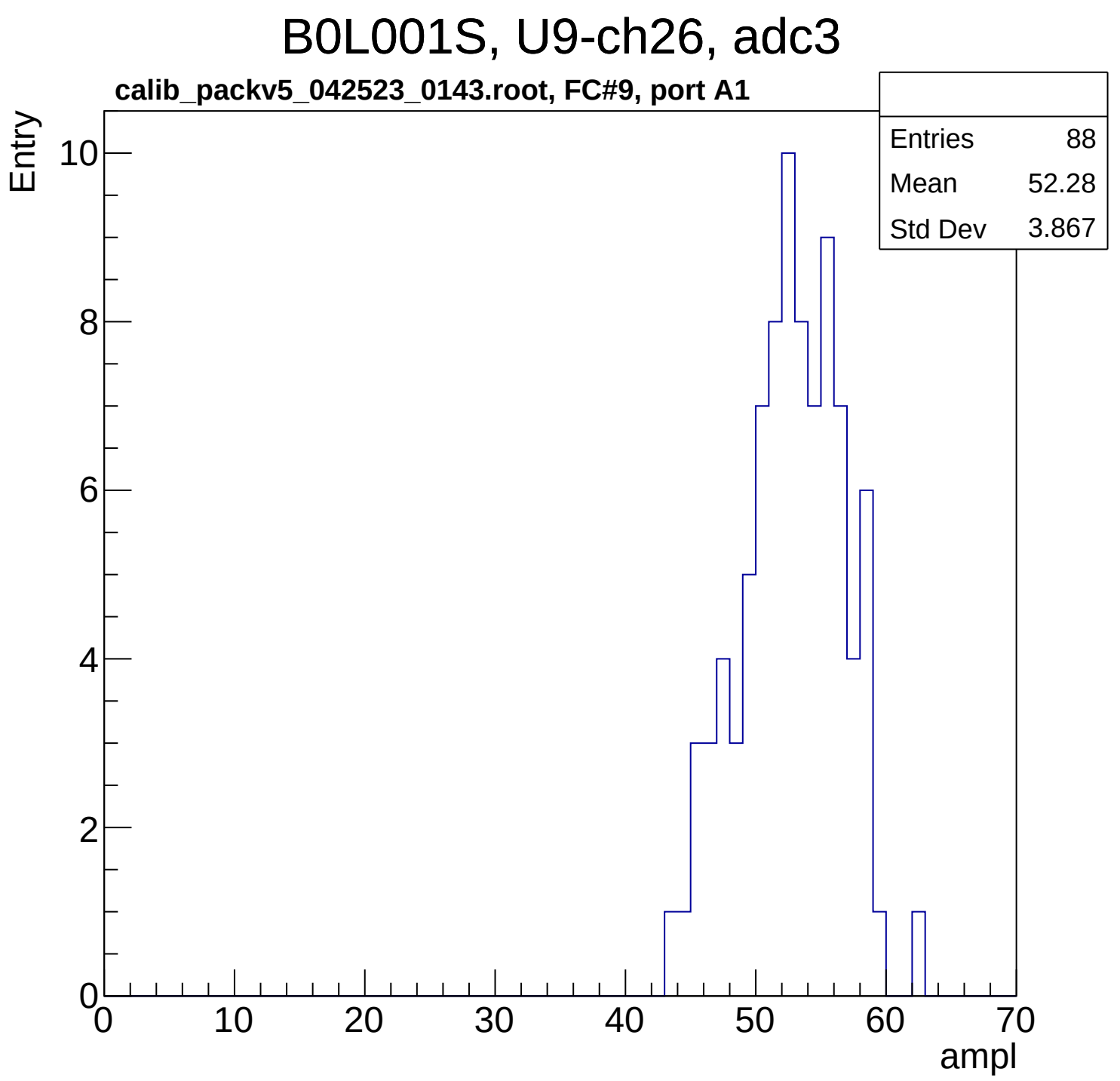
B0L001S, U9-ch26, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 52.28
Std Dev 3.867
Entry
ampl
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0
10
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40
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60
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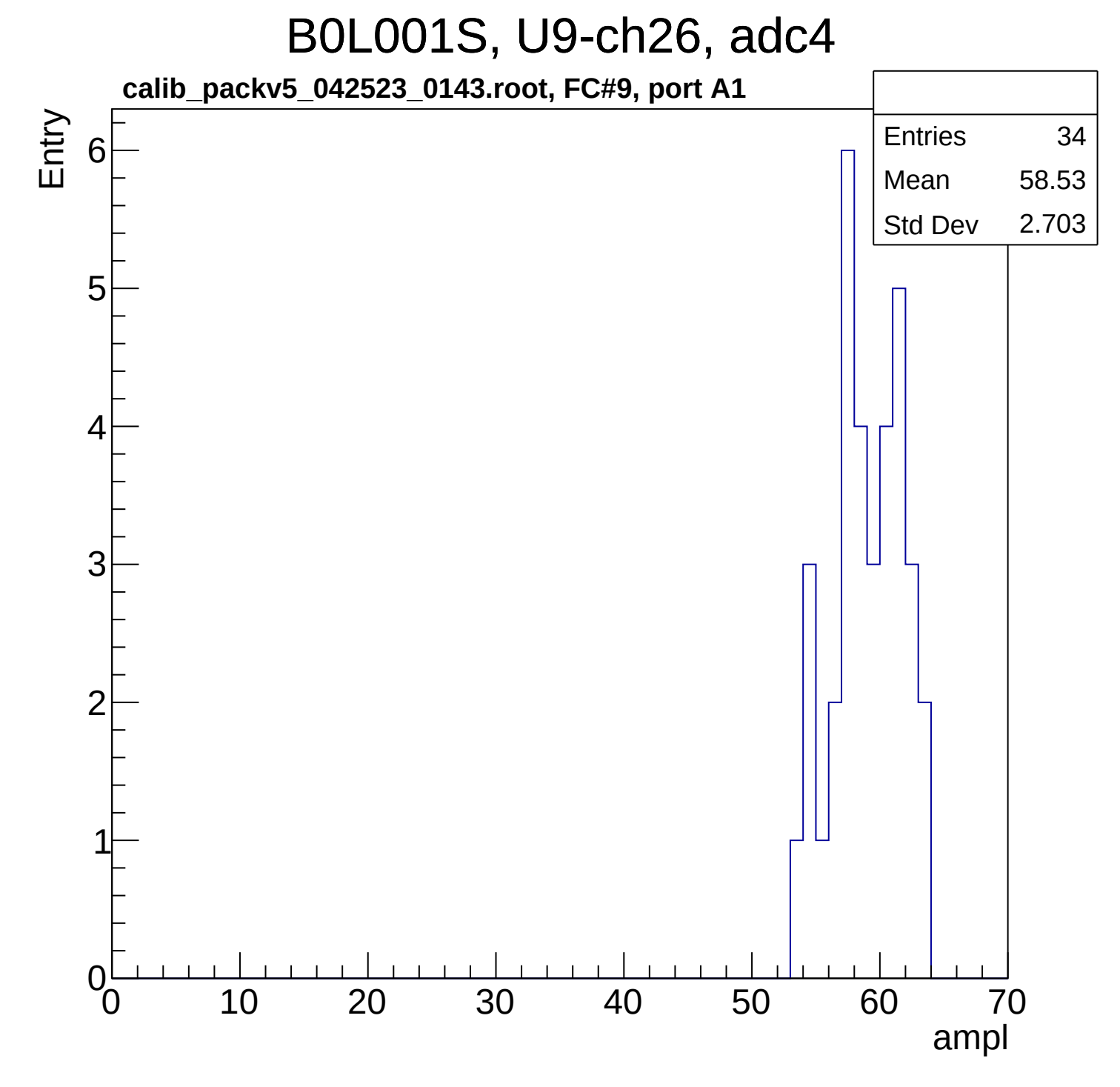
B0L001S, U9-ch26, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.53
Std Dev 2.703
Entry
ampl
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2
3
4
5
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0
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20
30
40
50
60
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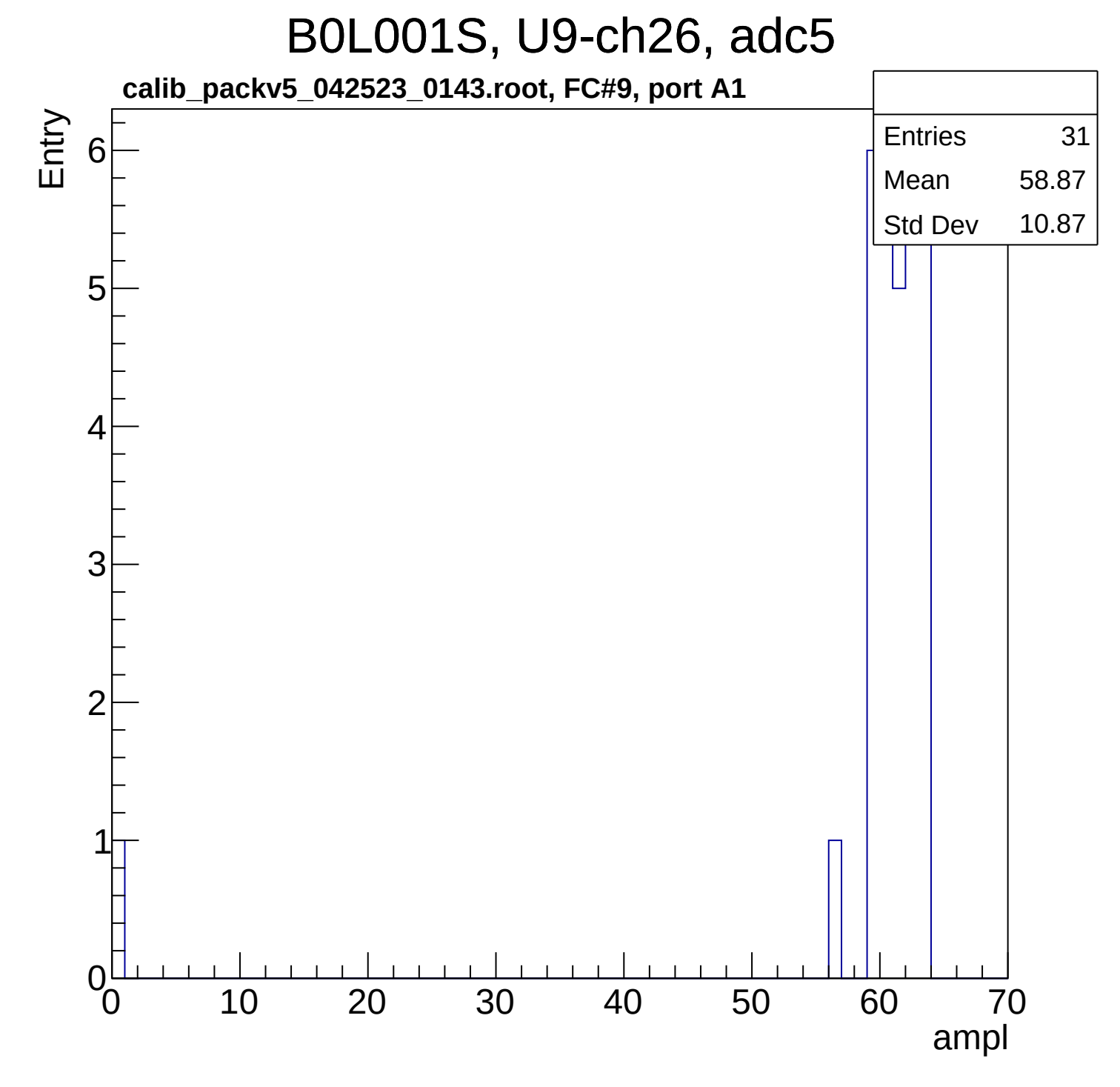
B0L001S, U9-ch26, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.87
Std Dev 10.87
Entry
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10
20
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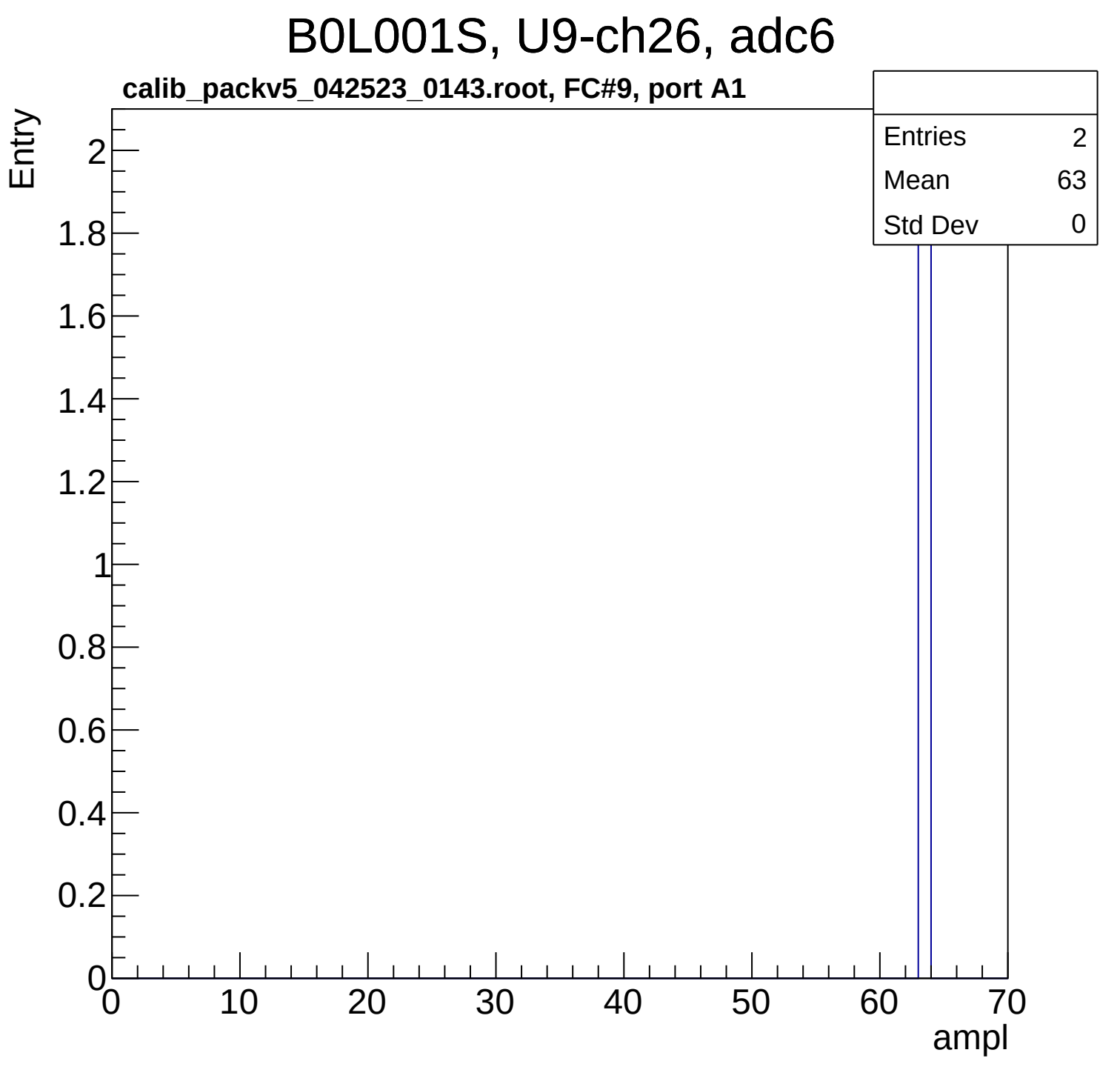
50
60
70
ampl


B0L001S, U9-ch26, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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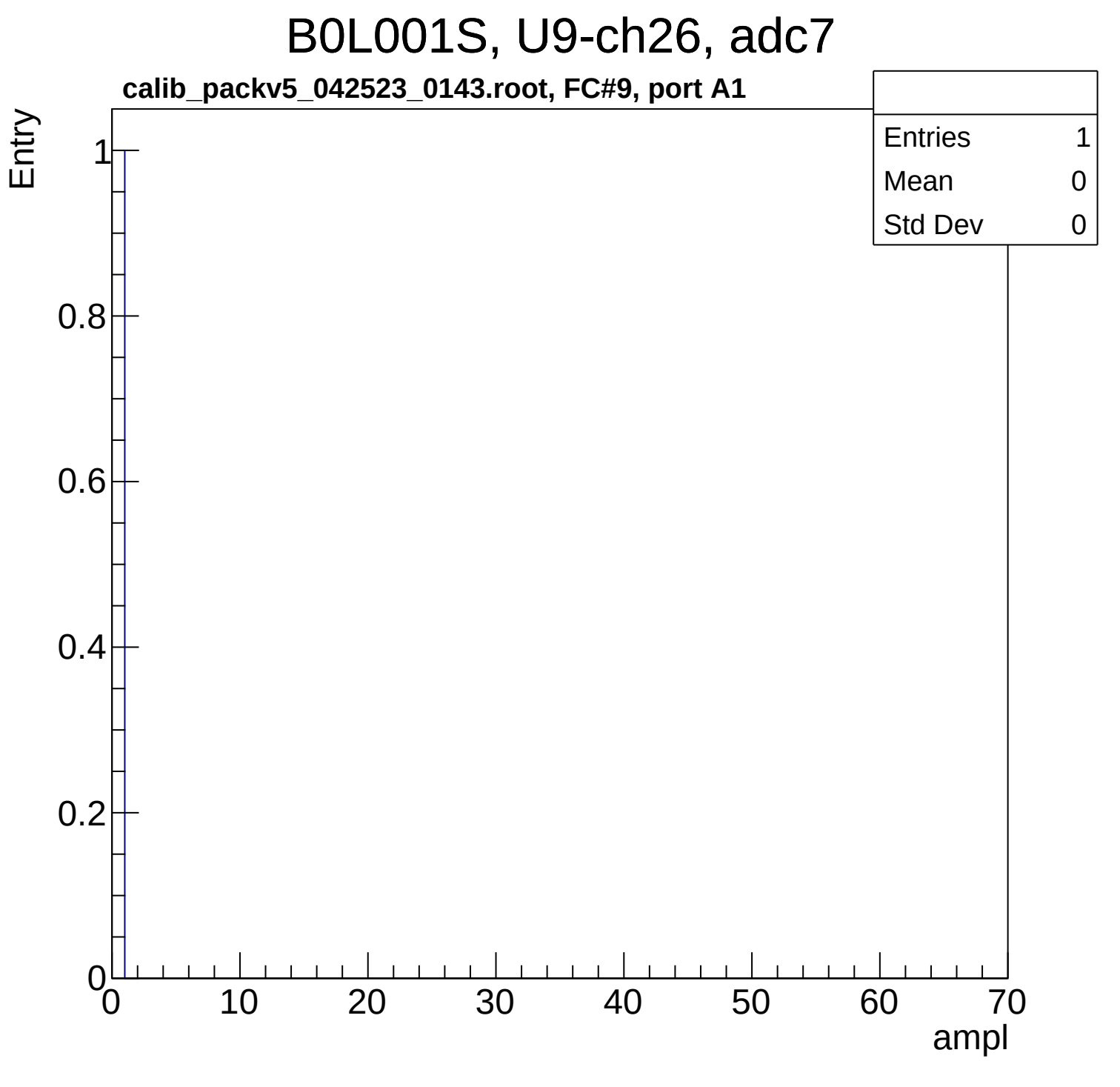
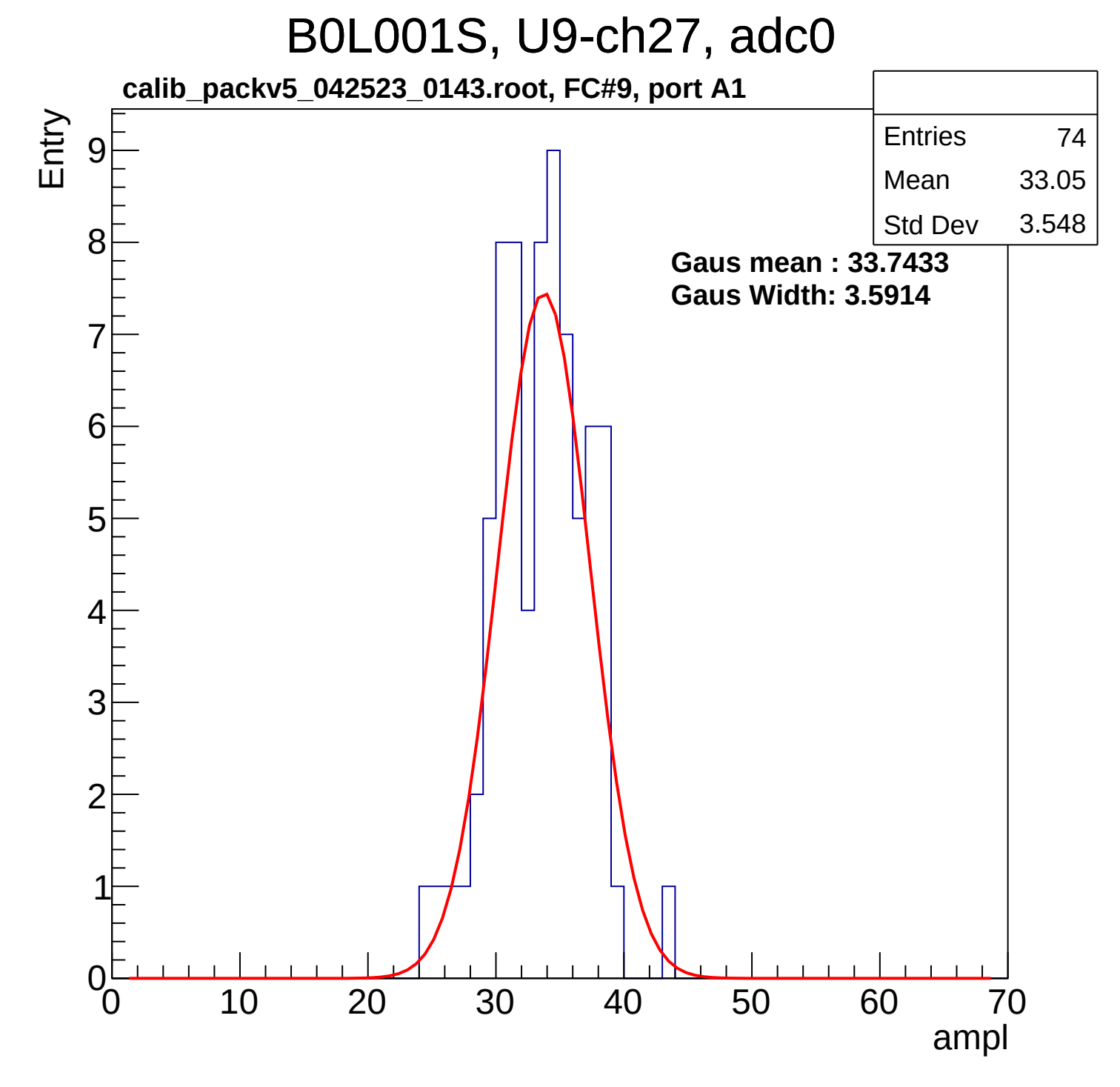
40
50
60
70


B0L001S, U9-ch26, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch27, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 33.05
Std Dev 3.548
Gaus mean : 33.7433
Gaus Width: 3.5914
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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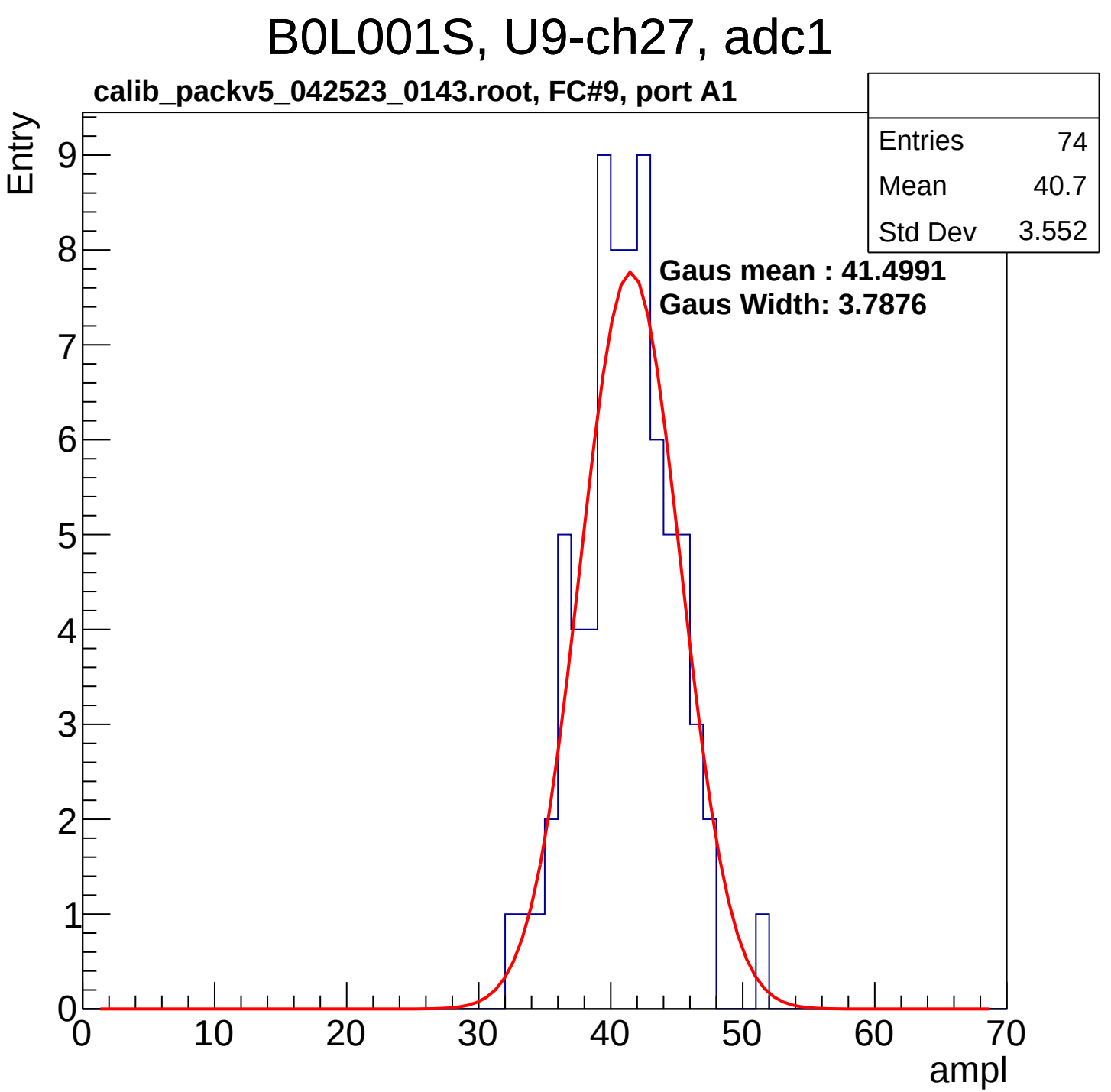
B0L001S, U9-ch27, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 40.7
Std Dev 3.552
Gaus mean : 41.4991
Gaus Width: 3.7876
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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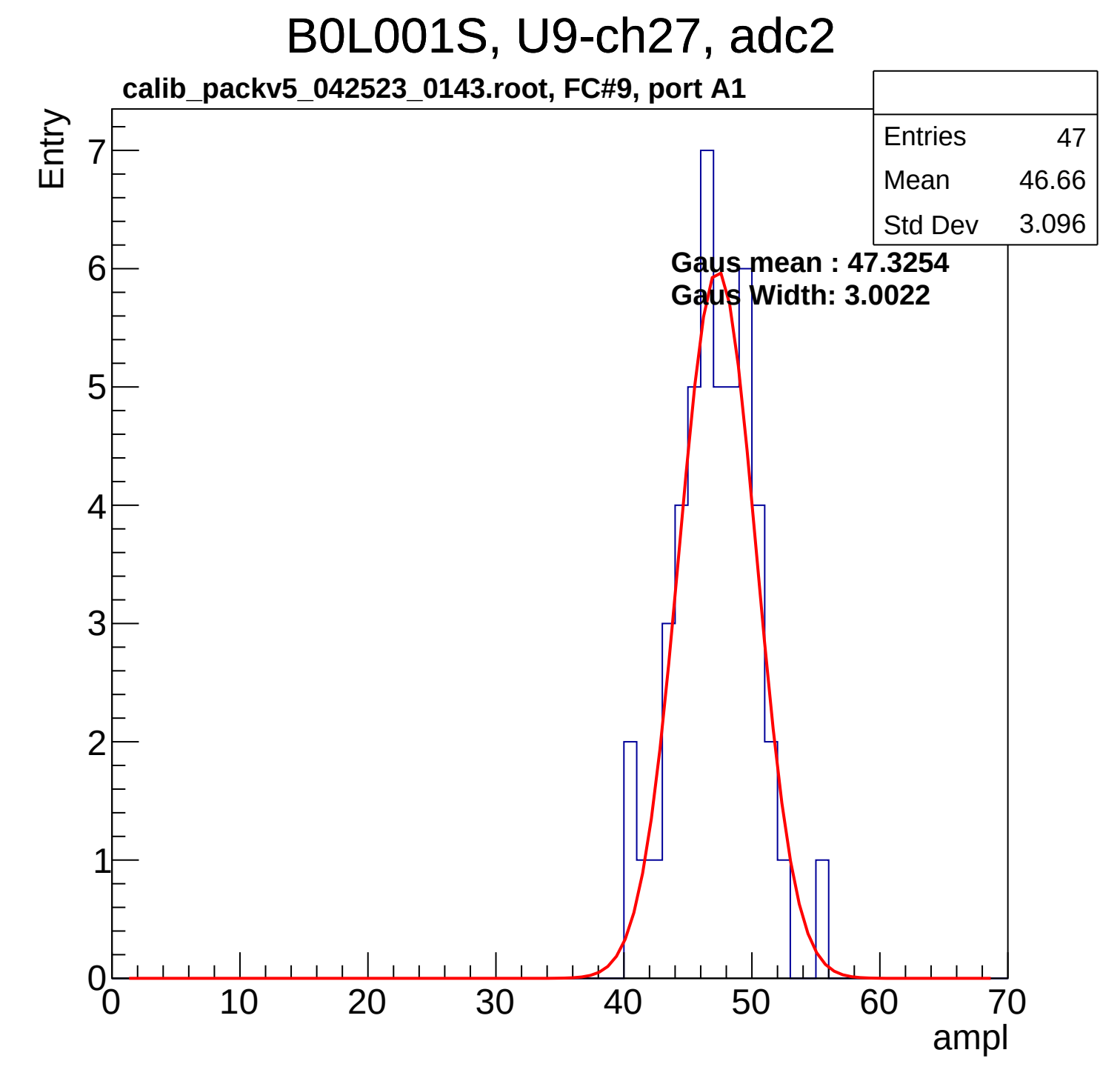
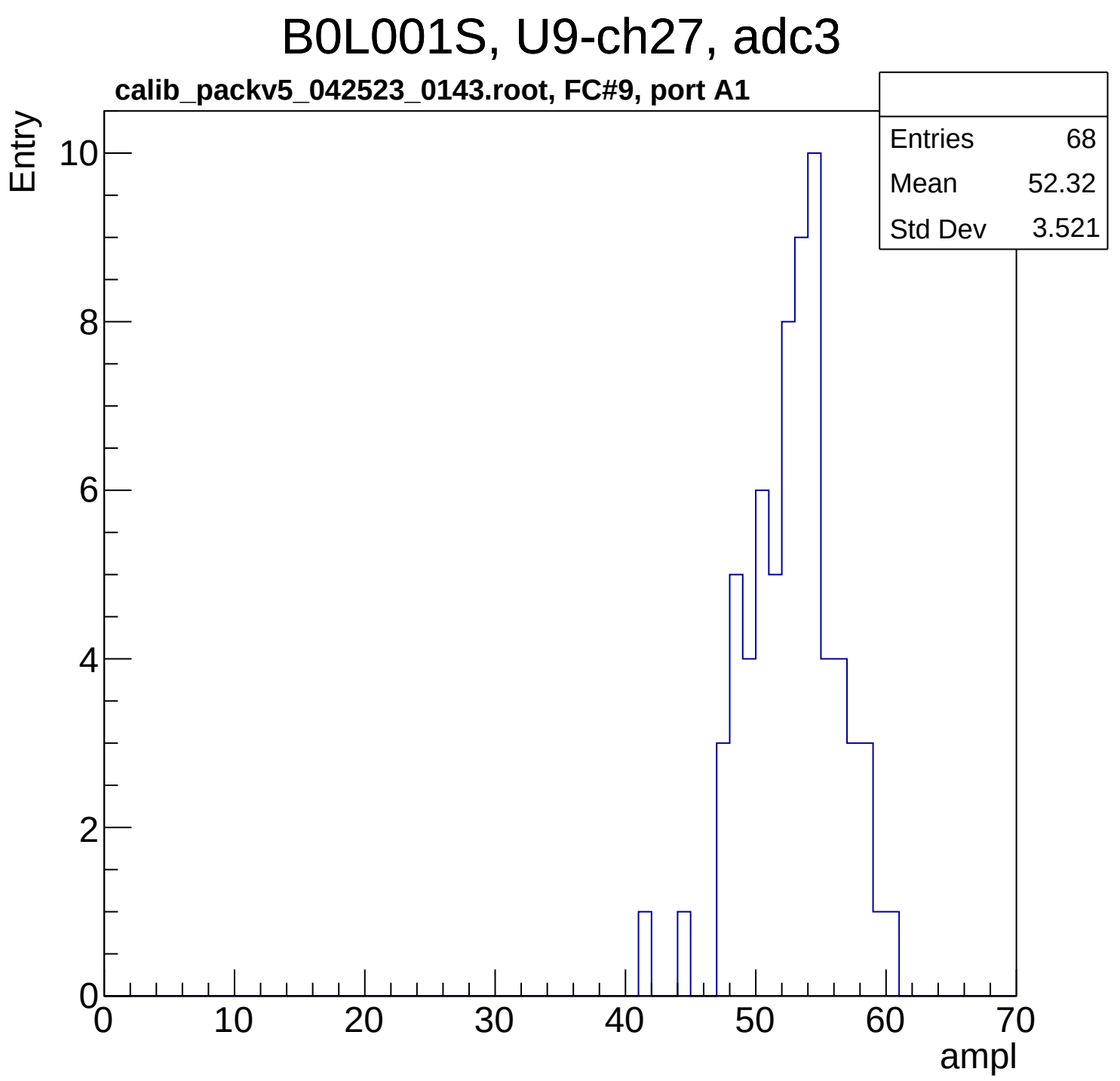
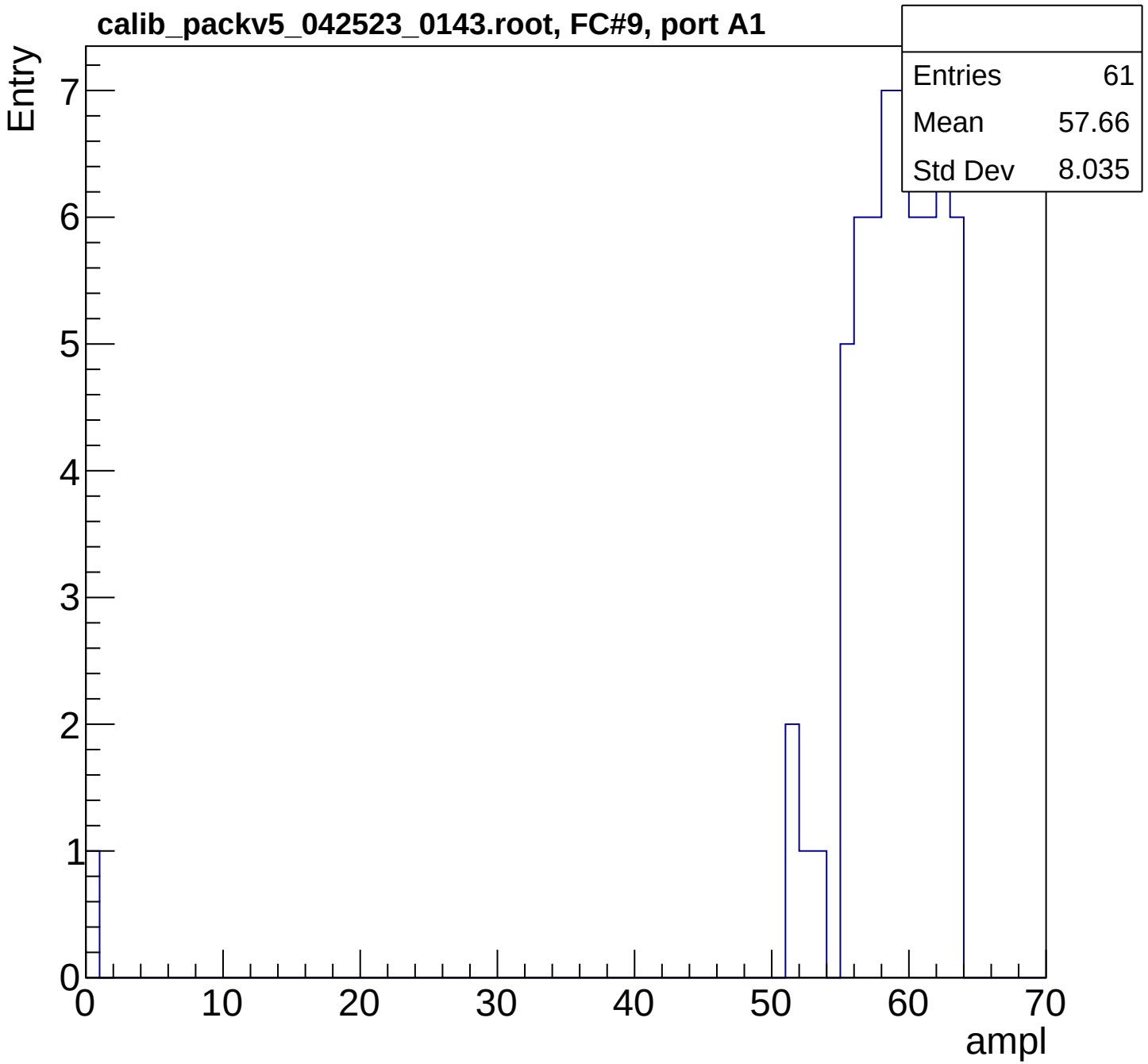
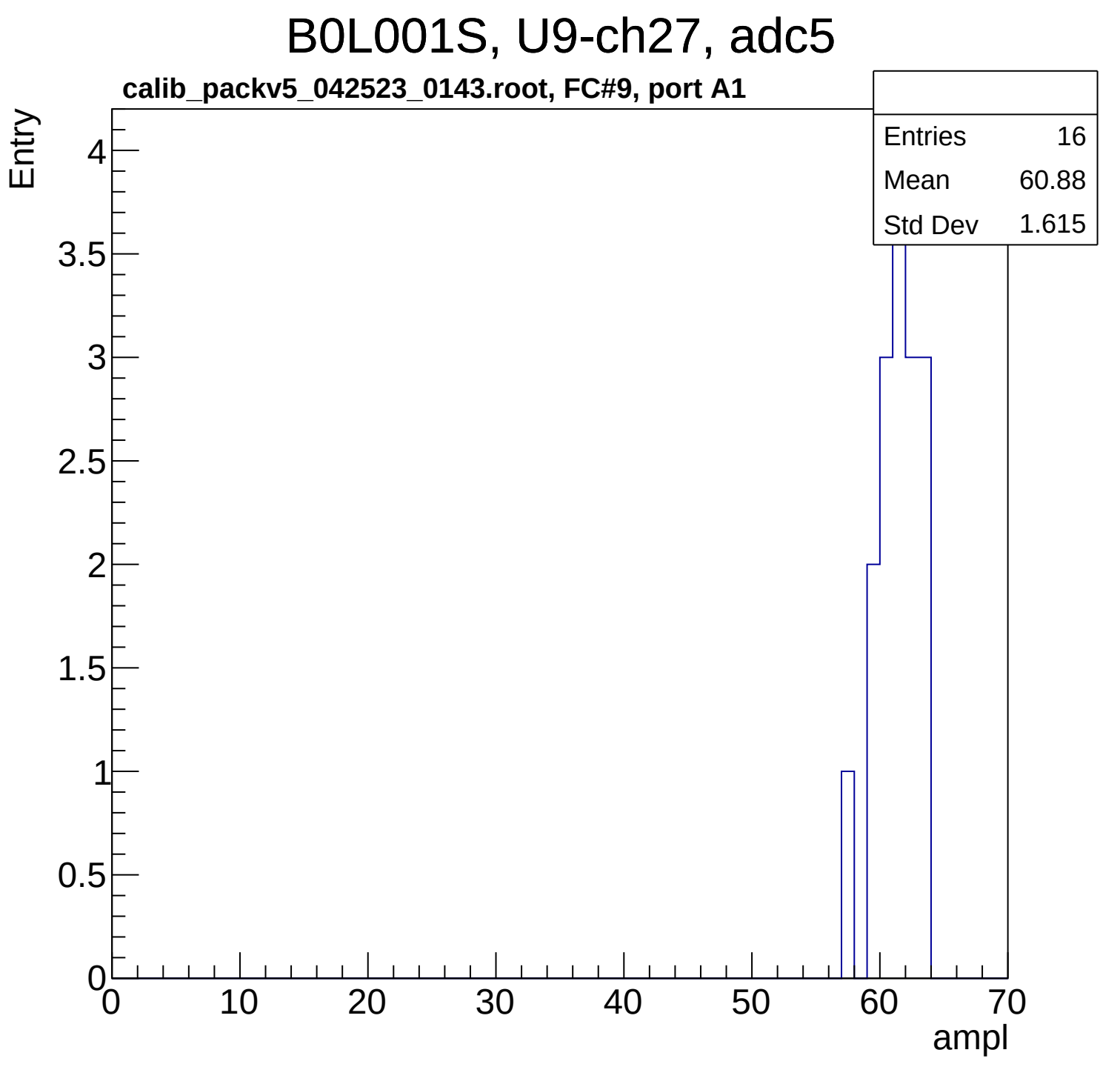
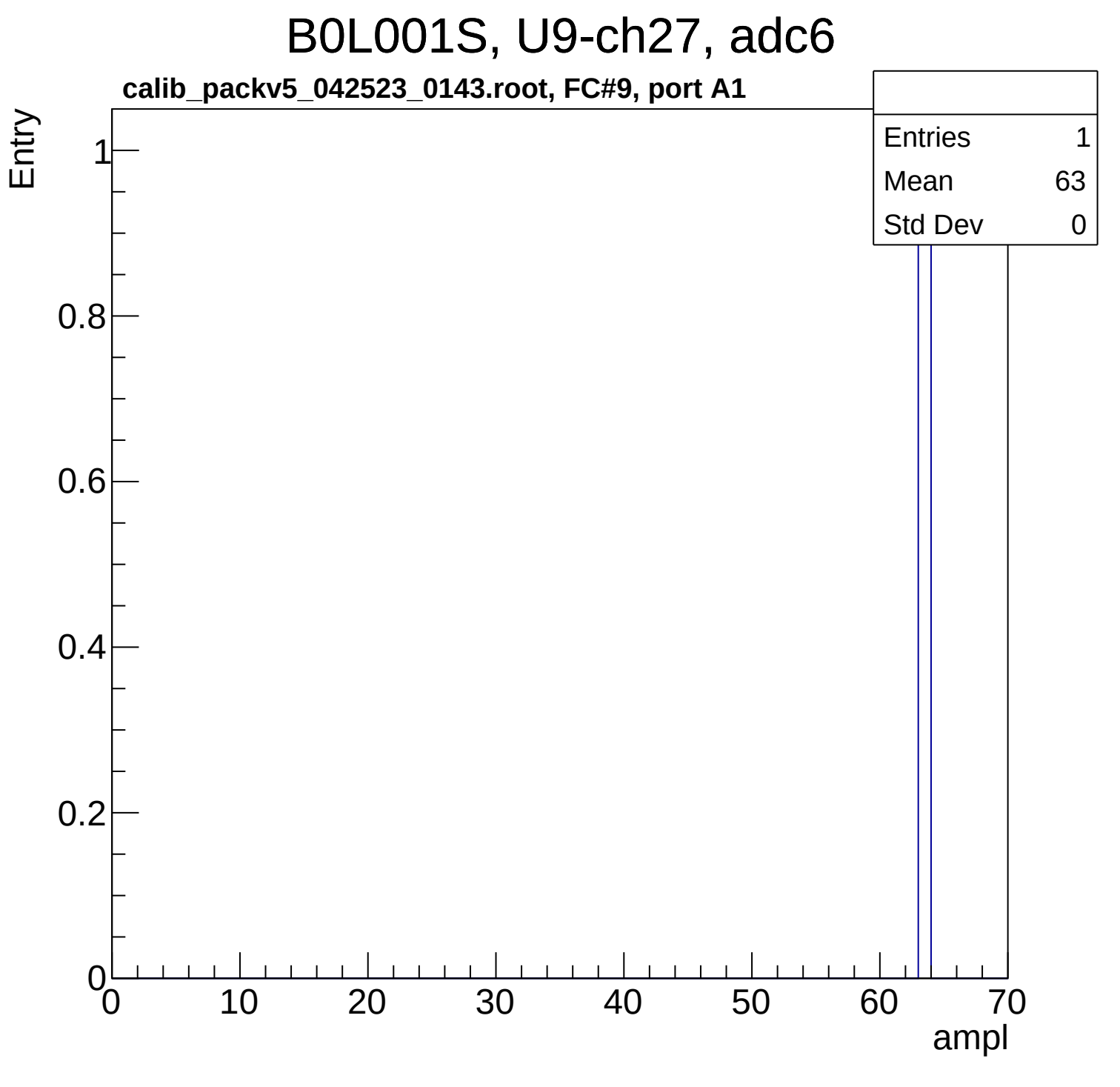
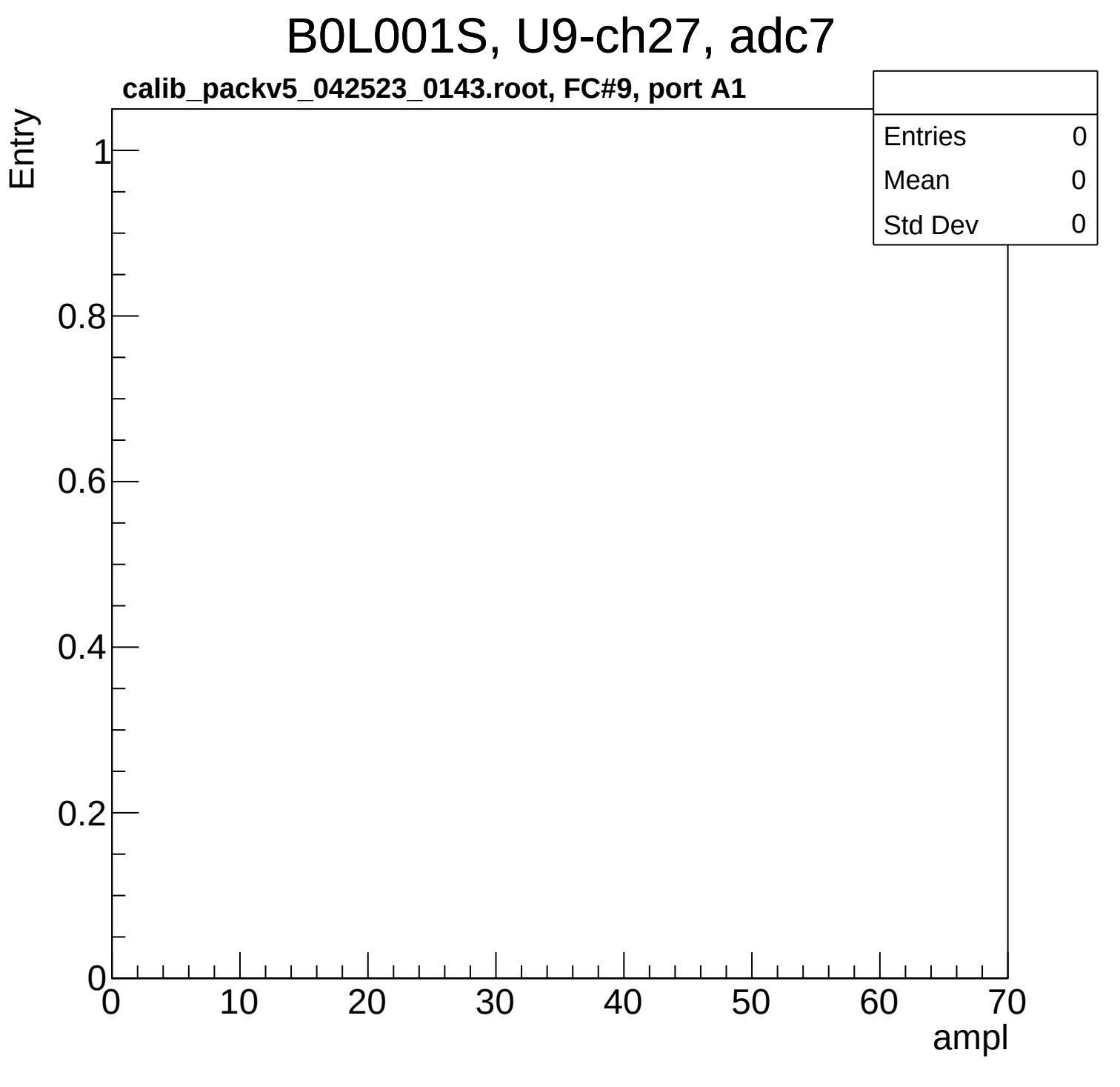

B0L001S, U9-ch27, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 46.66
Std Dev 3.096
Gaus mean : 47.3254
Gaus Width: 3.0022
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch27, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 52.32
Std Dev 3.521
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch27, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.66
Std Dev 8.035
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch27, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 60.88
Std Dev 1.615
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U9-ch27, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch27, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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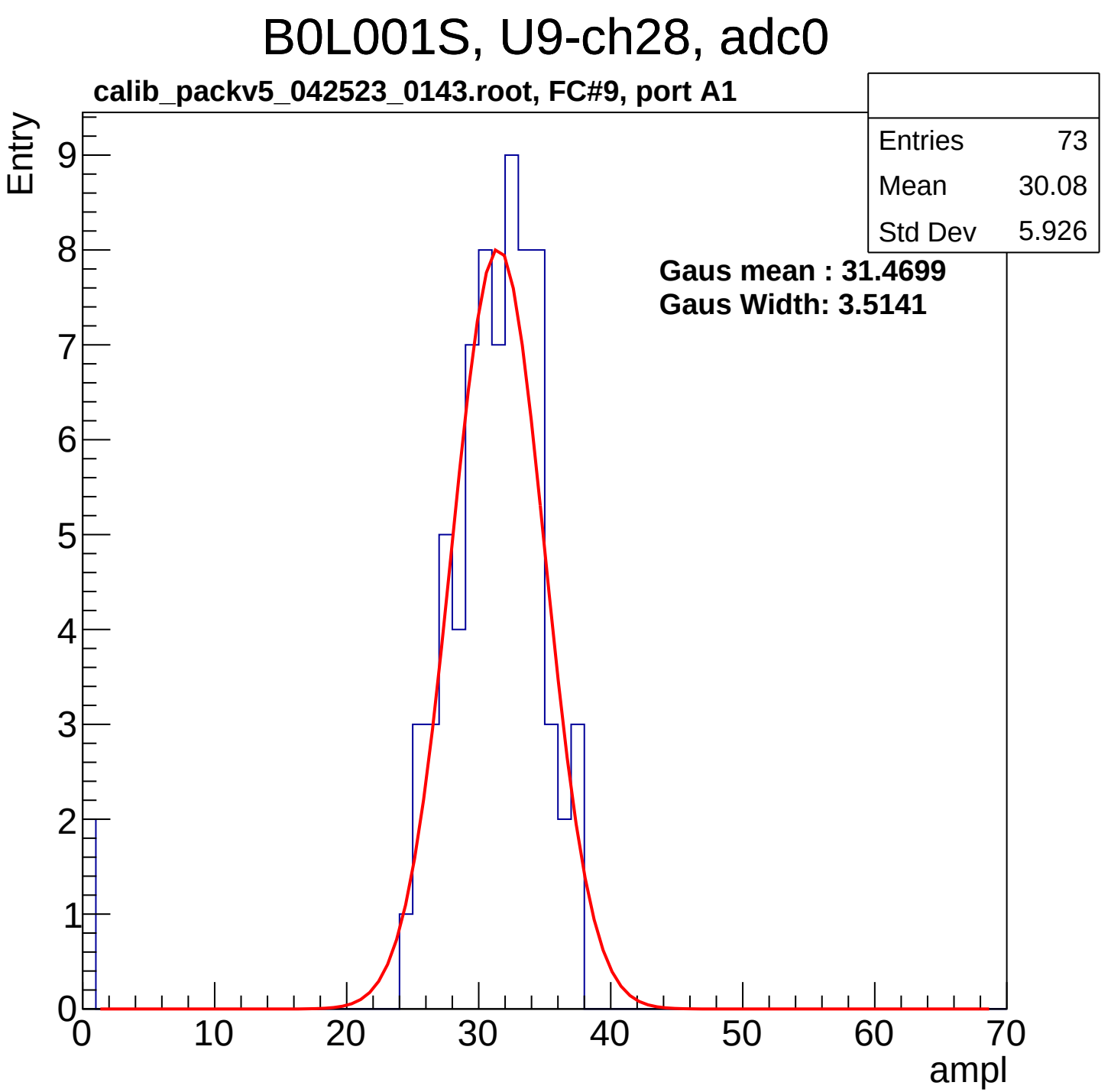
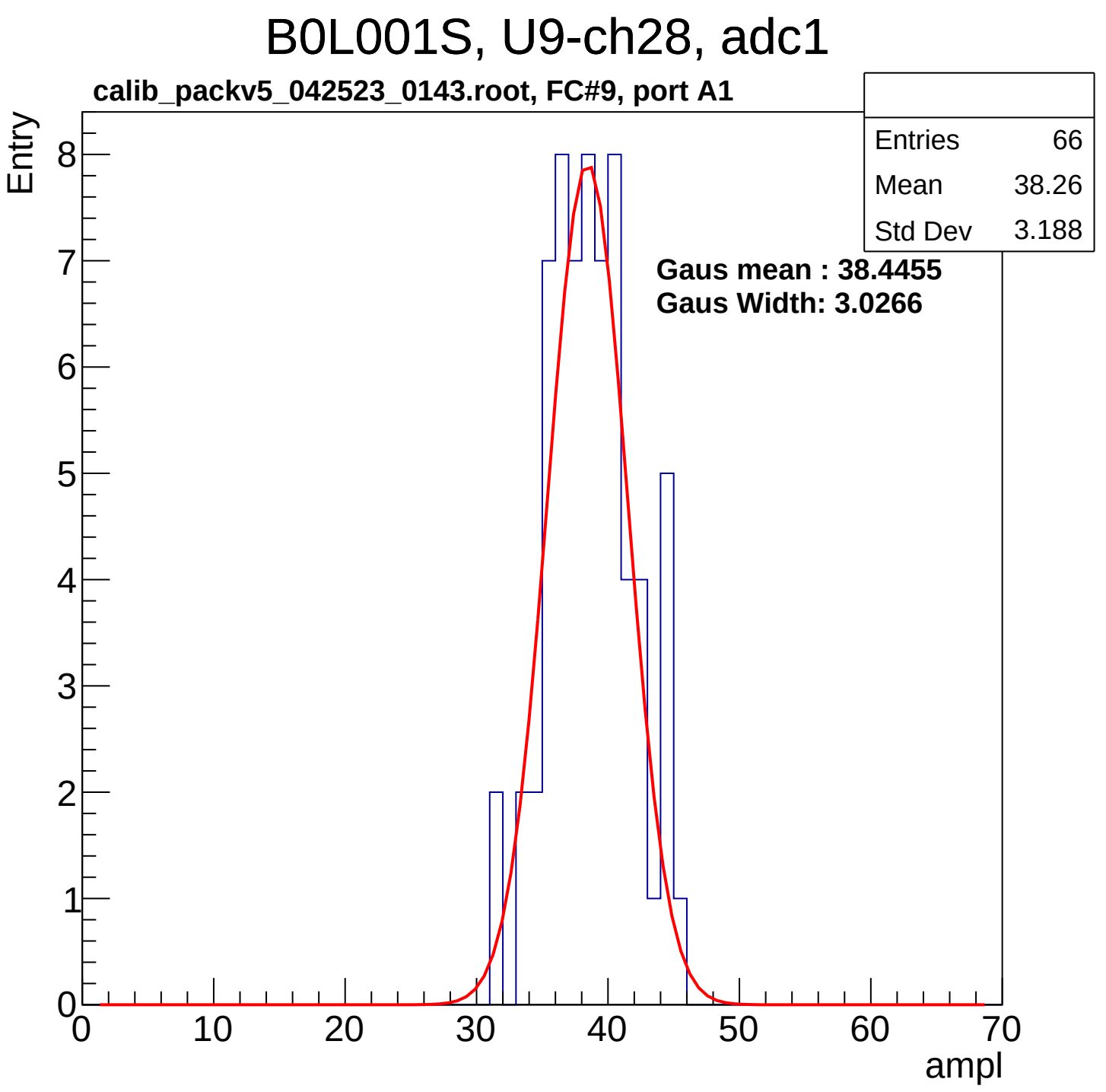
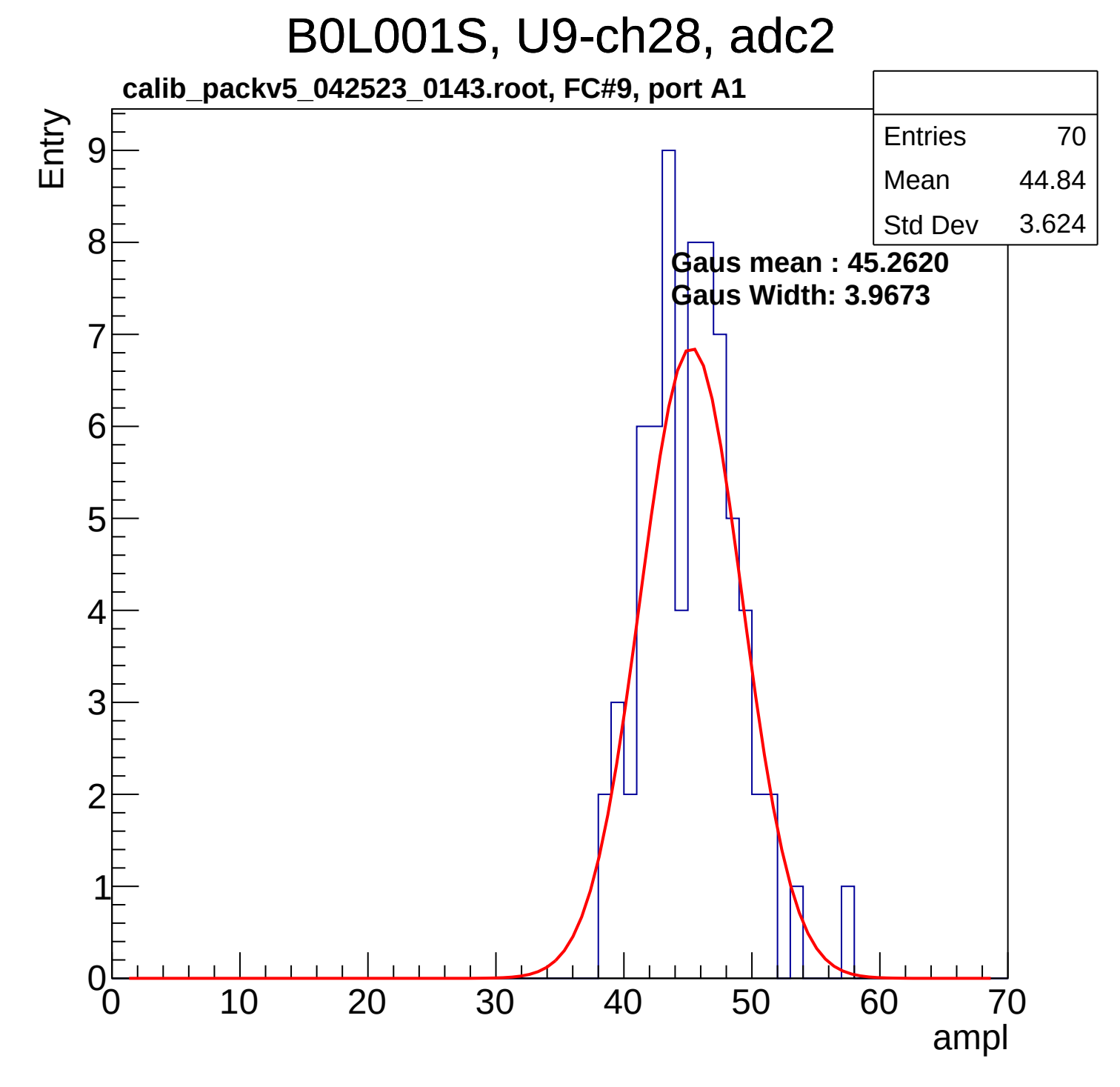
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch28, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.08
Std Dev 5.926
Gaus mean : 31.4699
Gaus Width: 3.5141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch28, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.26
Std Dev 3.188
Gaus mean : 38.4455
Gaus Width: 3.0266
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch28, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.84
Std Dev 3.624
Gaus mean : 45.2620
Gaus Width: 3.9673
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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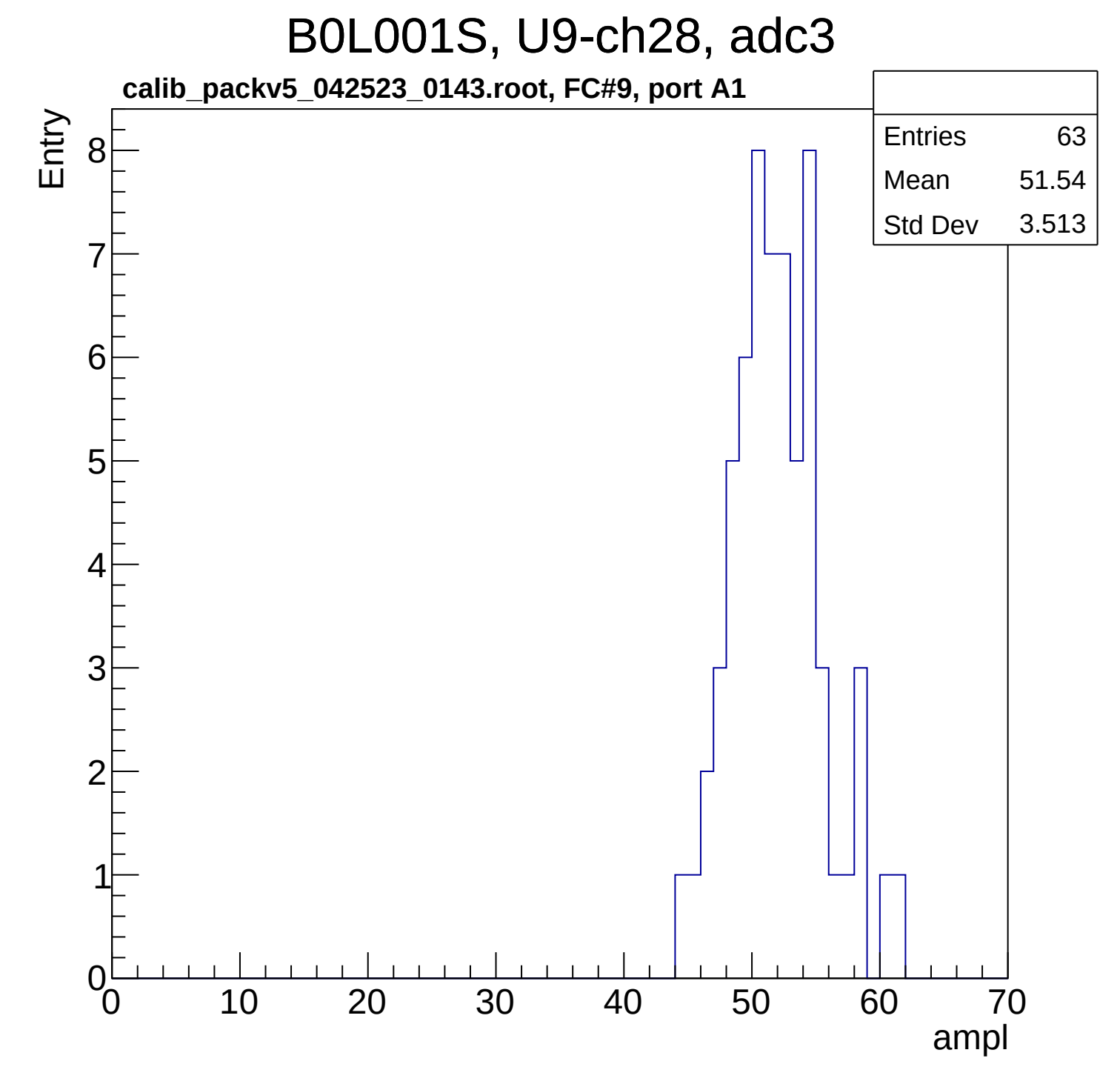
B0L001S, U9-ch28, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.54
Std Dev 3.513
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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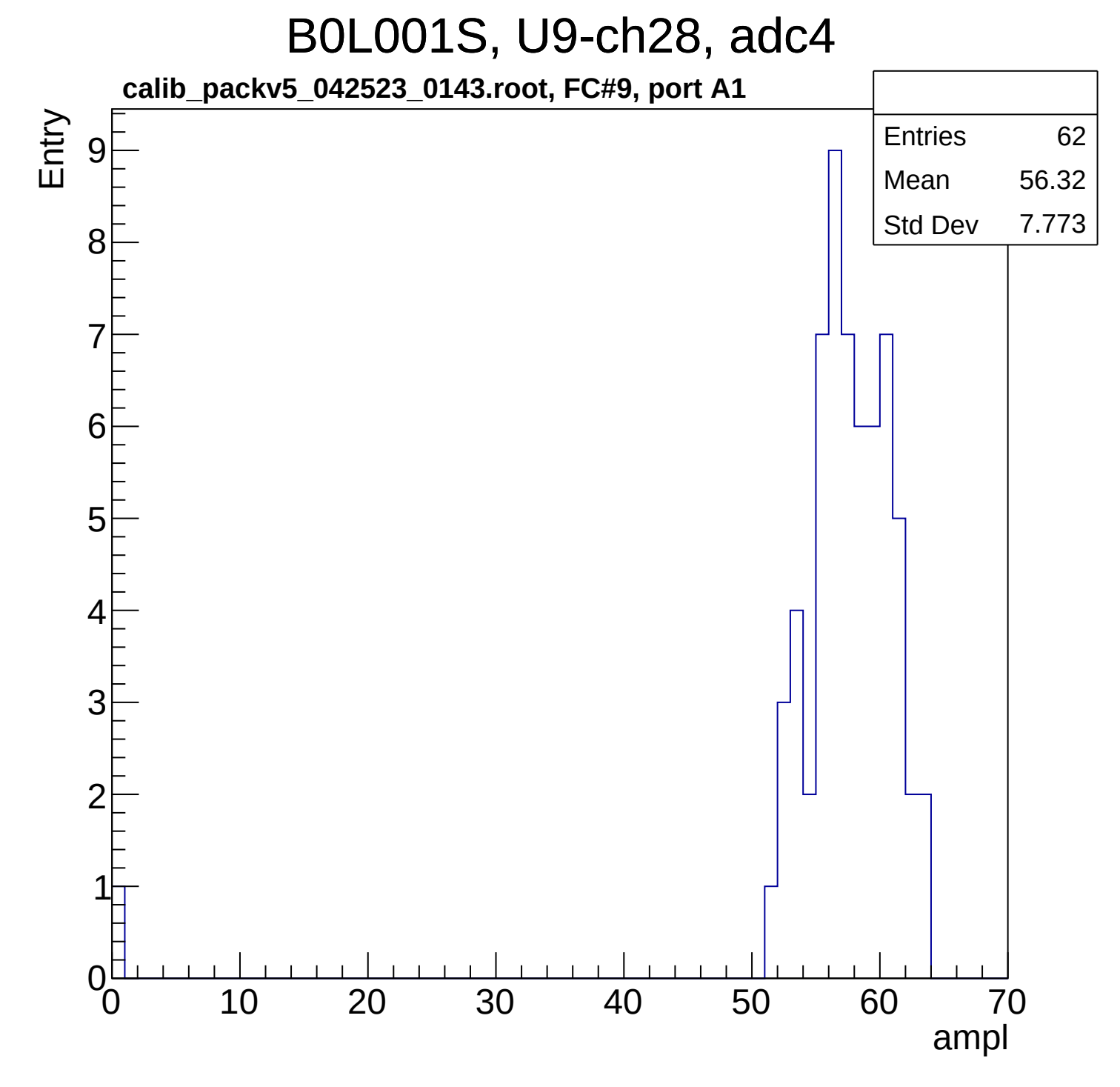
B0L001S, U9-ch28, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.32
Std Dev 7.773
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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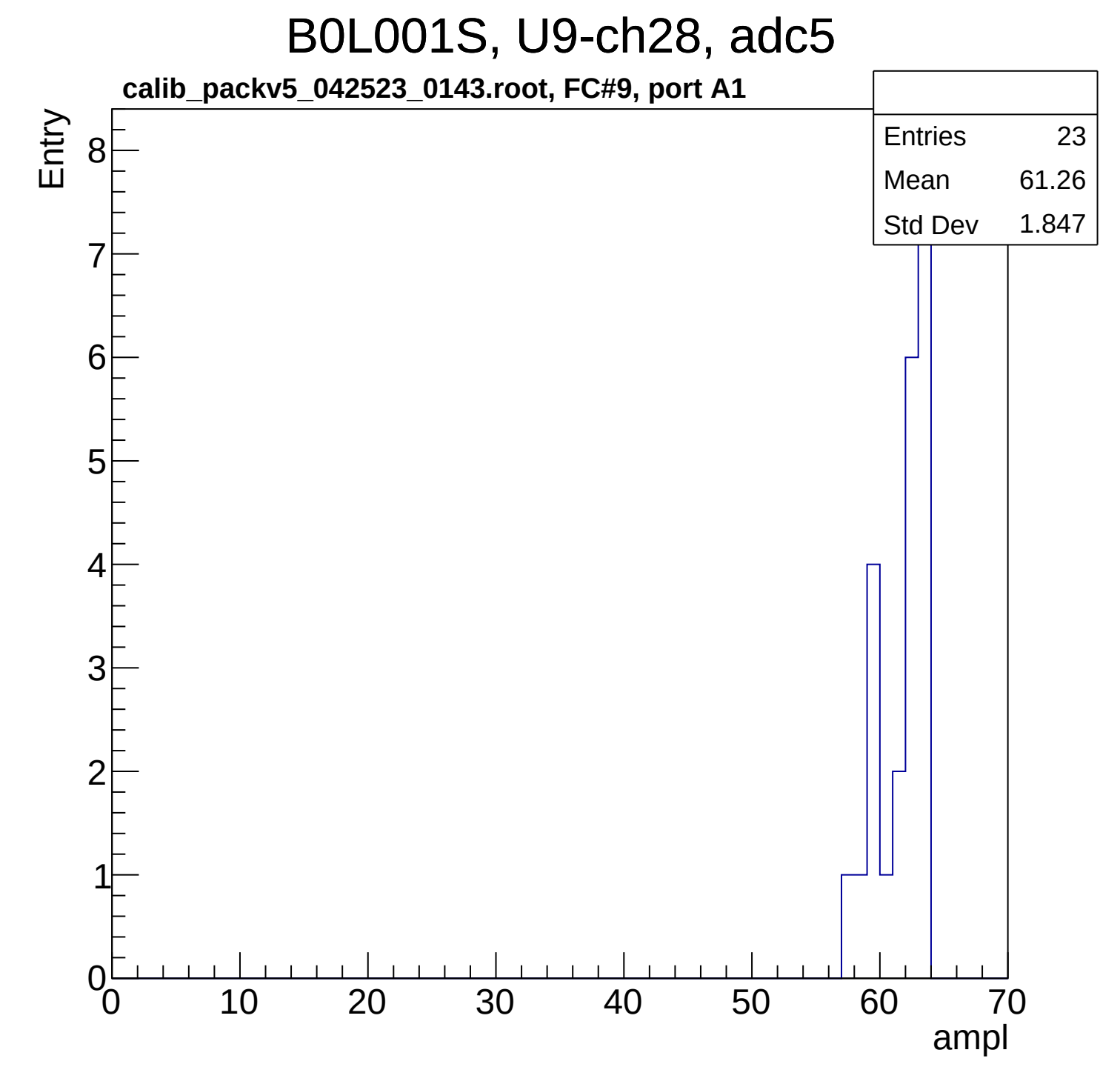
B0L001S, U9-ch28, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 61.26
Std Dev 1.847
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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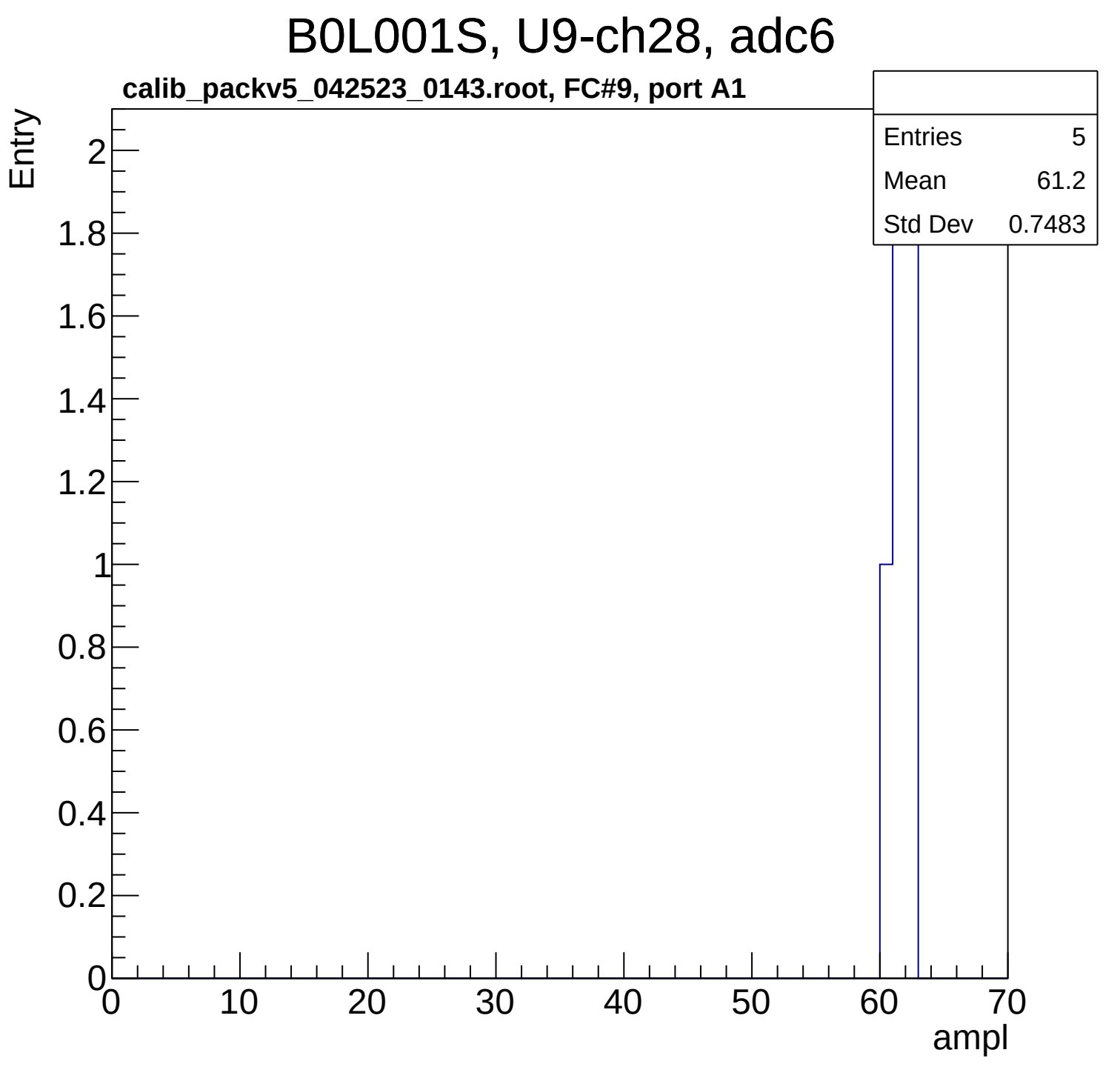
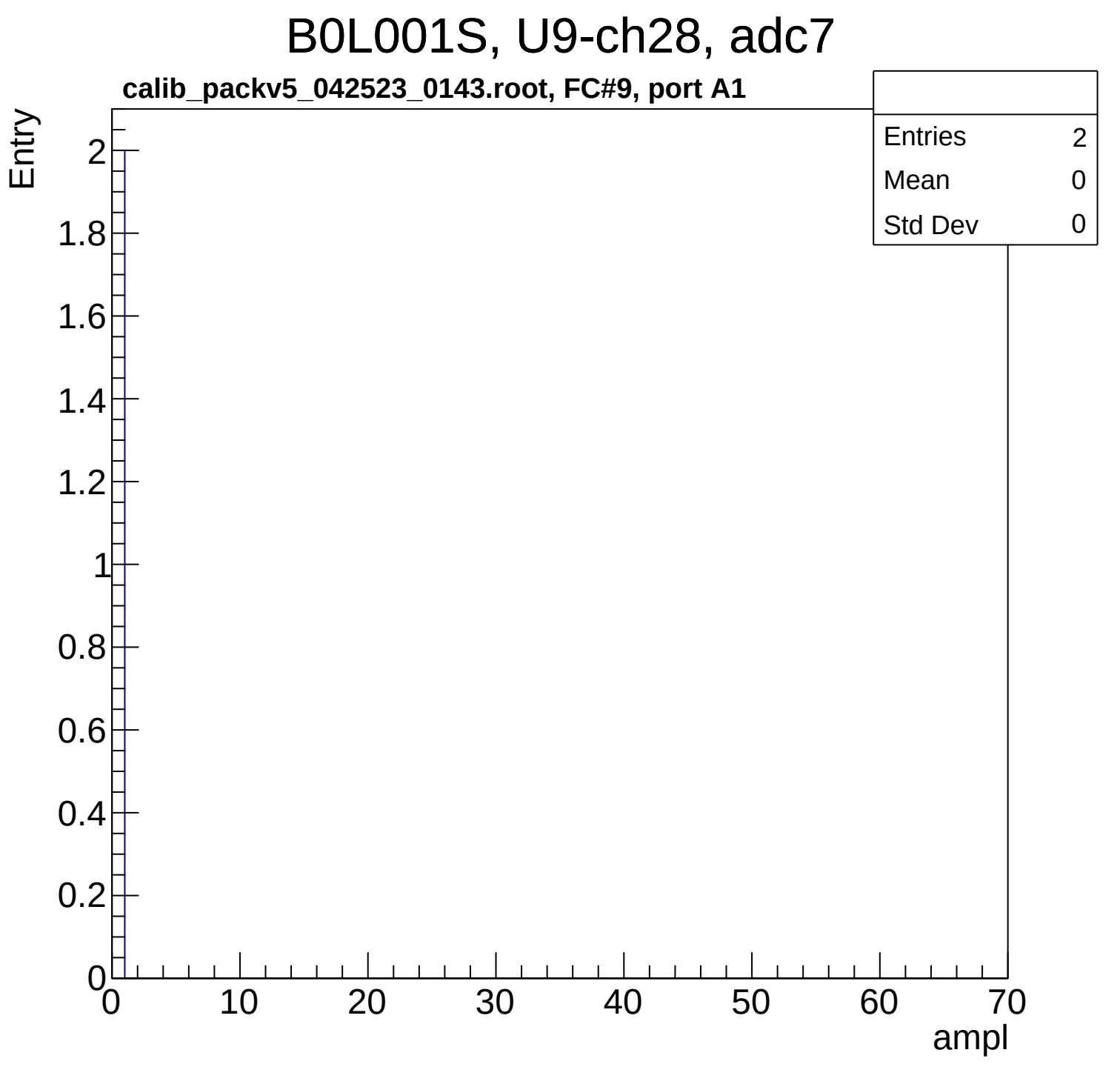
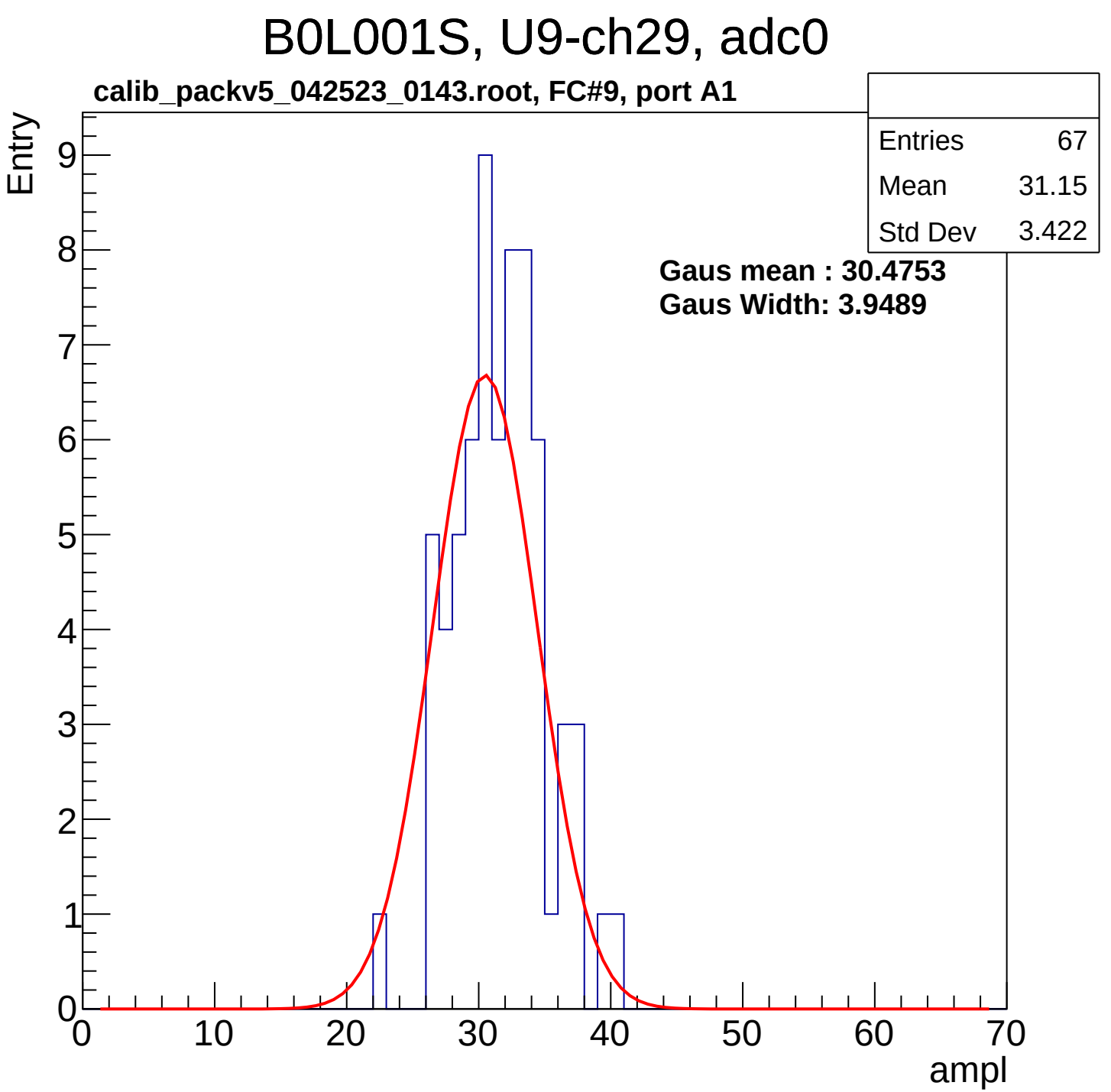

B0L001S, U9-ch28, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch28, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch29, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.15
Std Dev 3.422
Gaus mean : 30.4753
Gaus Width: 3.9489
Entry
ampl
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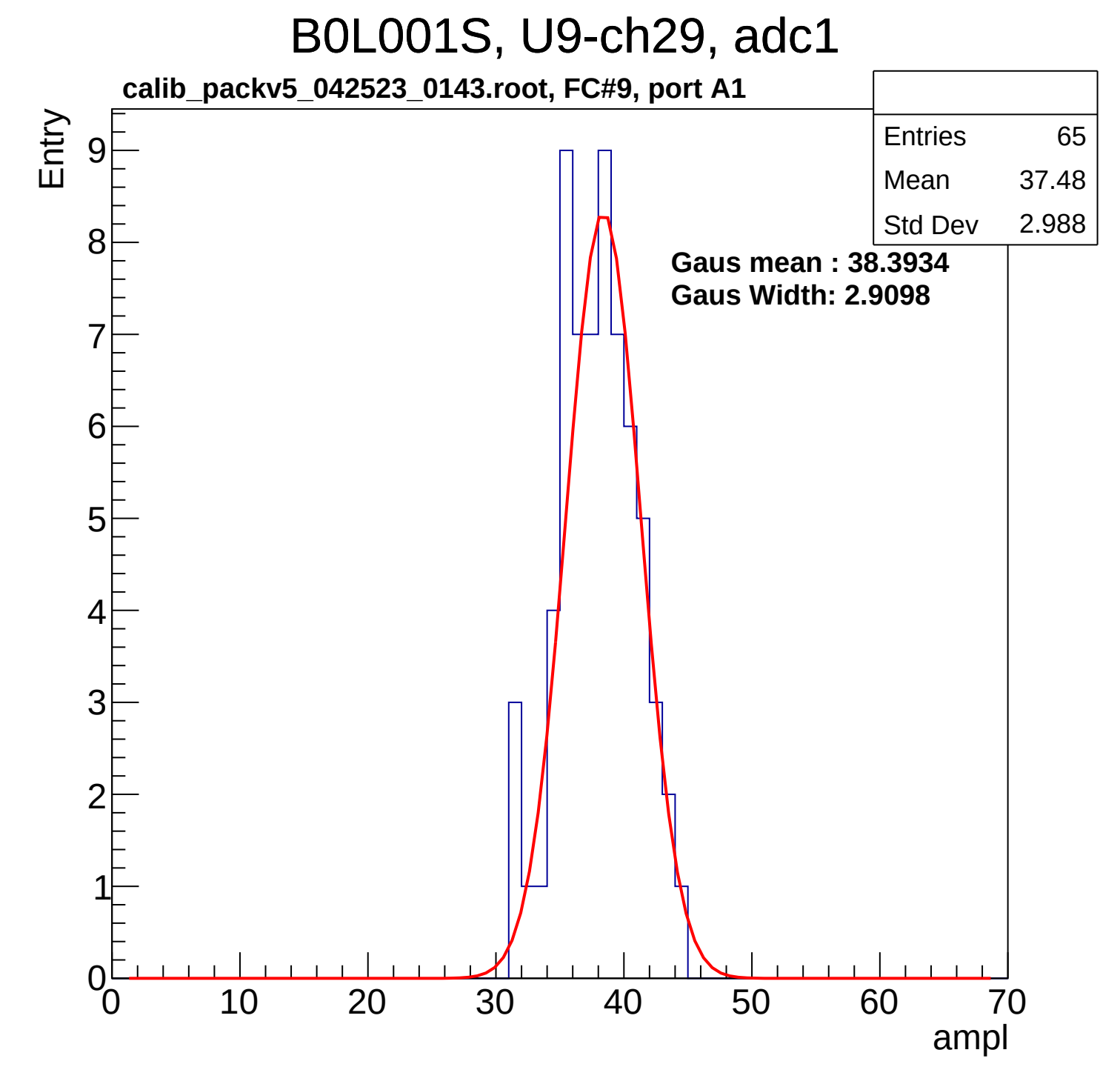
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch29, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 37.48
Std Dev 2.988
Gaus mean : 38.3934
Gaus Width: 2.9098
Entry
ampl
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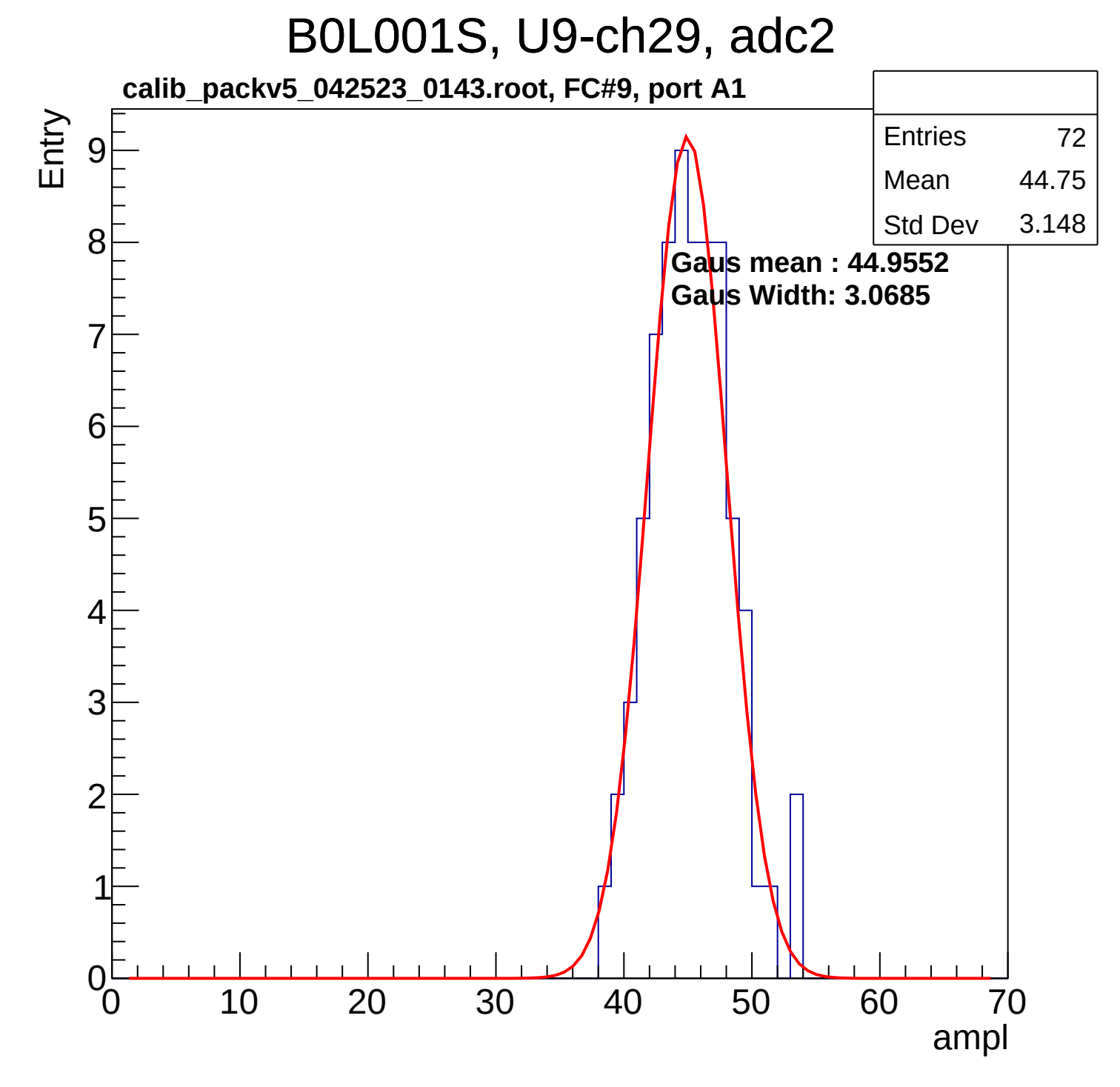
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U9-ch29, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.75
Std Dev 3.148
Gaus mean : 44.9552
Gaus Width: 3.0685
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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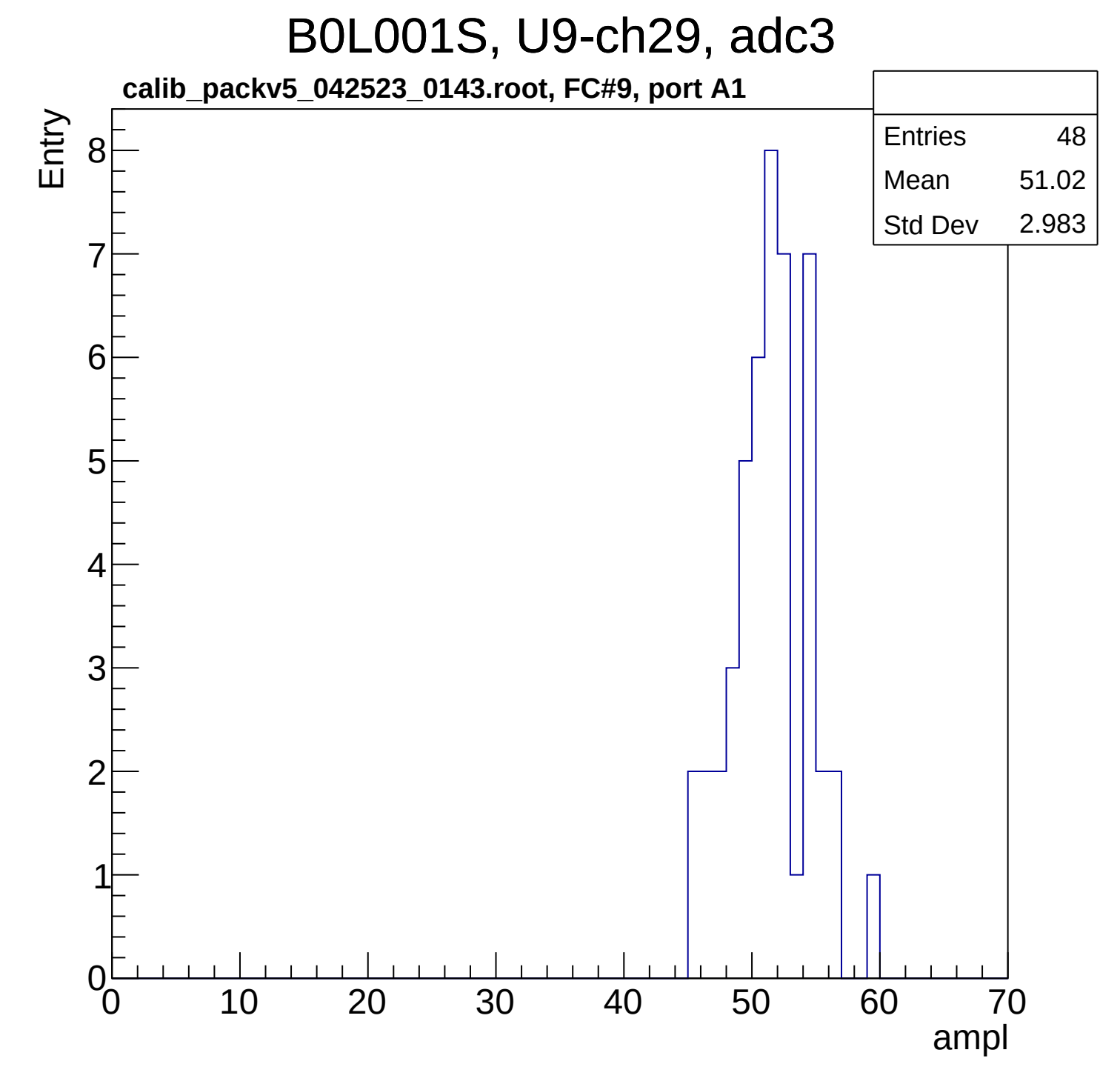
B0L001S, U9-ch29, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 51.02
Std Dev 2.983
Entry
ampl
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2
3
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5
6
7
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0
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60
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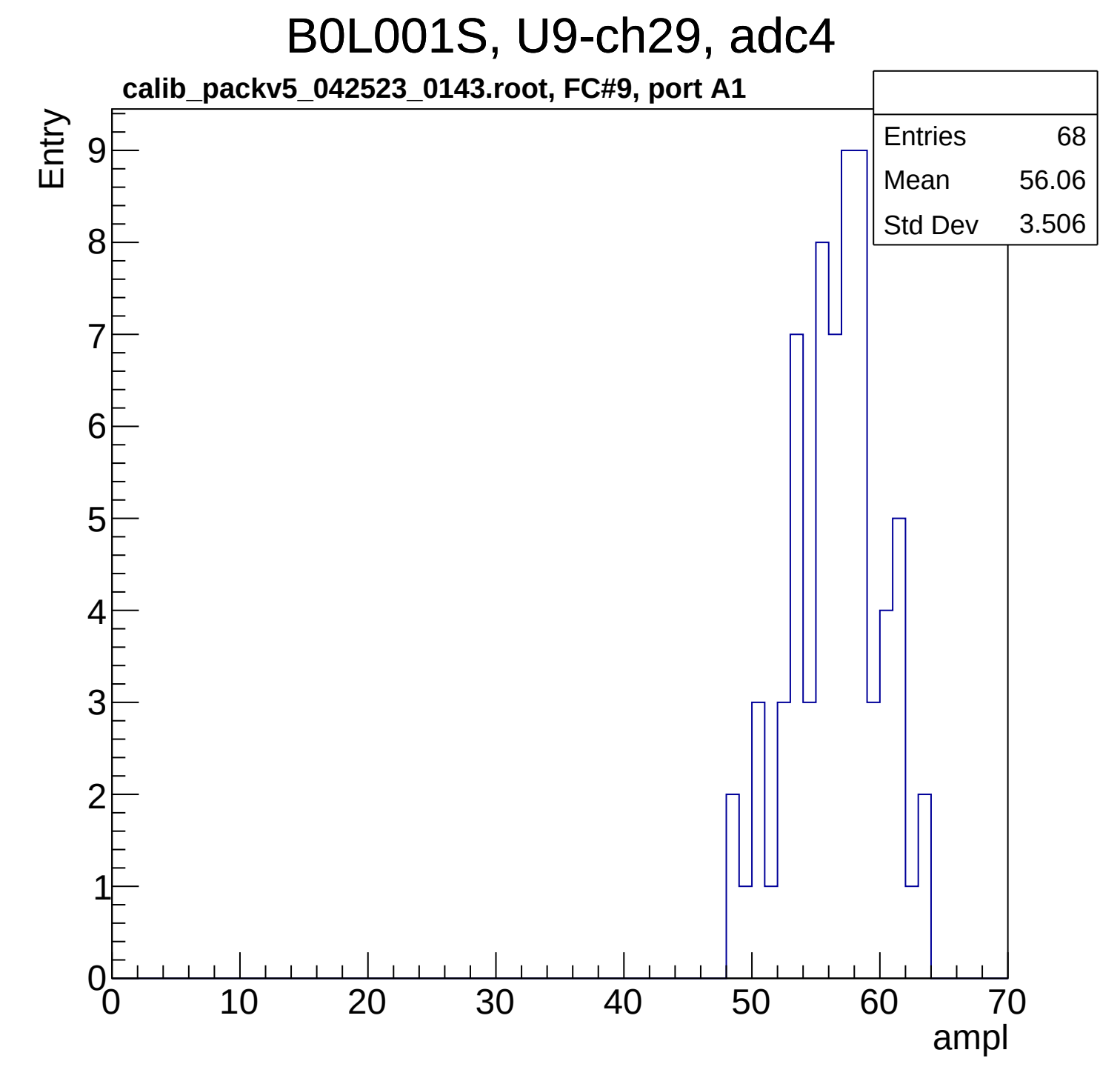
B0L001S, U9-ch29, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 56.06
Std Dev 3.506
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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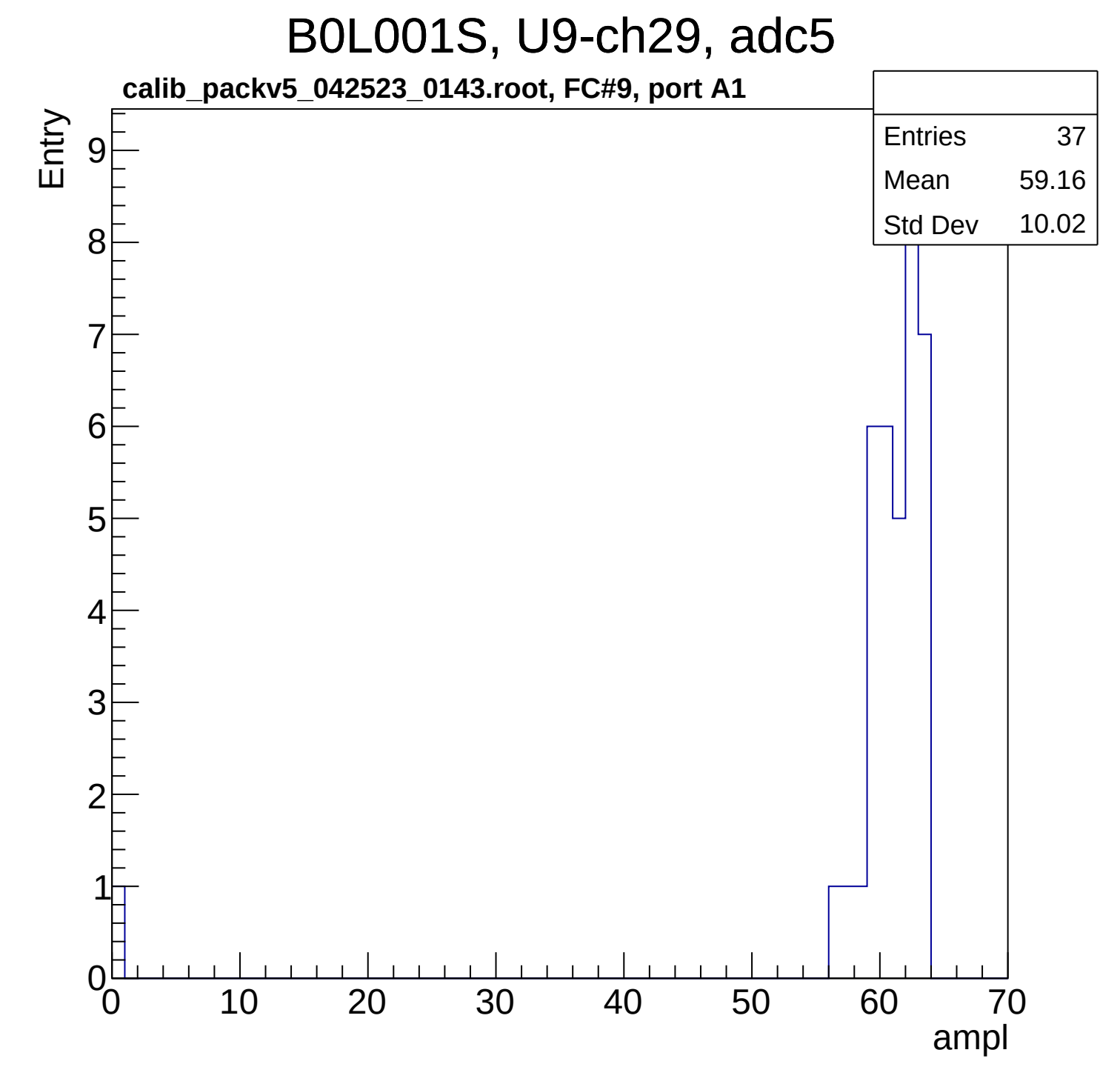
B0L001S, U9-ch29, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.16
Std Dev 10.02
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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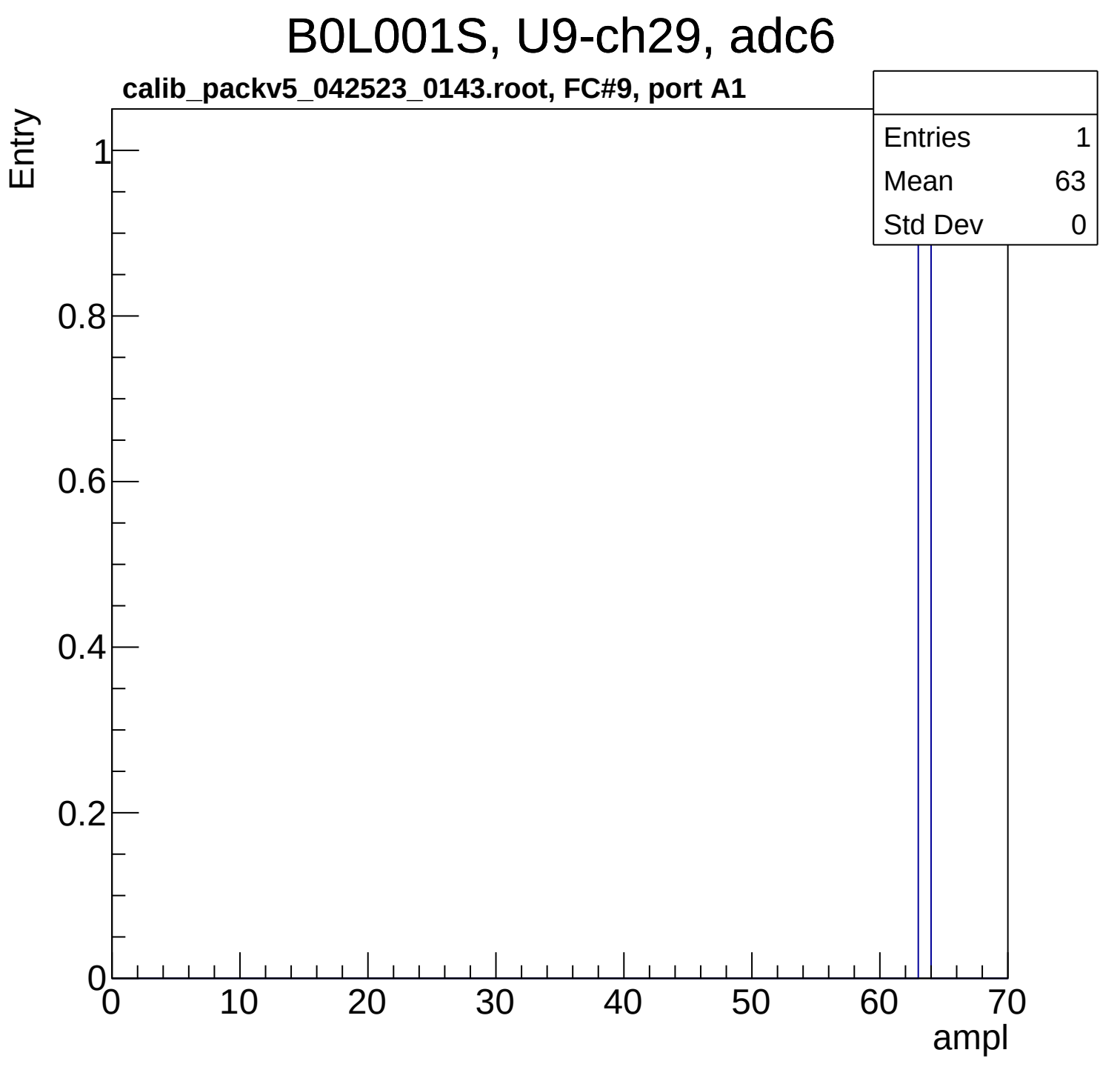
B0L001S, U9-ch29, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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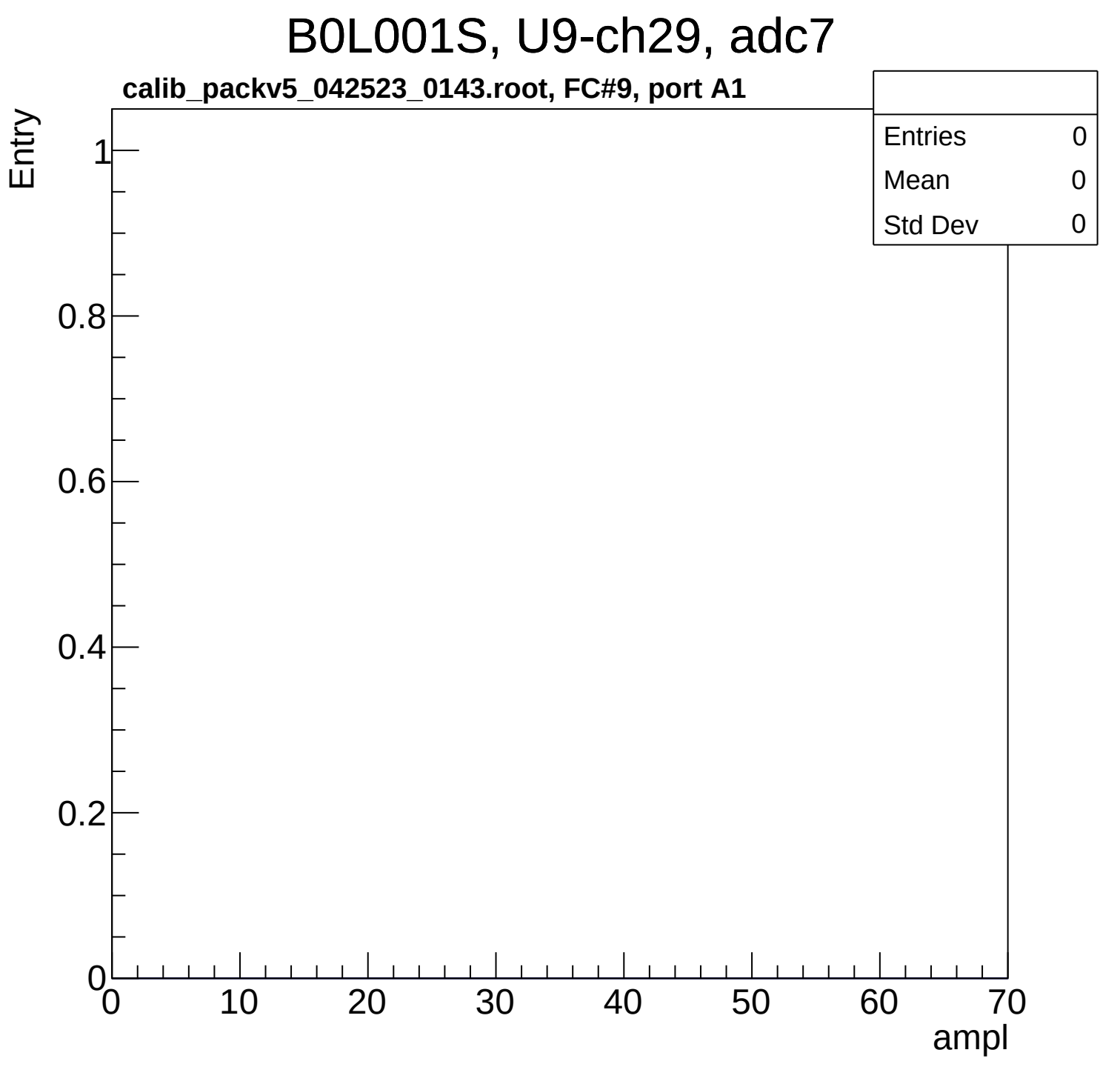
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch29, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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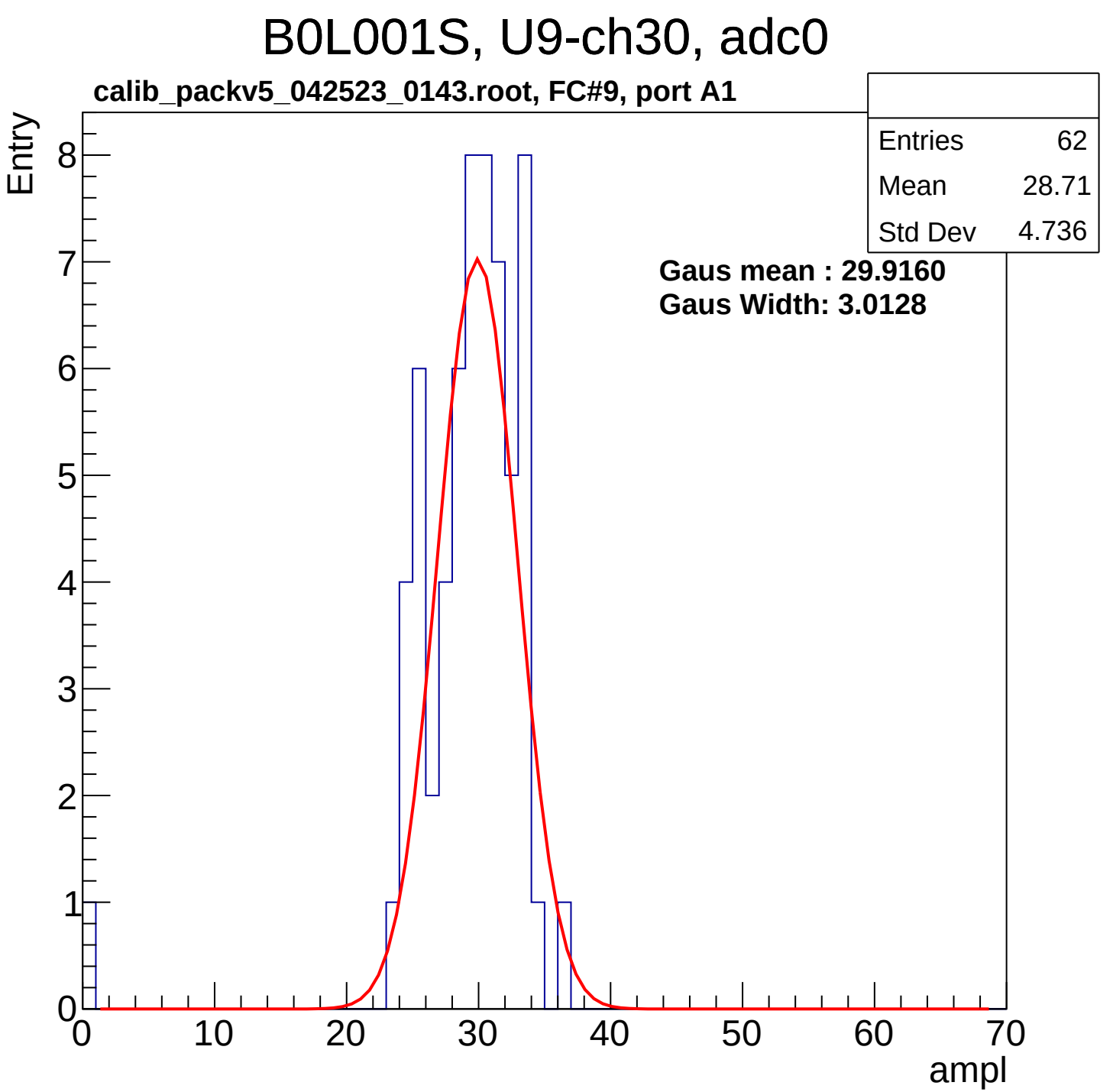
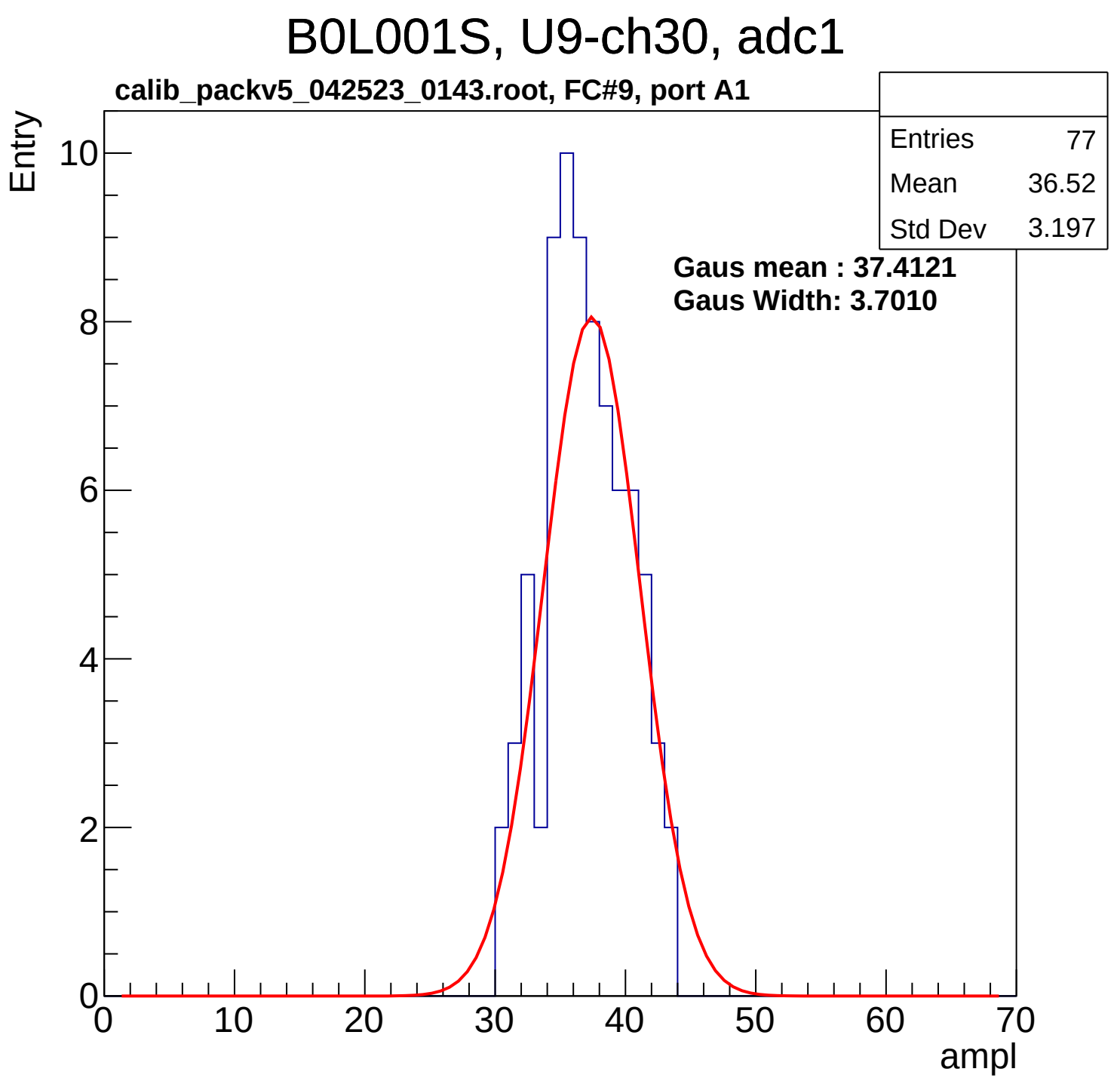
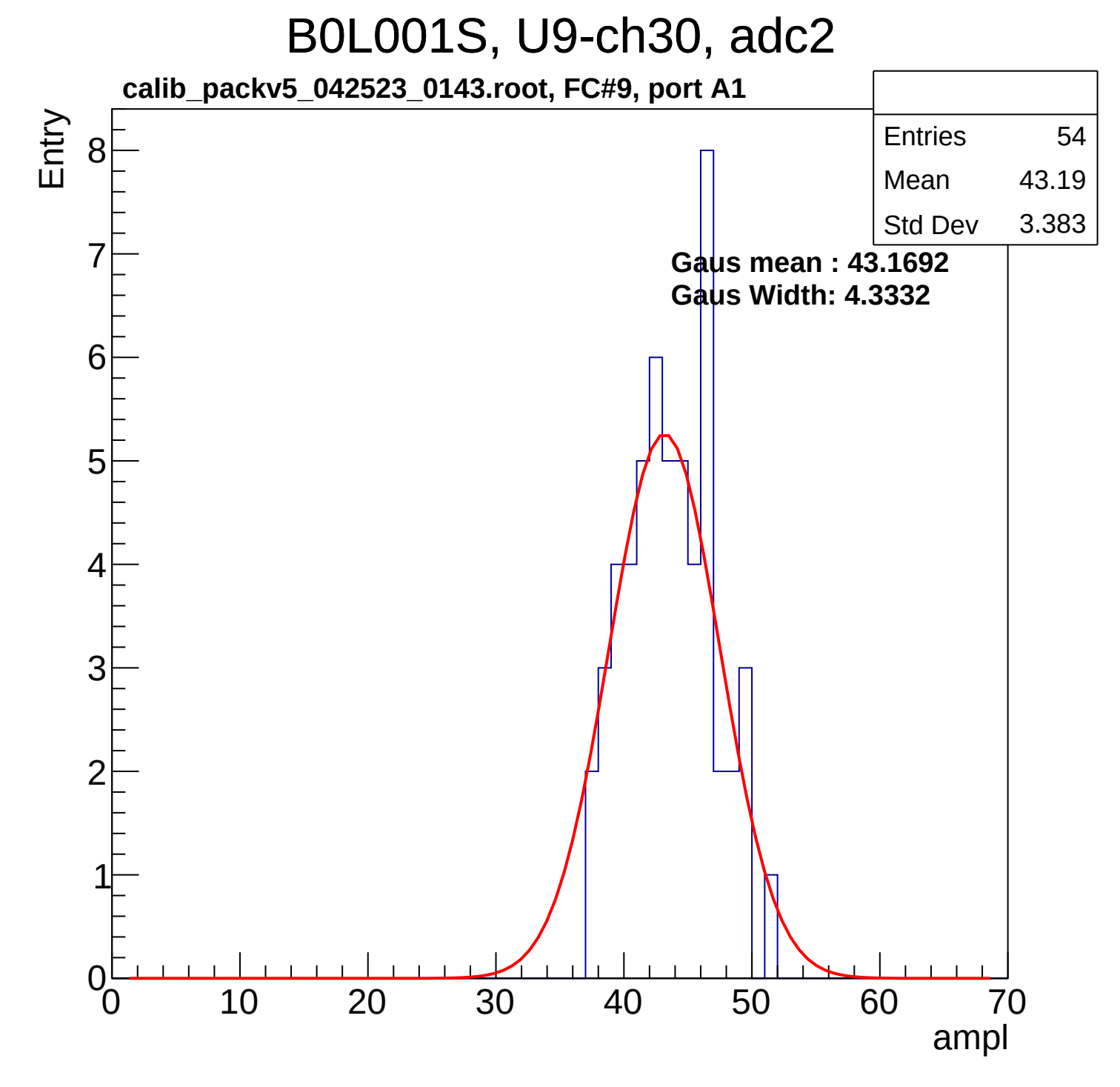
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch30, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 28.71
Std Dev 4.736
Gaus mean : 29.9160
Gaus Width: 3.0128
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch30, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 36.52
Std Dev 3.197
Gaus mean : 37.4121
Gaus Width: 3.7010
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch30, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 43.19
Std Dev 3.383
Gaus mean : 43.1692
Gaus Width: 4.3332
Entry
ampl
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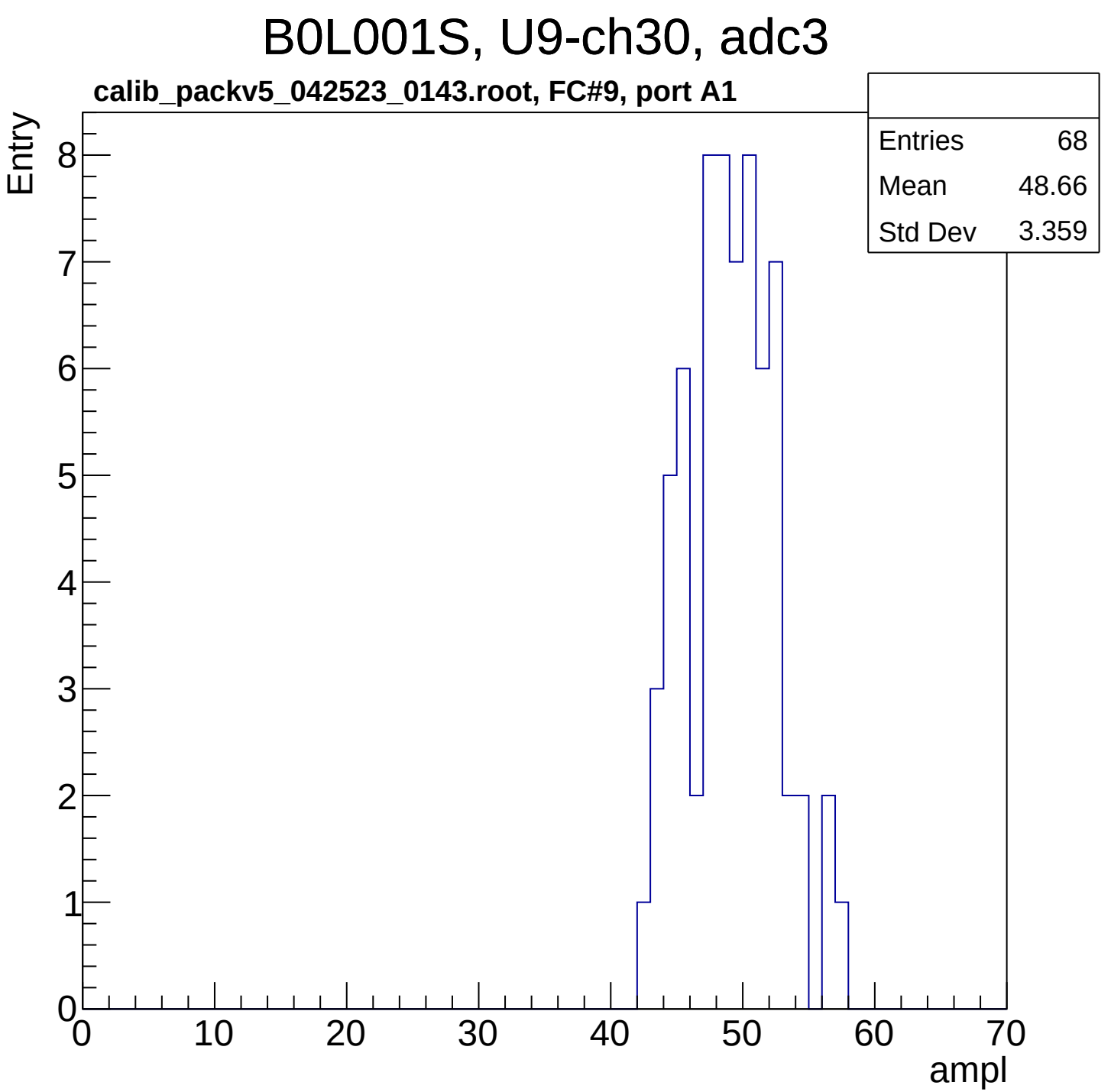
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch30, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 48.66
Std Dev 3.359
Entry
ampl
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1
2
3
4
5
6
7
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60
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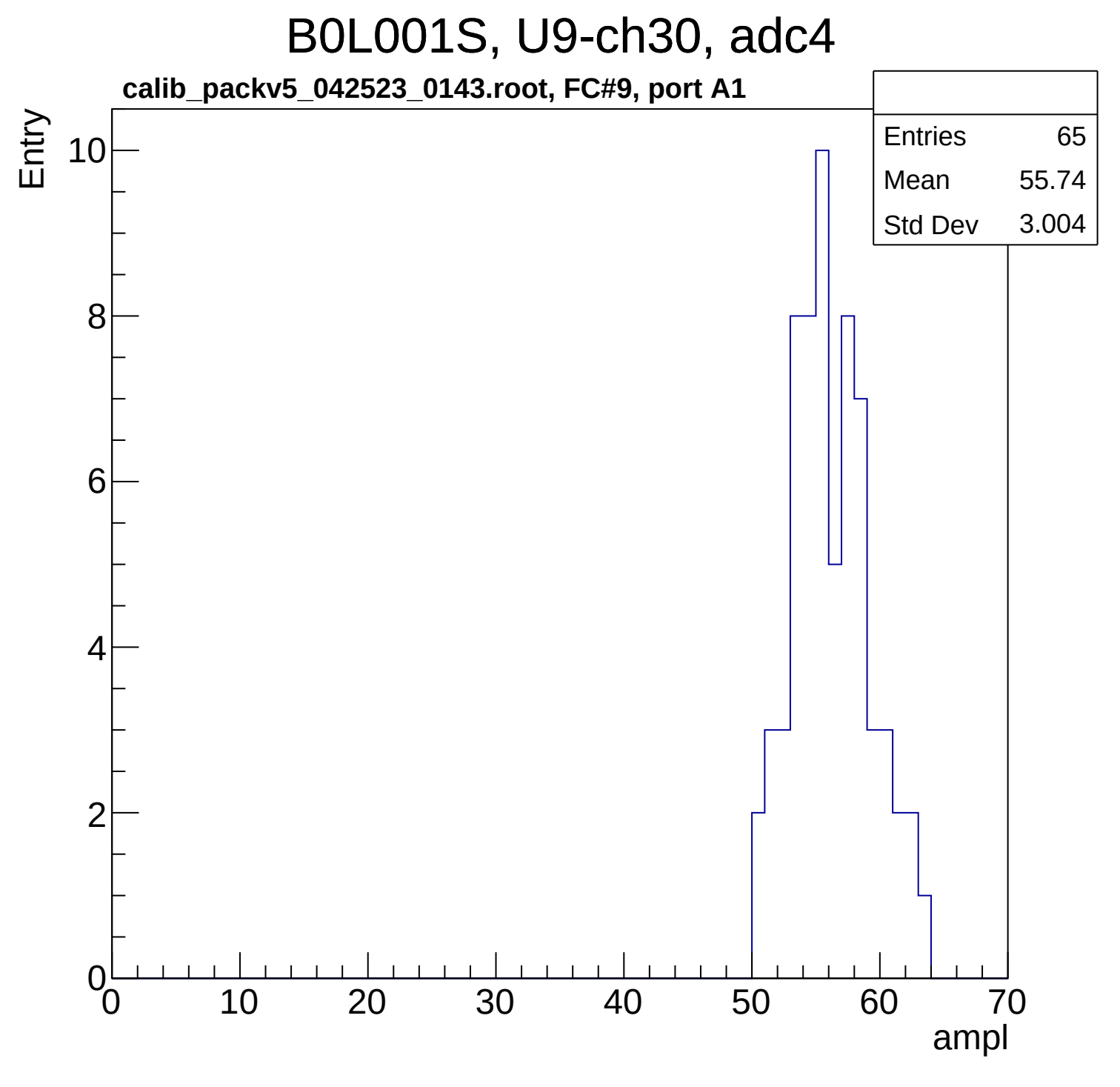
B0L001S, U9-ch30, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 55.74
Std Dev 3.004
Entry
ampl
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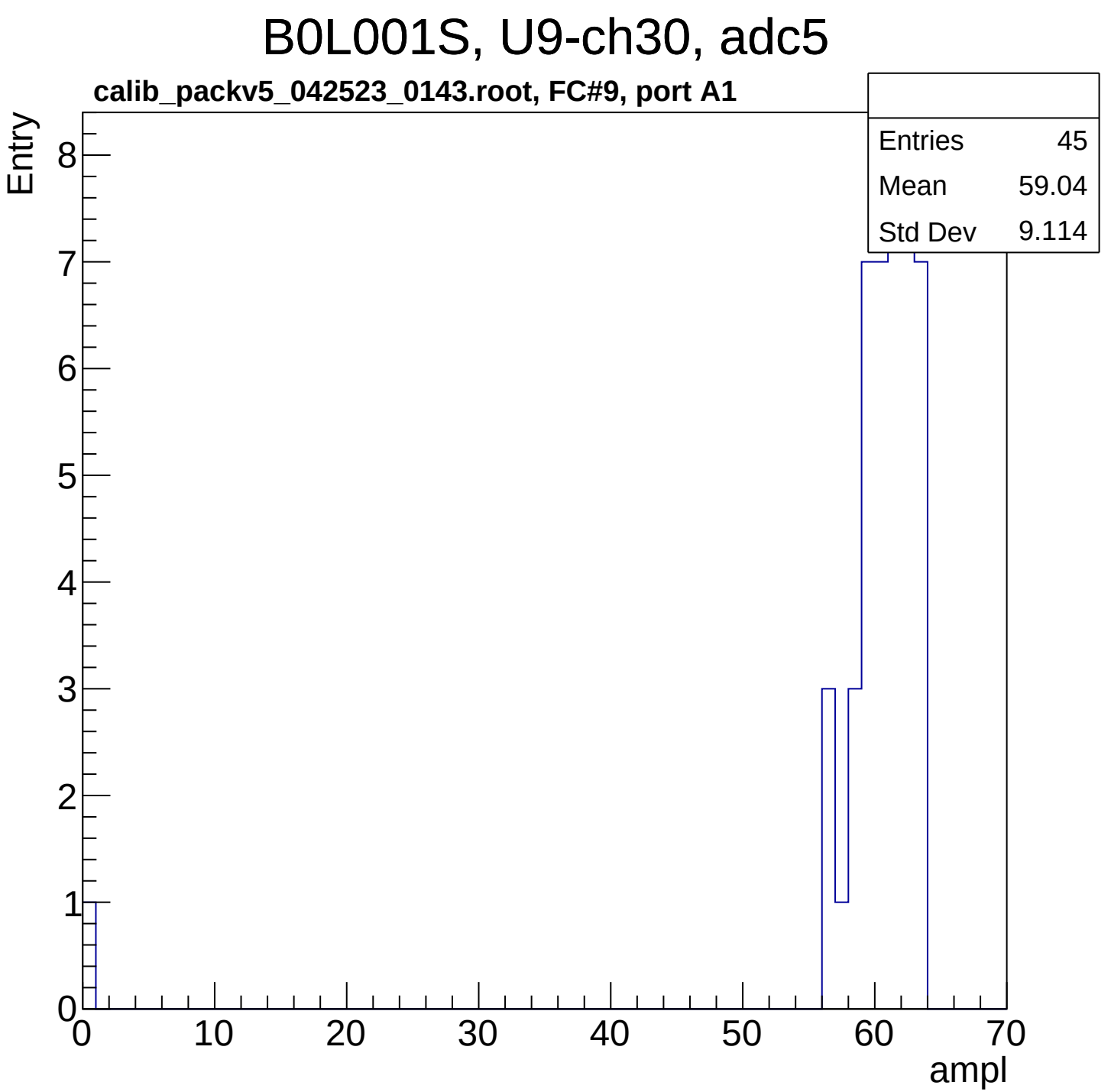
B0L001S, U9-ch30, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 59.04
Std Dev 9.114
Entry
ampl
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4
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40
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60
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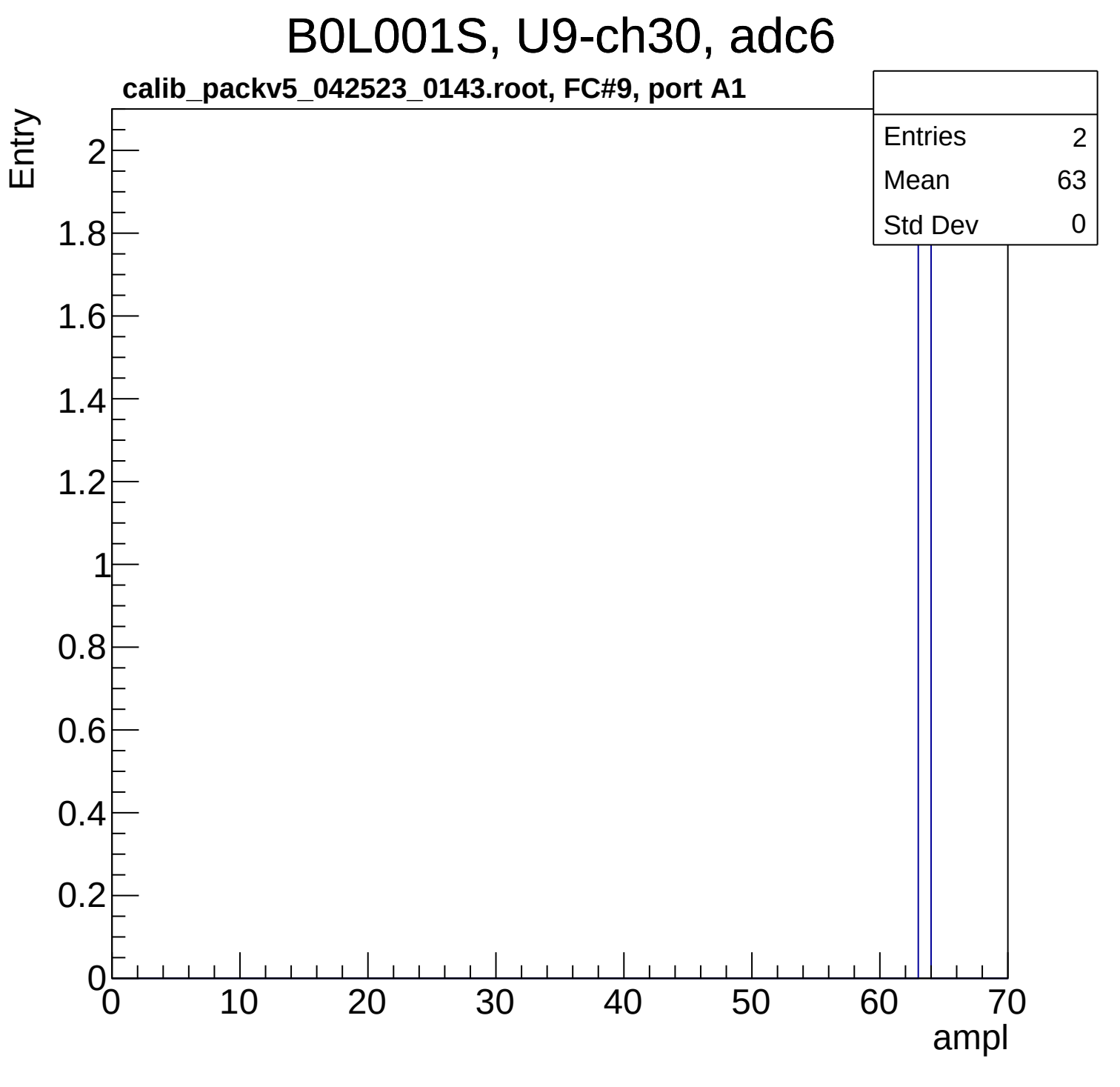
B0L001S, U9-ch30, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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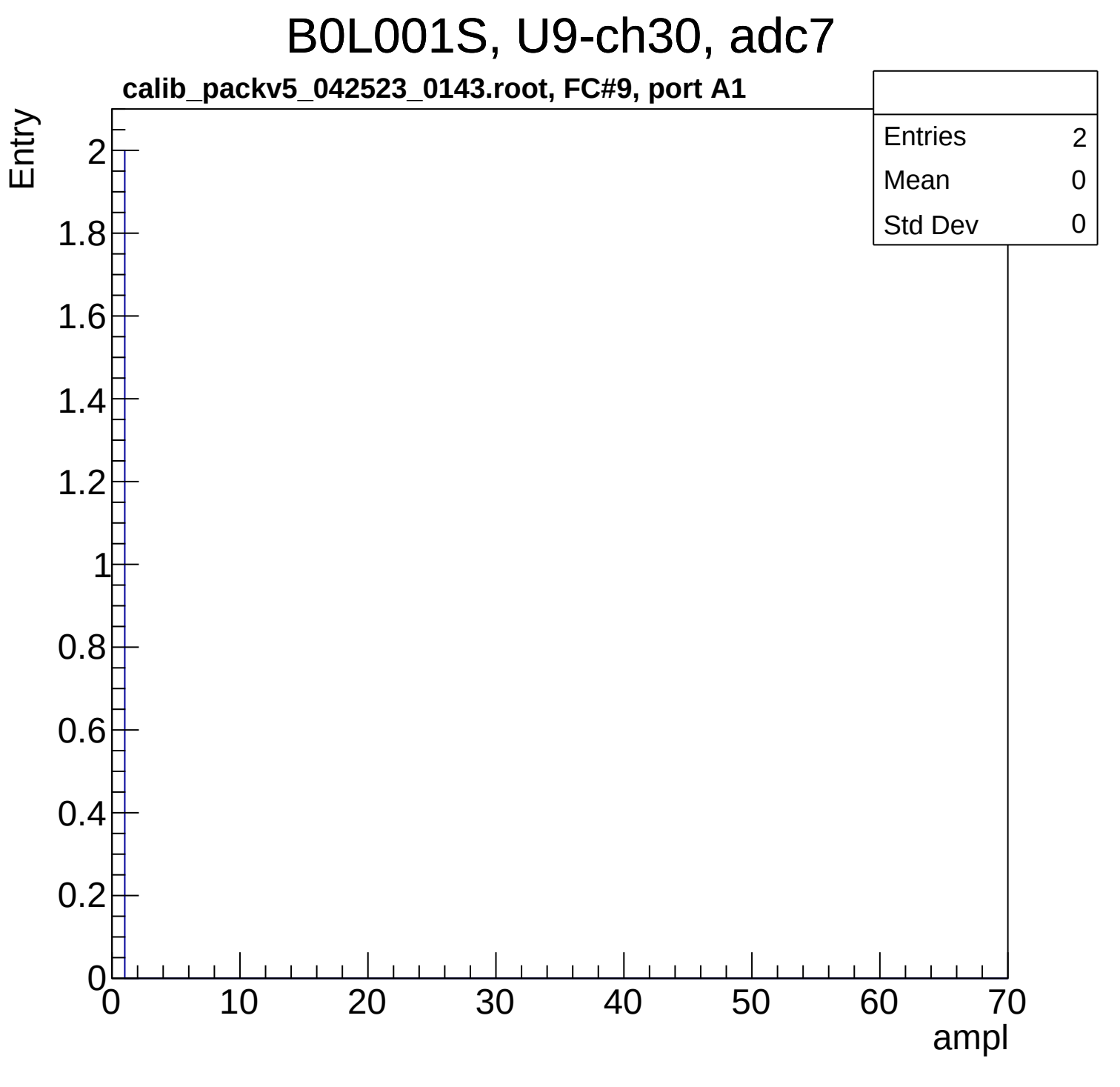
40
50
60
70


B0L001S, U9-ch30, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
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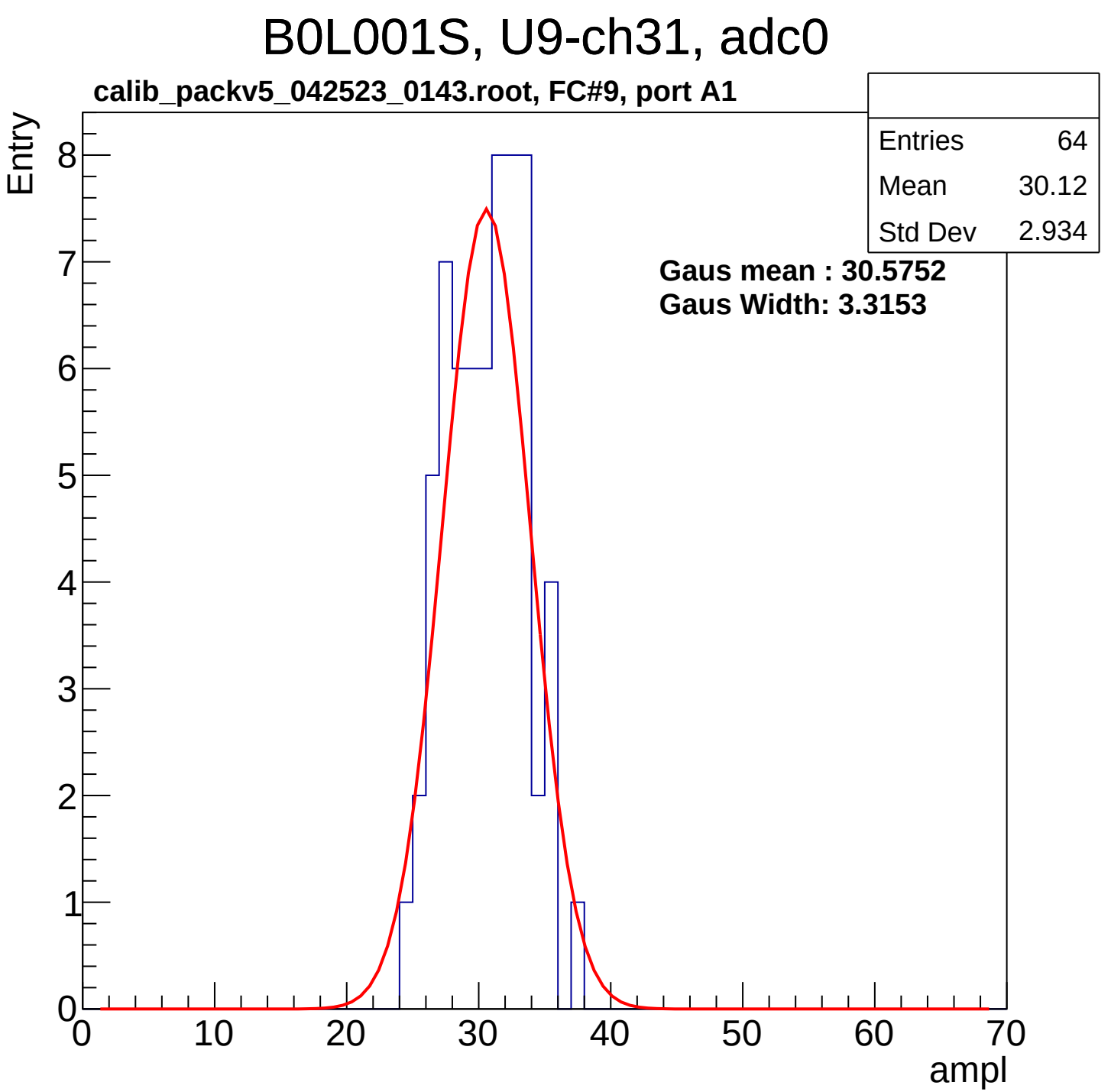
50
60
70
ampl


B0L001S, U9-ch31, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.12
Std Dev 2.934
Gaus mean : 30.5752
Gaus Width: 3.3153
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
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60
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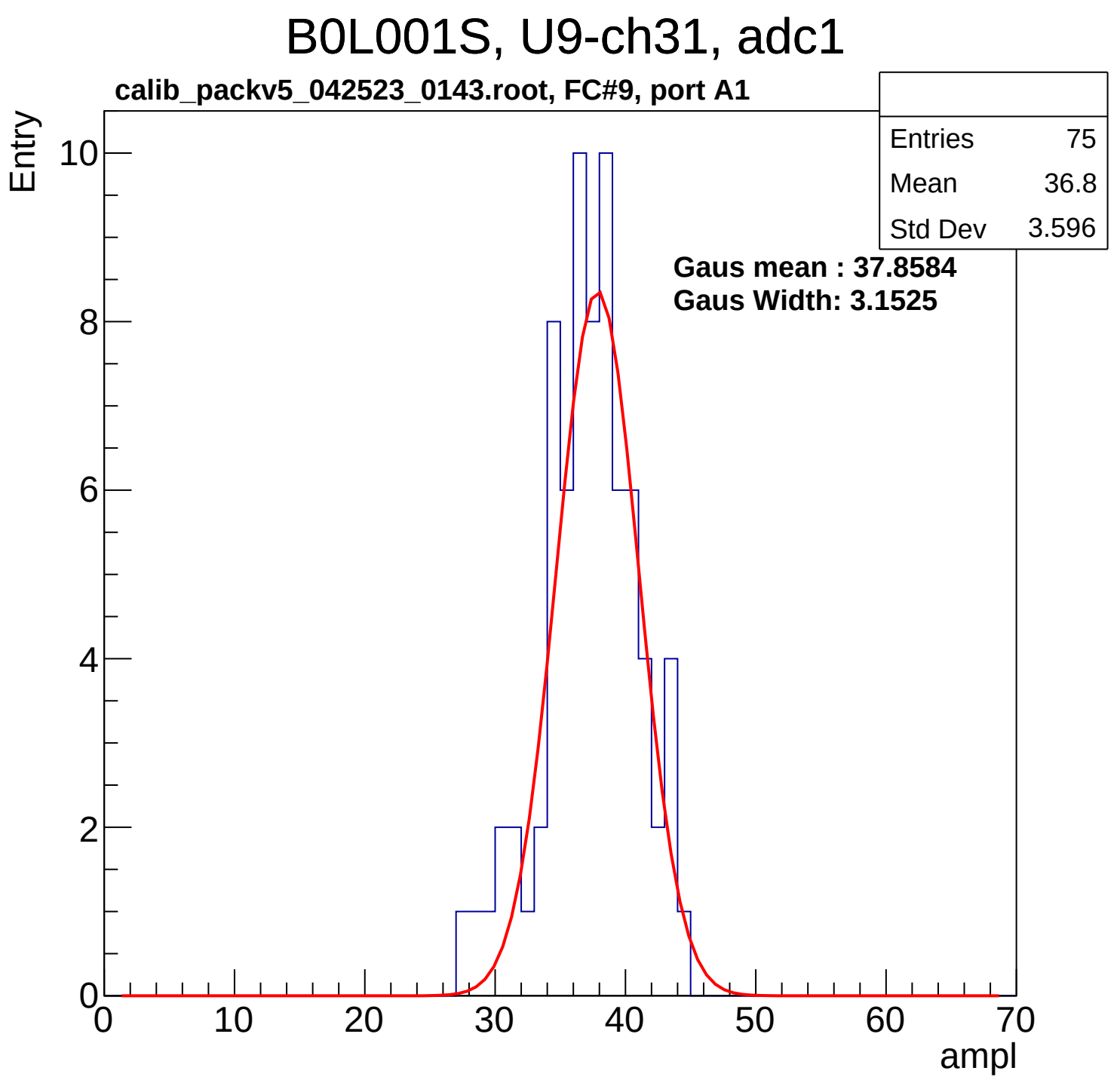
B0L001S, U9-ch31, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 36.8
Std Dev 3.596
Gaus mean : 37.8584
Gaus Width: 3.1525
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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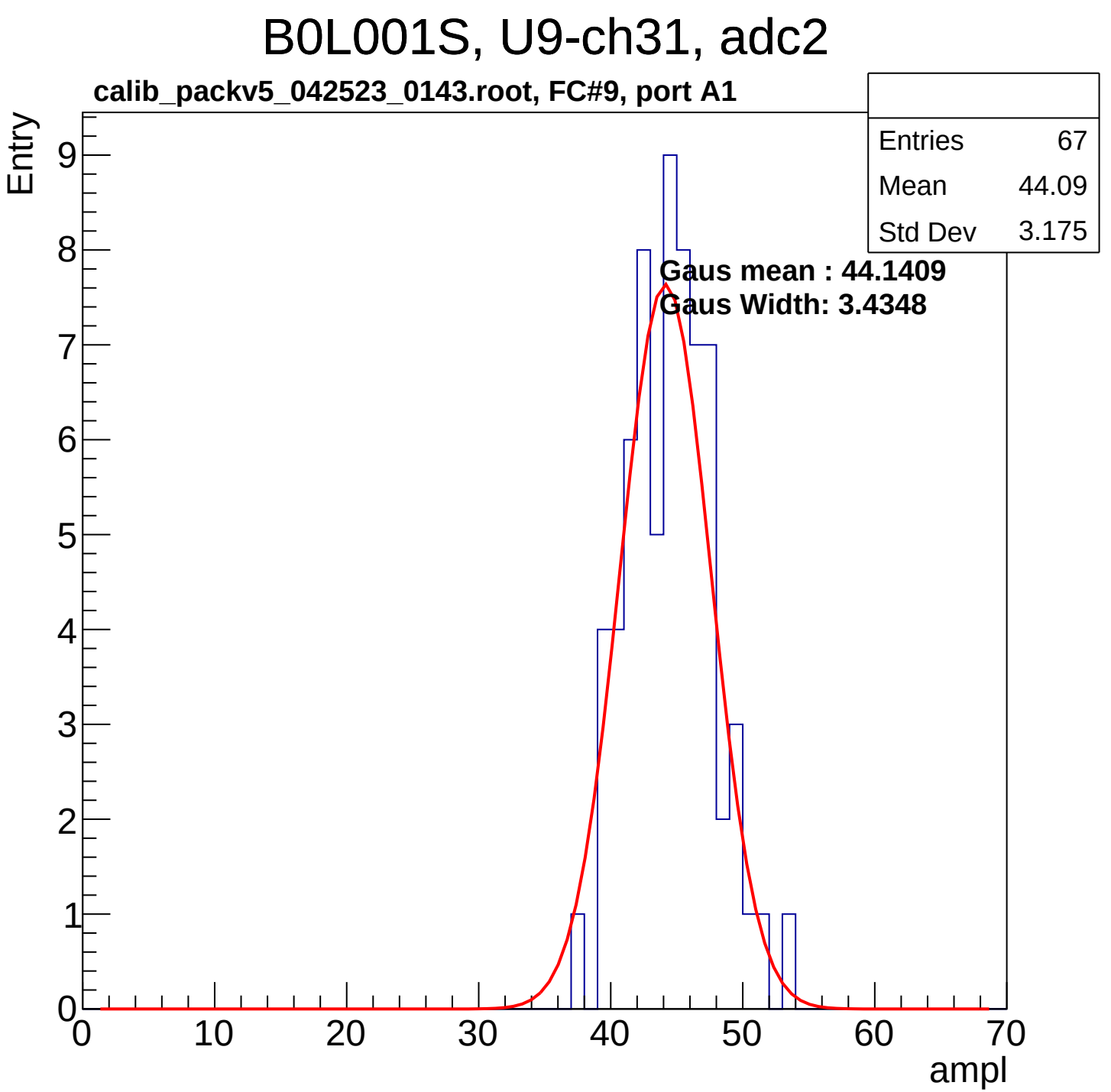
B0L001S, U9-ch31, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.09
Std Dev 3.175
Gaus mean : 44.1409
Gaus Width: 3.4348
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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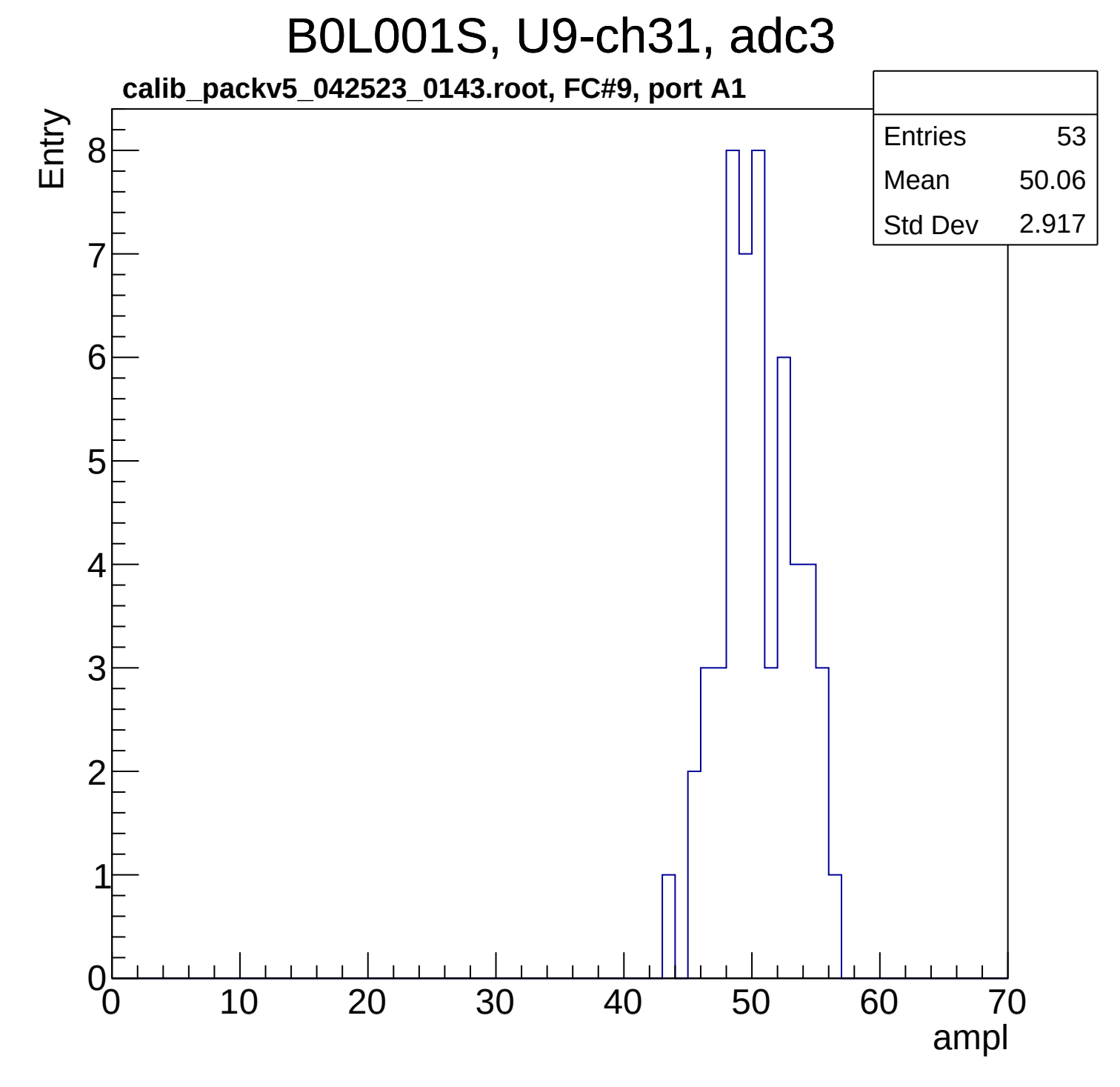
B0L001S, U9-ch31, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 50.06
Std Dev 2.917
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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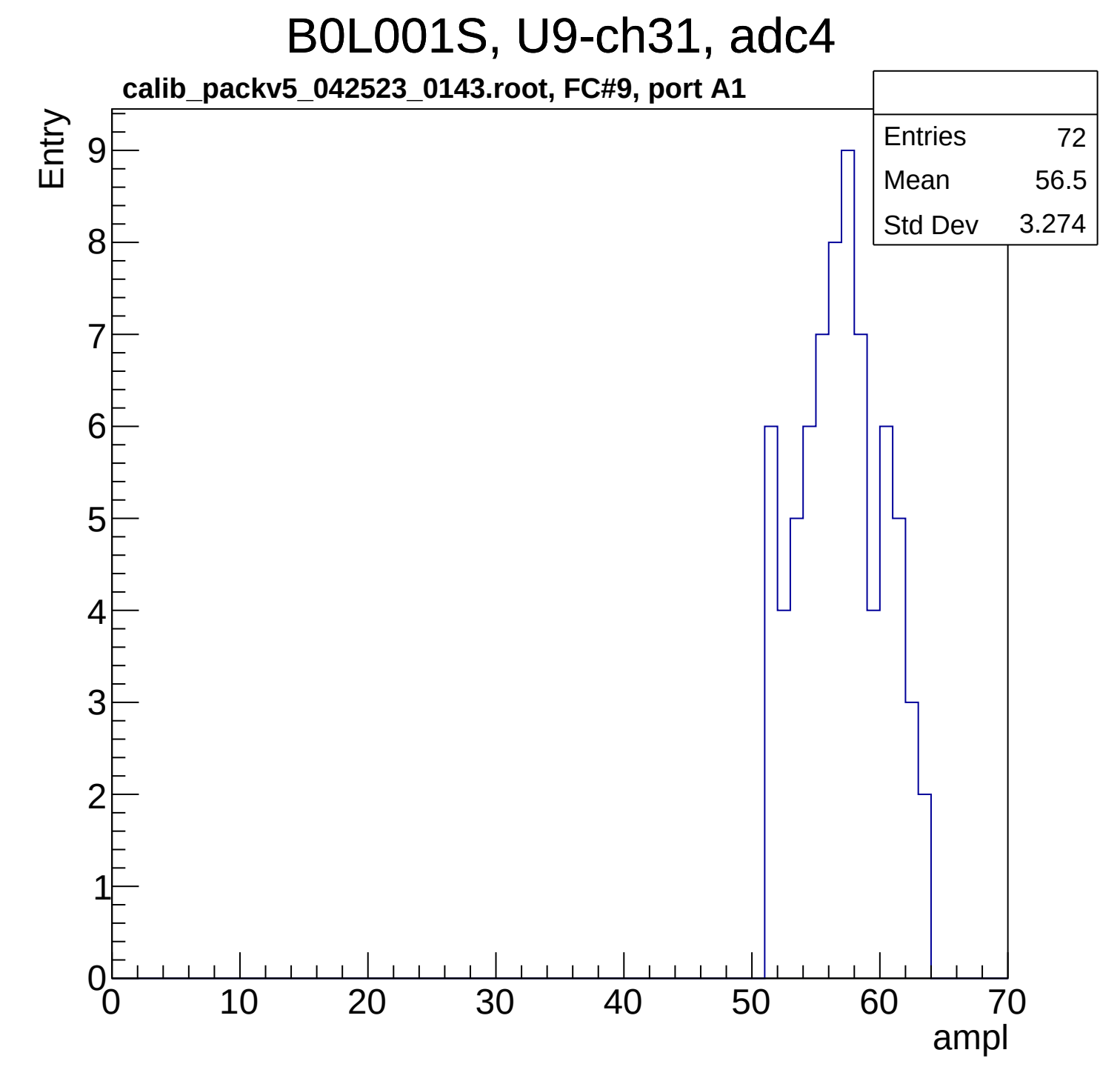
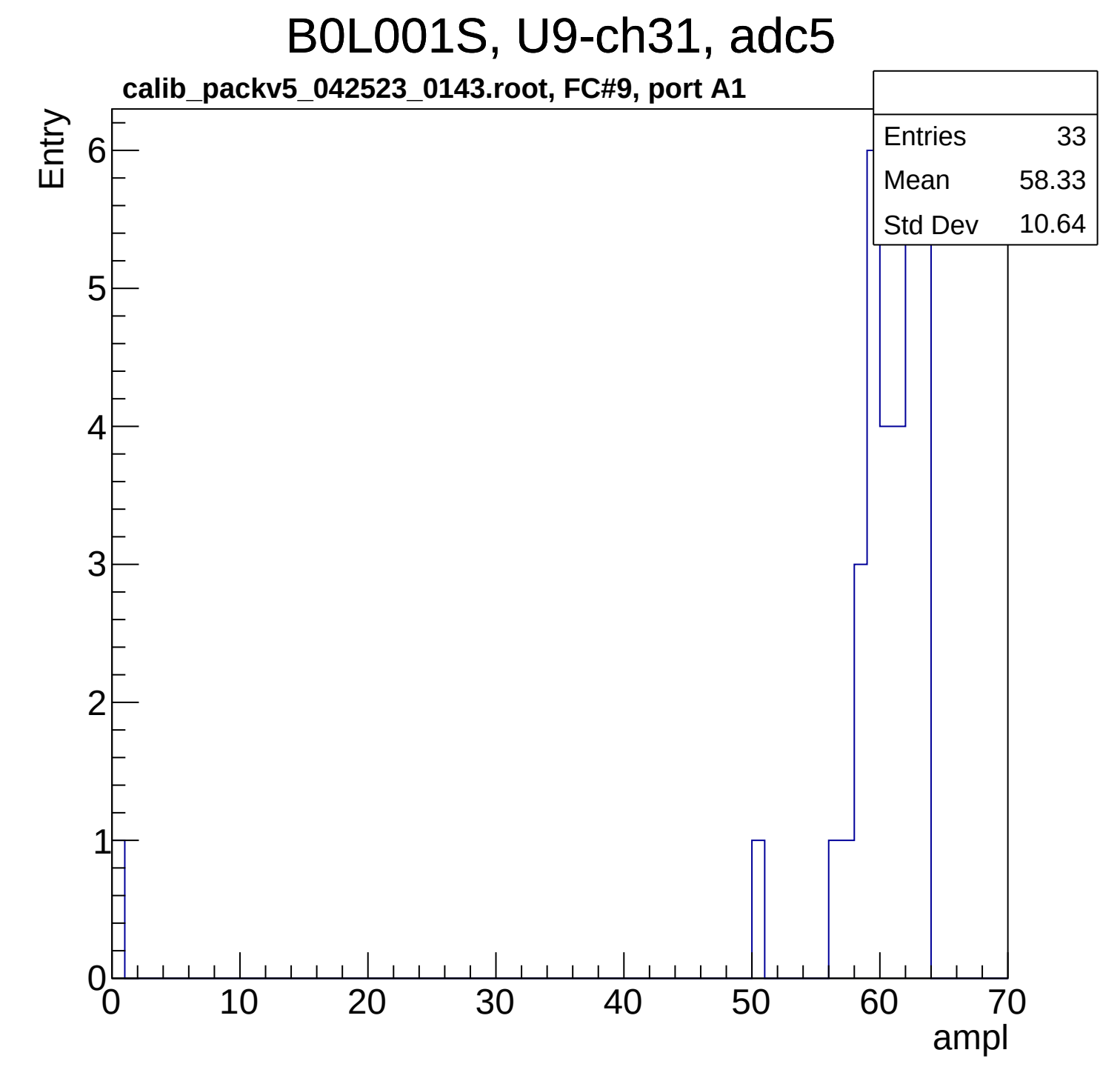
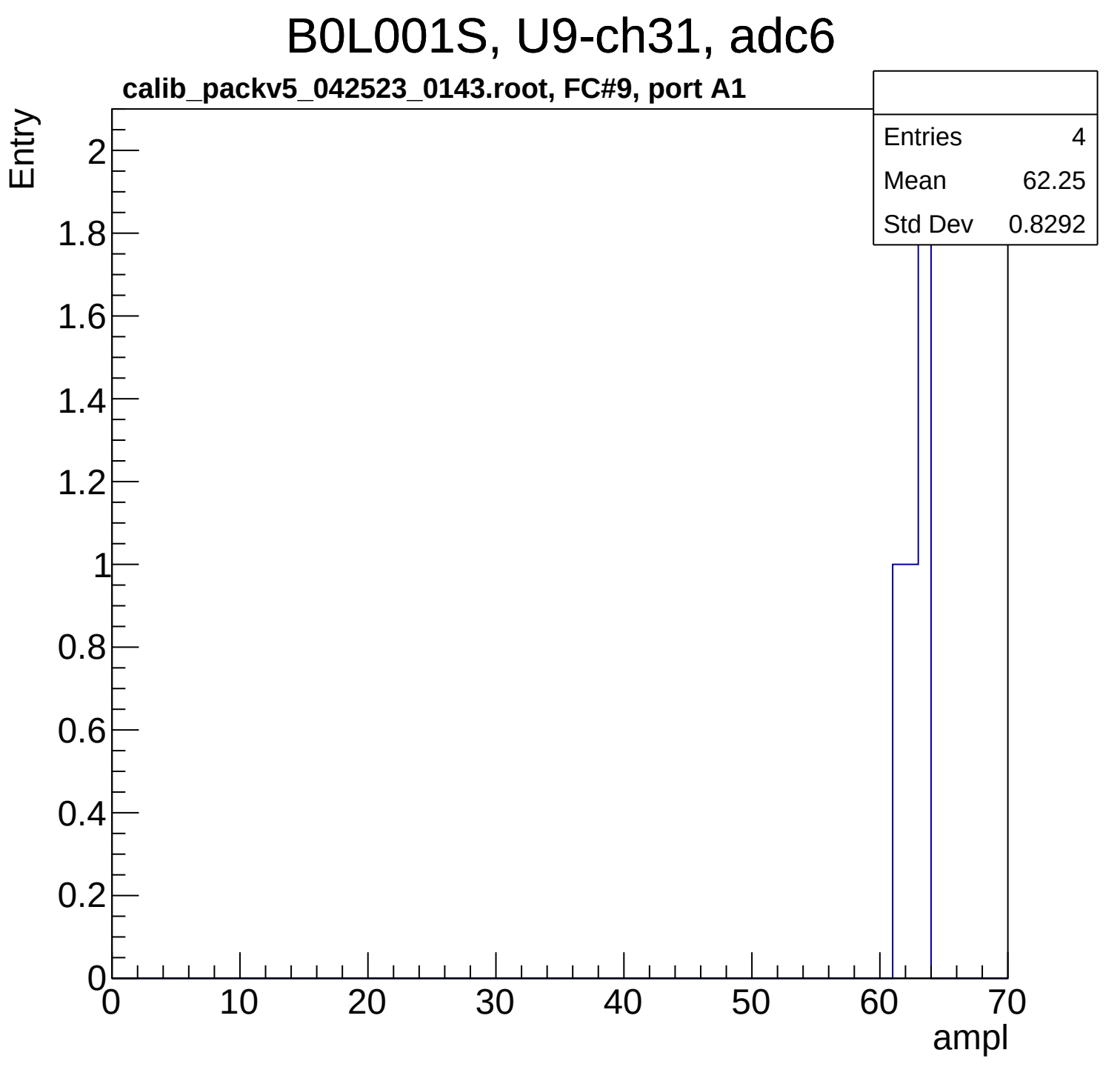
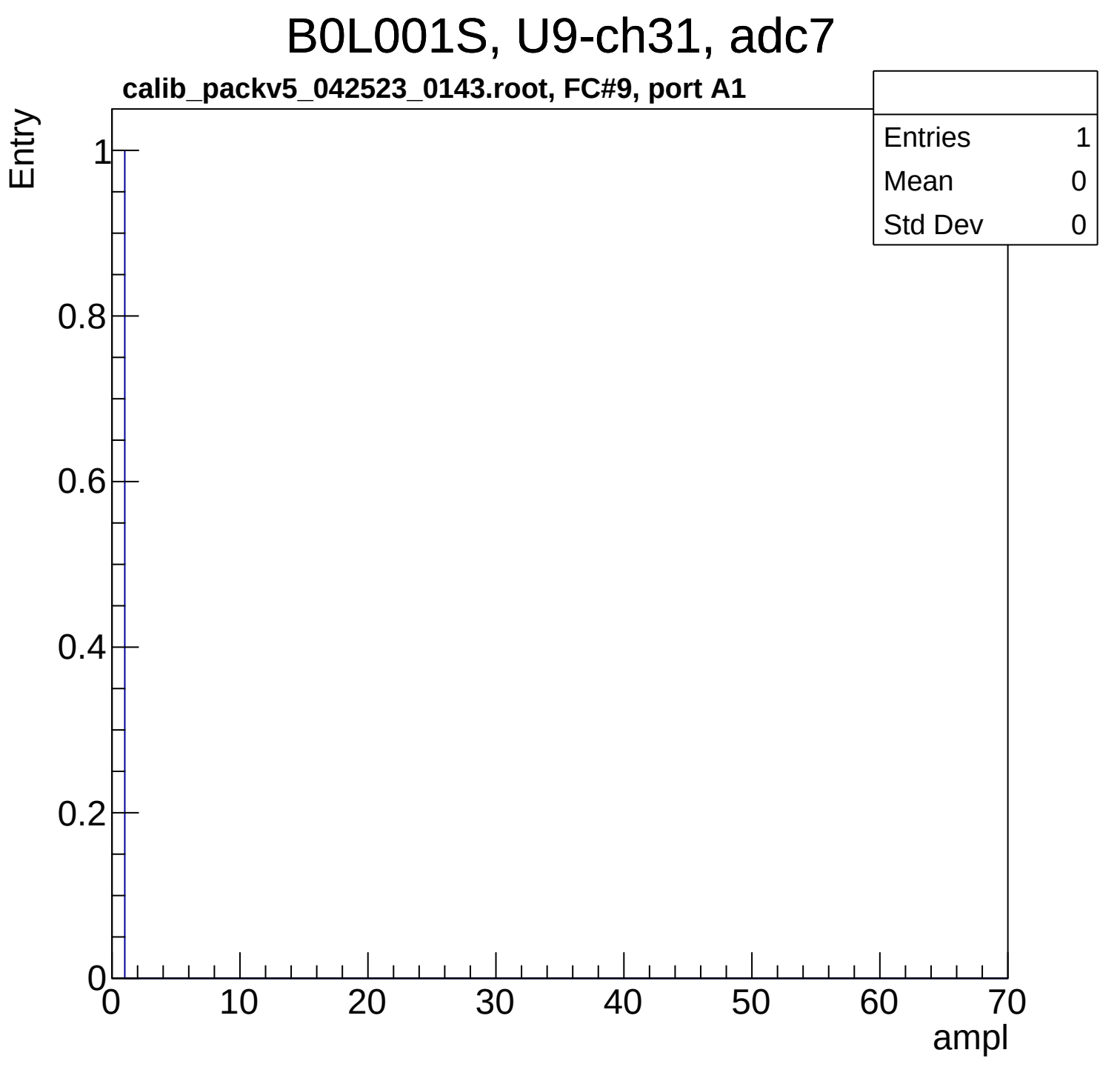
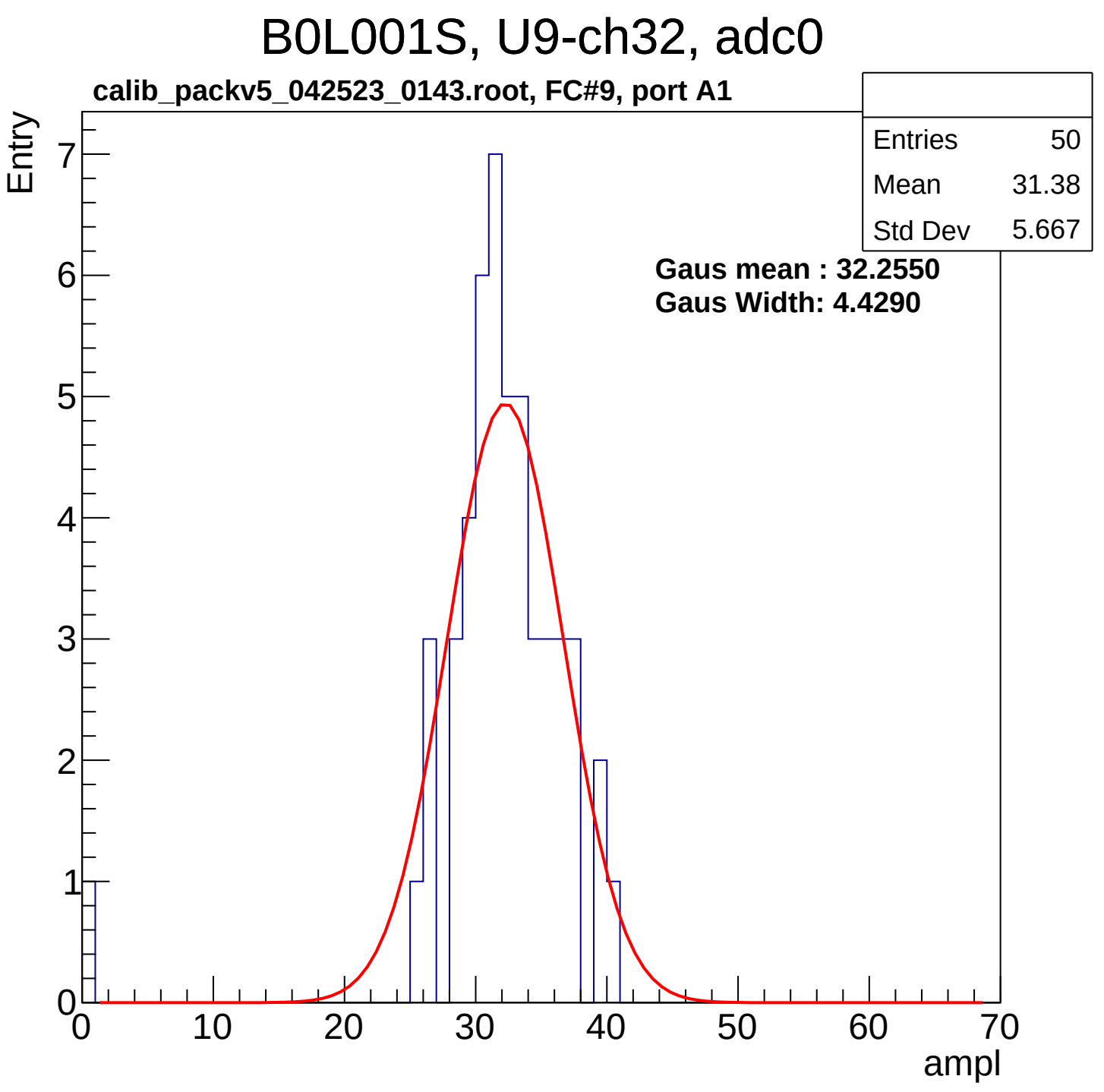
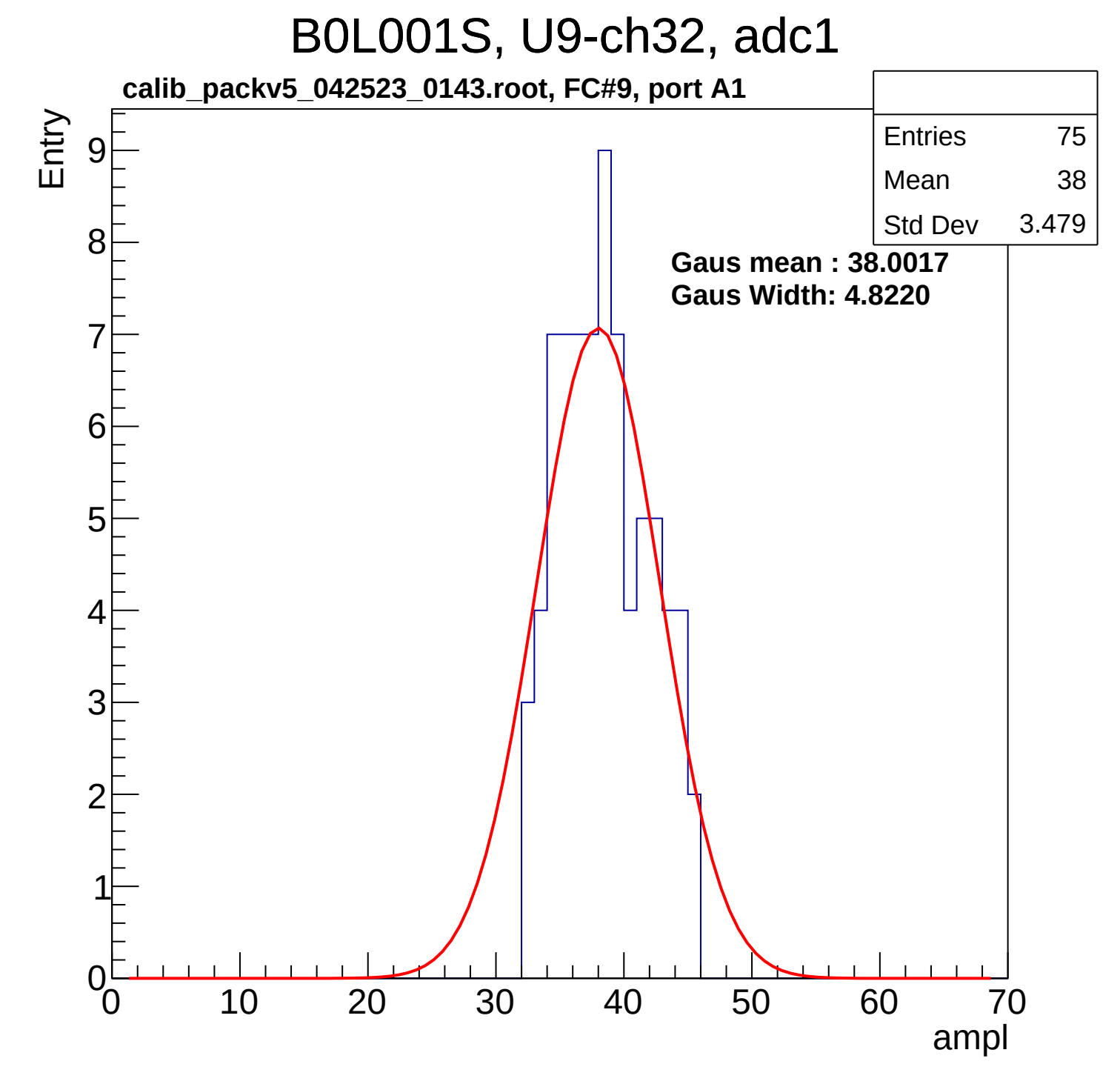

B0L001S, U9-ch31, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 56.5
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch31, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.33
Std Dev 10.64
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch31, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch31, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch32, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 31.38
Std Dev 5.667
Gaus mean : 32.2550
Gaus Width: 4.4290
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch32, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38
Std Dev 3.479
Gaus mean : 38.0017
Gaus Width: 4.8220
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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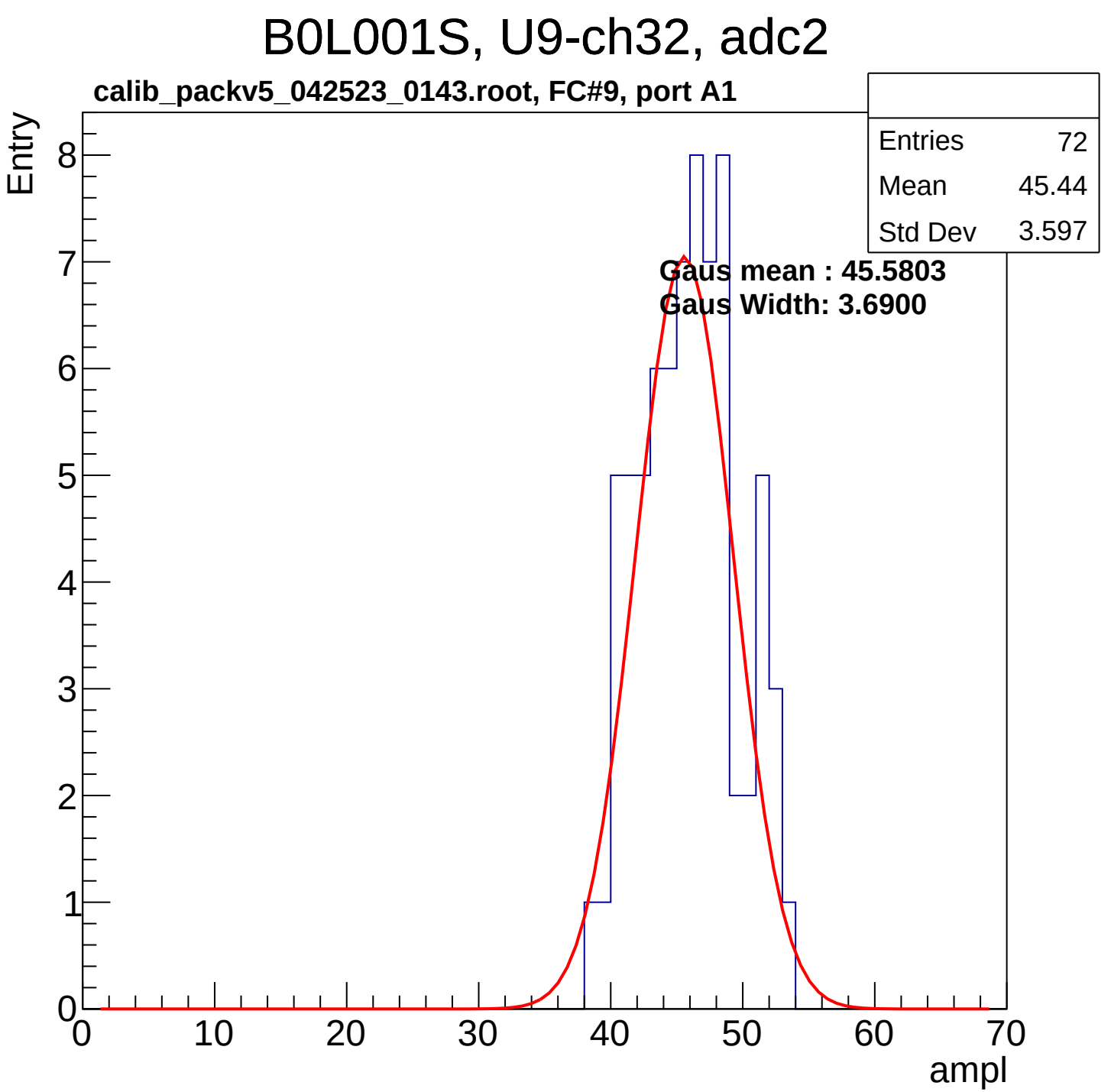
B0L001S, U9-ch32, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 45.44
Std Dev 3.597
Gaus mean : 45.5803
Gaus Width: 3.6900
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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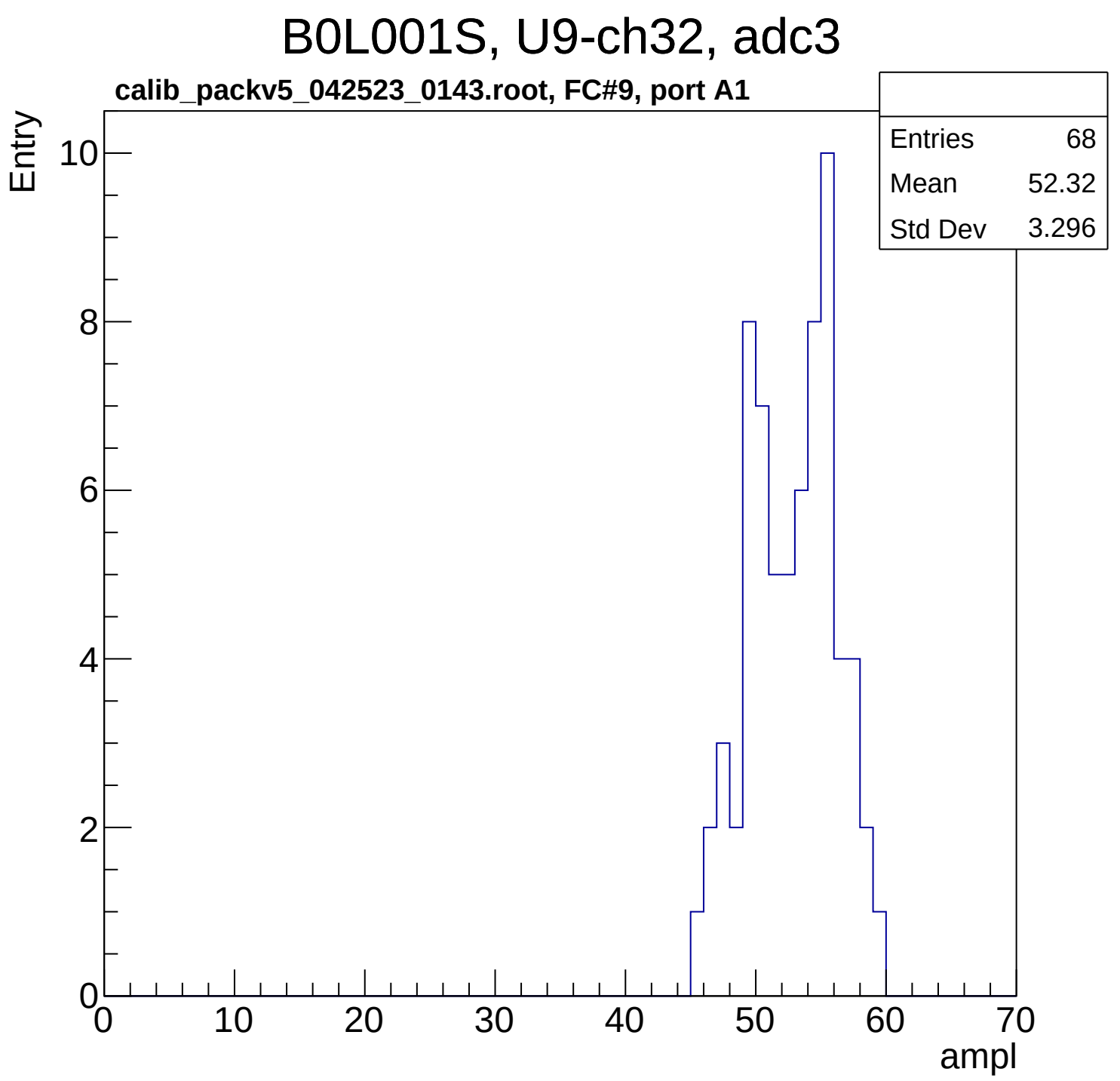
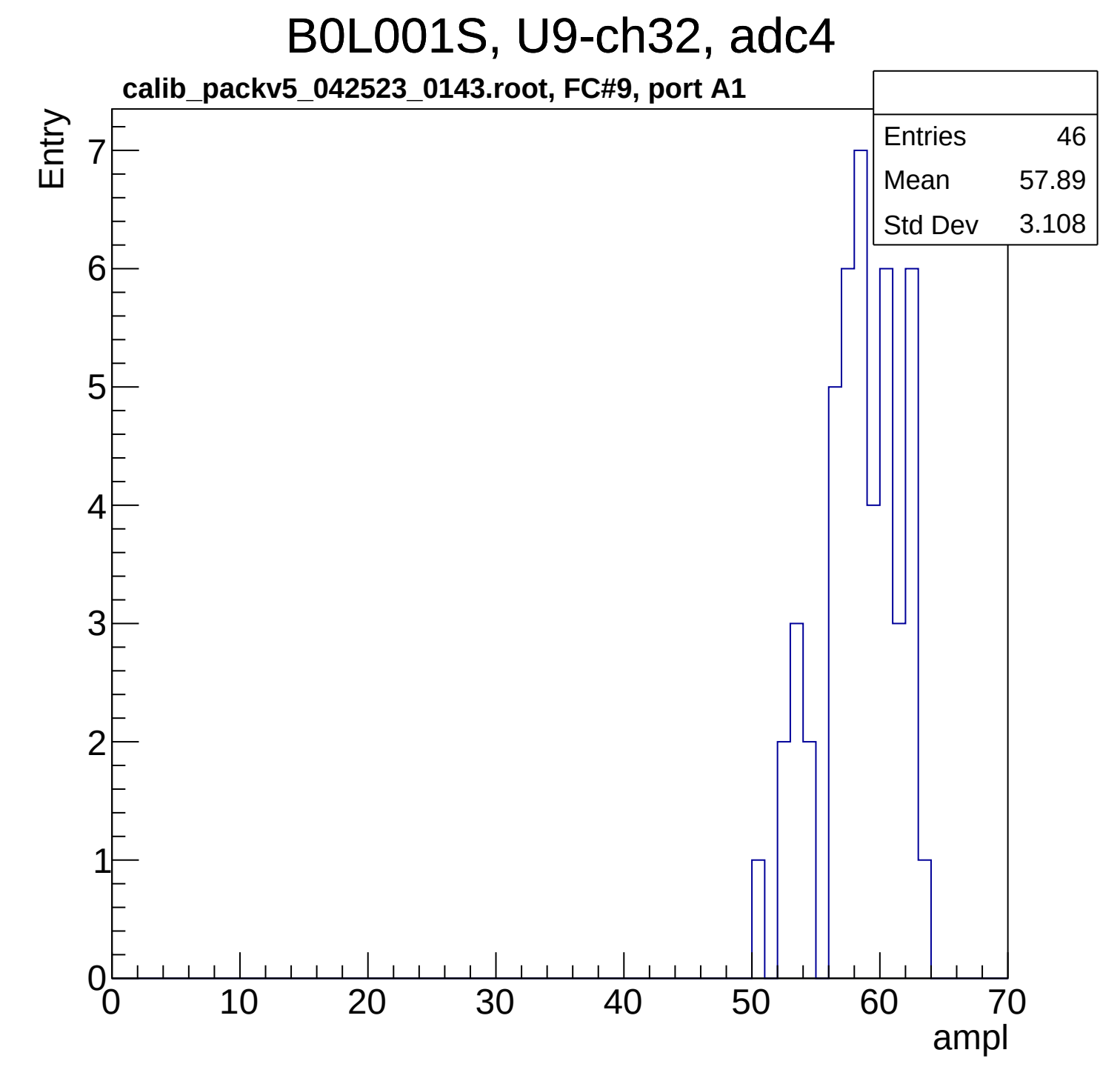
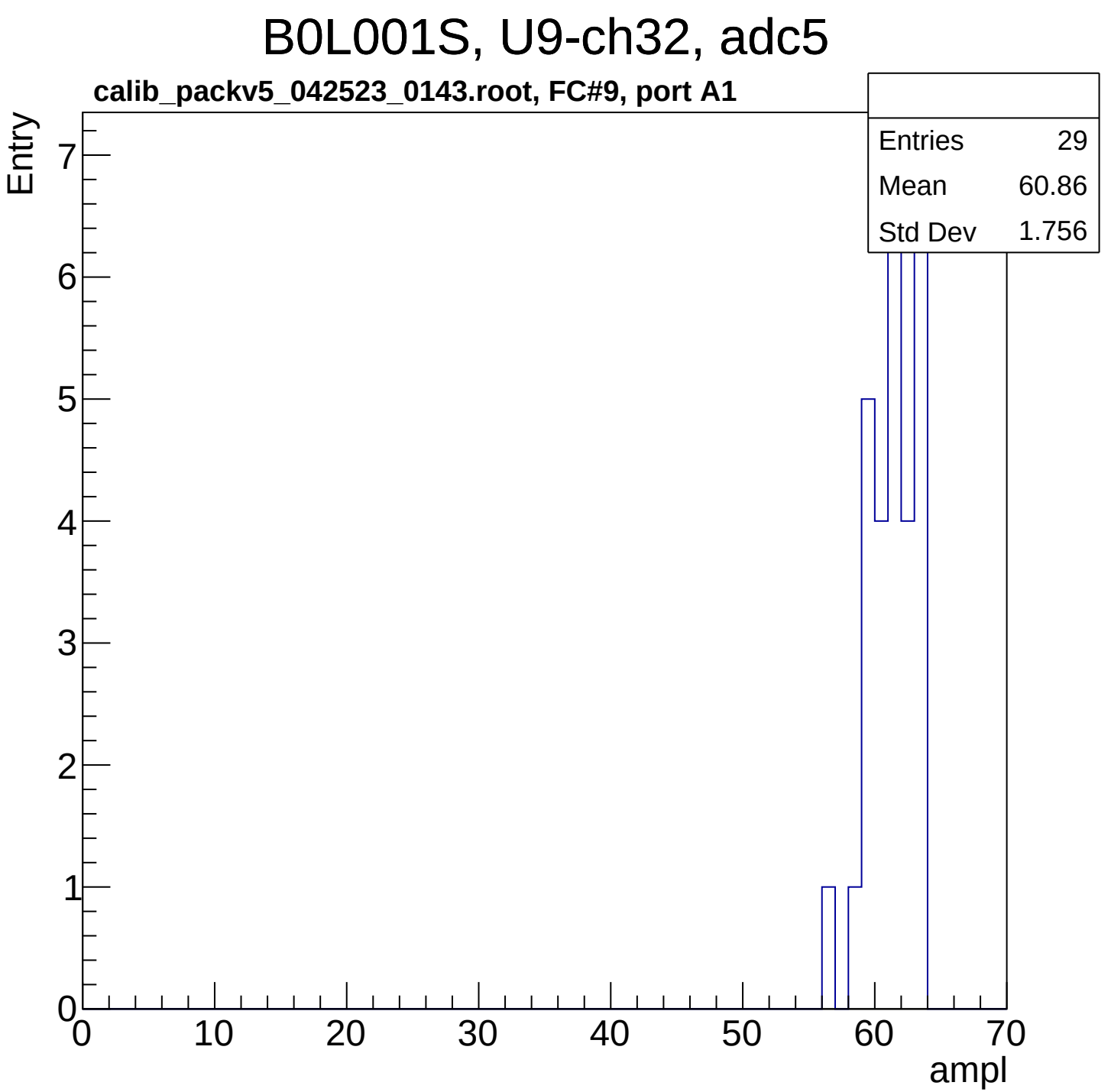
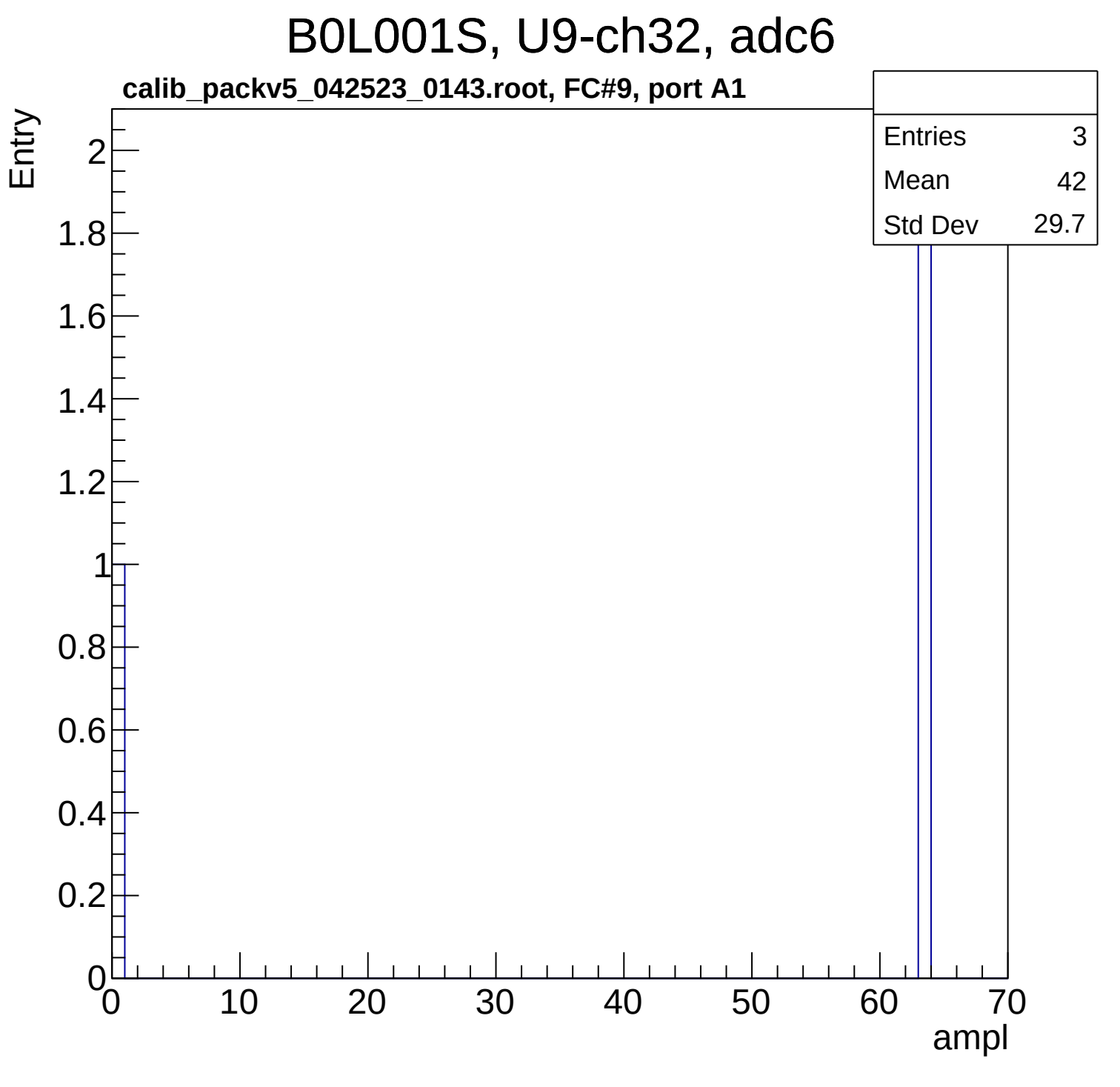
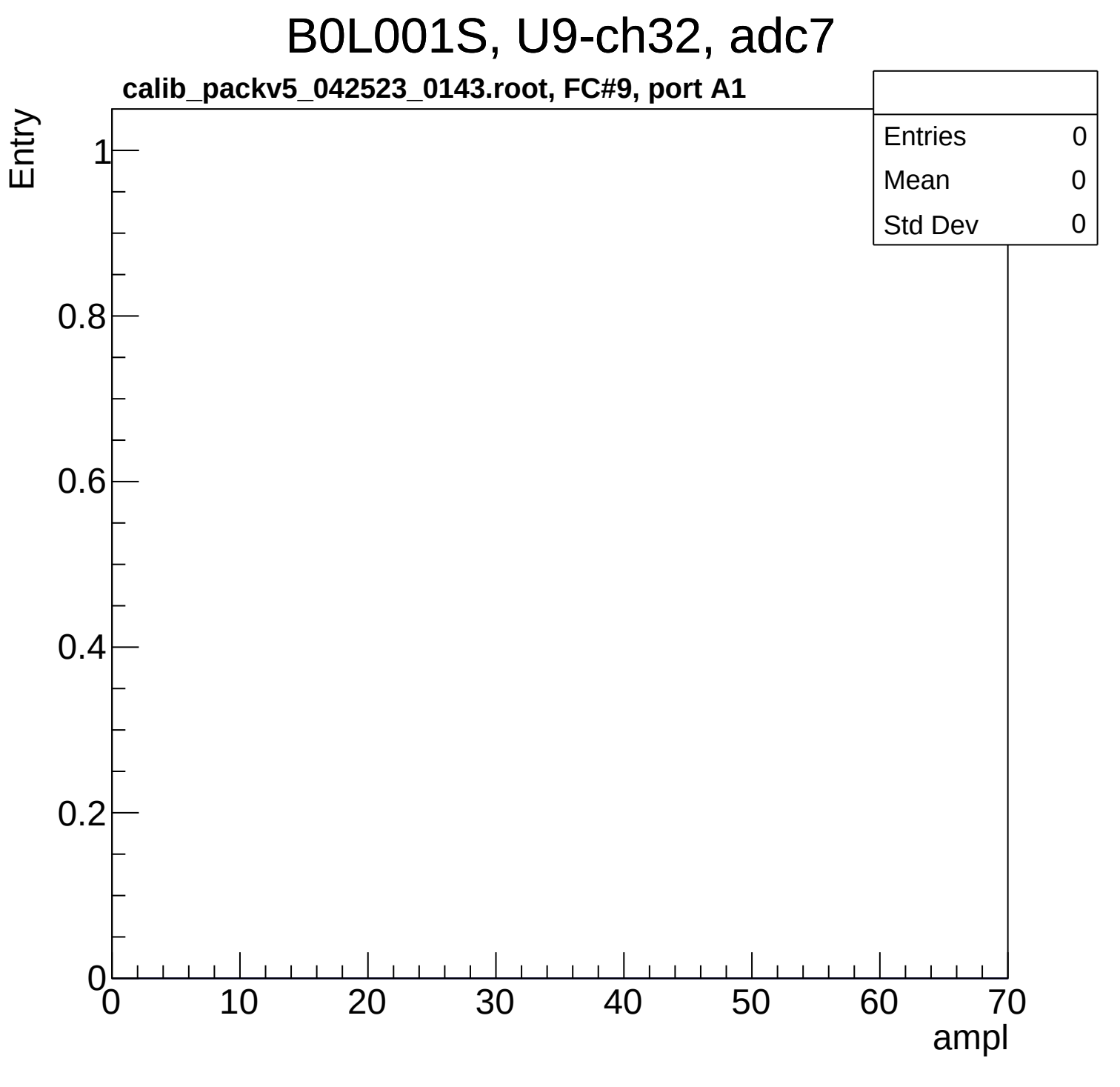
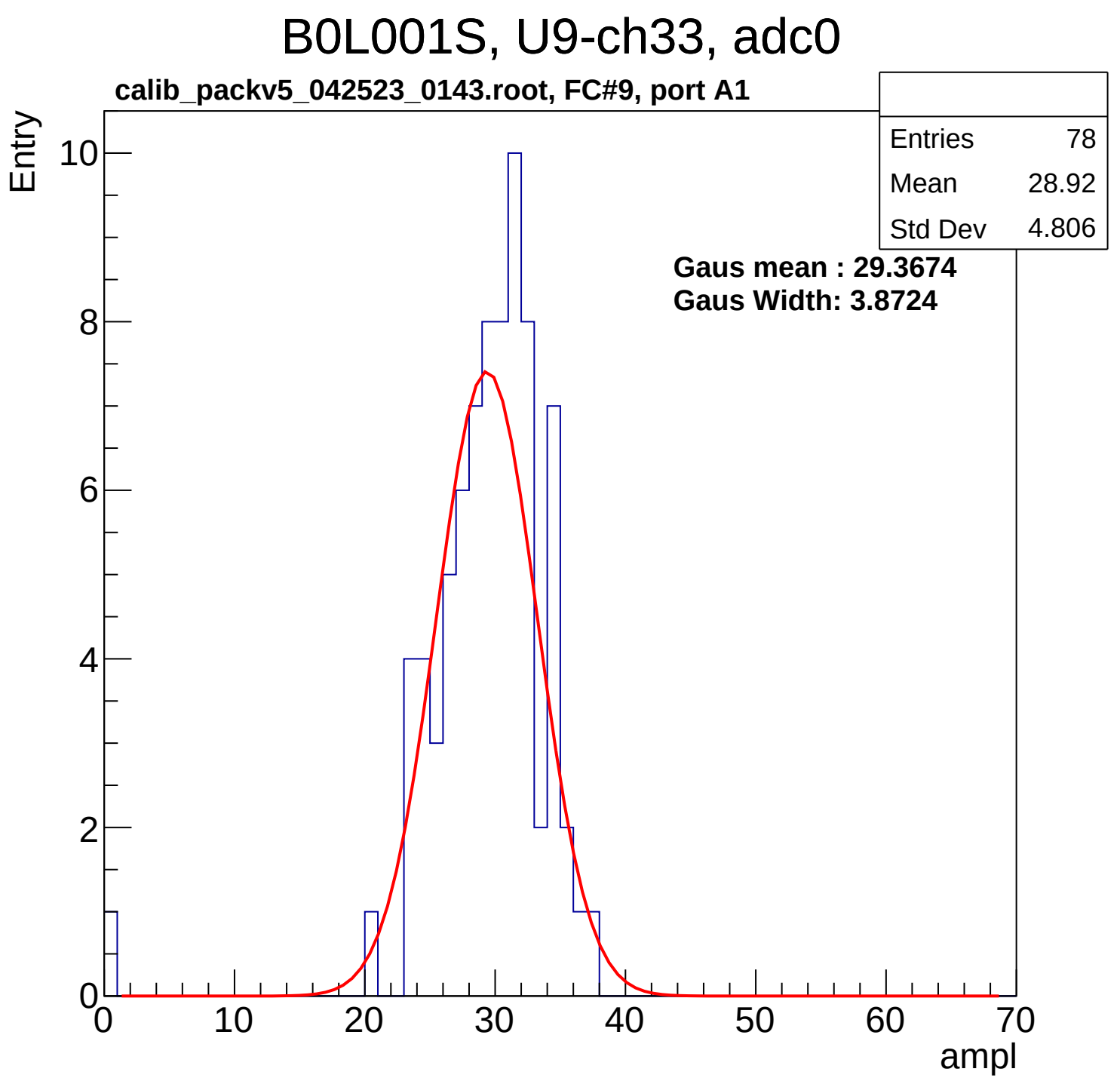

B0L001S, U9-ch32, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 52.32
Std Dev 3.296
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

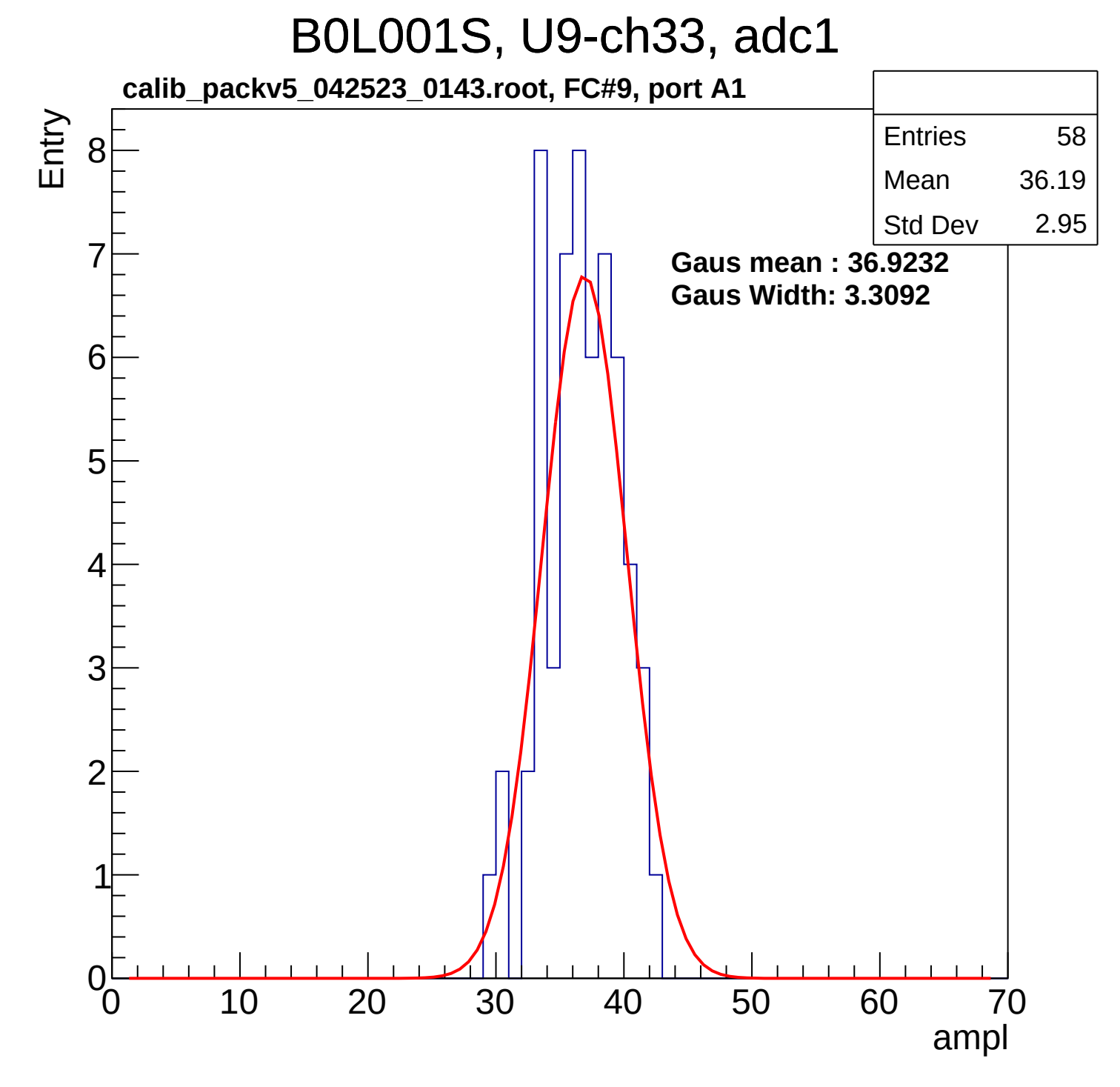

B0L001S, U9-ch32, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 57.89
Std Dev 3.108
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

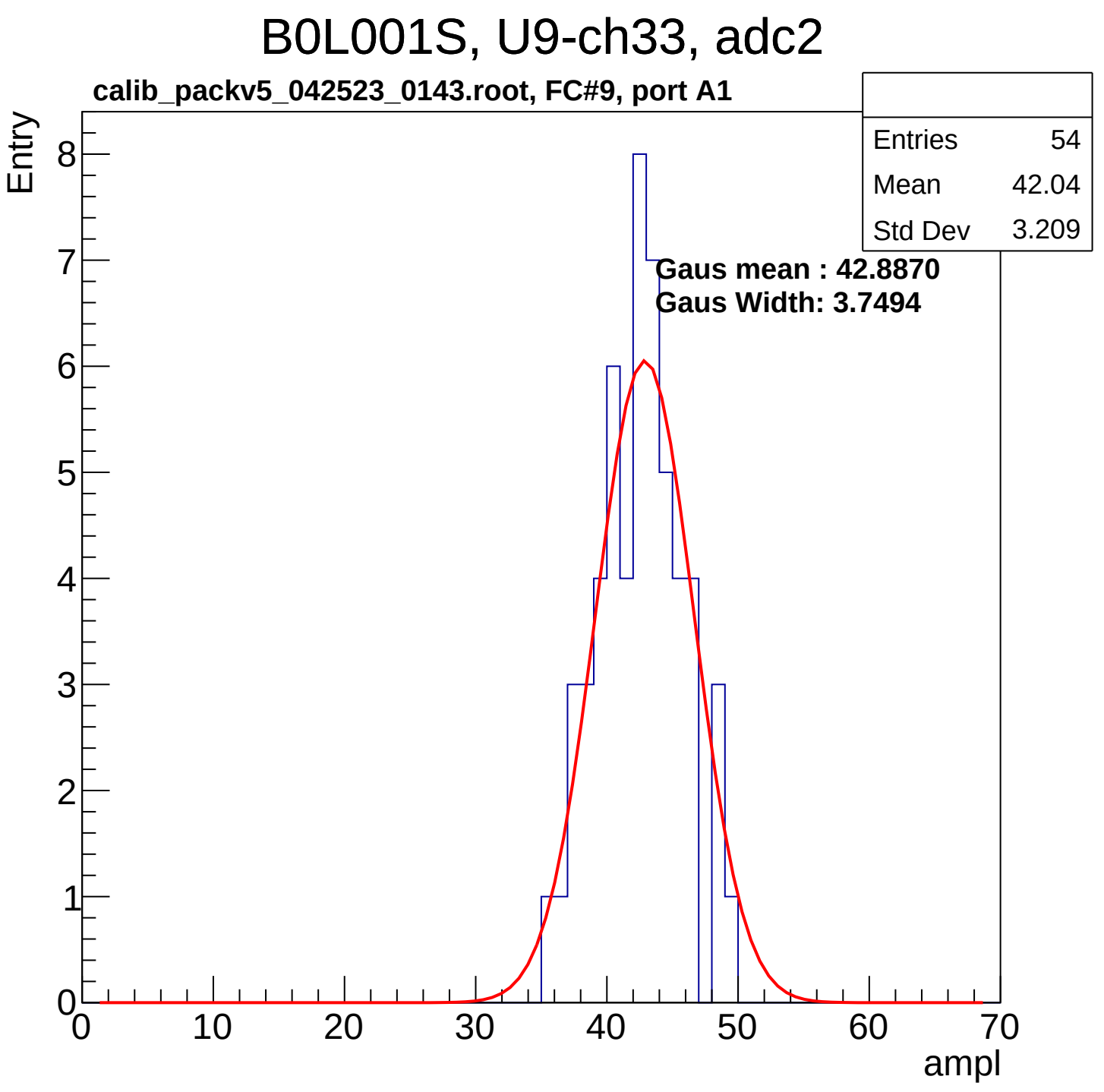

B0L001S, U9-ch32, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 60.86
Std Dev 1.756
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch32, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch32, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch33, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 28.92
Std Dev 4.806
Gaus mean : 29.3674
Gaus Width: 3.8724
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch33, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 36.19
Std Dev 2.95
Gaus mean : 36.9232
Gaus Width: 3.3092
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch33, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 42.04
Std Dev 3.209
Gaus mean : 42.8870
Gaus Width: 3.7494
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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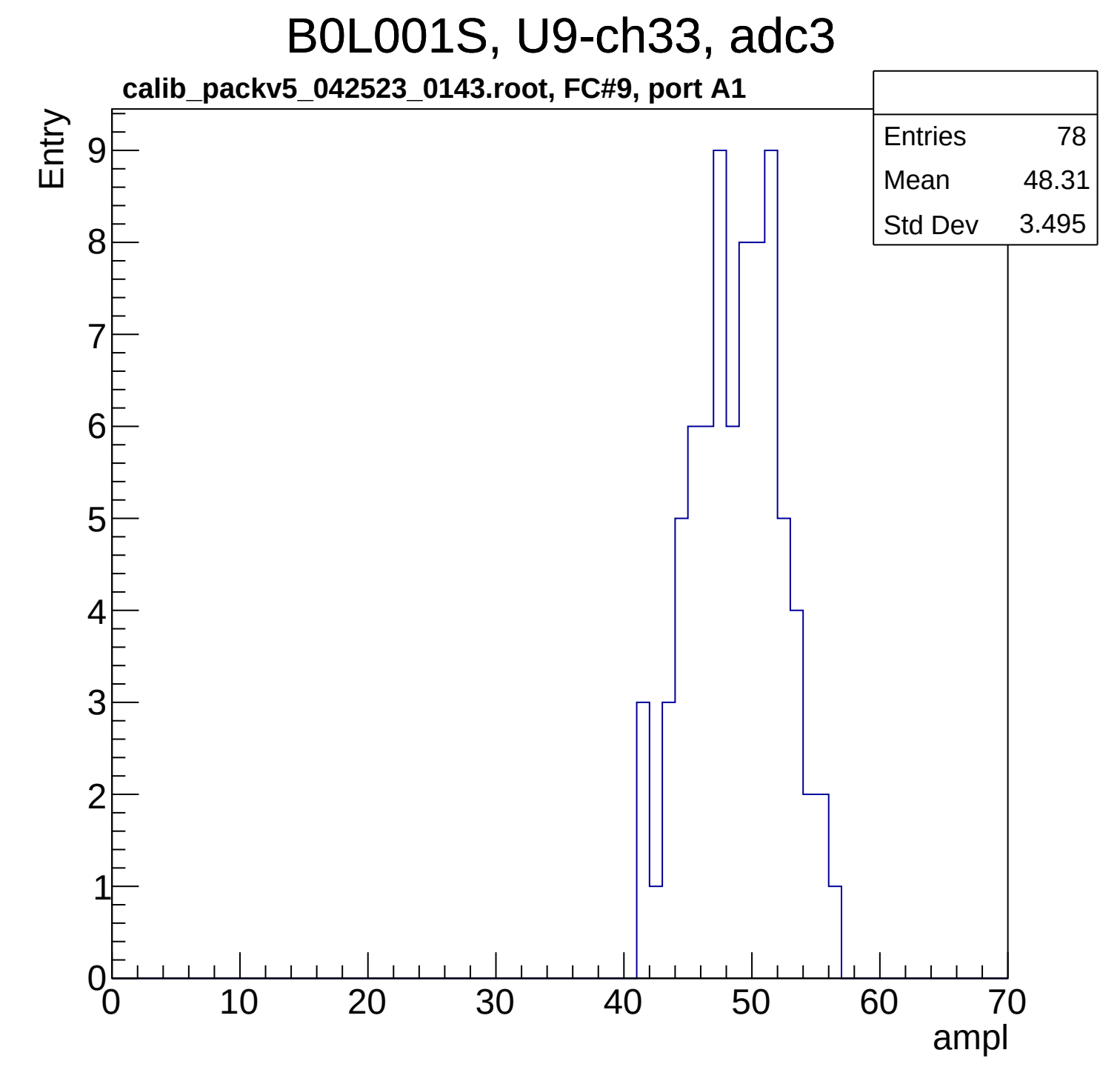
B0L001S, U9-ch33, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 48.31
Std Dev 3.495
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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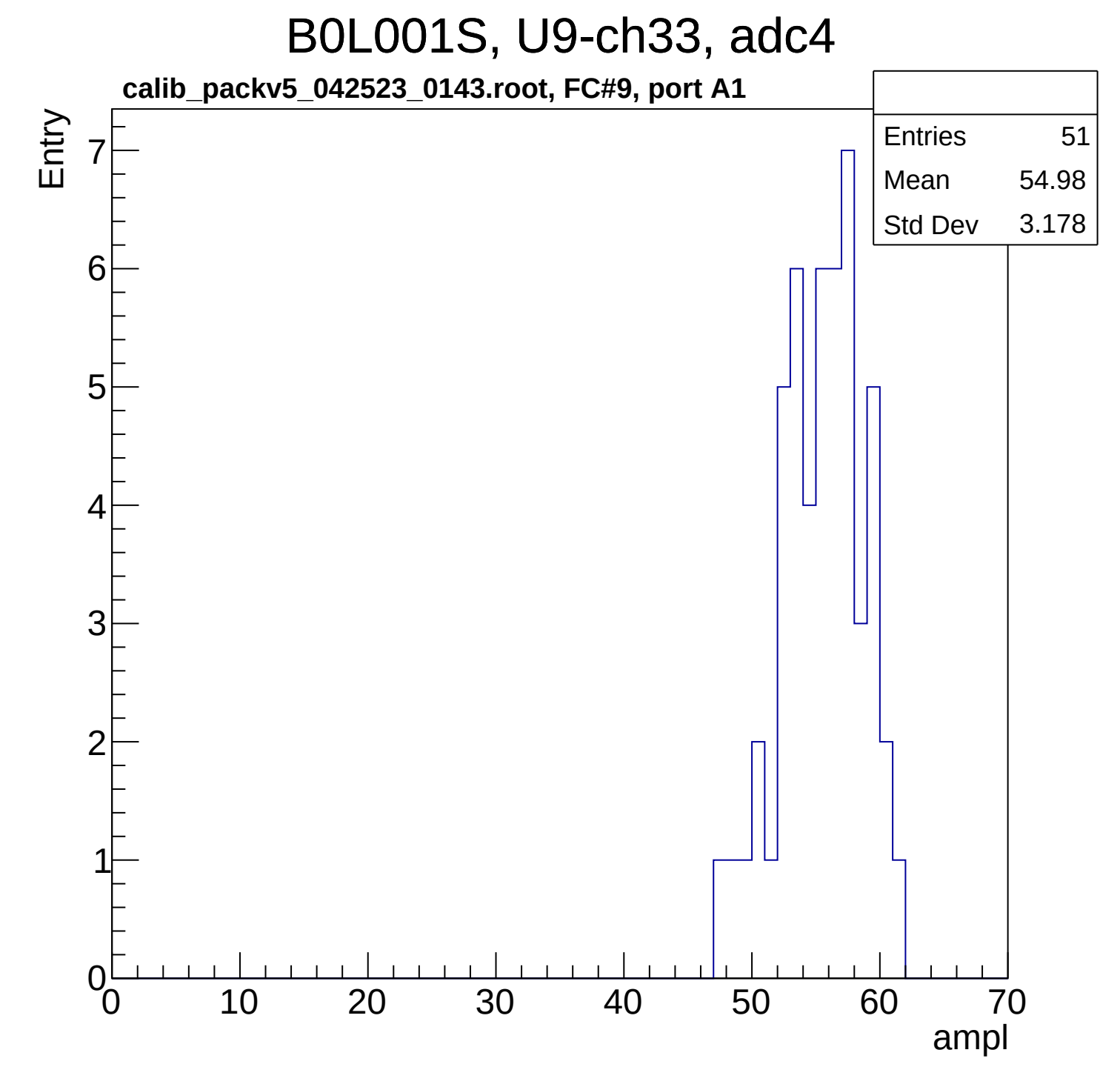
B0L001S, U9-ch33, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 54.98
Std Dev 3.178
Entry
ampl
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1
2
3
4
5
6
7
0
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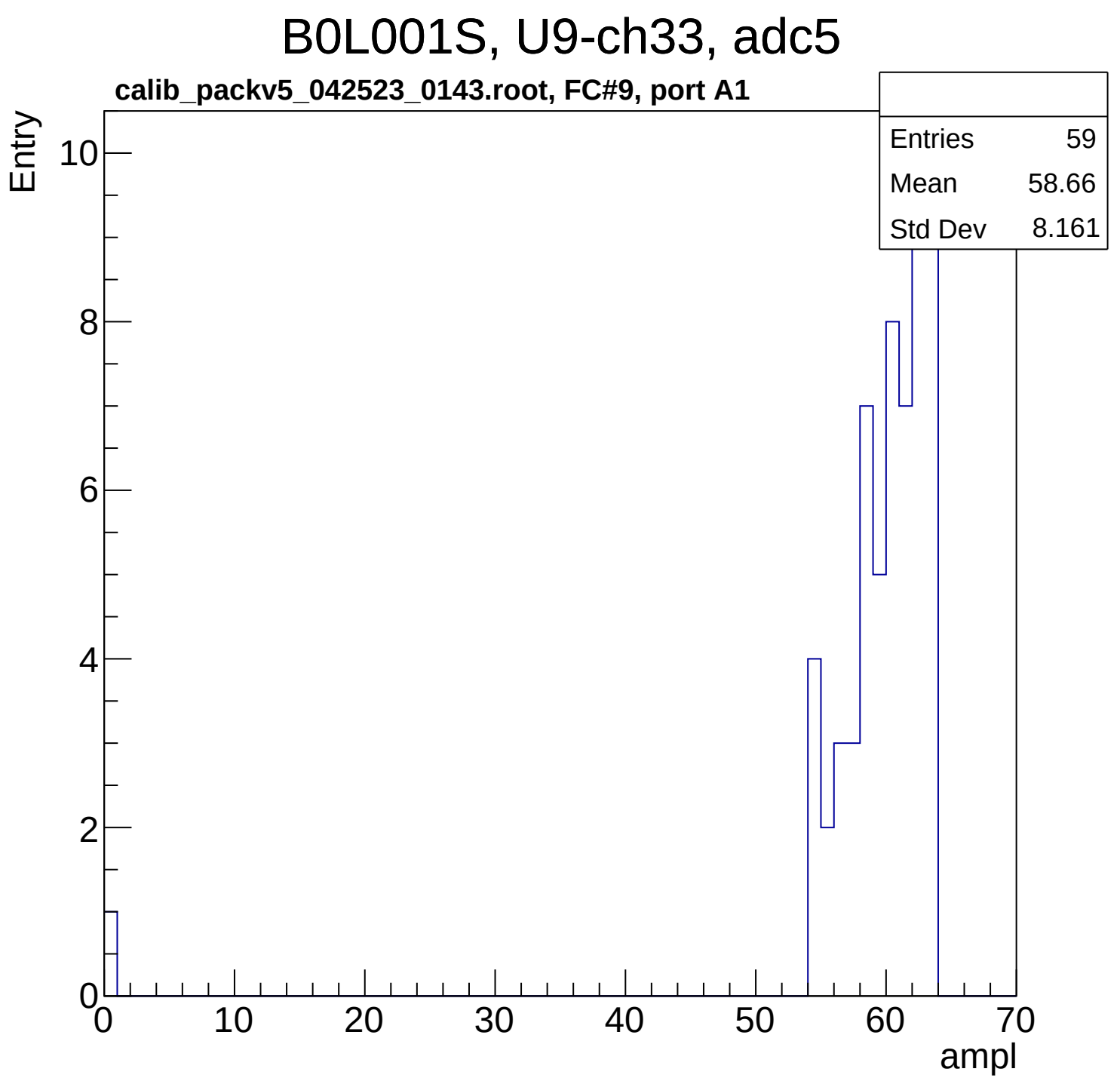
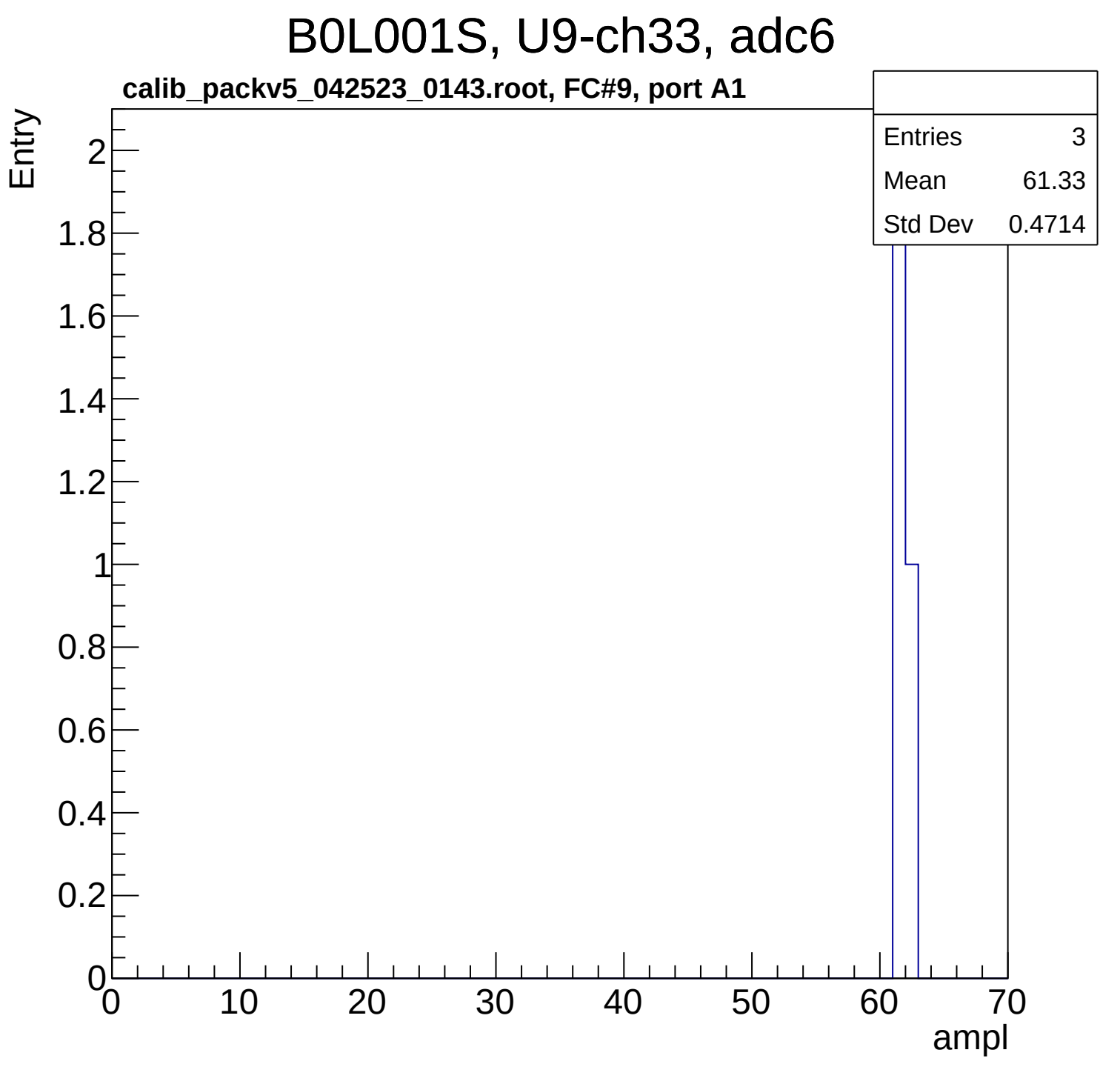
20
30
40
50
60
70


B0L001S, U9-ch33, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 58.66
Std Dev 8.161
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch33, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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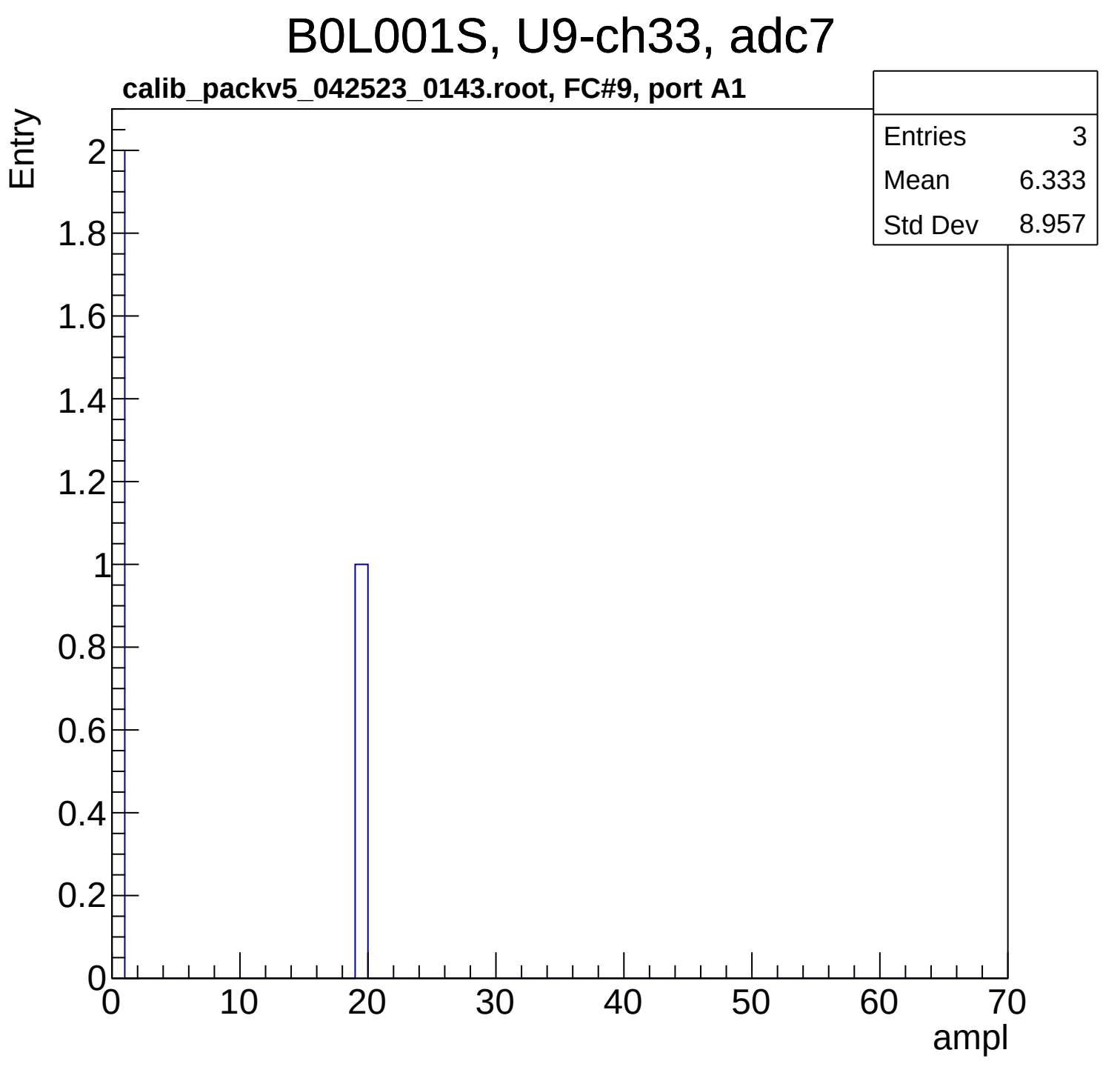
20
30
40
50
60
70


B0L001S, U9-ch33, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 6.333
Std Dev 8.957
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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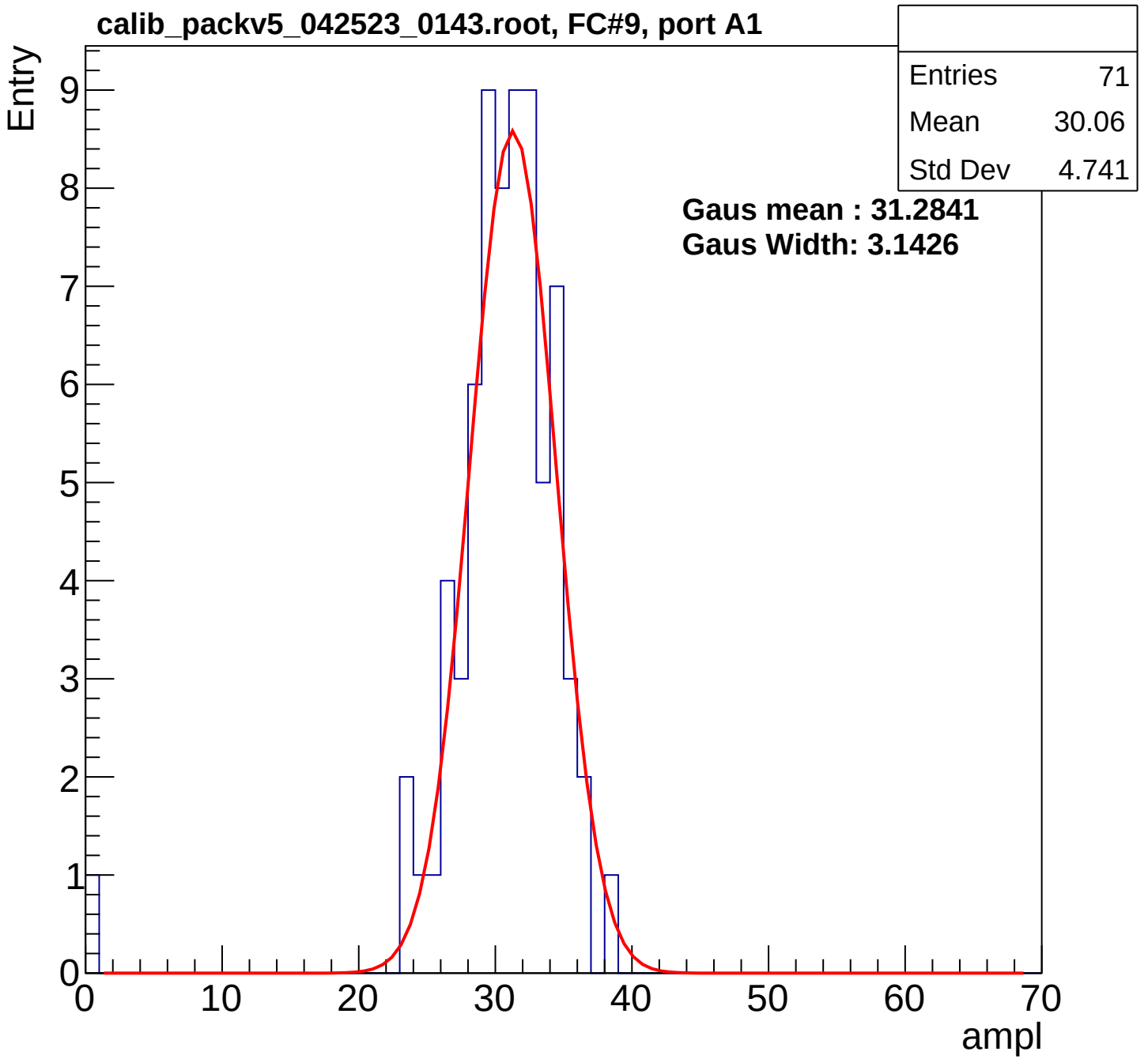
40
50
60
70


B0L001S, U9-ch34, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.06
Std Dev 4.741
Gaus mean : 31.2841
Gaus Width: 3.1426
Entry
ampl
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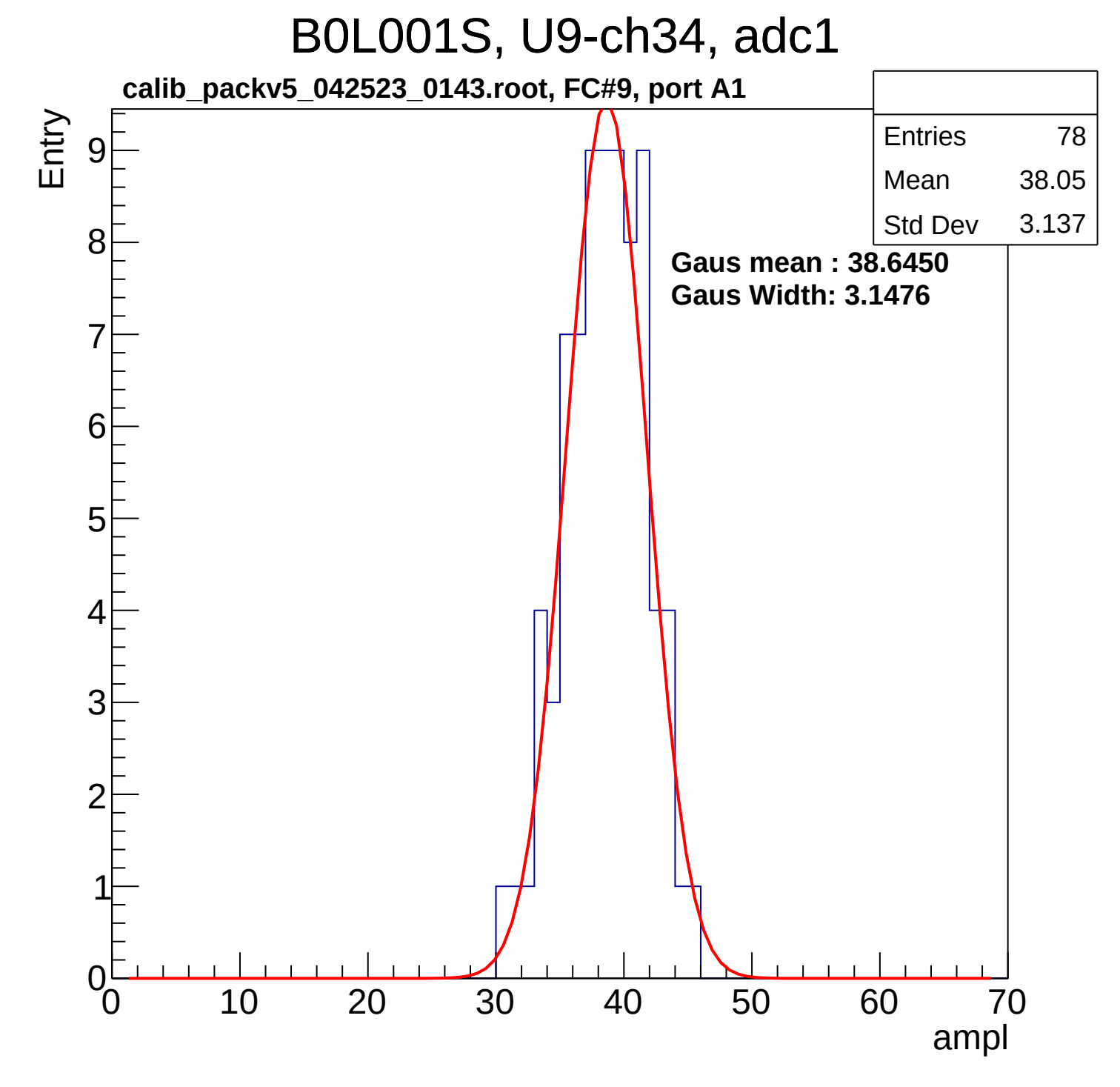
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch34, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 38.05
Std Dev 3.137
Gaus mean : 38.6450
Gaus Width: 3.1476
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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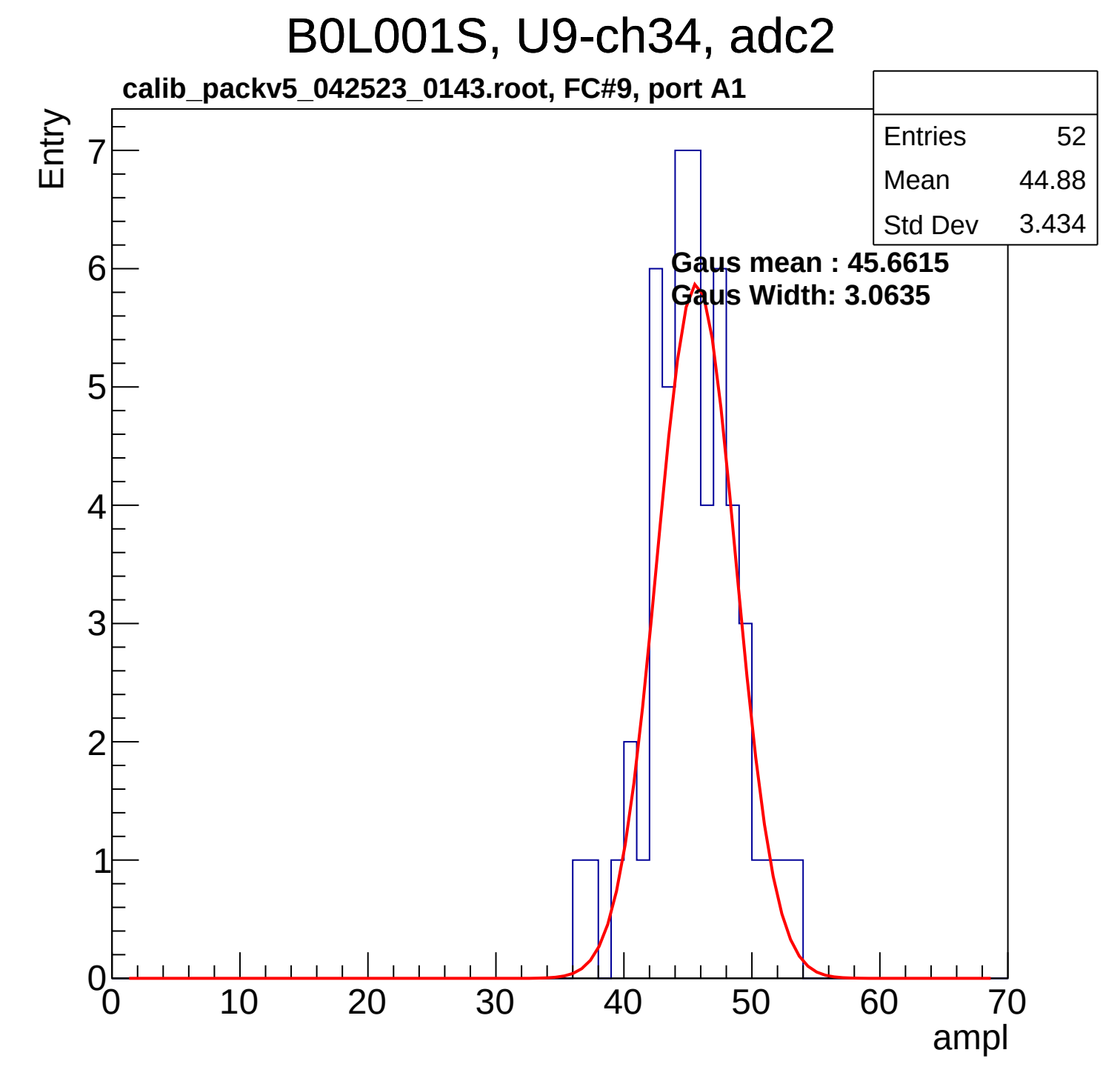
B0L001S, U9-ch34, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 44.88
Std Dev 3.434
Gaus mean : 45.6615
Gaus Width: 3.0635
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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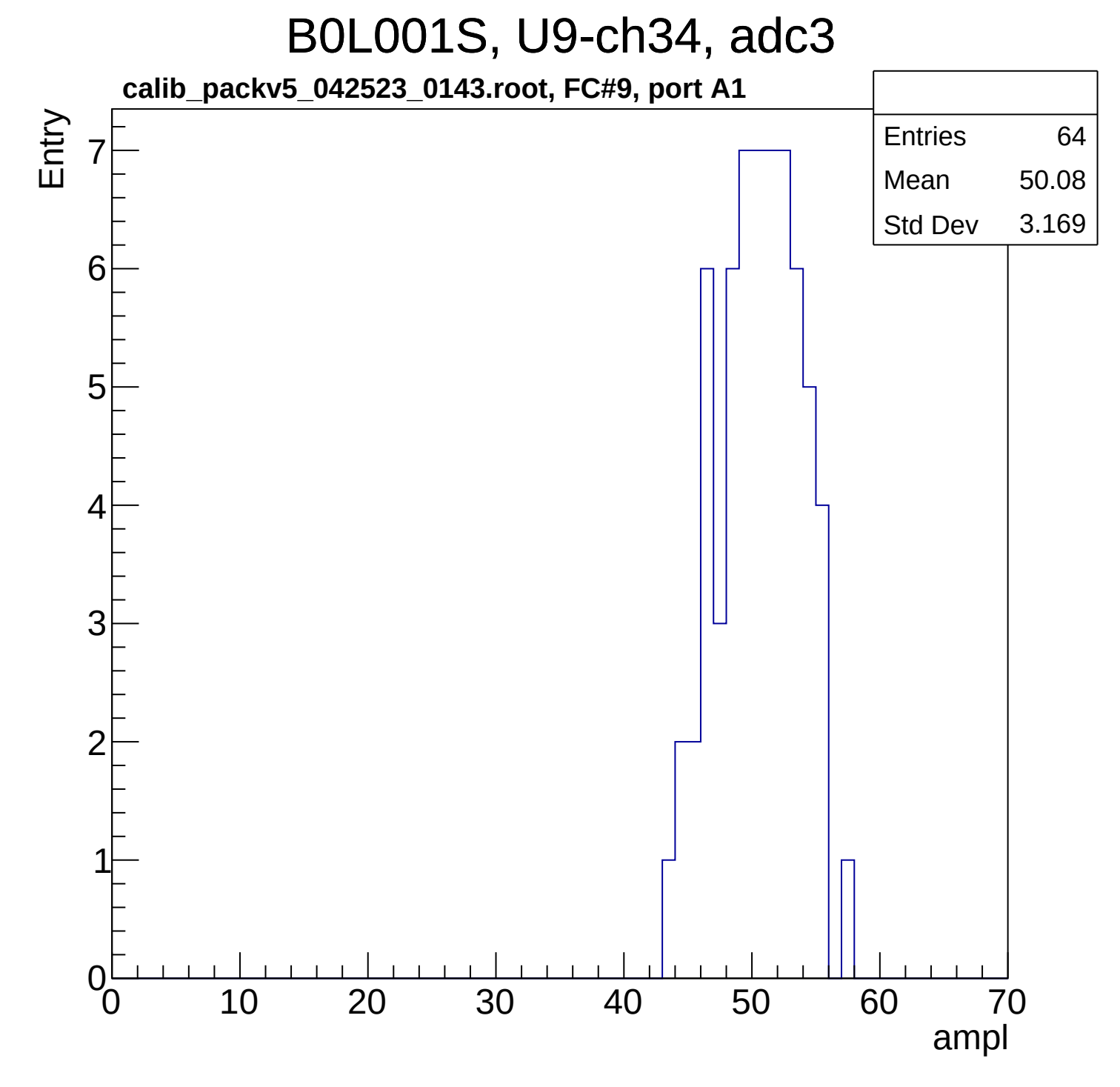
B0L001S, U9-ch34, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.08
Std Dev 3.169
Entry
ampl
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1
2
3
4
5
6
7
0
10
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30
40
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60
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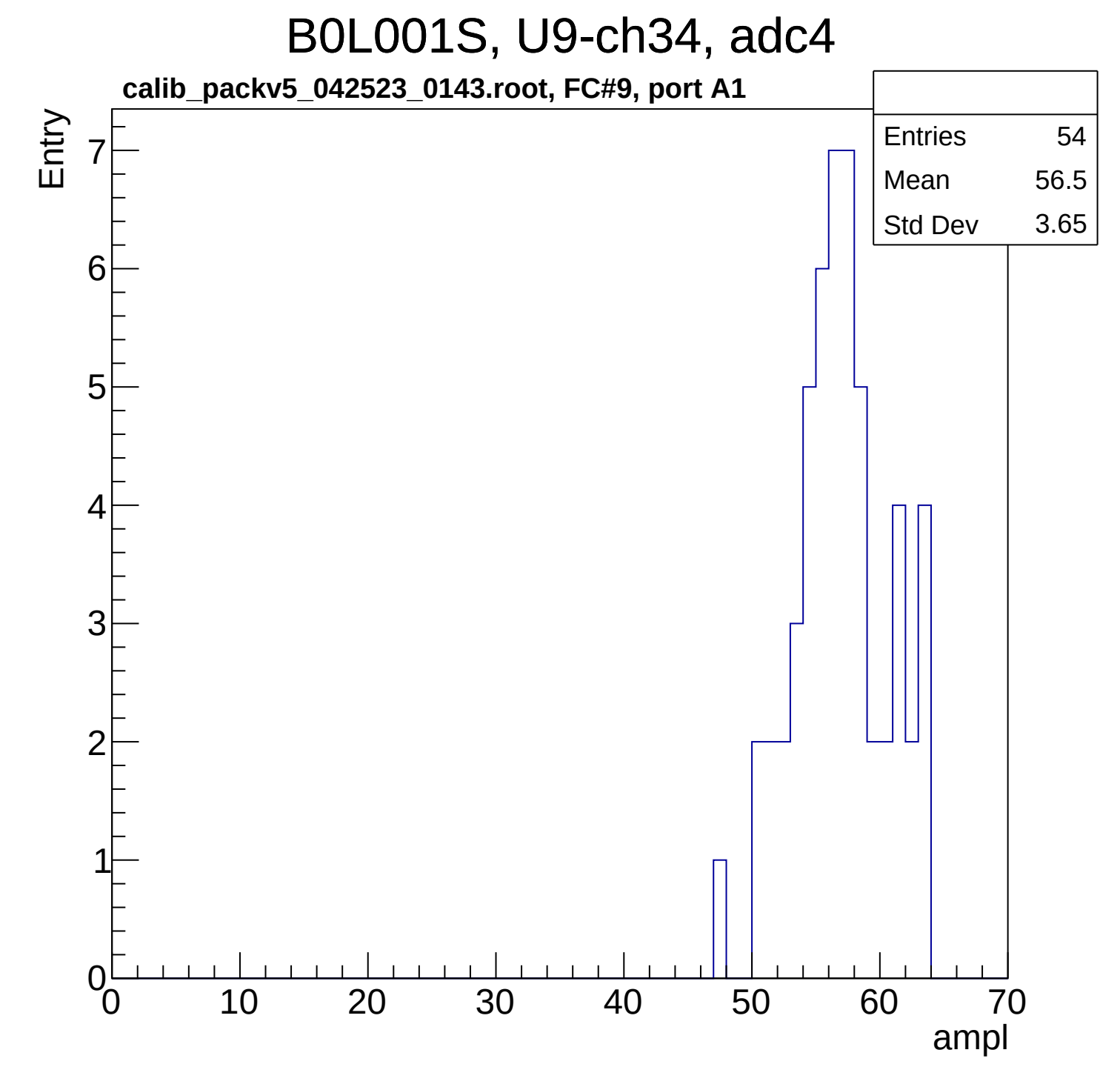
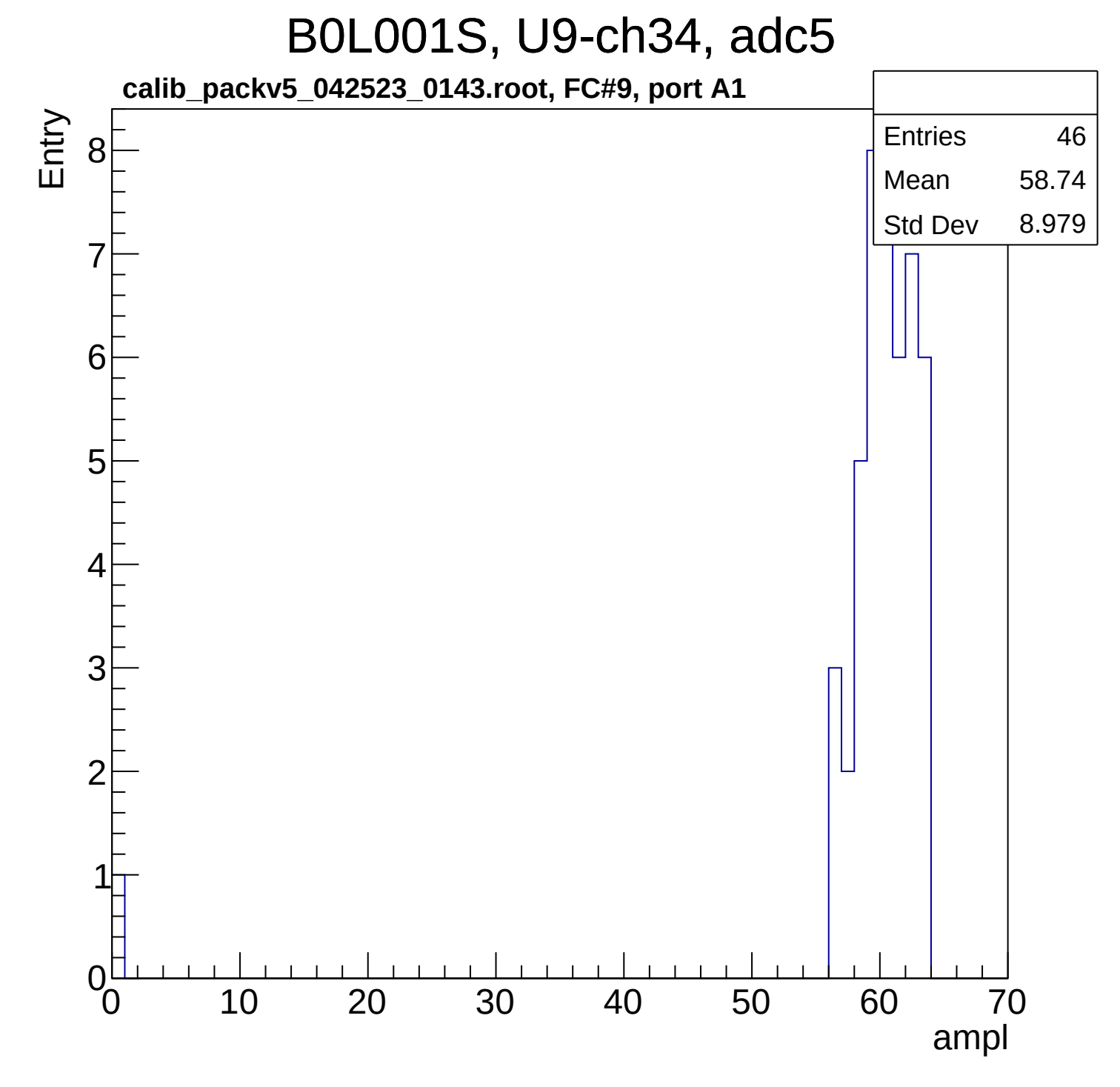

B0L001S, U9-ch34, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.5
Std Dev 3.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch34, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.74
Std Dev 8.979
Entry
ampl
0
1
2
3
4
5
6
7
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60
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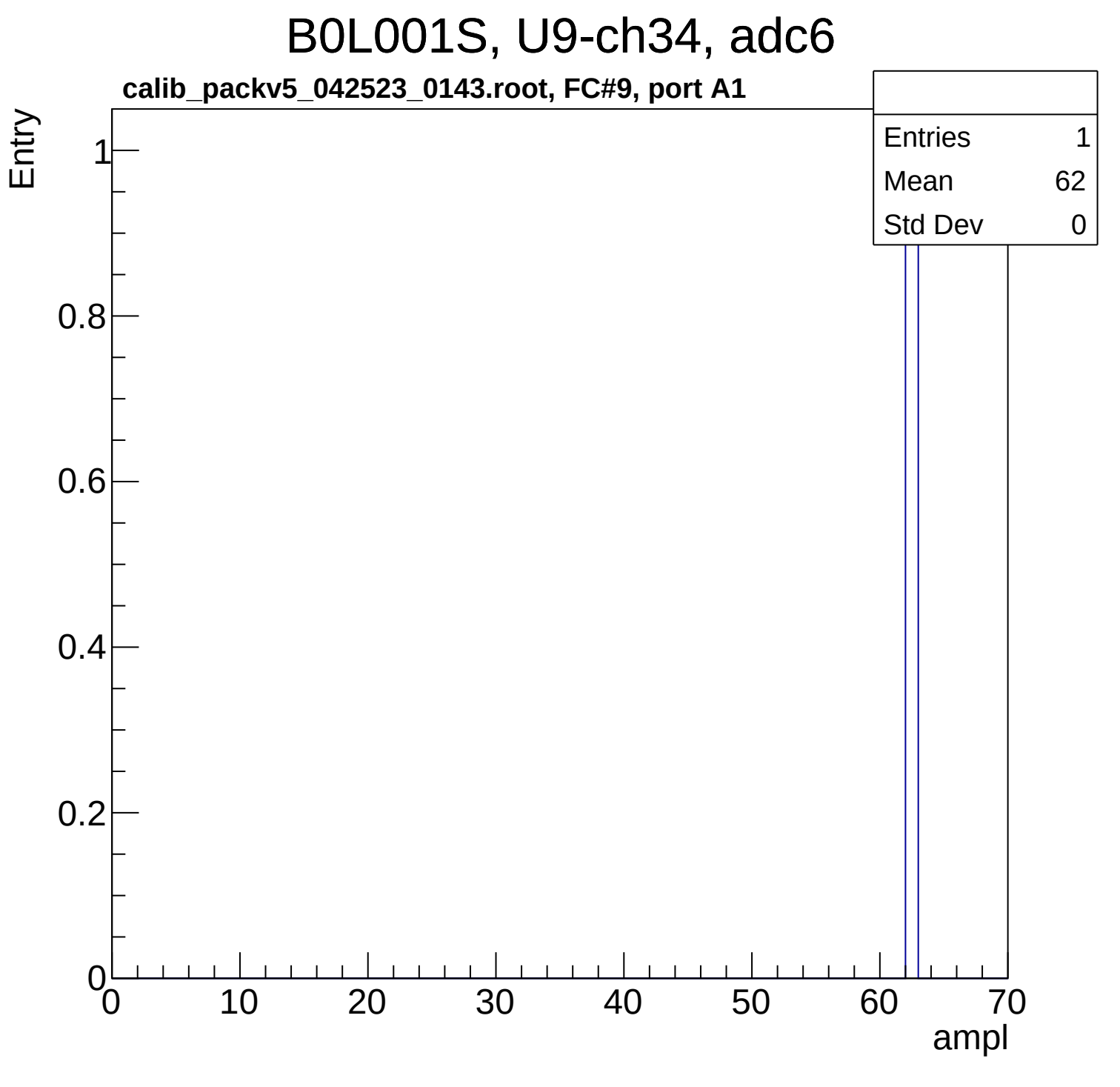
B0L001S, U9-ch34, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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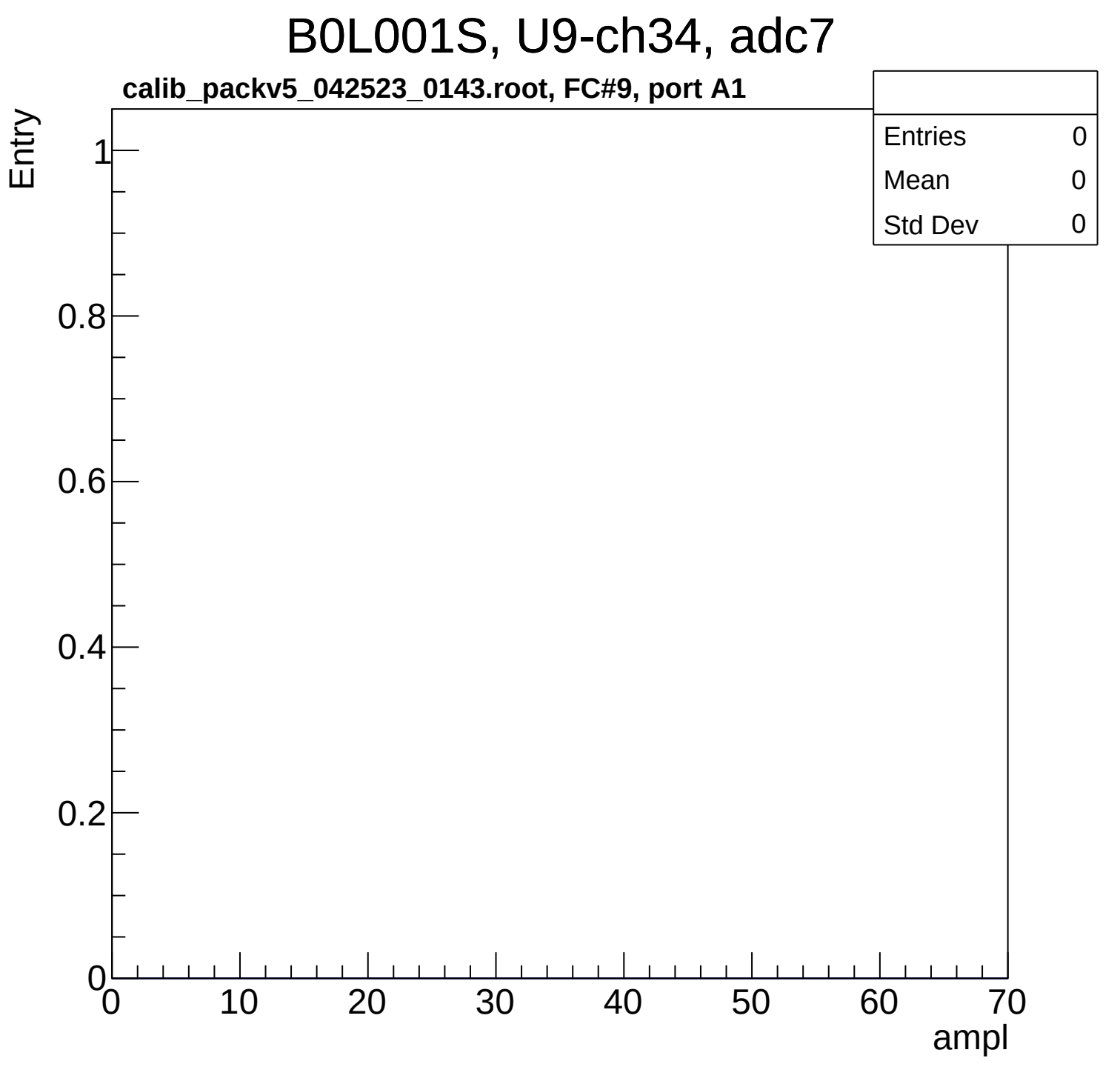
40
50
60
70
ampl


B0L001S, U9-ch34, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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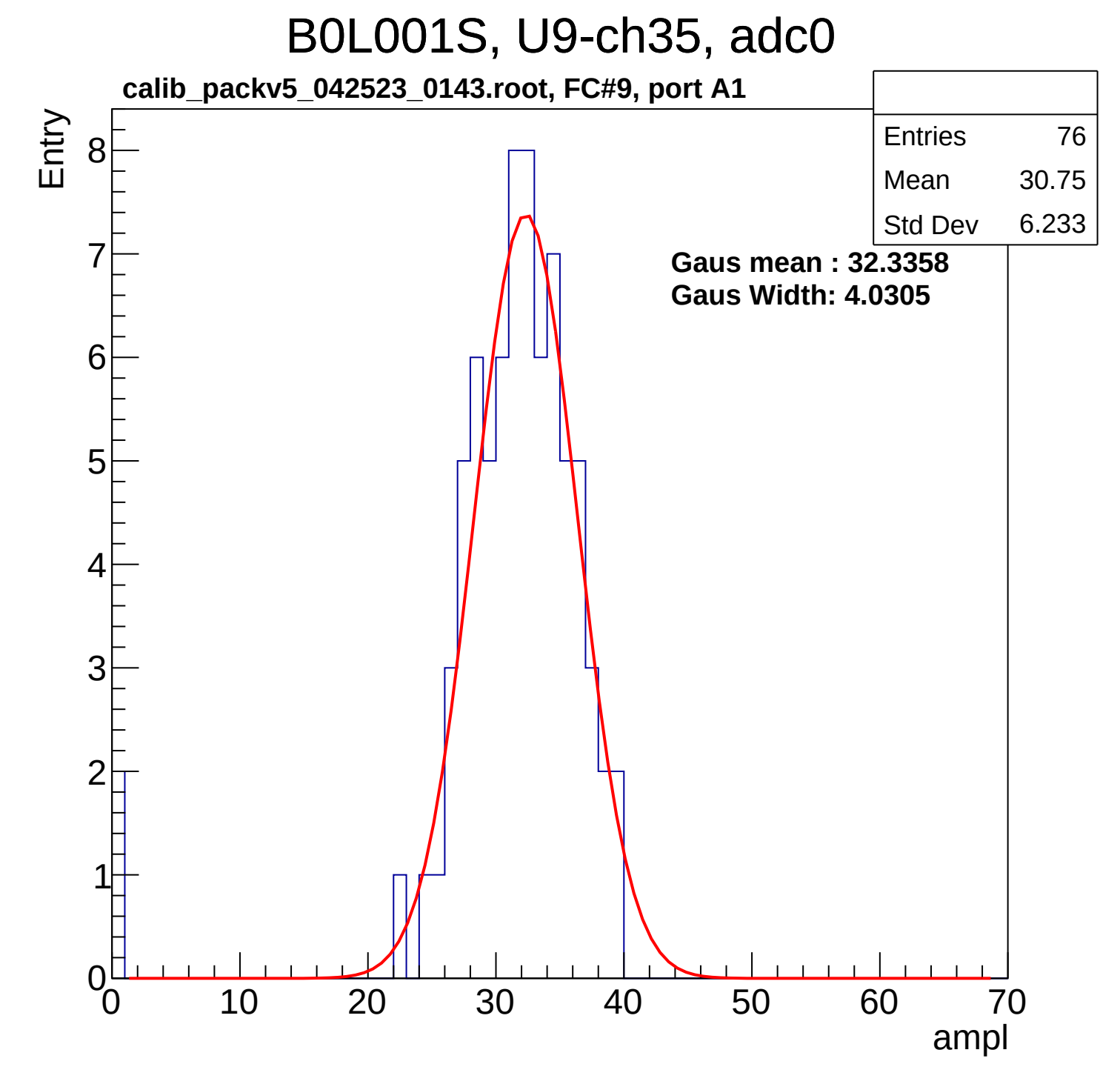
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch35, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 30.75
Std Dev 6.233
Gaus mean : 32.3358
Gaus Width: 4.0305
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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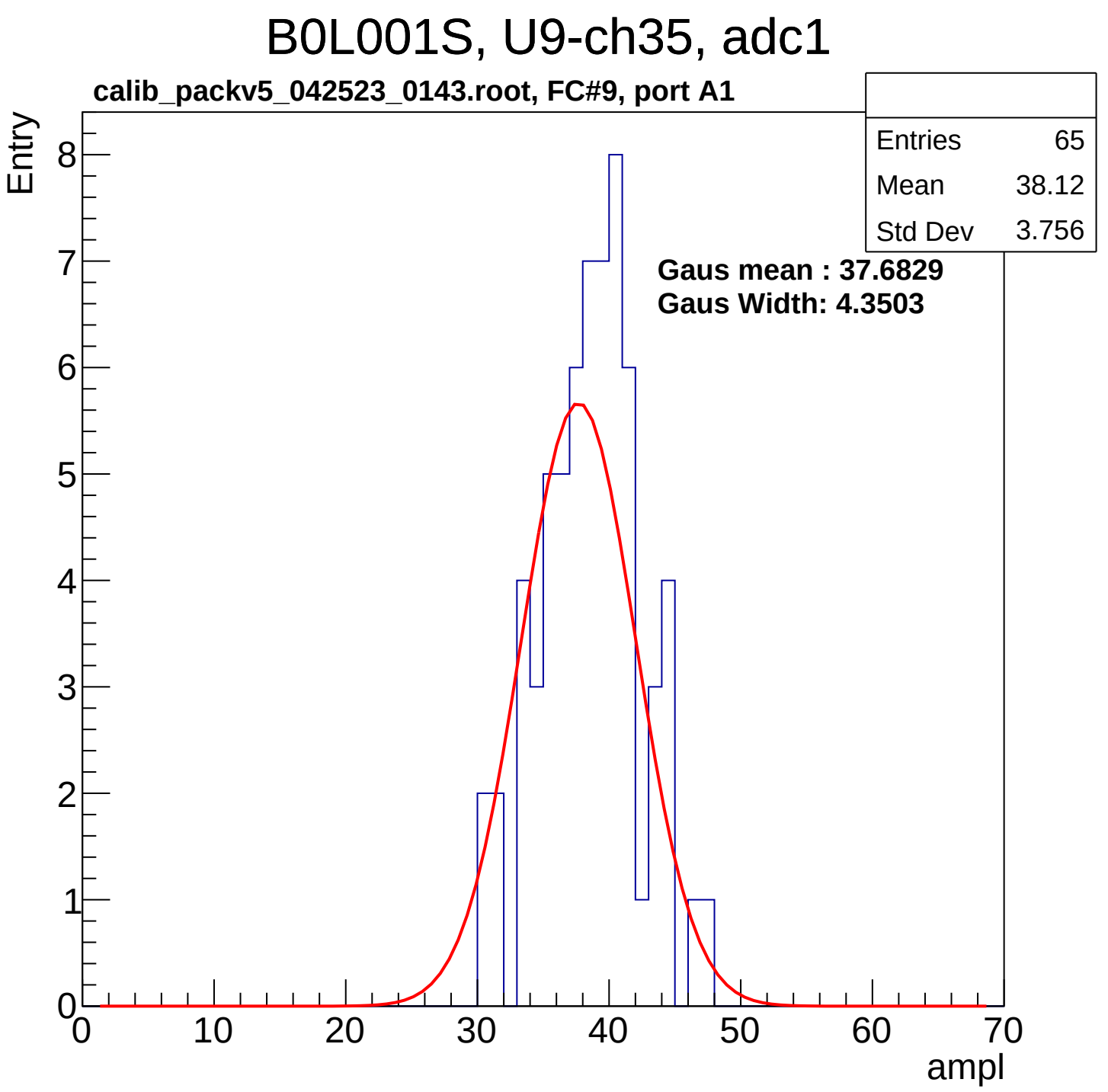
B0L001S, U9-ch35, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38.12
Std Dev 3.756
Gaus mean : 37.6829
Gaus Width: 4.3503
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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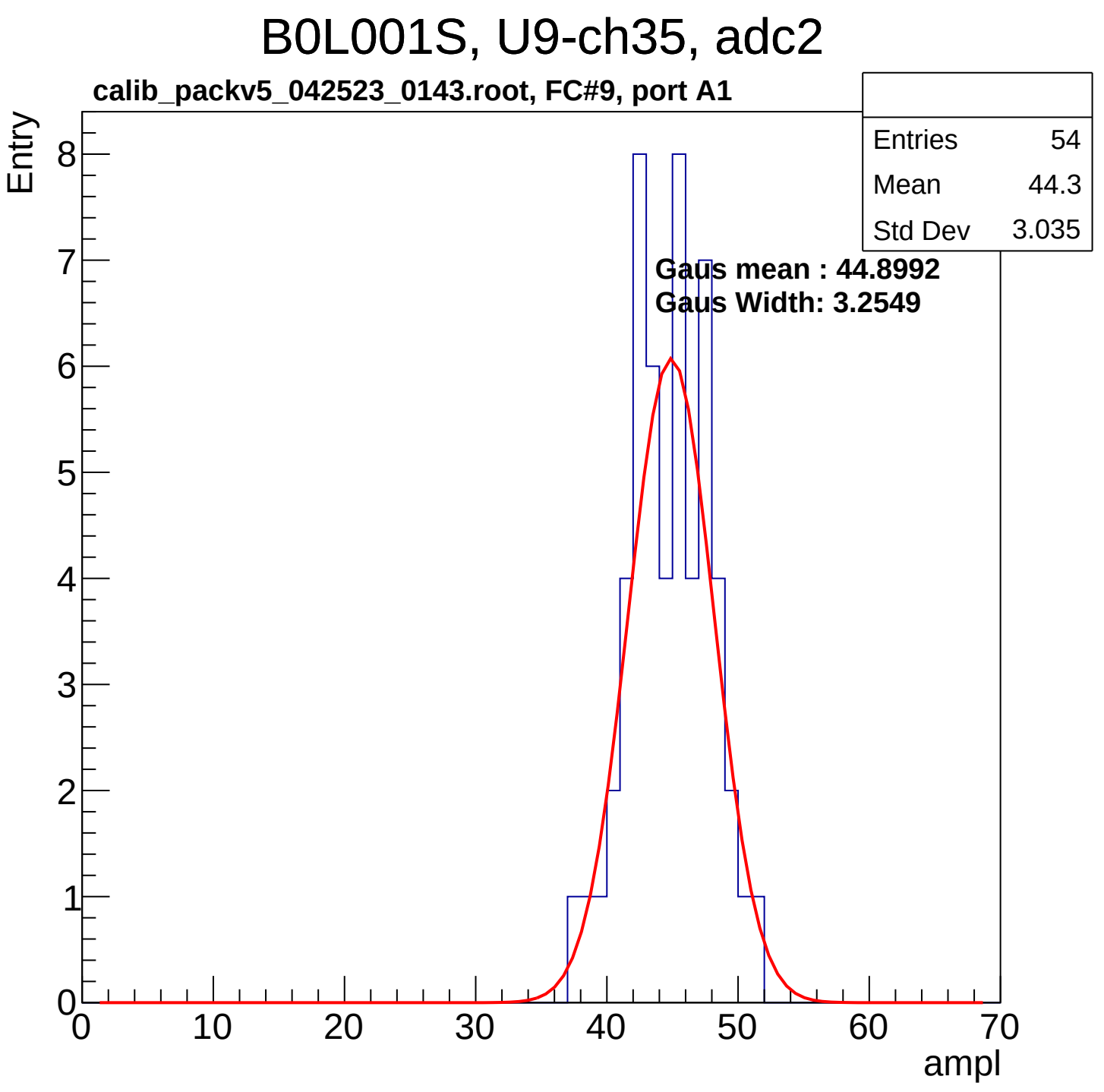
B0L001S, U9-ch35, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 44.3
Std Dev 3.035
Gaus mean : 44.8992
Gaus Width: 3.2549
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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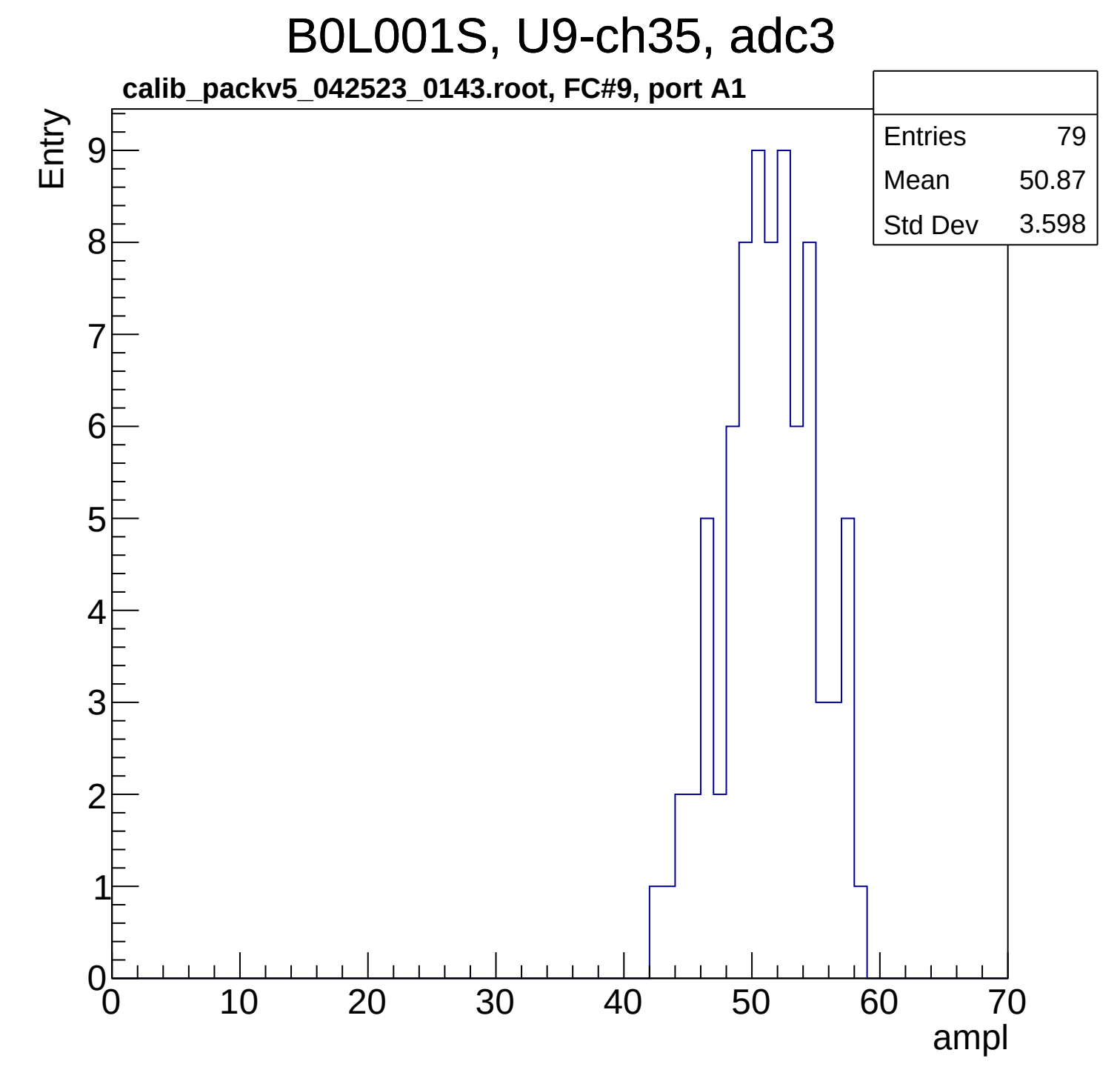
B0L001S, U9-ch35, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 50.87
Std Dev 3.598
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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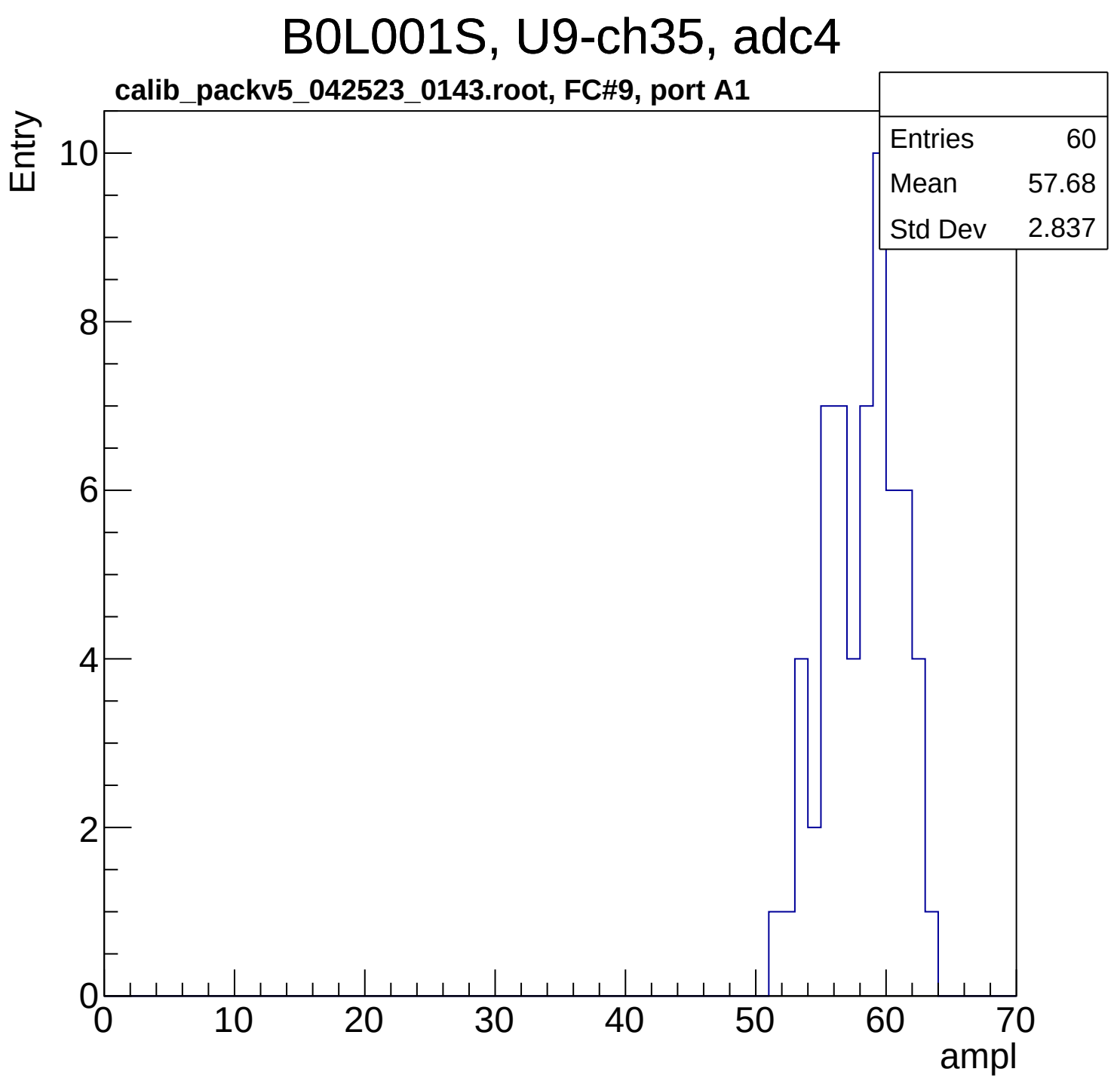
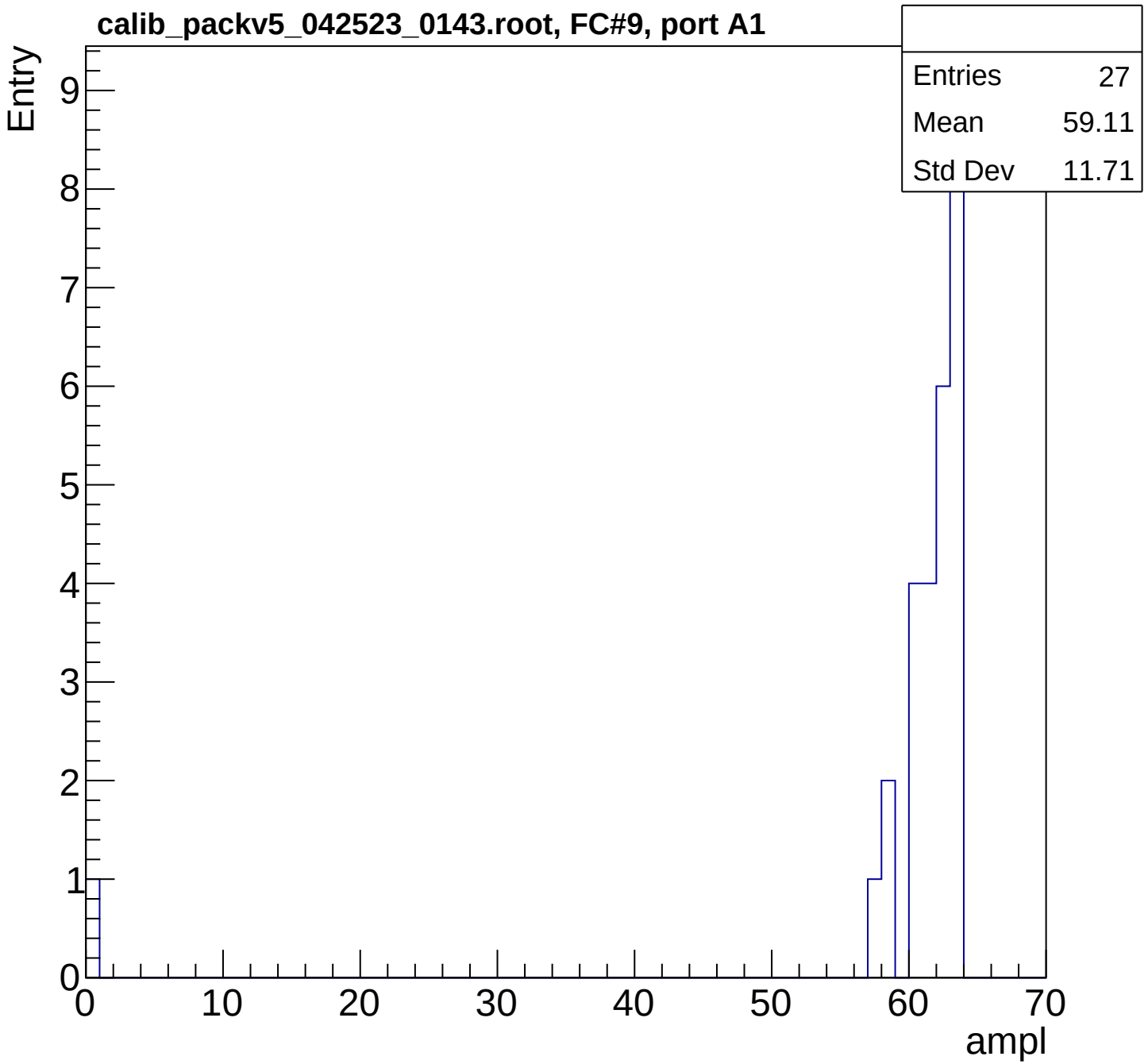
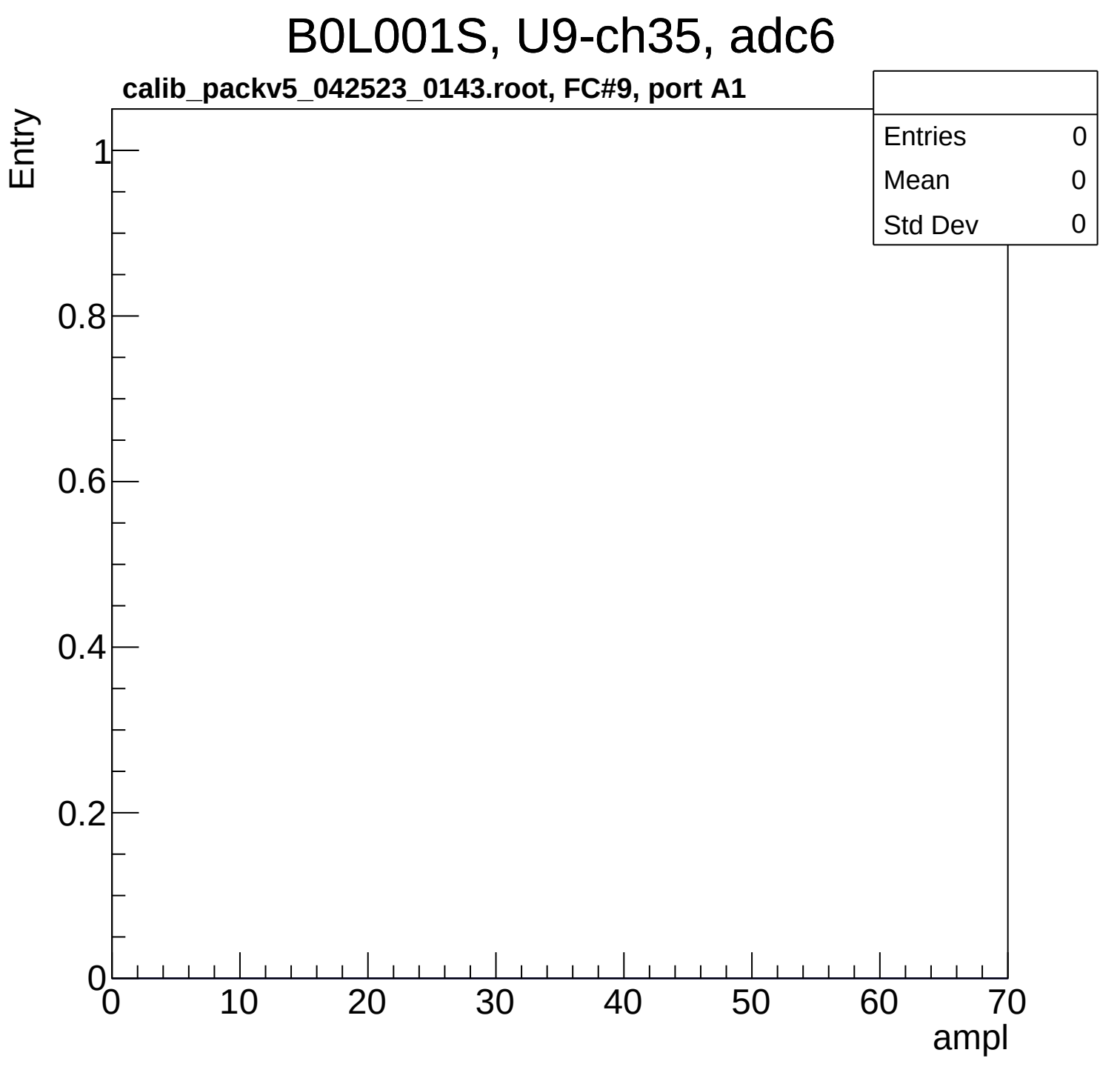
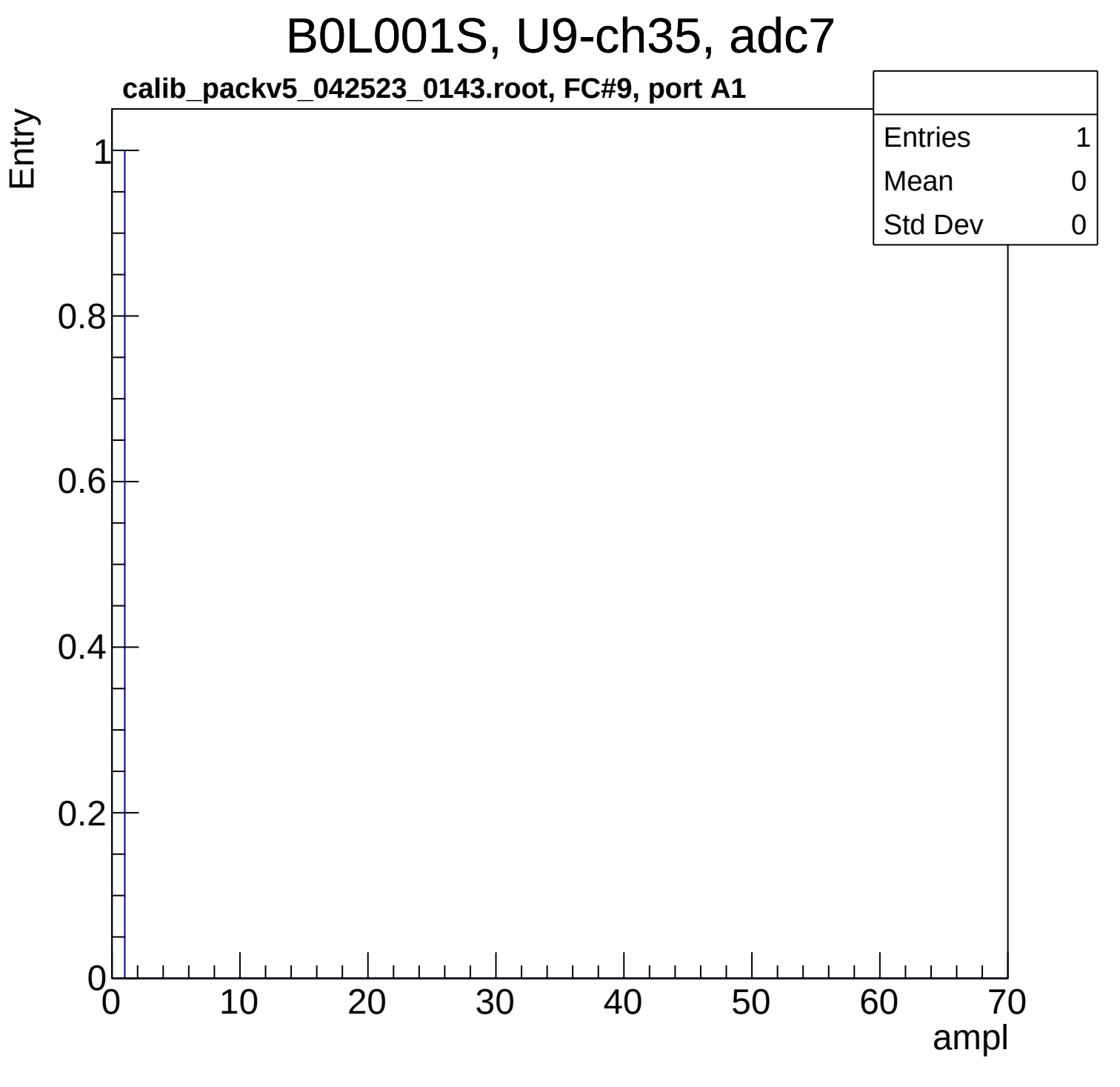

B0L001S, U9-ch35, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.68
Std Dev 2.837
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch35, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 59.11
Std Dev 11.71
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch35, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch35, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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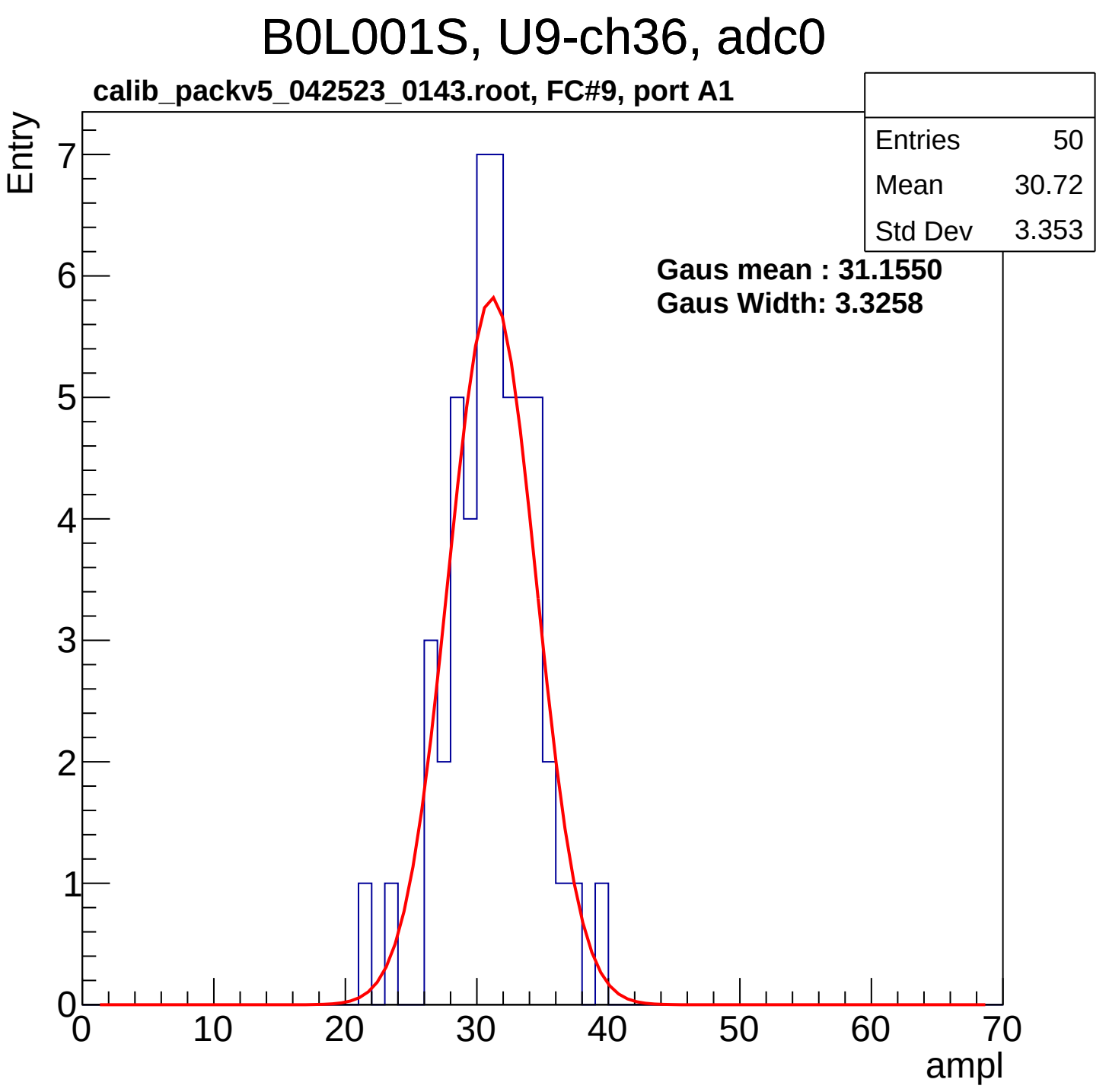
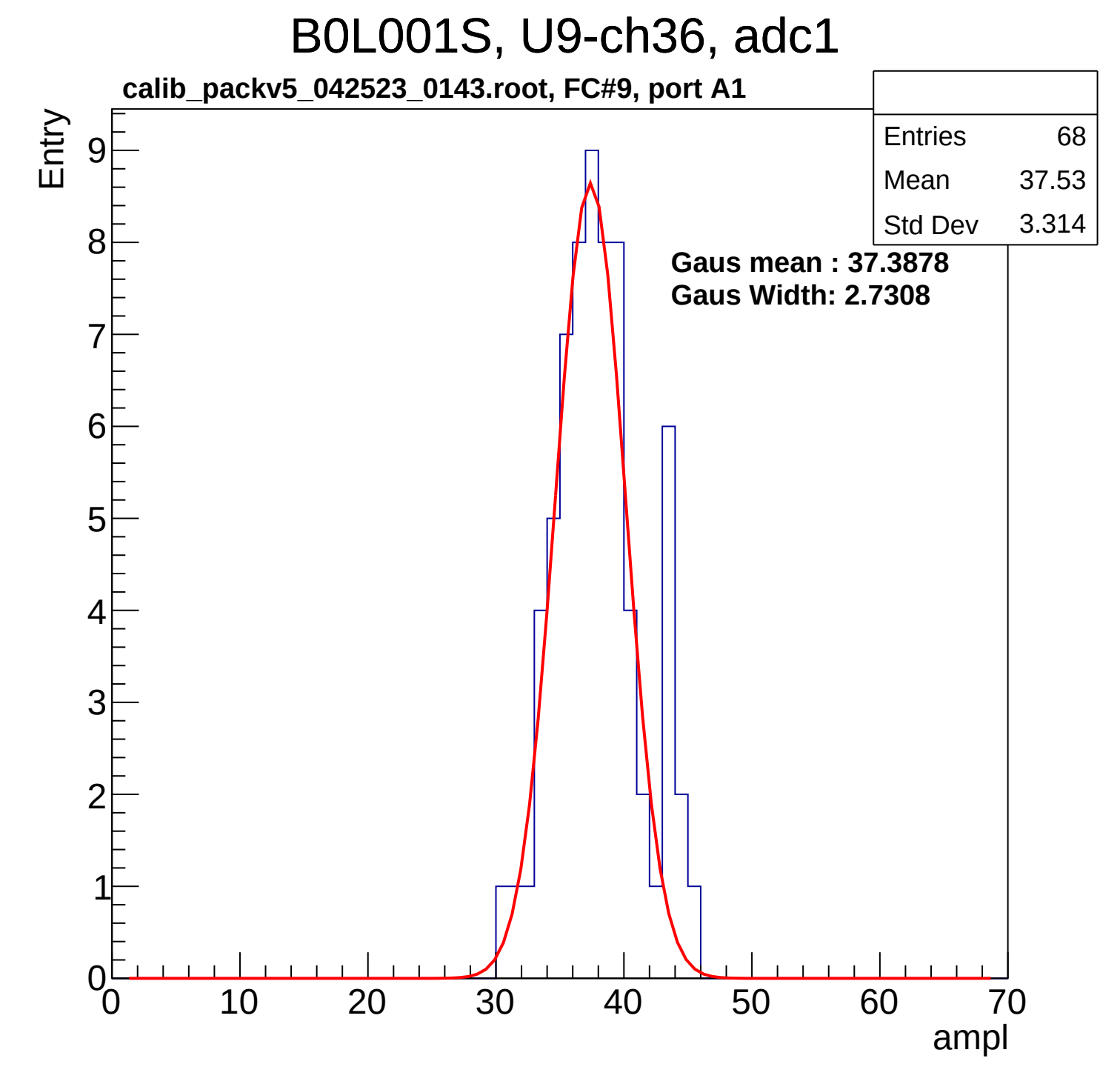
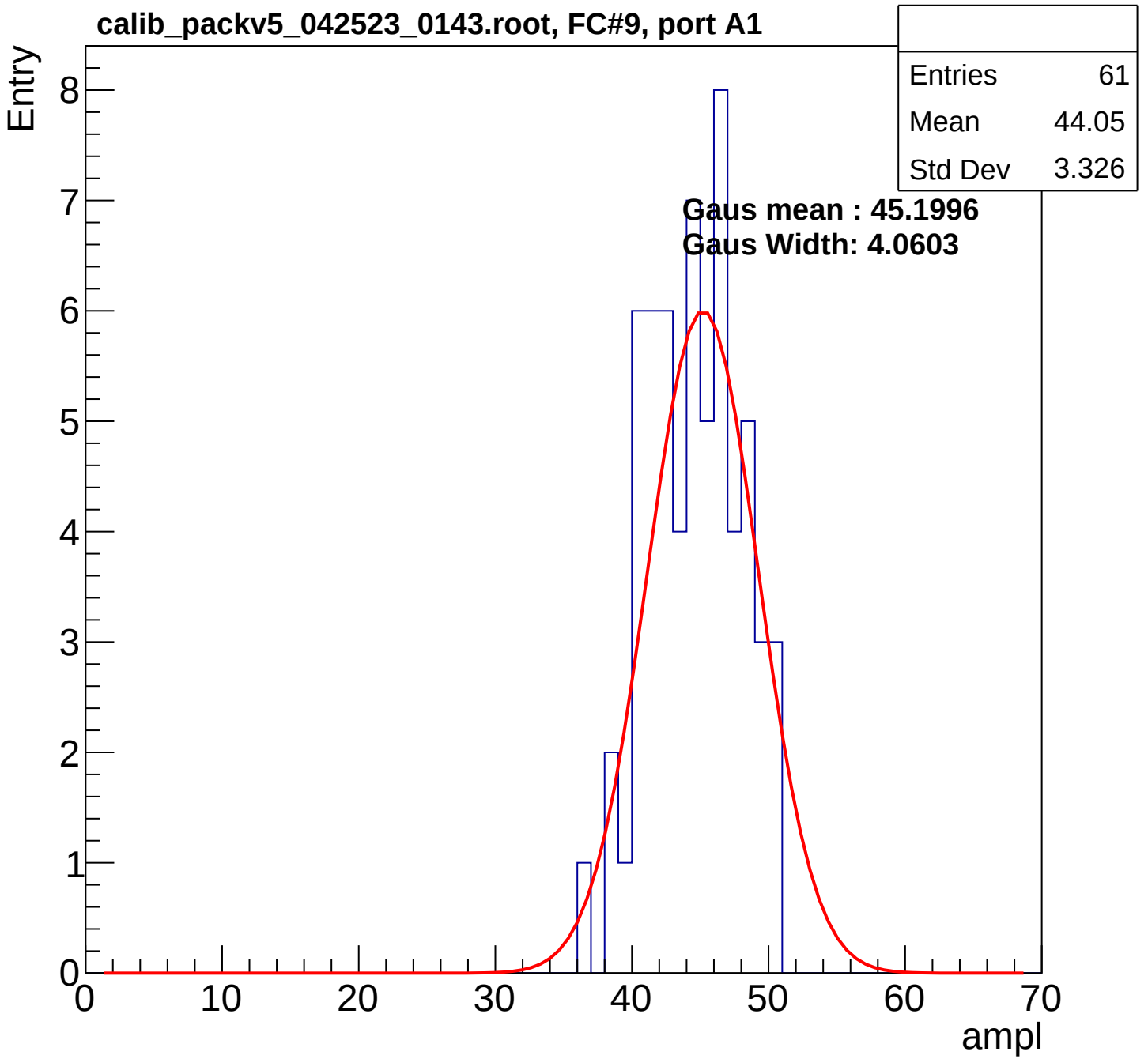
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch36, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 30.72
Std Dev 3.353
Gaus mean : 31.1550
Gaus Width: 3.3258
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch36, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 37.53
Std Dev 3.314
Gaus mean : 37.3878
Gaus Width: 2.7308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch36, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.05
Std Dev 3.326
Gaus mean : 45.1996
Gaus Width: 4.0603
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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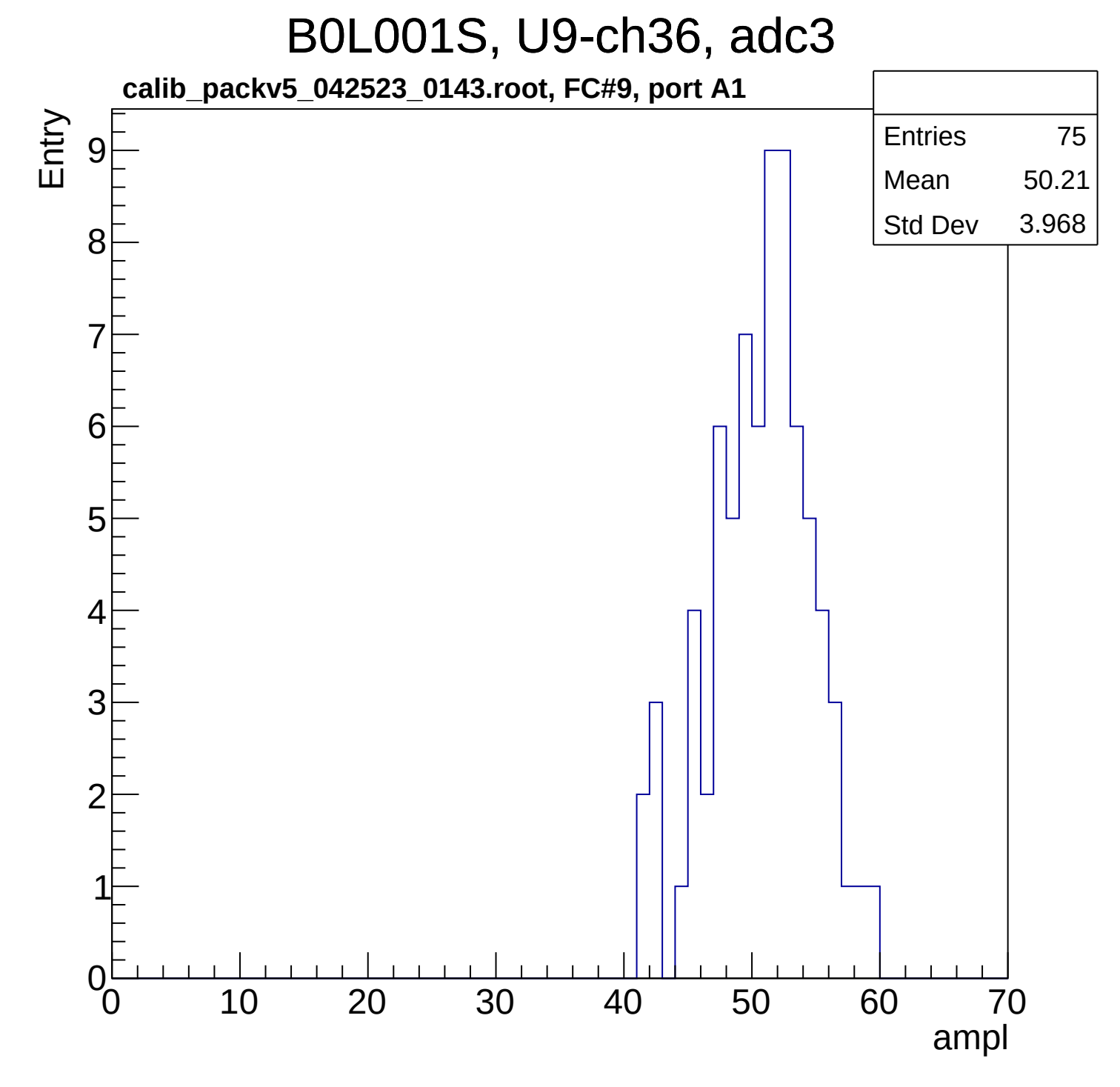
B0L001S, U9-ch36, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 50.21
Std Dev 3.968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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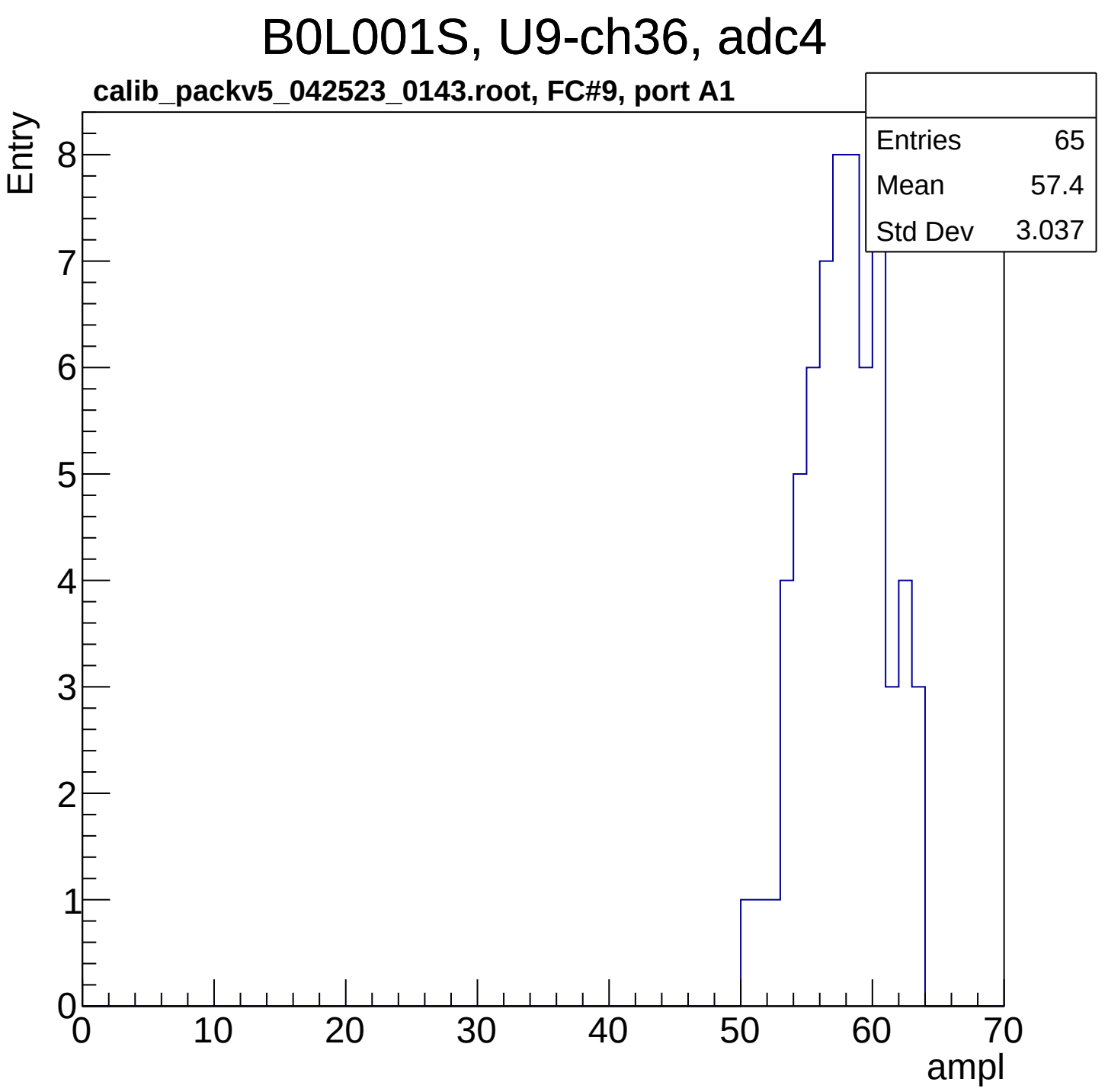
B0L001S, U9-ch36, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 57.4
Std Dev 3.037
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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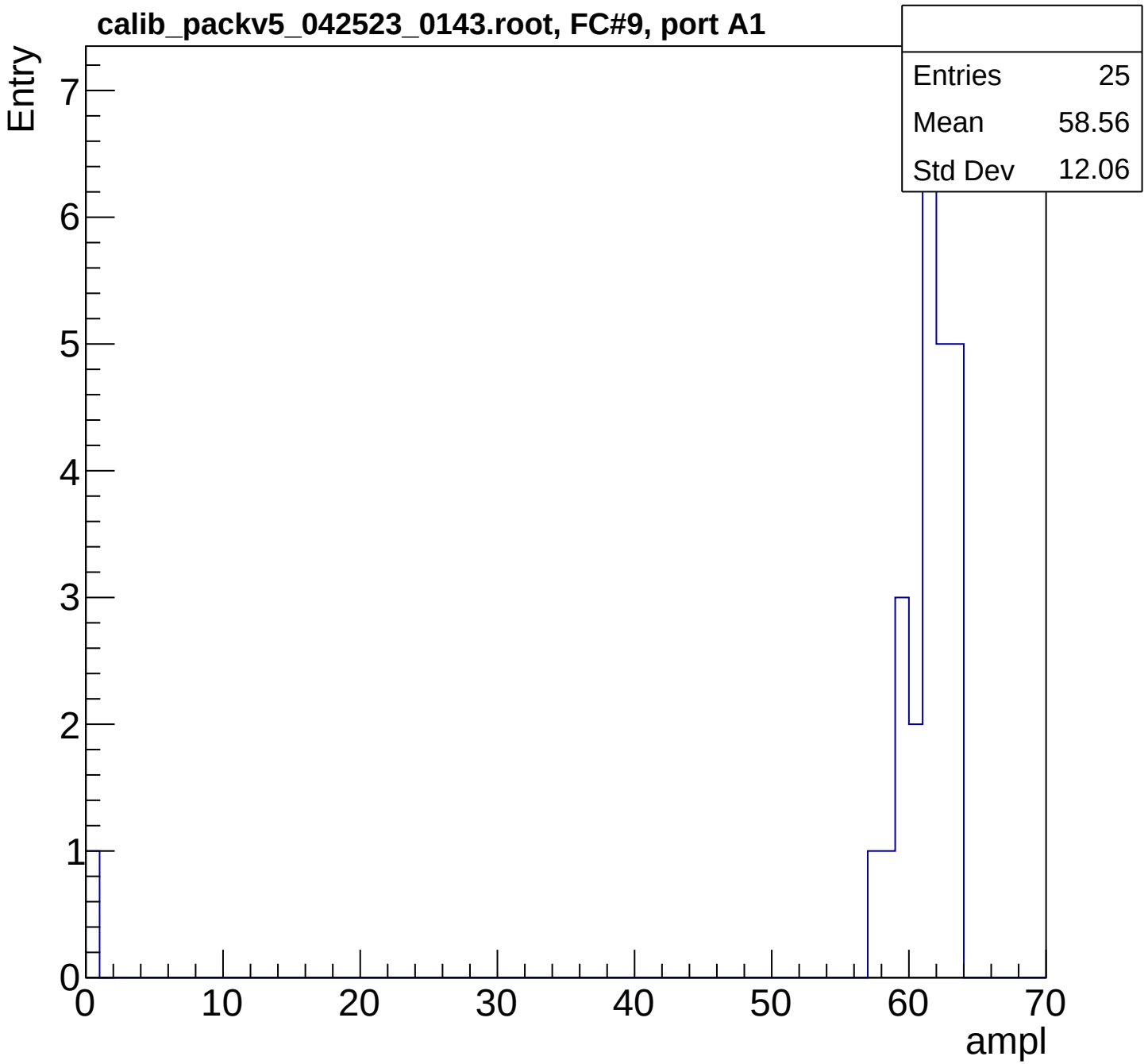
B0L001S, U9-ch36, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.56
Std Dev 12.06
Entry
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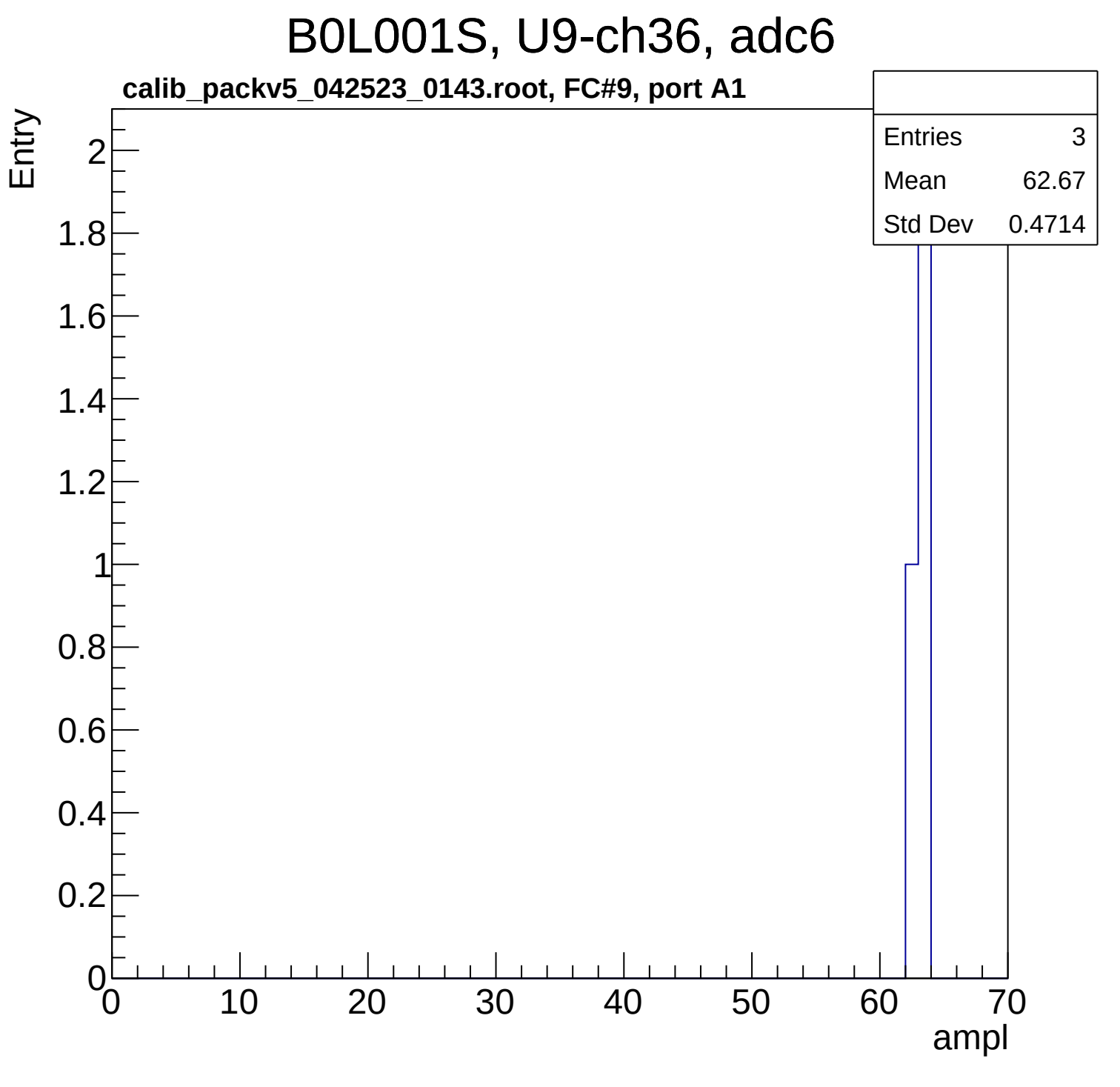
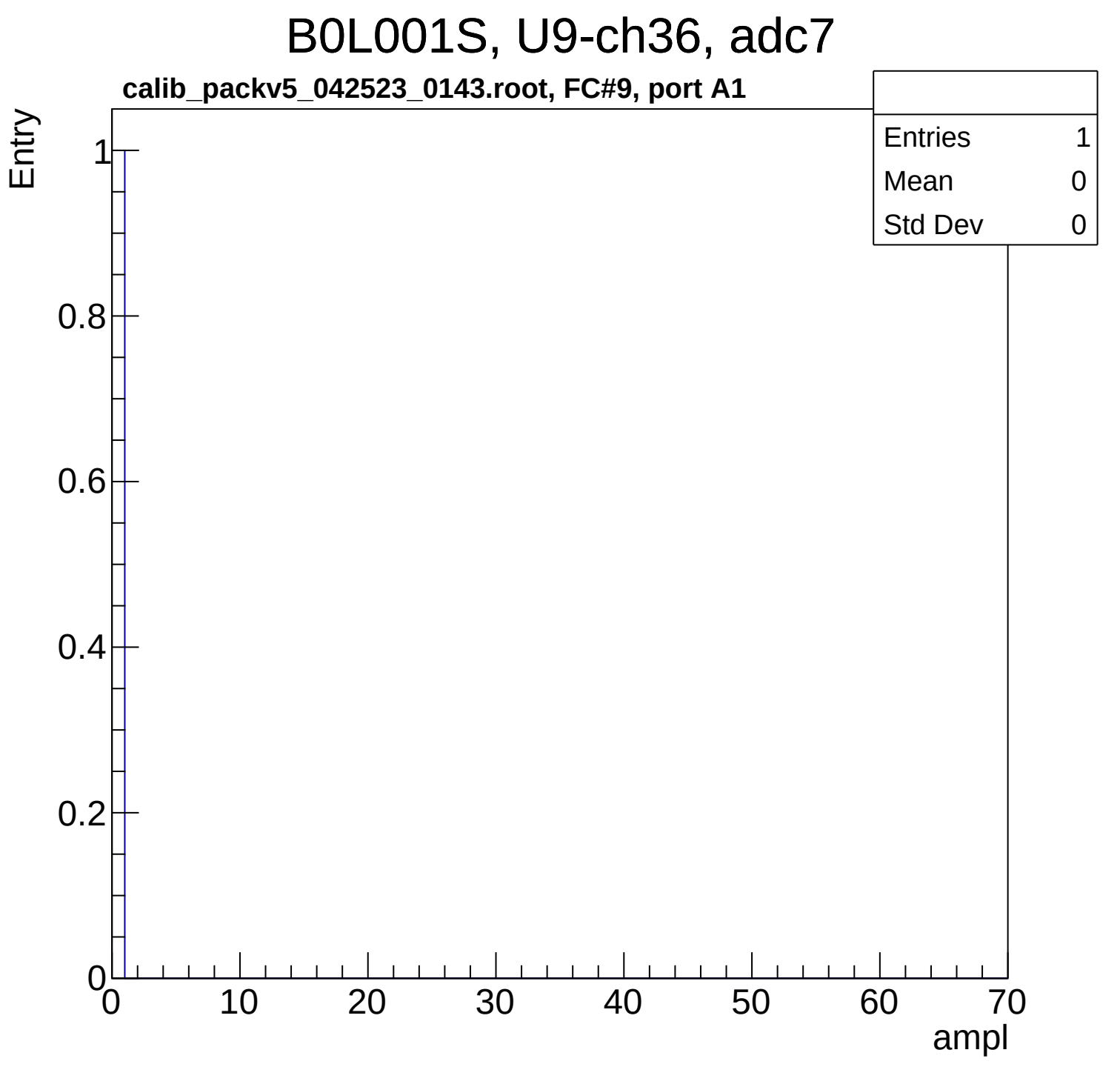
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch36, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch36, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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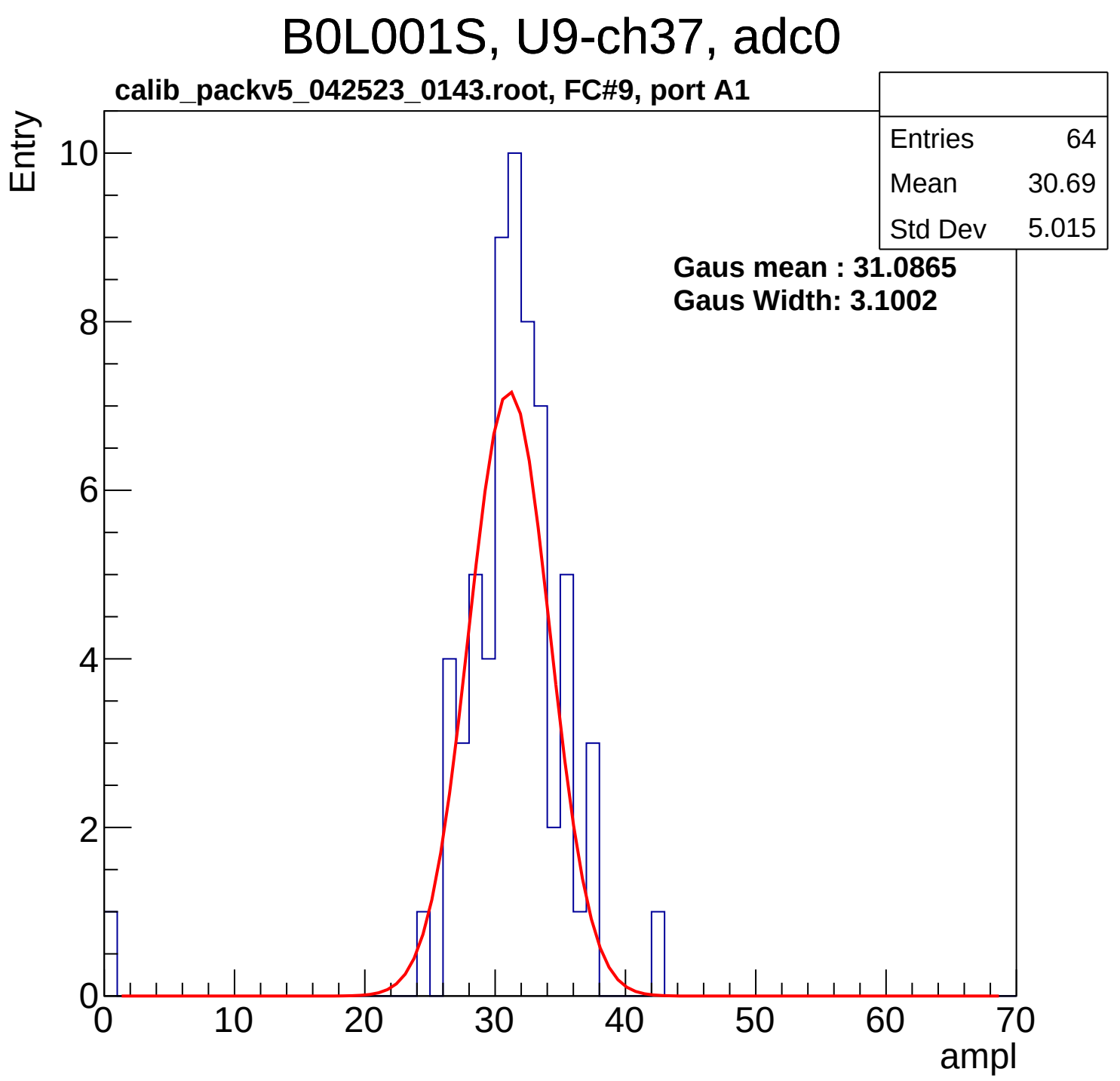
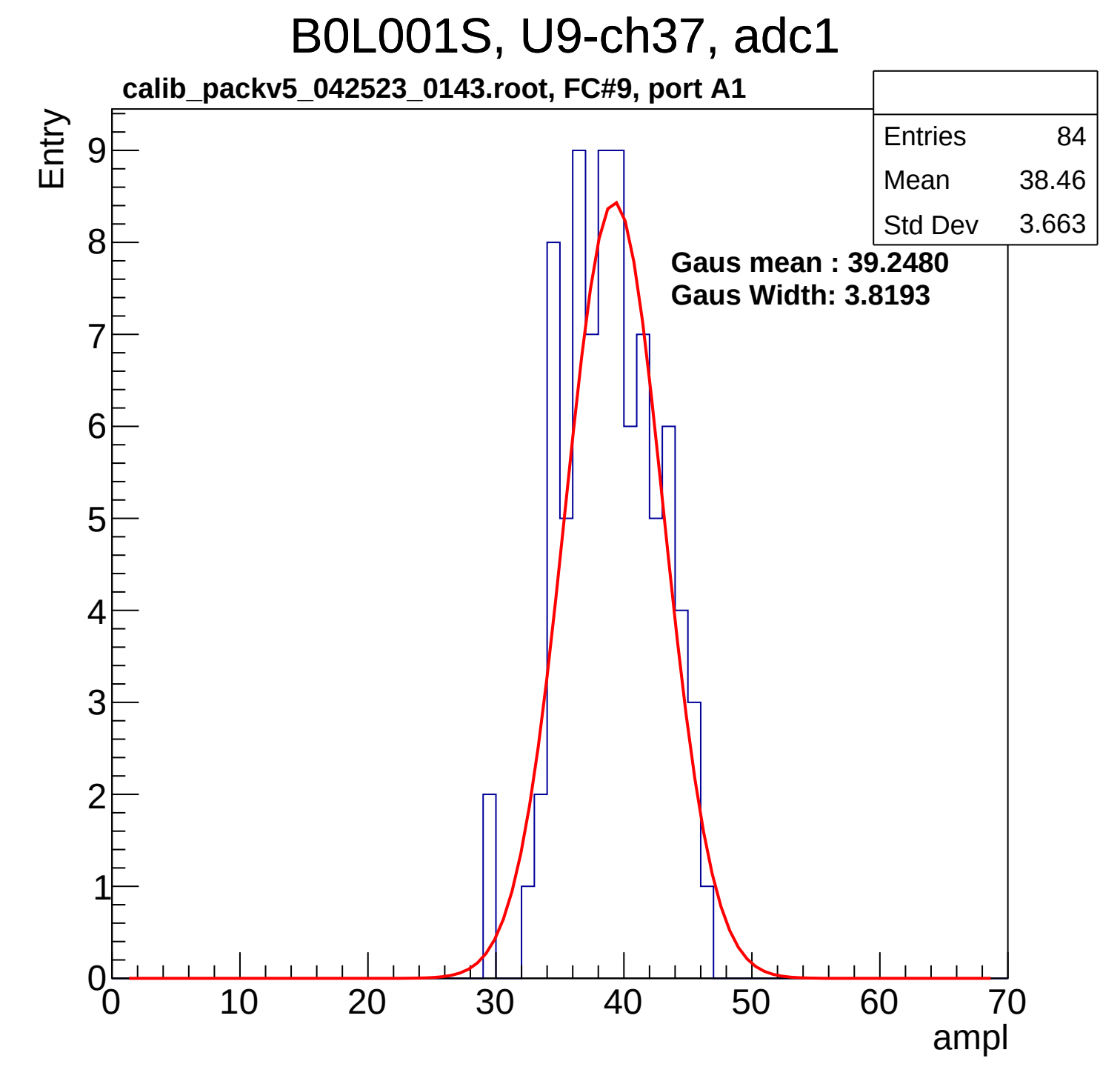
30
40
50
60
70
ampl


B0L001S, U9-ch37, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.69
Std Dev 5.015
Gaus mean : 31.0865
Gaus Width: 3.1002
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch37, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 38.46
Std Dev 3.663
Gaus mean : 39.2480
Gaus Width: 3.8193
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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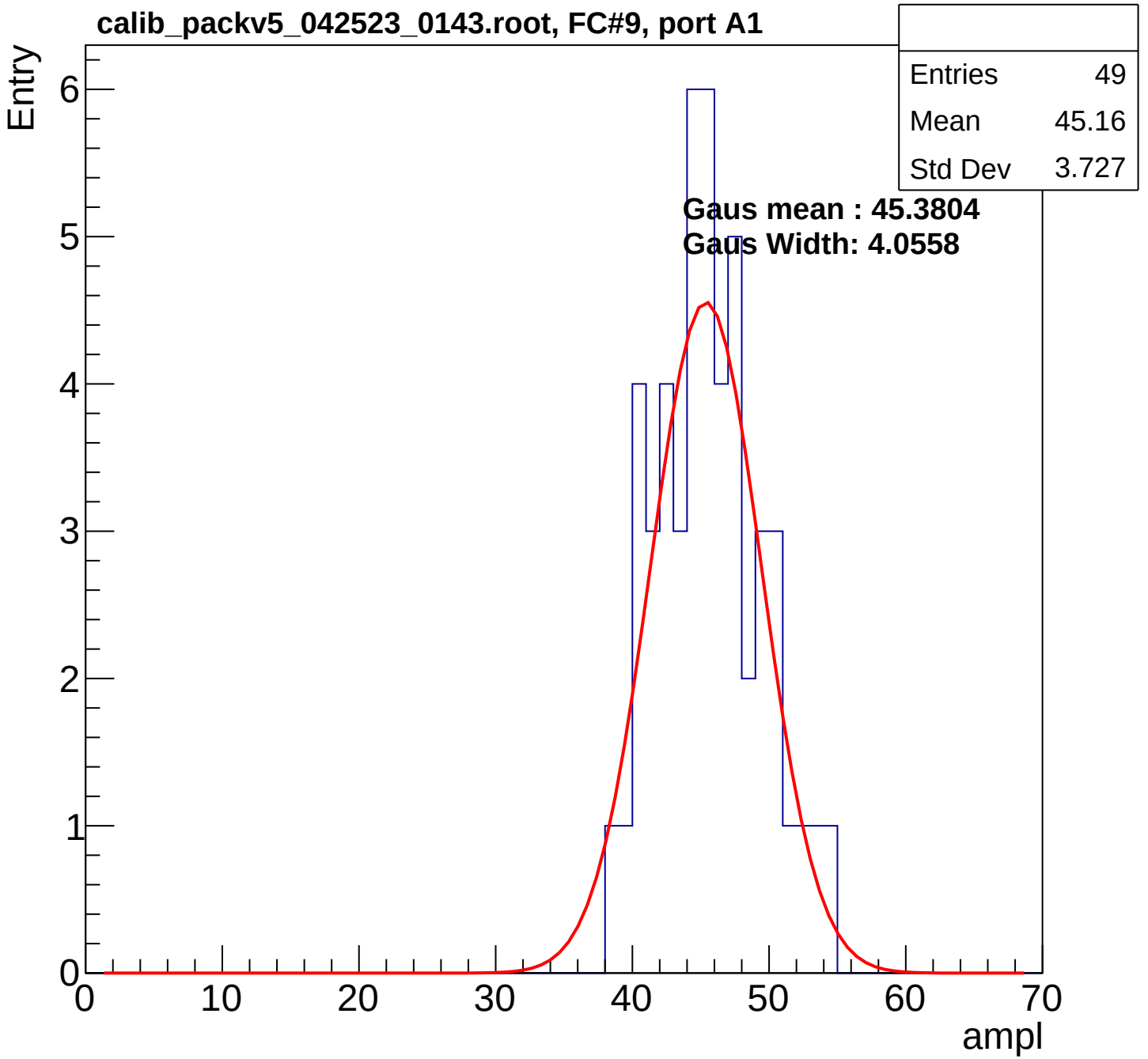
B0L001S, U9-ch37, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 45.16
Std Dev 3.727
Gaus mean : 45.3804
Gaus Width: 4.0558
Entry
ampl
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1
2
3
4
5
6
0
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60
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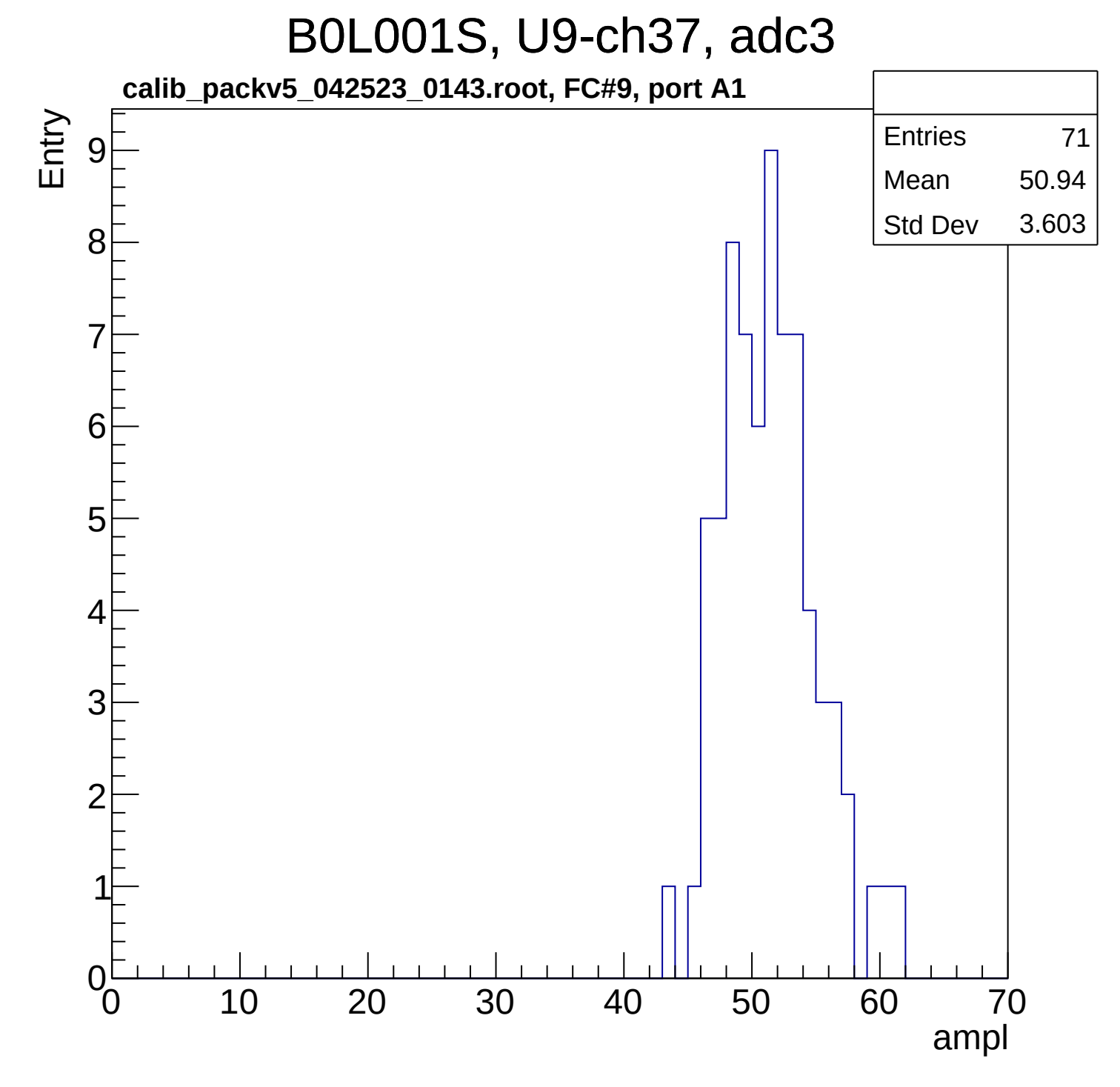
B0L001S, U9-ch37, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.94
Std Dev 3.603
Entry
ampl
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4
5
6
7
8
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0
10
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40
50
60
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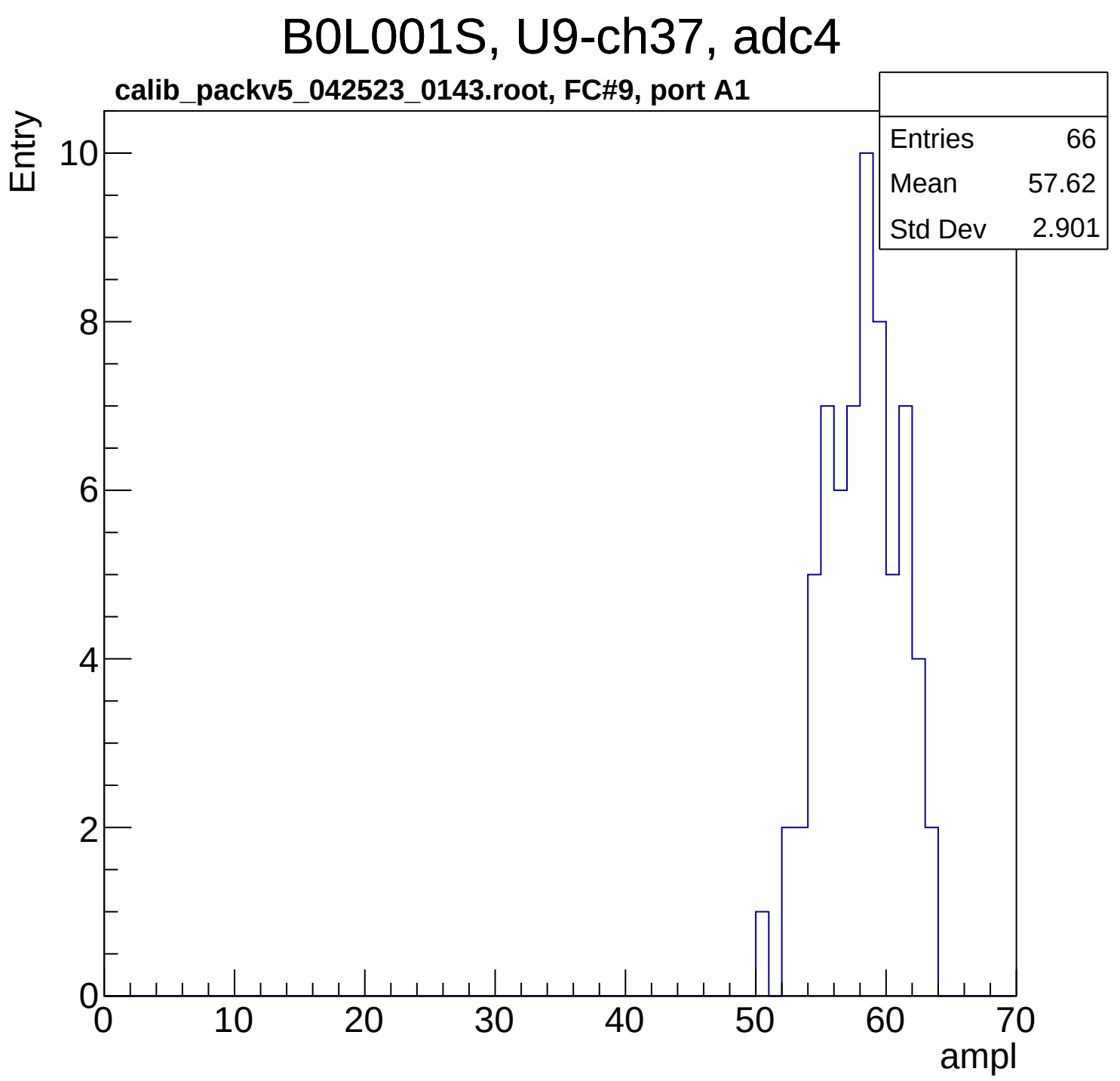
B0L001S, U9-ch37, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.62
Std Dev 2.901
Entry
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10
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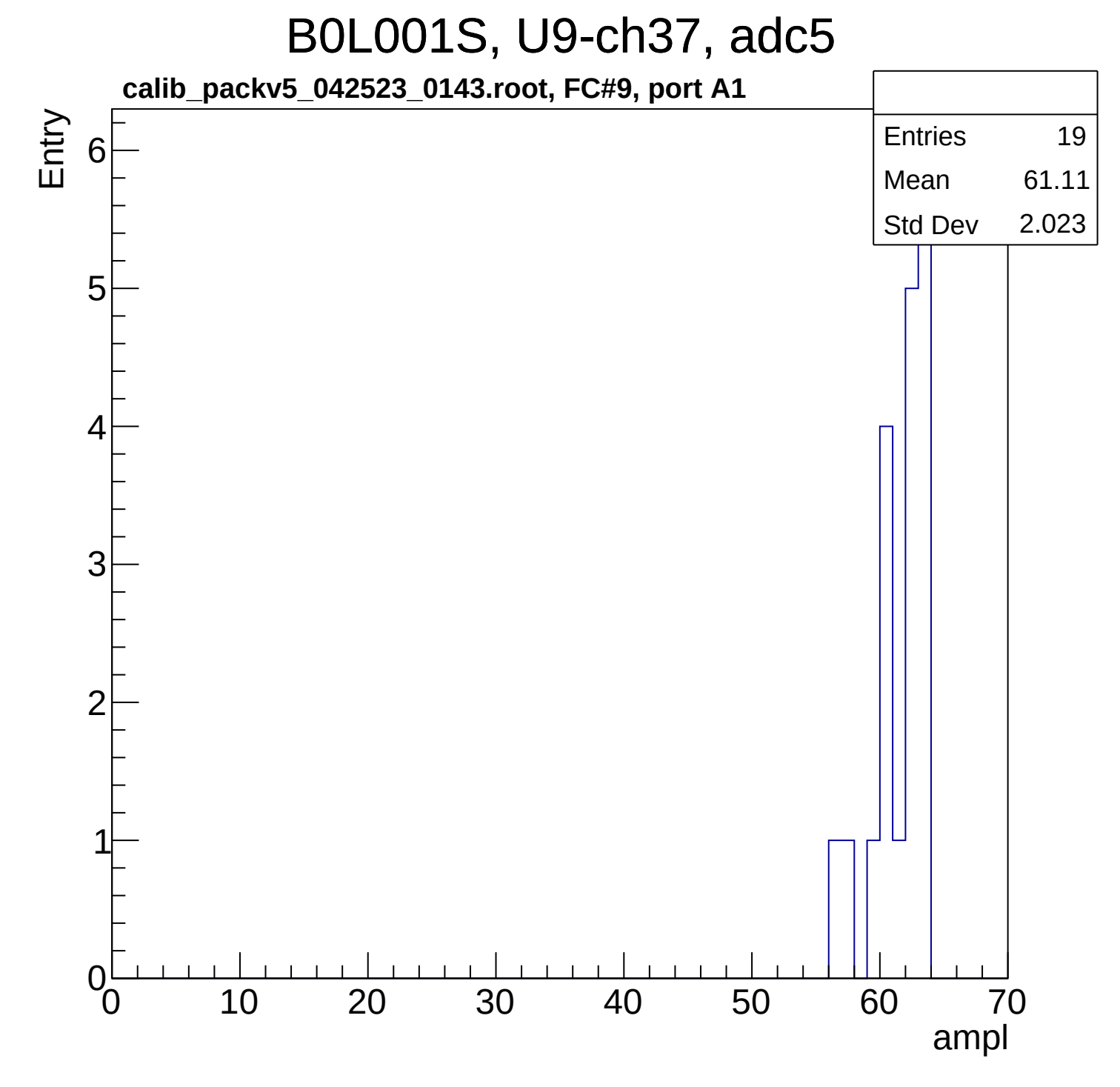
50
60
70
ampl


B0L001S, U9-ch37, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 61.11
Std Dev 2.023
Entry
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2
3
4
5
6
0
10
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30
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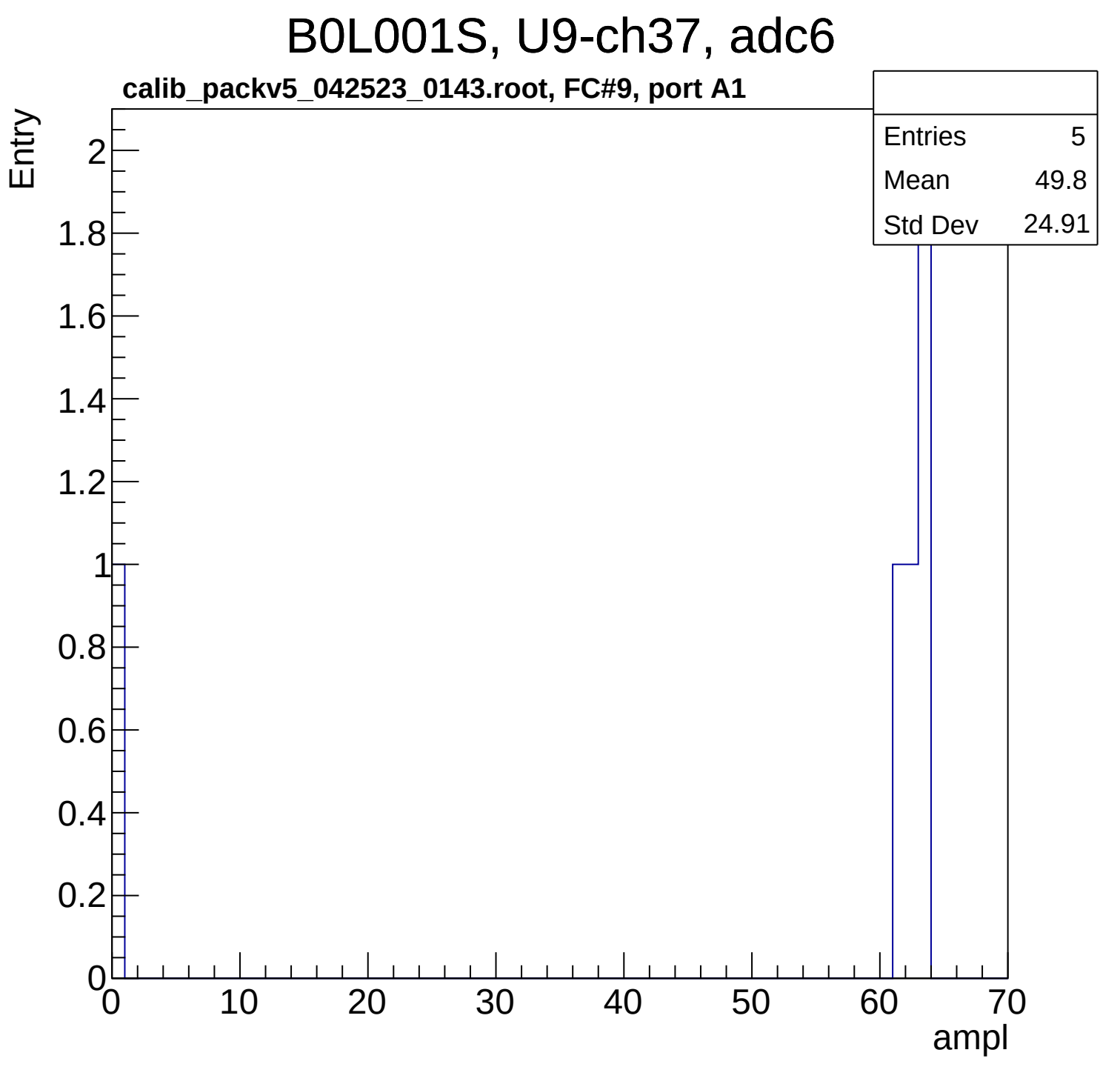
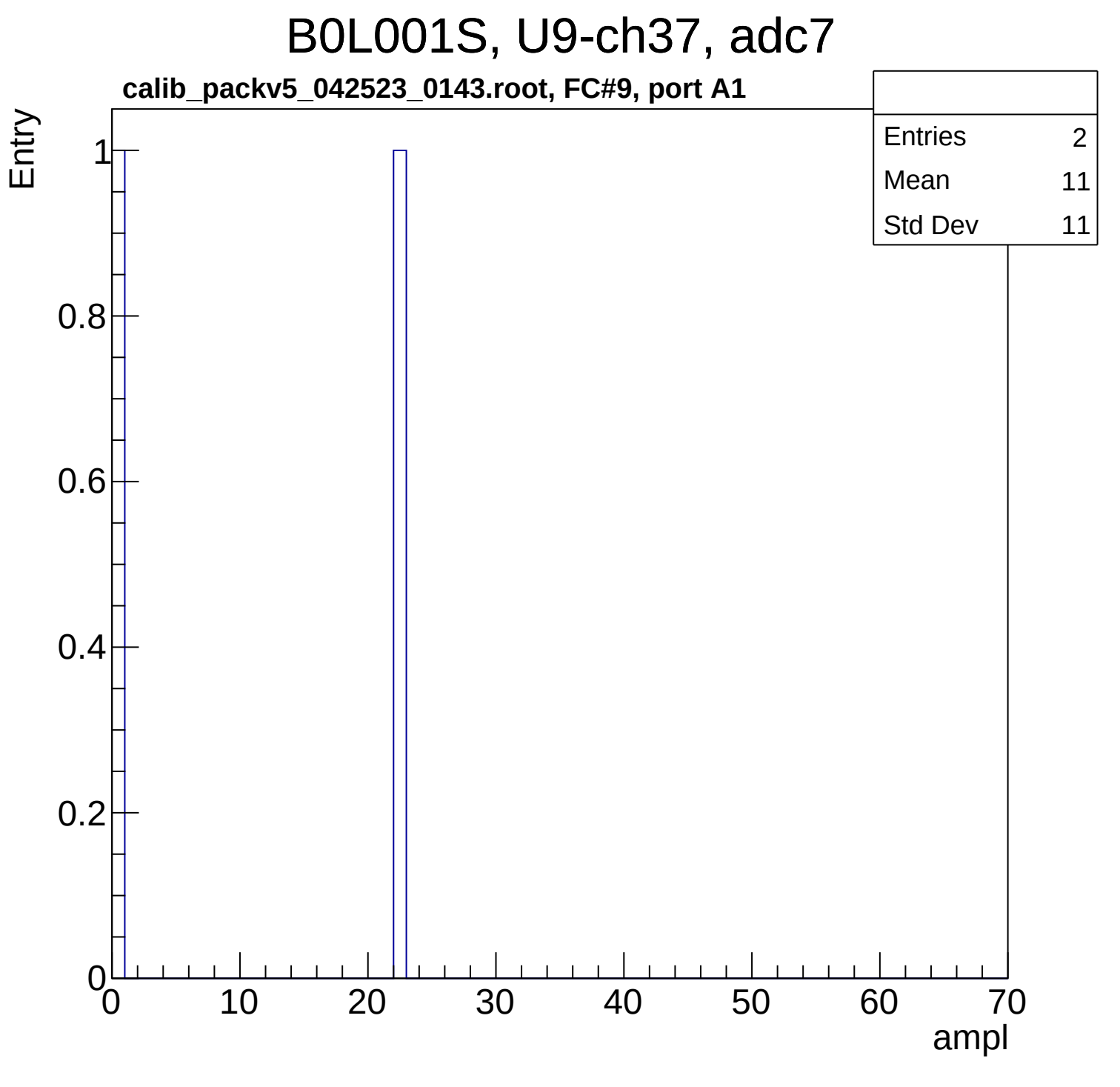
50
60
70
ampl


B0L001S, U9-ch37, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.8
Std Dev 24.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch37, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
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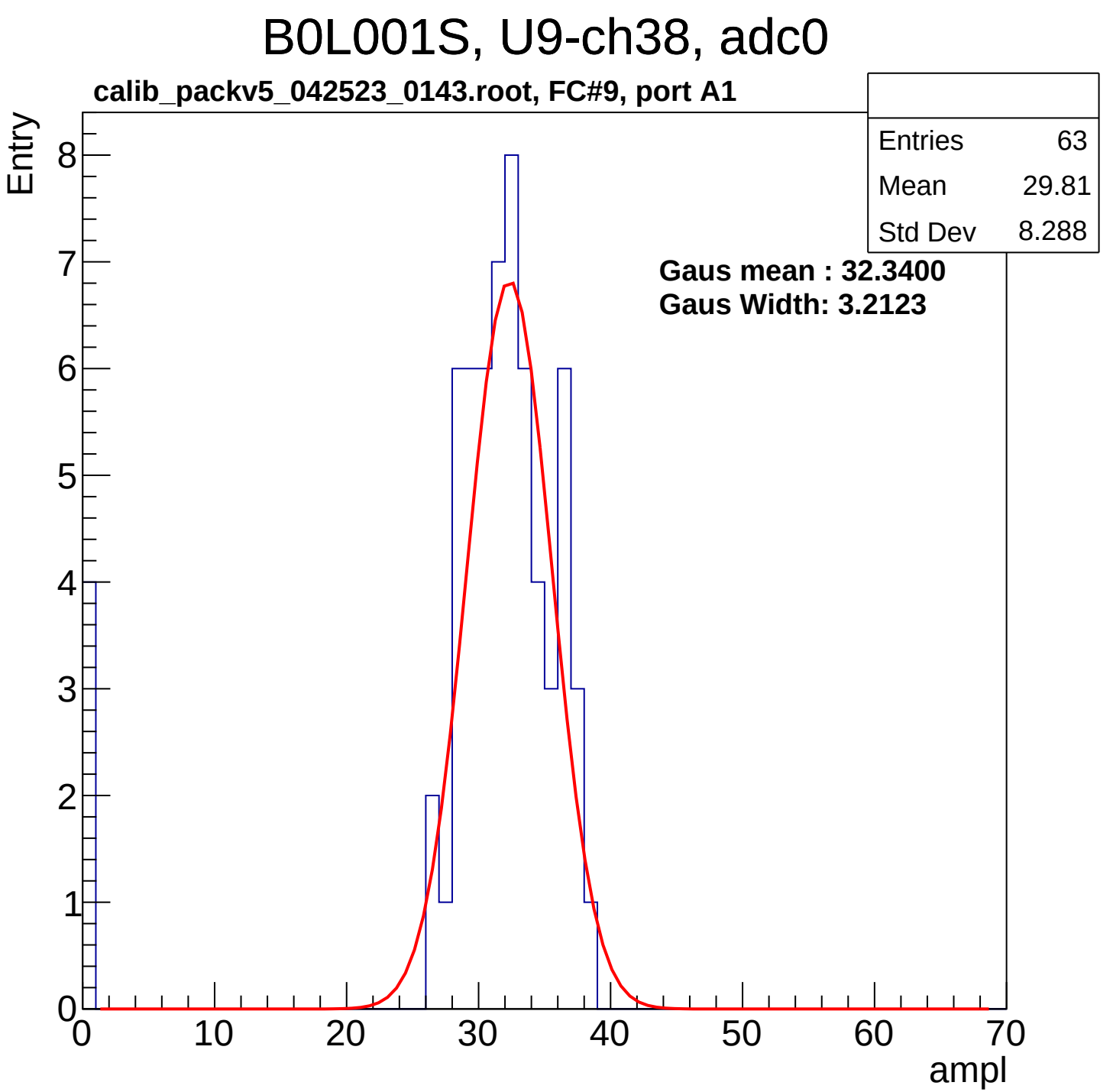
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch38, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.81
Std Dev 8.288
Gaus mean : 32.3400
Gaus Width: 3.2123
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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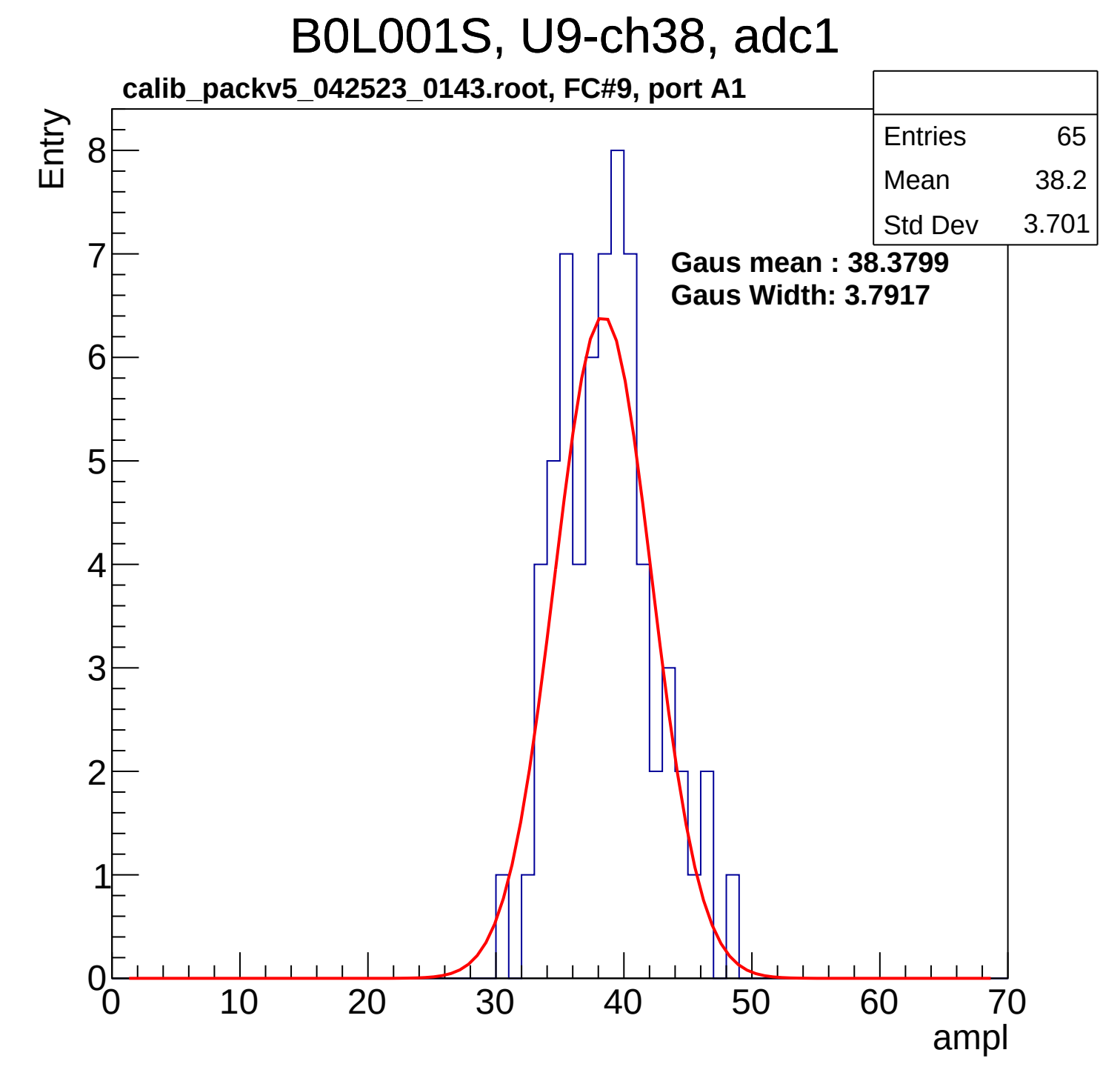
40
50
60
70


B0L001S, U9-ch38, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38.2
Std Dev 3.701
Gaus mean : 38.3799
Gaus Width: 3.7917
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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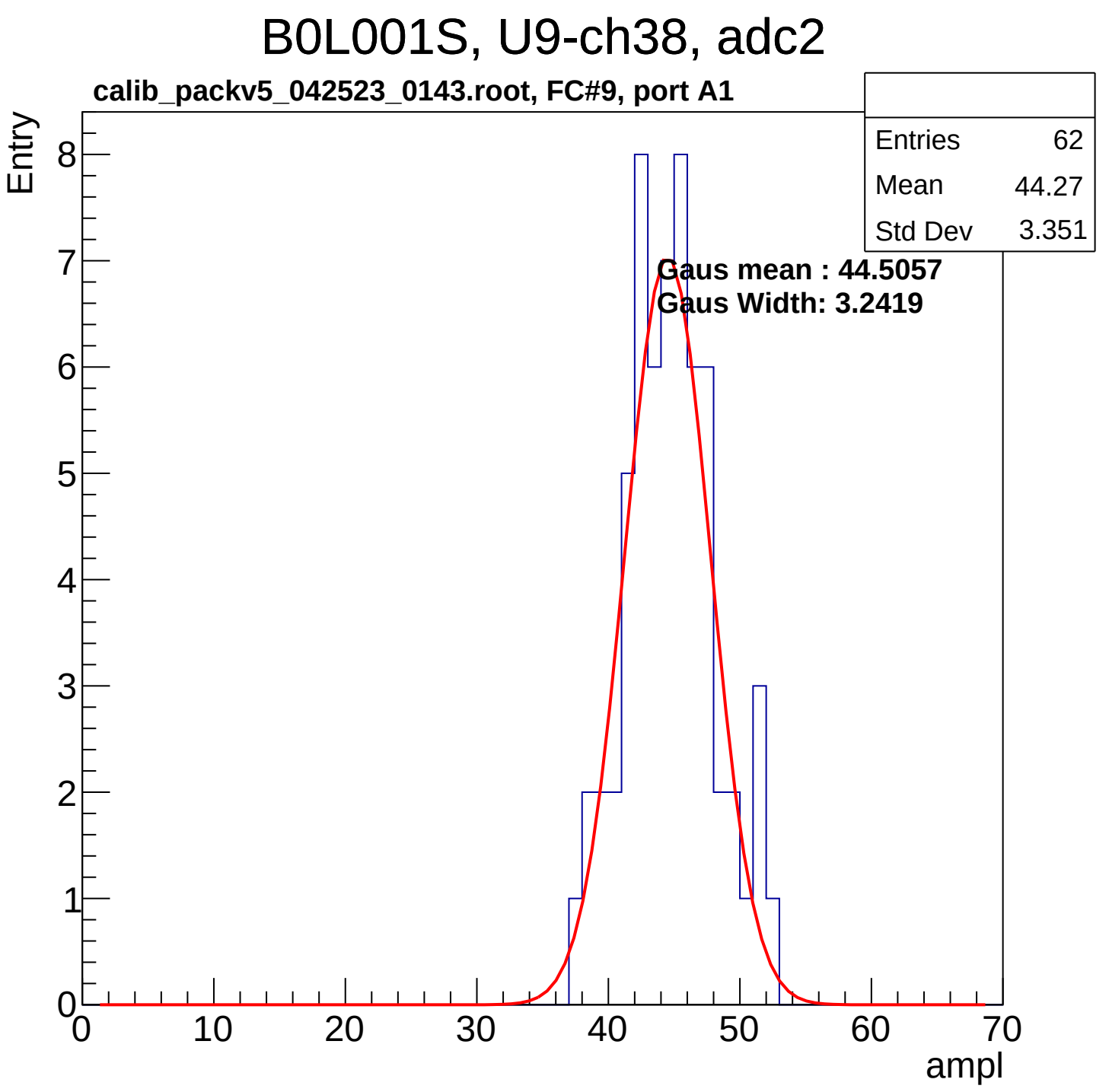
B0L001S, U9-ch38, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.27
Std Dev 3.351
Gaus mean : 44.5057
Gaus Width: 3.2419
Entry
ampl
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3
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5
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7
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0
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60
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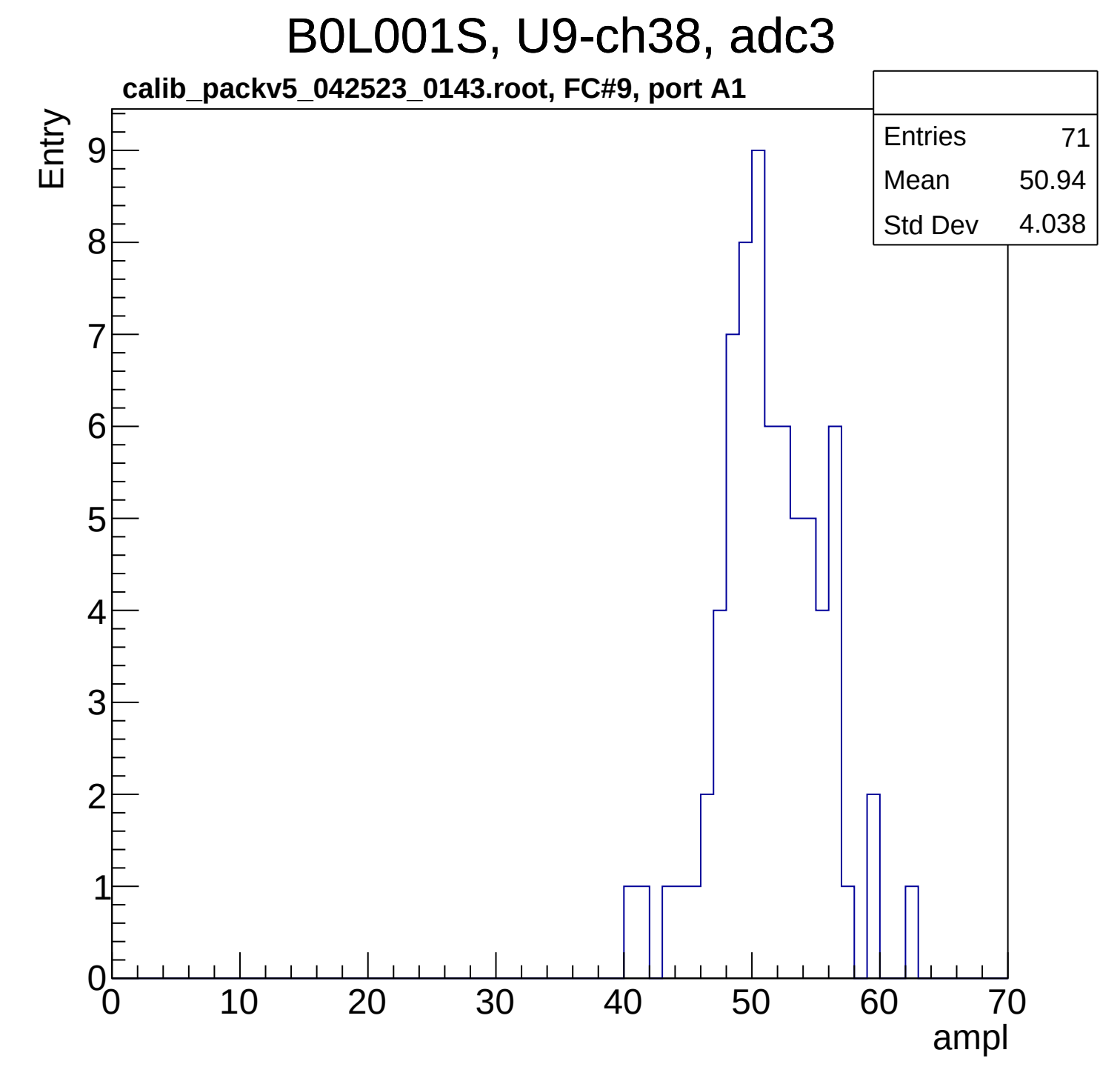
B0L001S, U9-ch38, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.94
Std Dev 4.038
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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40
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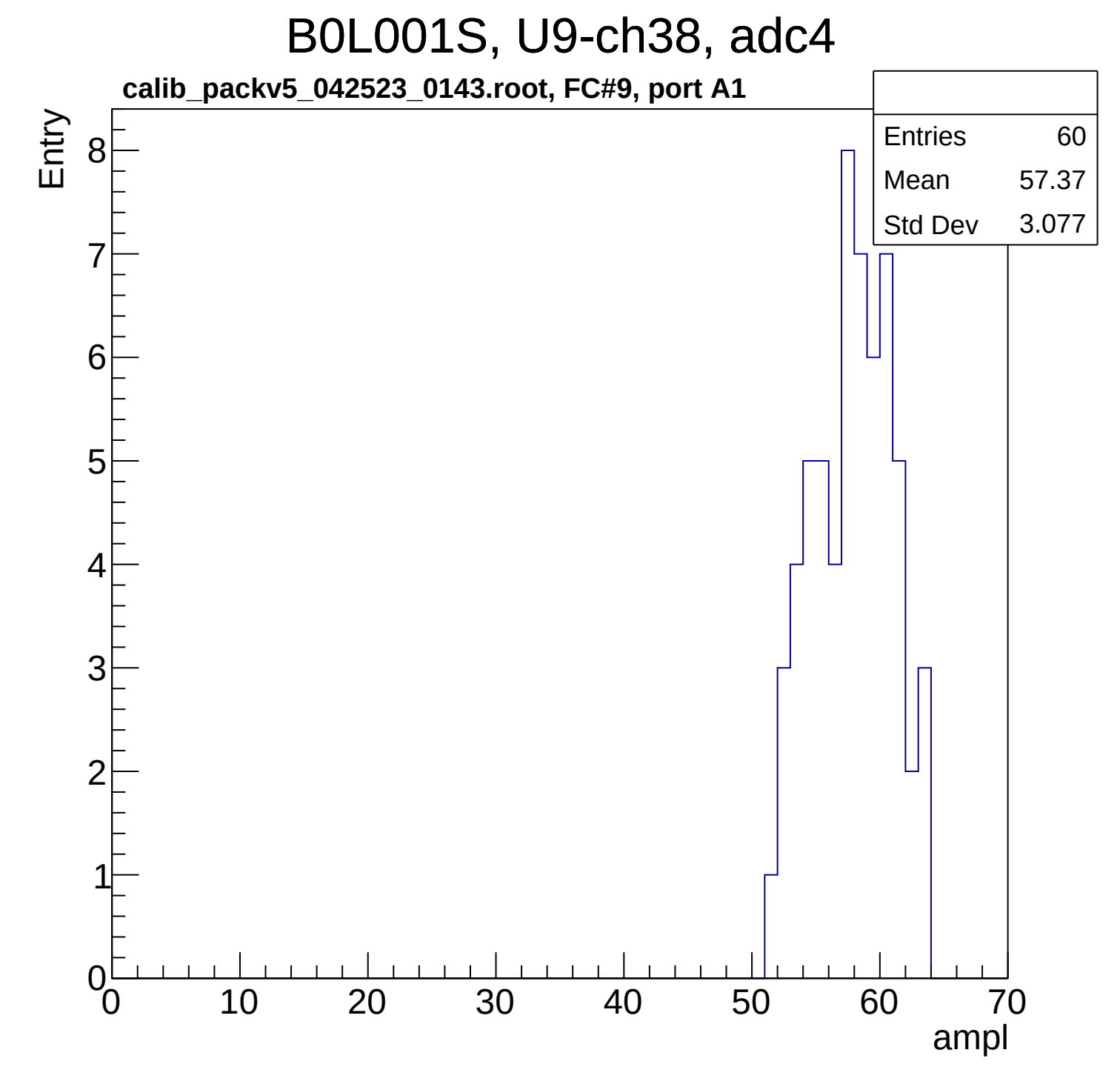
60
70


B0L001S, U9-ch38, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.37
Std Dev 3.077
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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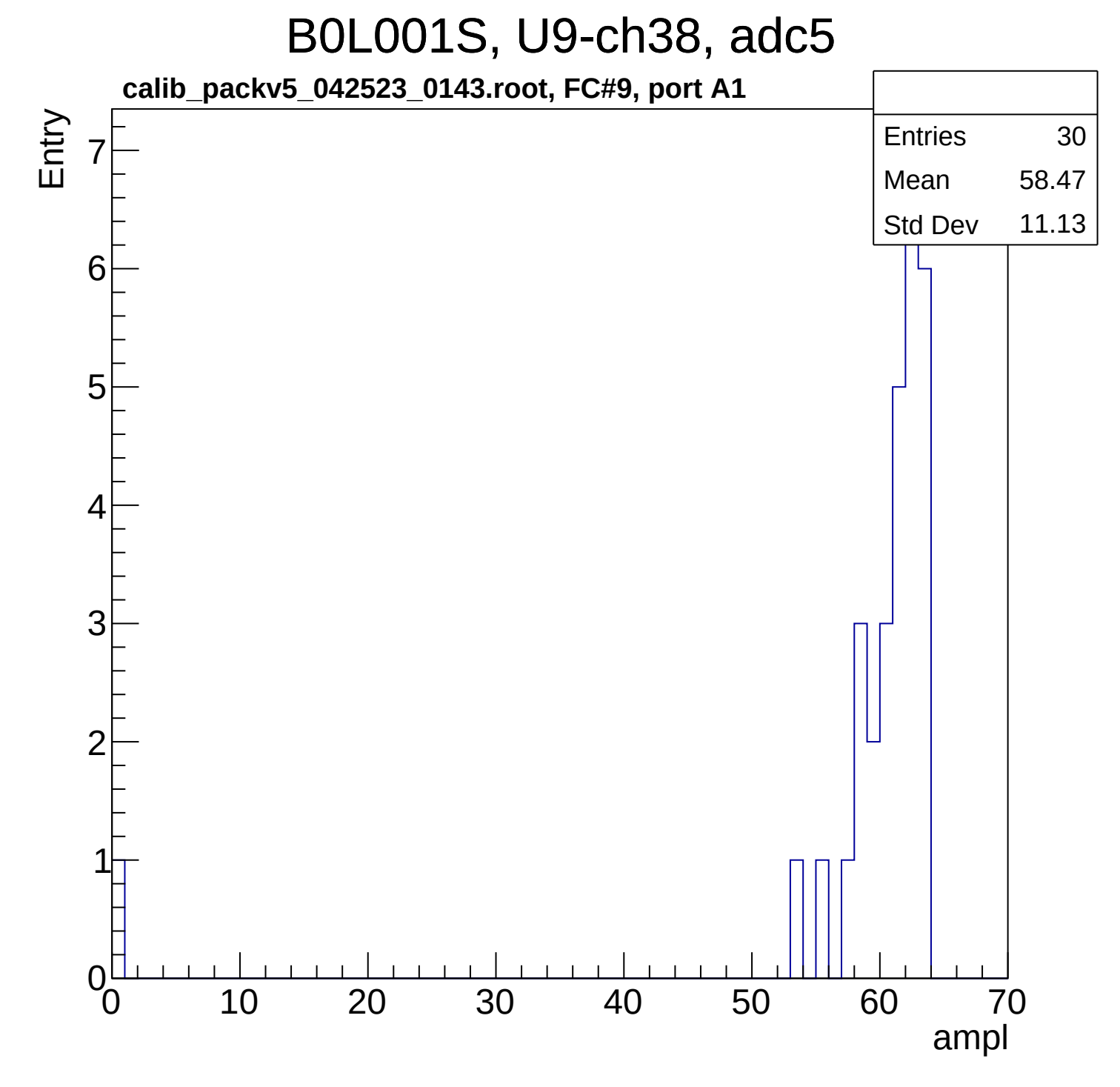
B0L001S, U9-ch38, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.47
Std Dev 11.13
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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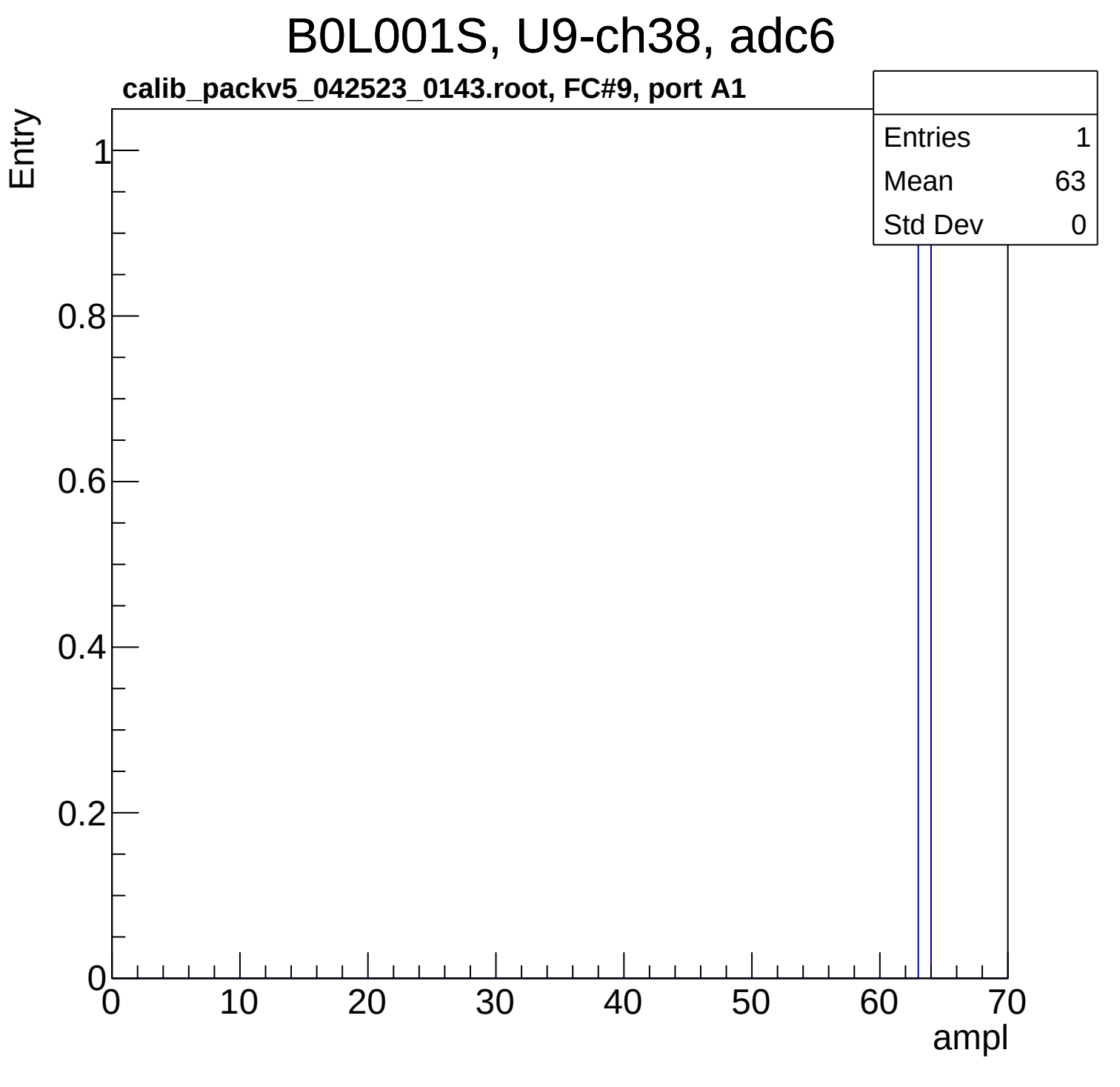
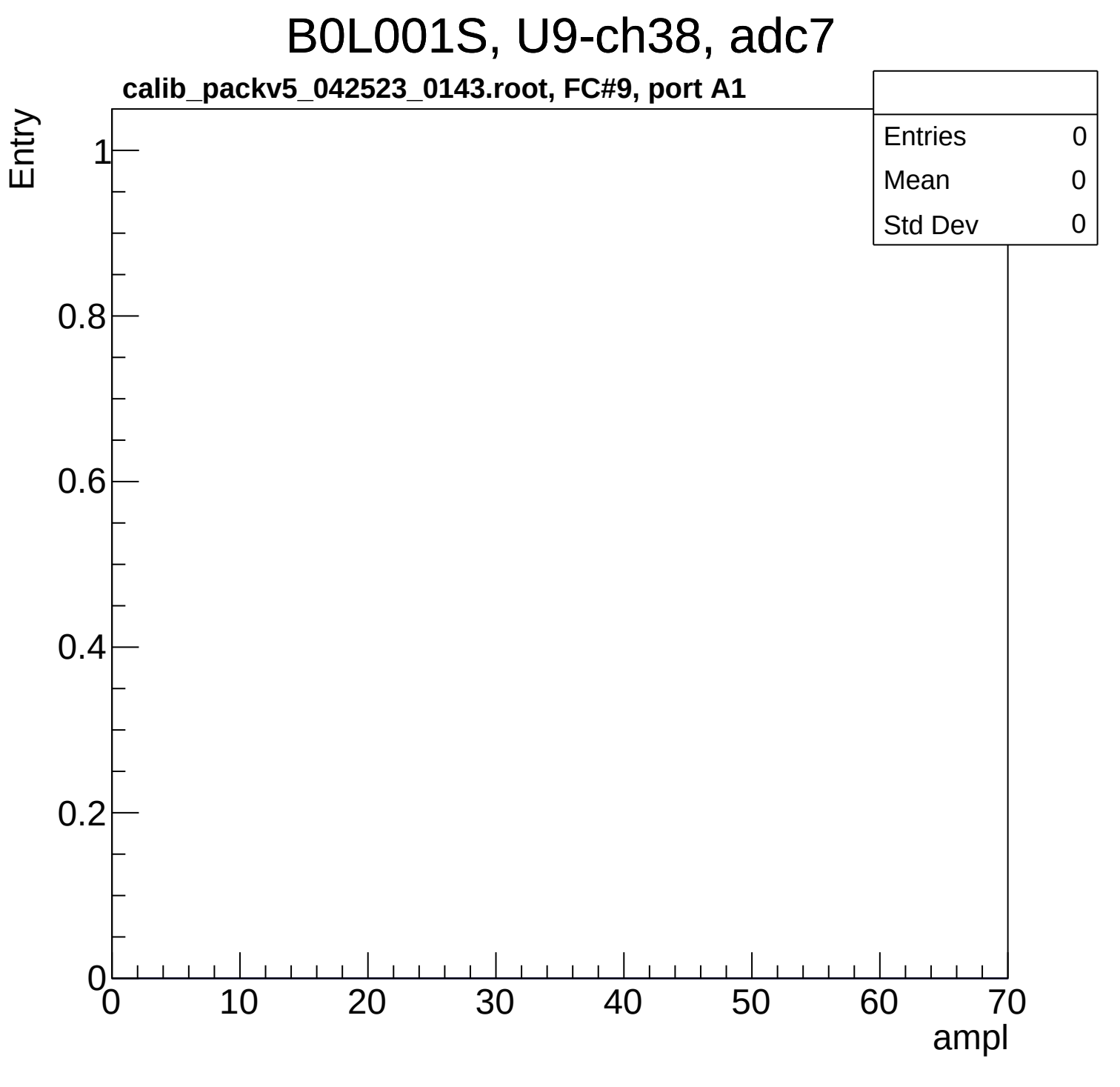
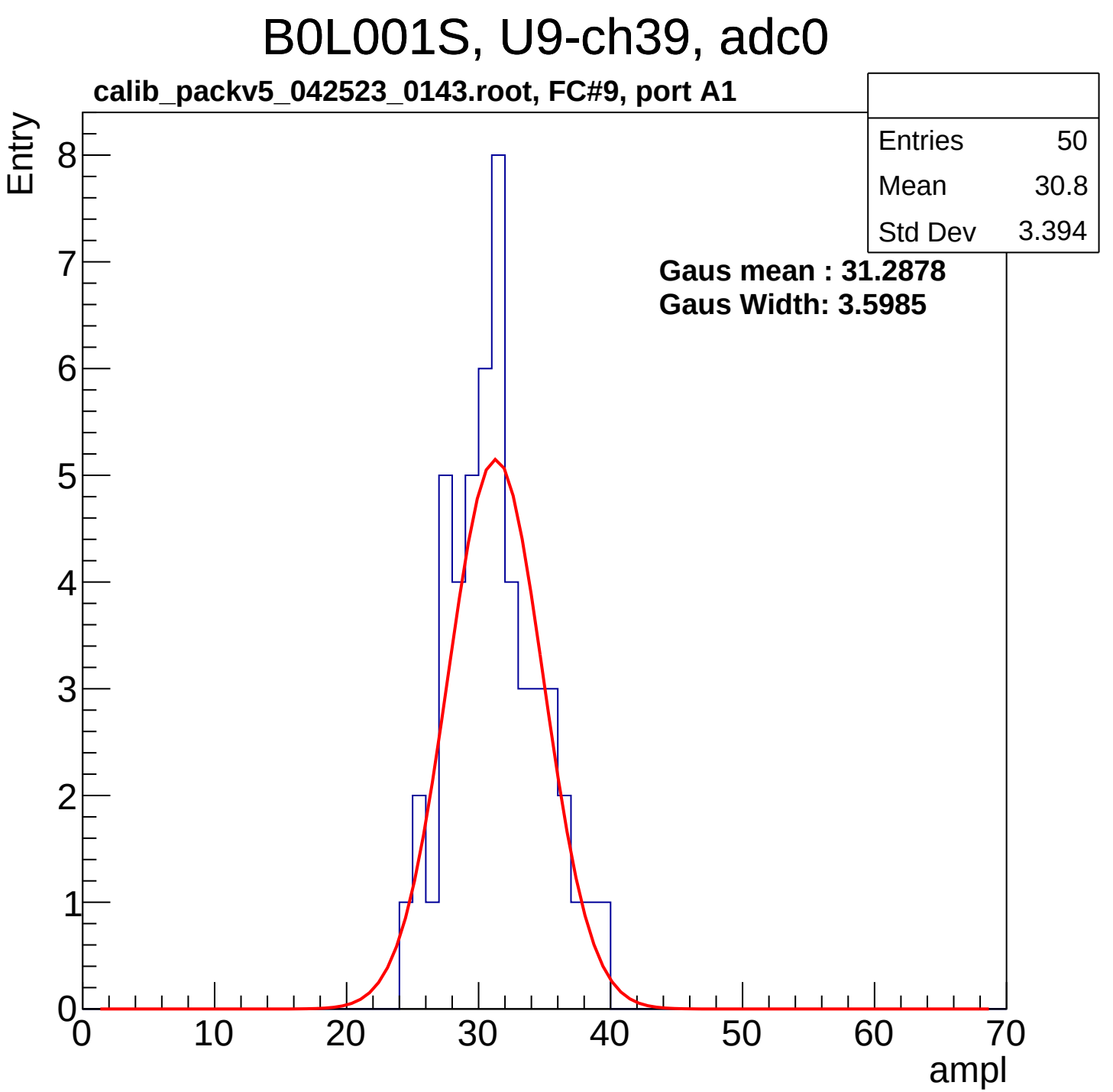

B0L001S, U9-ch38, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch38, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch39, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 30.8
Std Dev 3.394
Gaus mean : 31.2878
Gaus Width: 3.5985
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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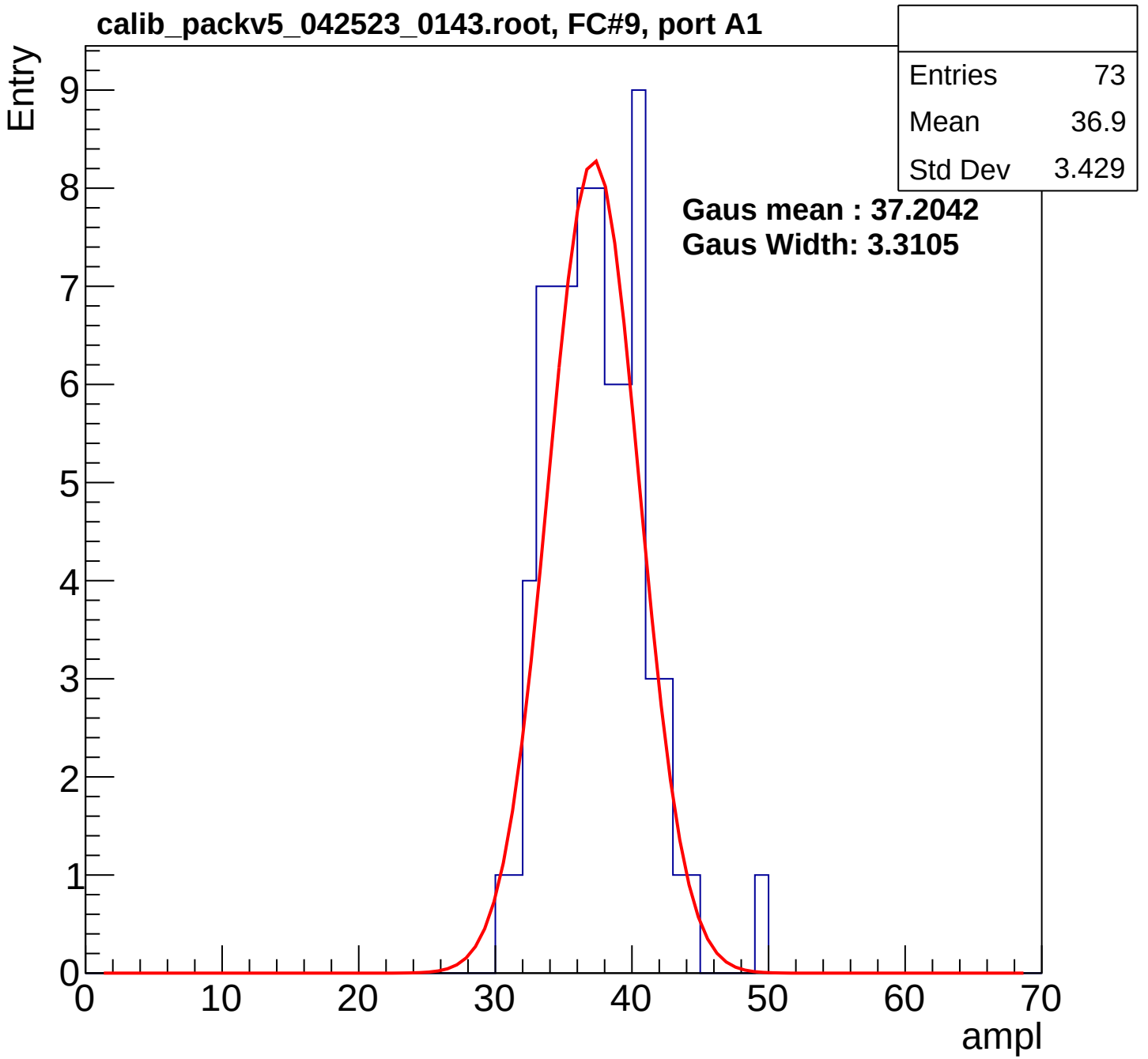
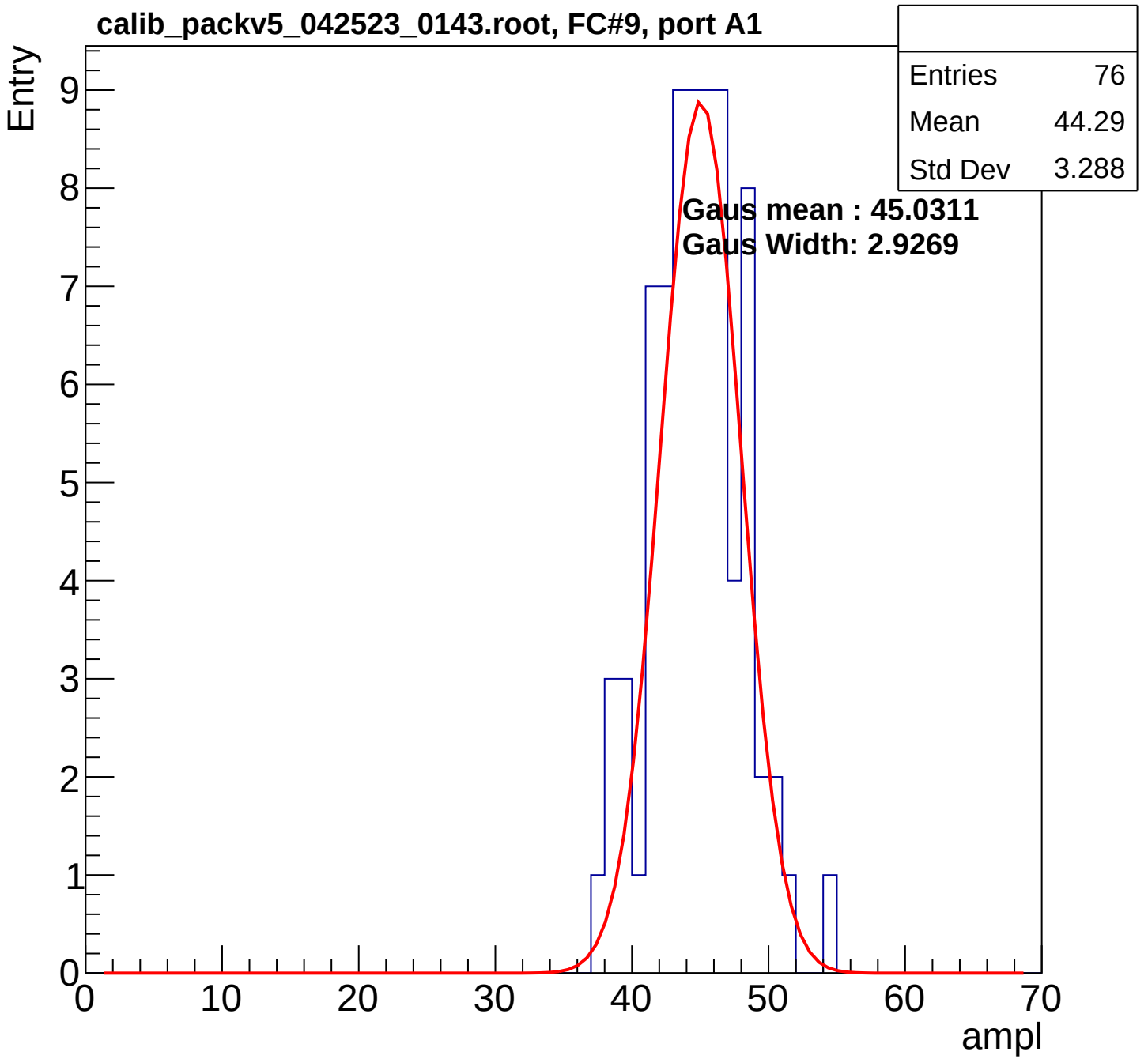

B0L001S, U9-ch39, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 36.9
Std Dev 3.429
Gaus mean : 37.2042
Gaus Width: 3.3105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch39, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 44.29
Std Dev 3.288
Gaus mean : 45.0311
Gaus Width: 2.9269
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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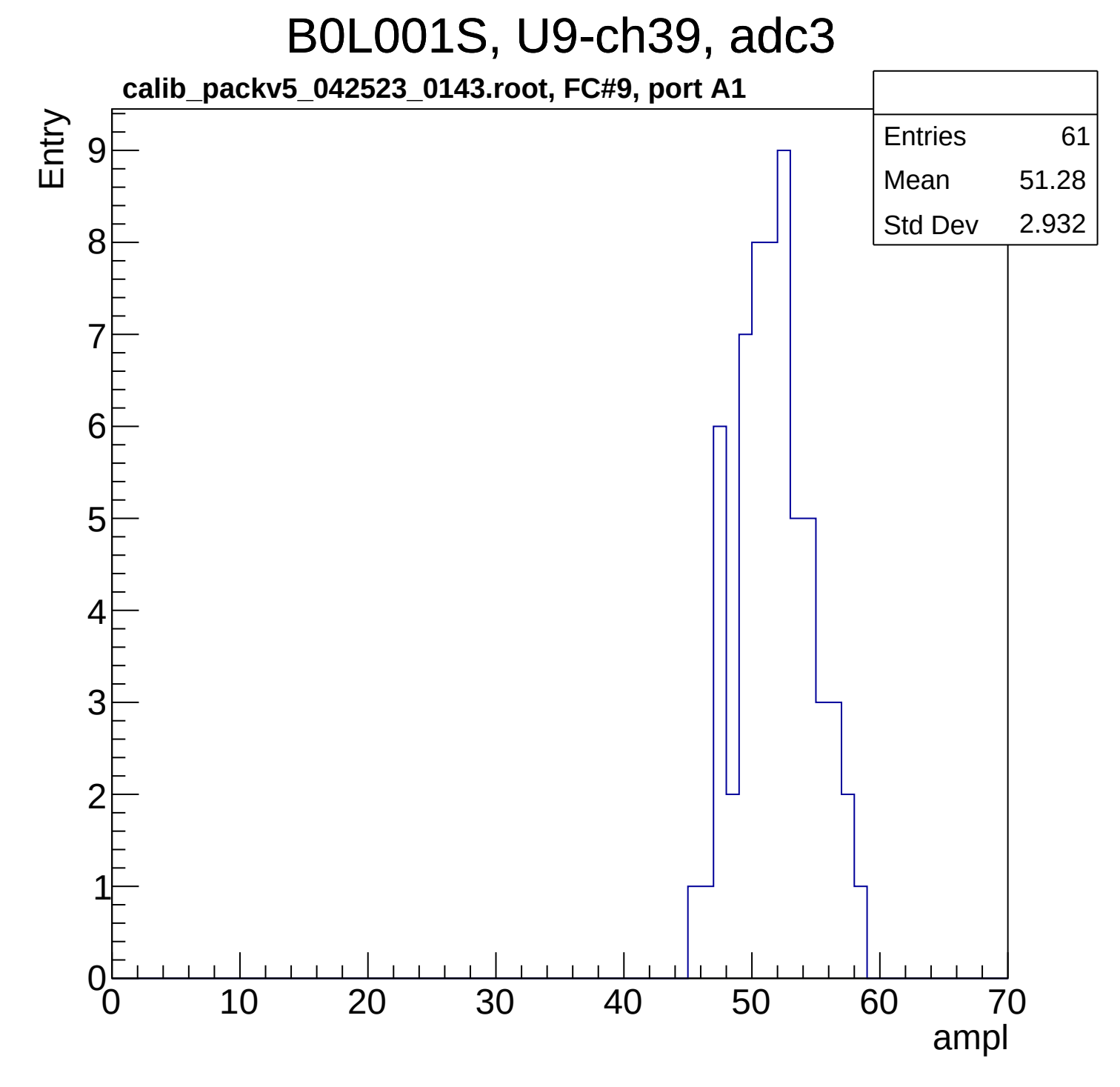
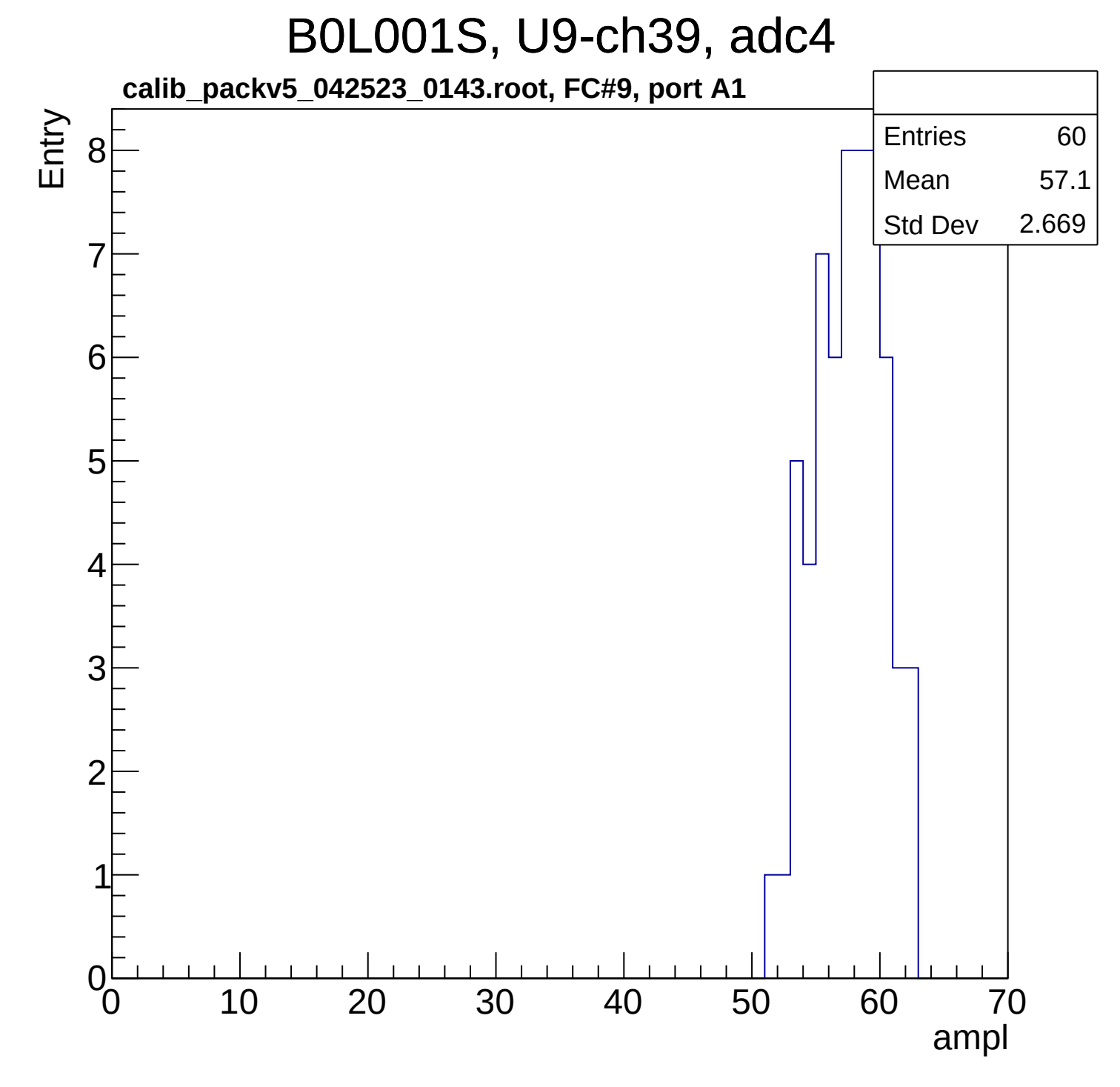
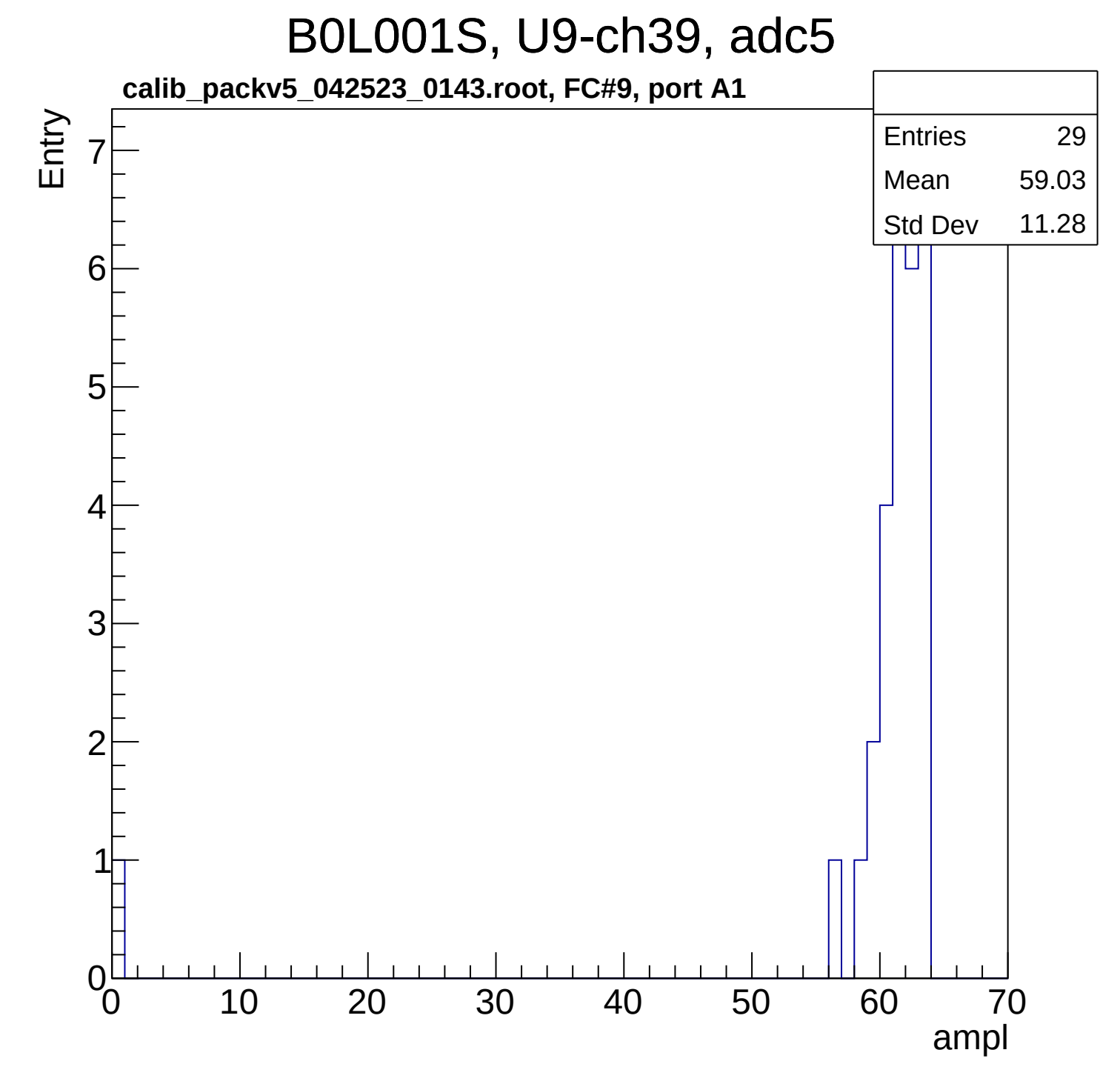
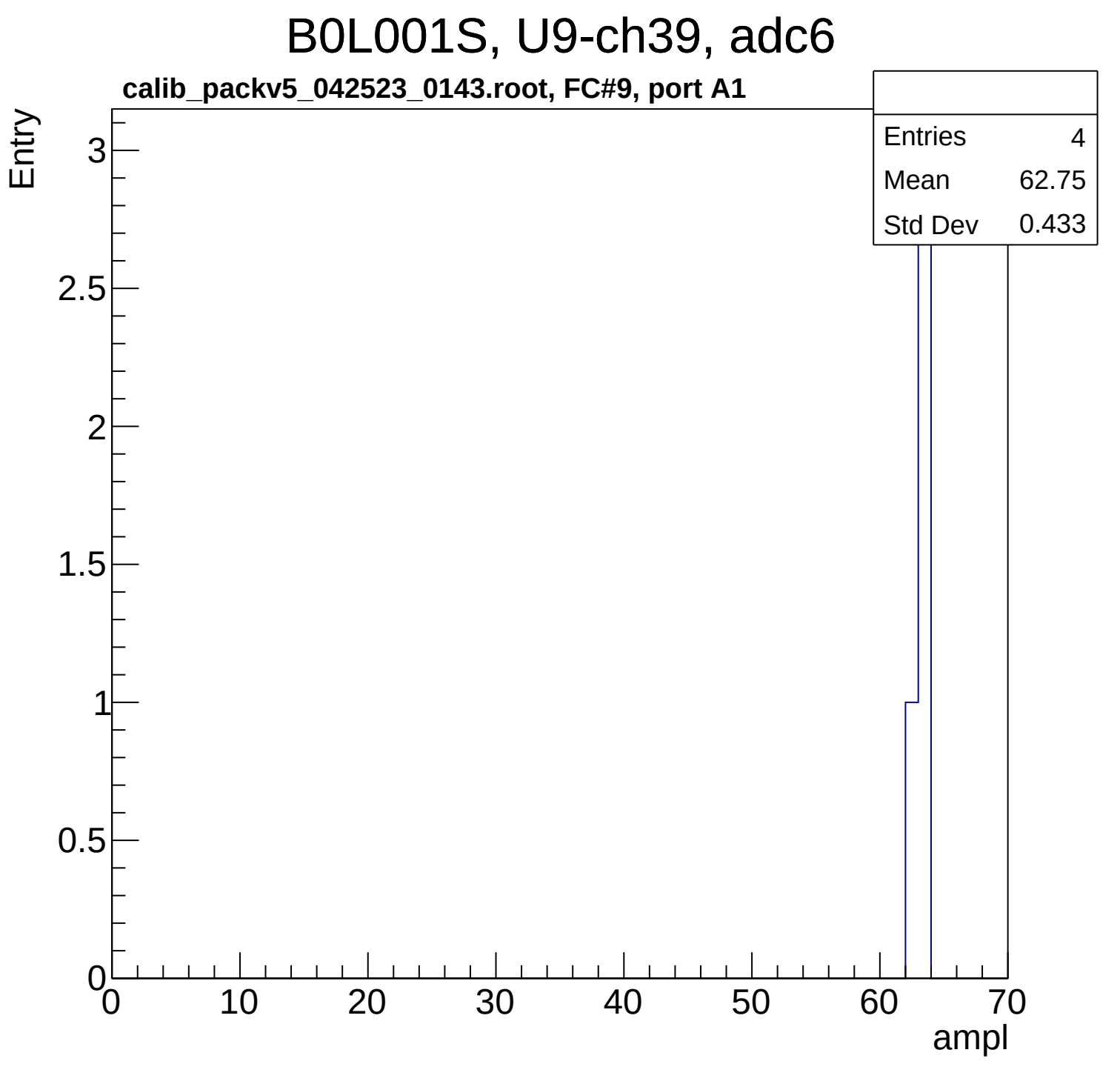
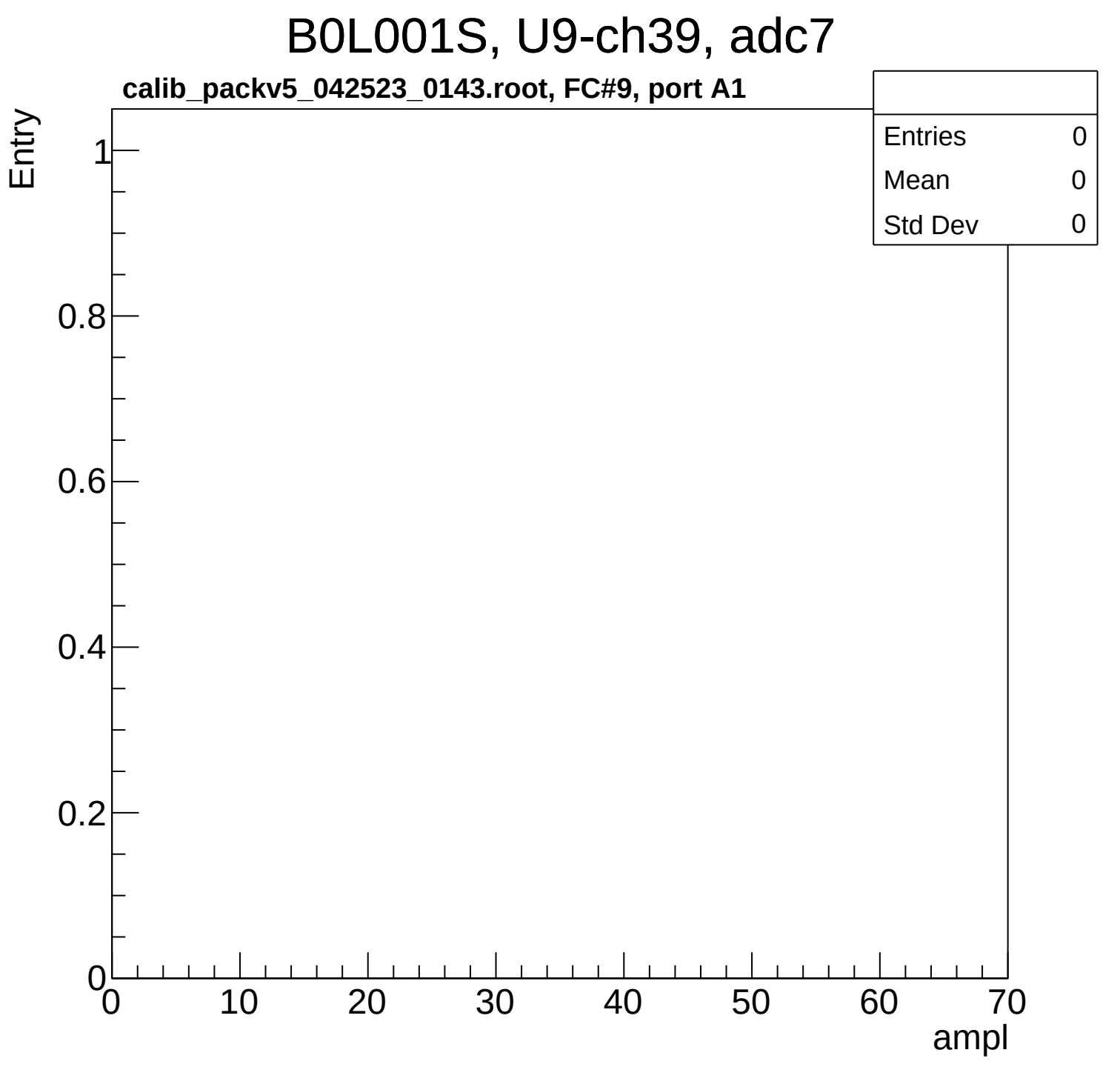
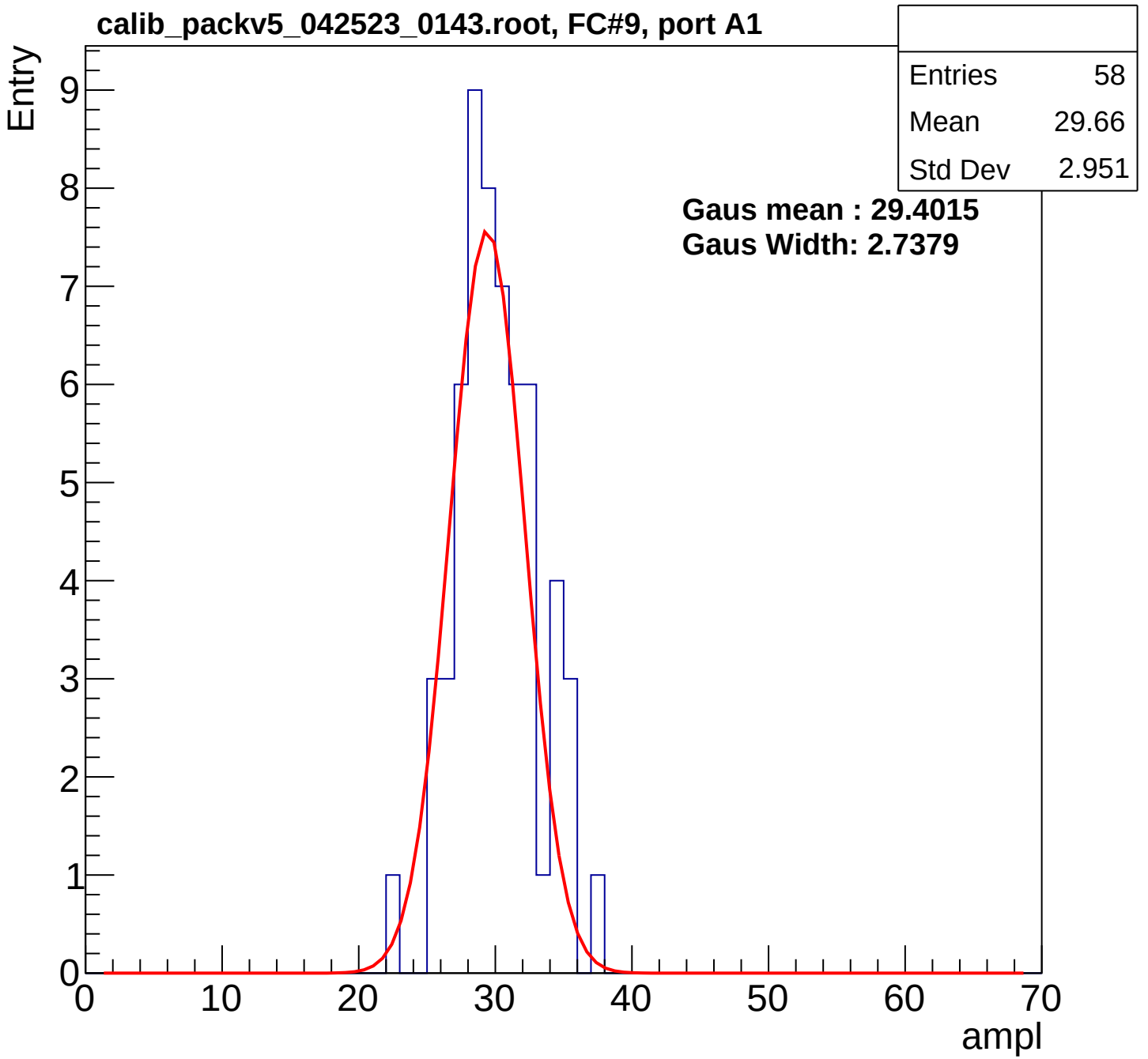

B0L001S, U9-ch39, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.28
Std Dev 2.932
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

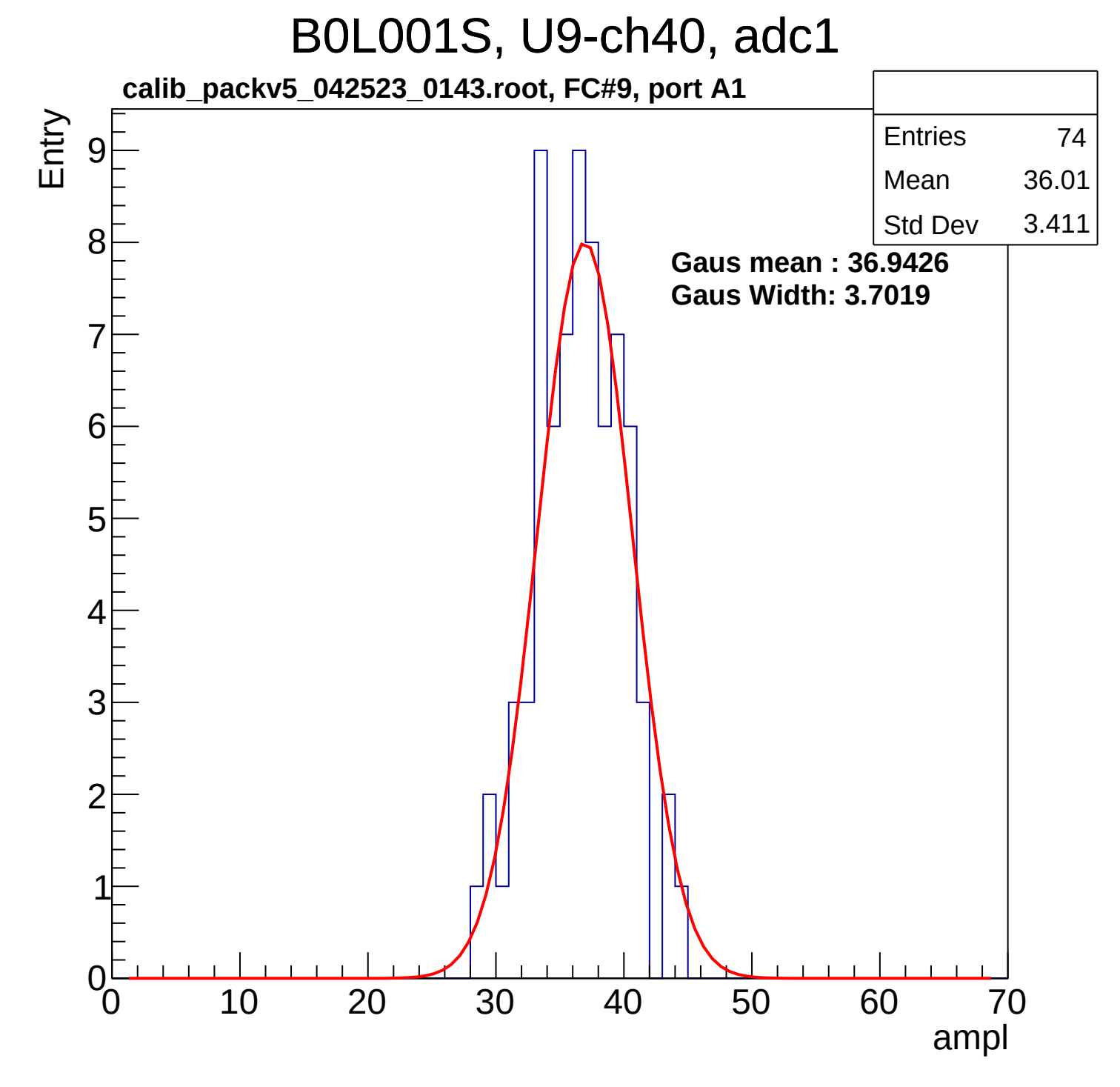

B0L001S, U9-ch39, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 57.1
Std Dev 2.669
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch39, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 59.03
Std Dev 11.28
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch39, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch39, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch40, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 29.66
Std Dev 2.951
Gaus mean : 29.4015
Gaus Width: 2.7379
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch40, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.01
Std Dev 3.411
Gaus mean : 36.9426
Gaus Width: 3.7019
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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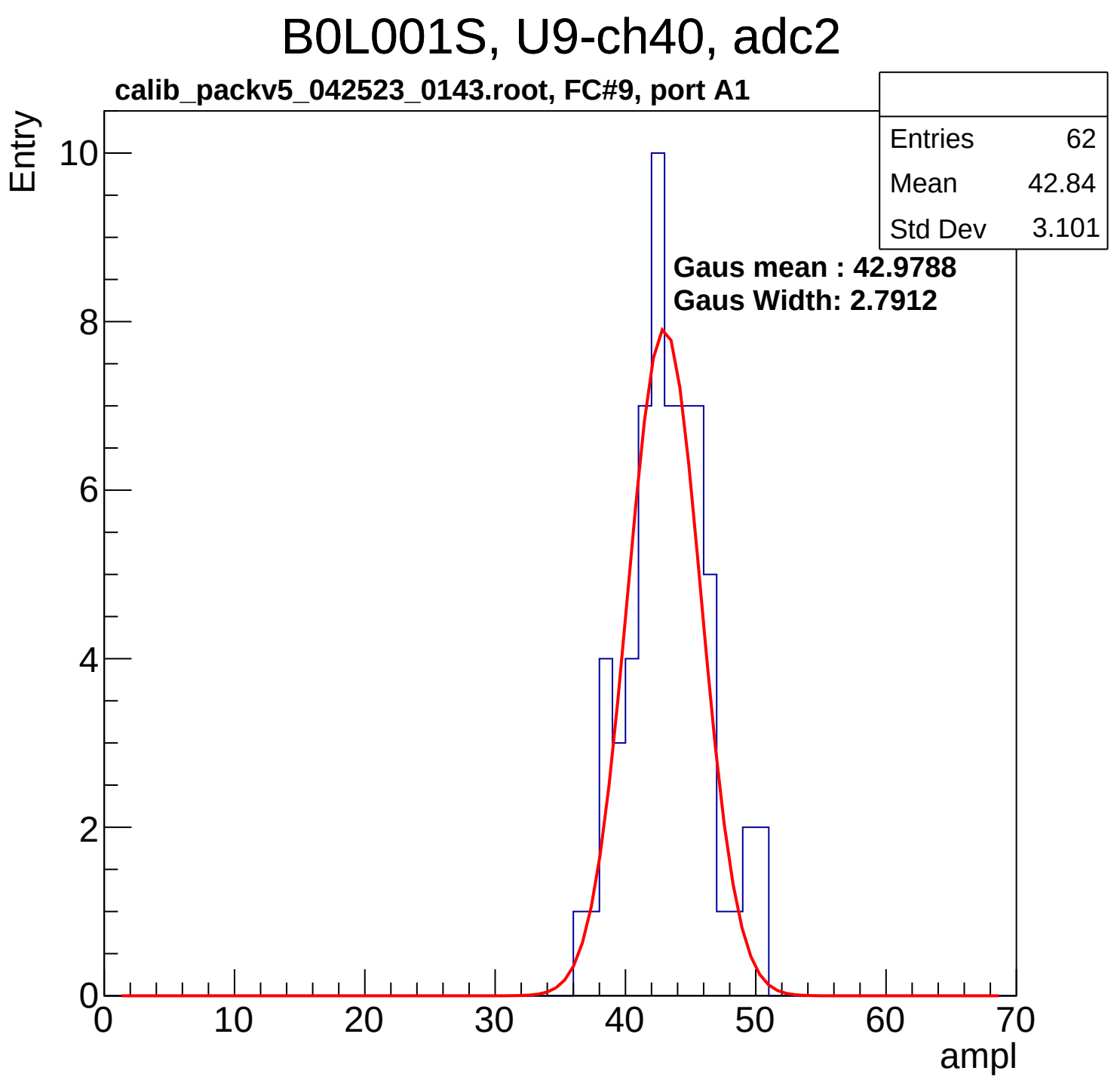
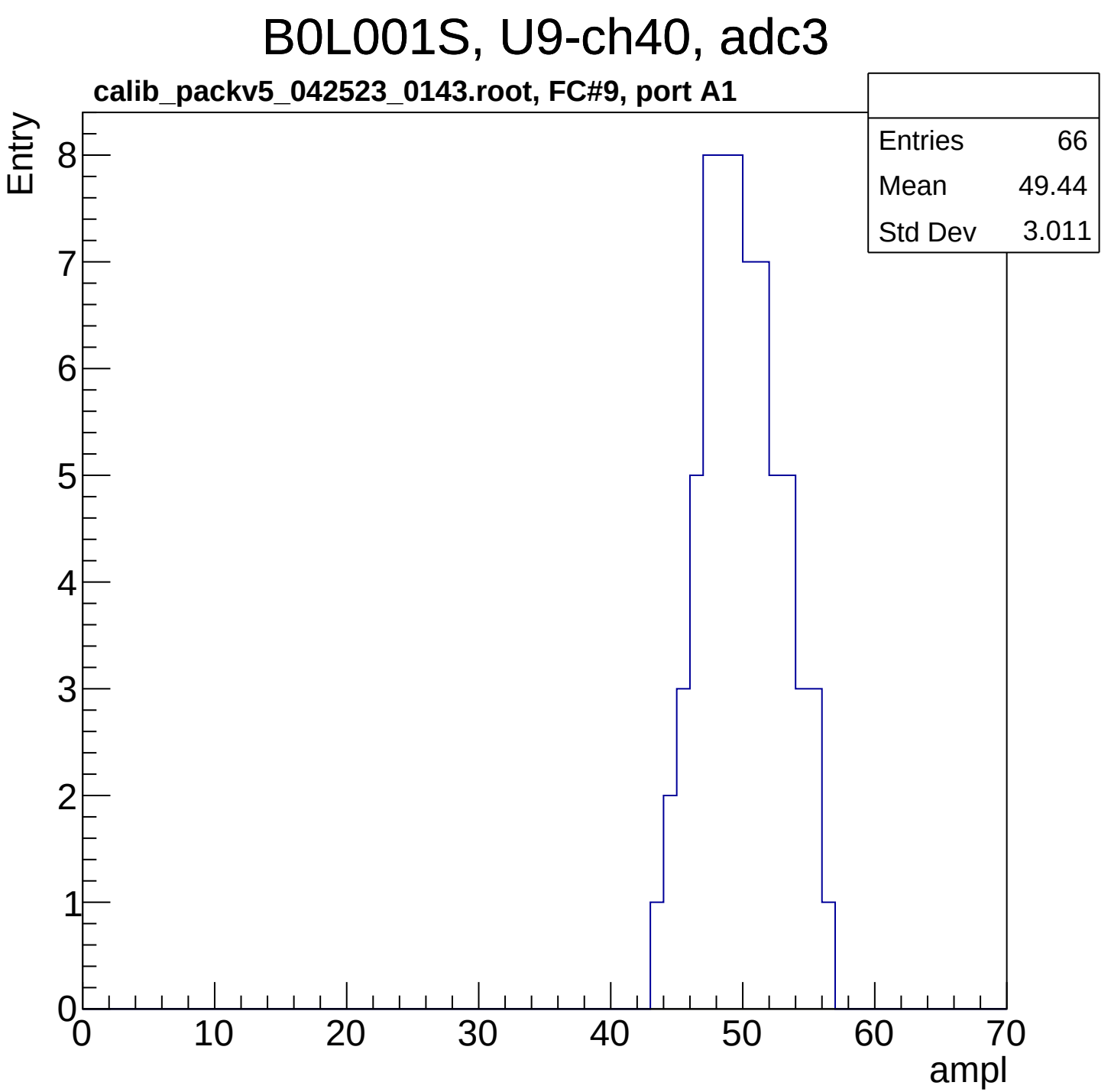

B0L001S, U9-ch40, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 42.84
Std Dev 3.101
Gaus mean : 42.9788
Gaus Width: 2.7912
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch40, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.44
Std Dev 3.011
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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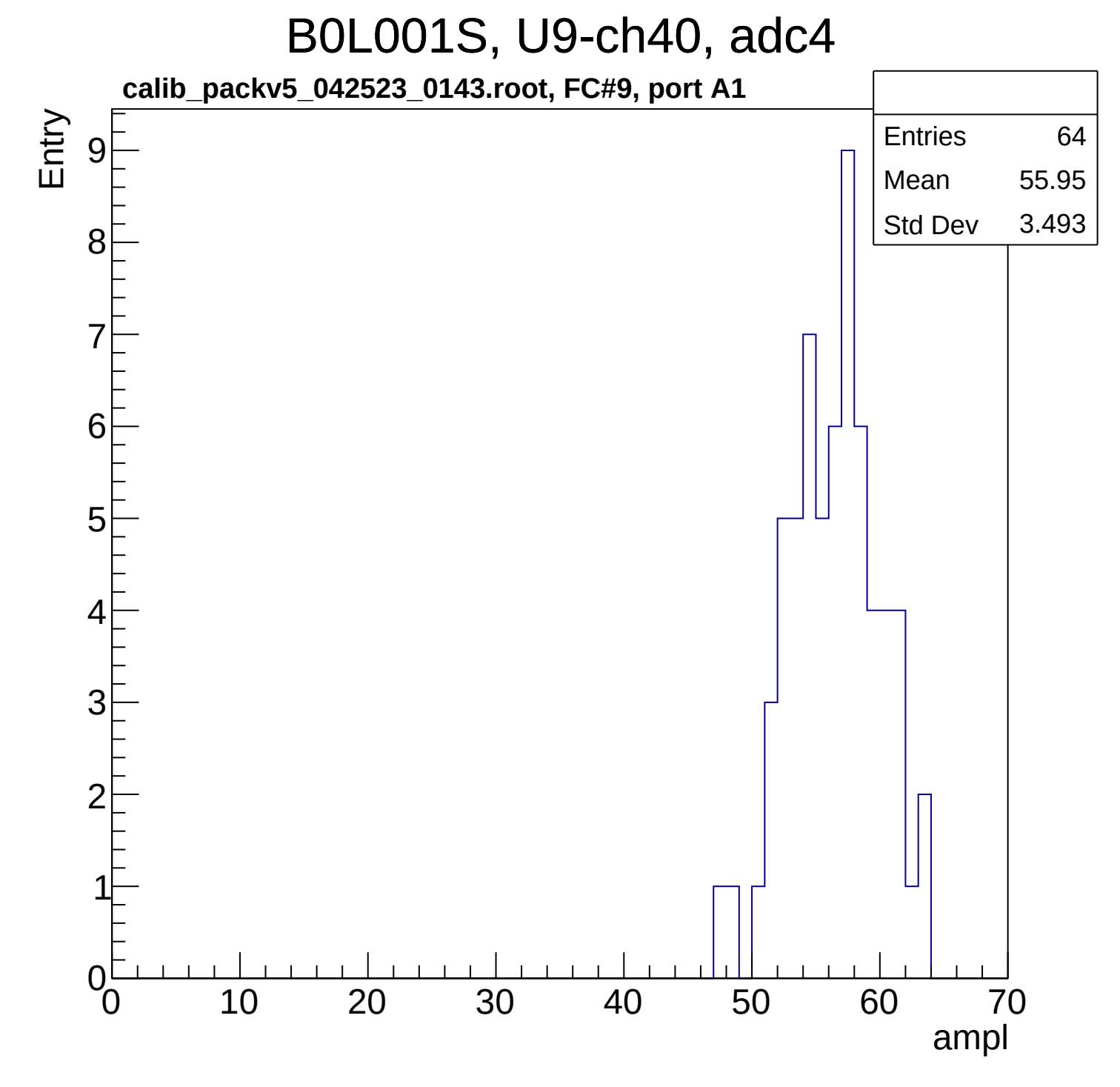
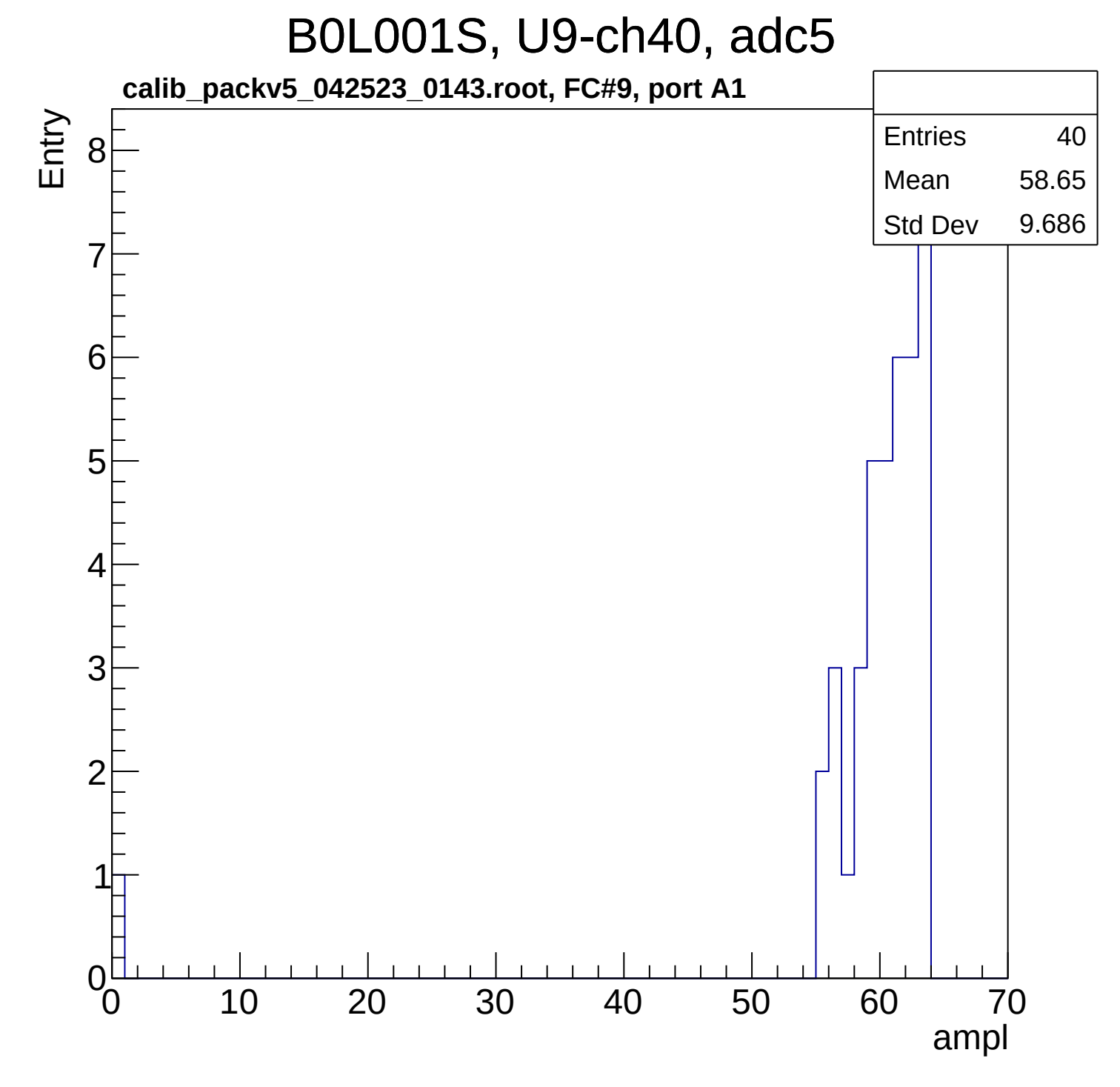
40
50
60
70


B0L001S, U9-ch40, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 55.95
Std Dev 3.493
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch40, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.65
Std Dev 9.686
Entry
ampl
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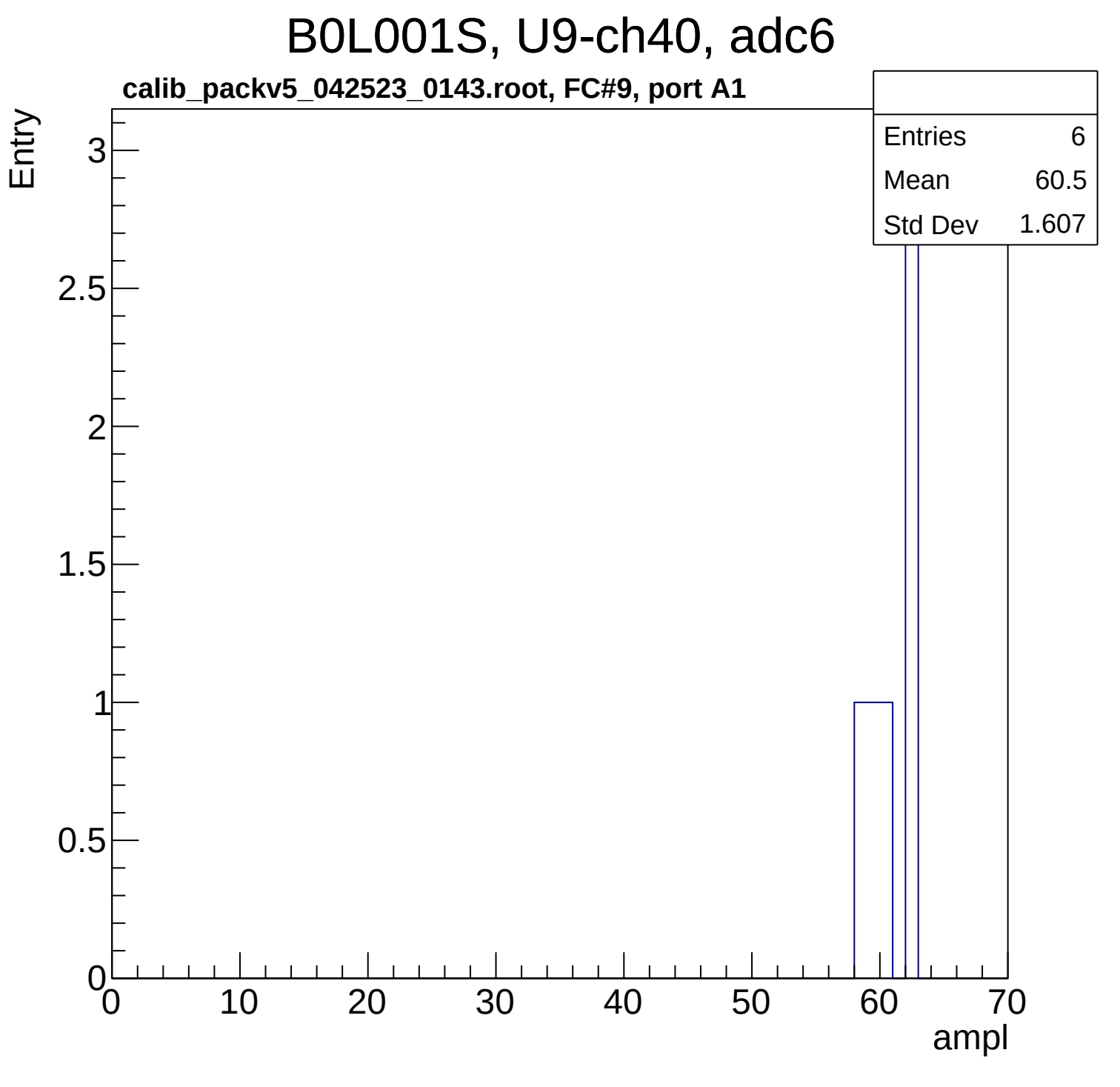
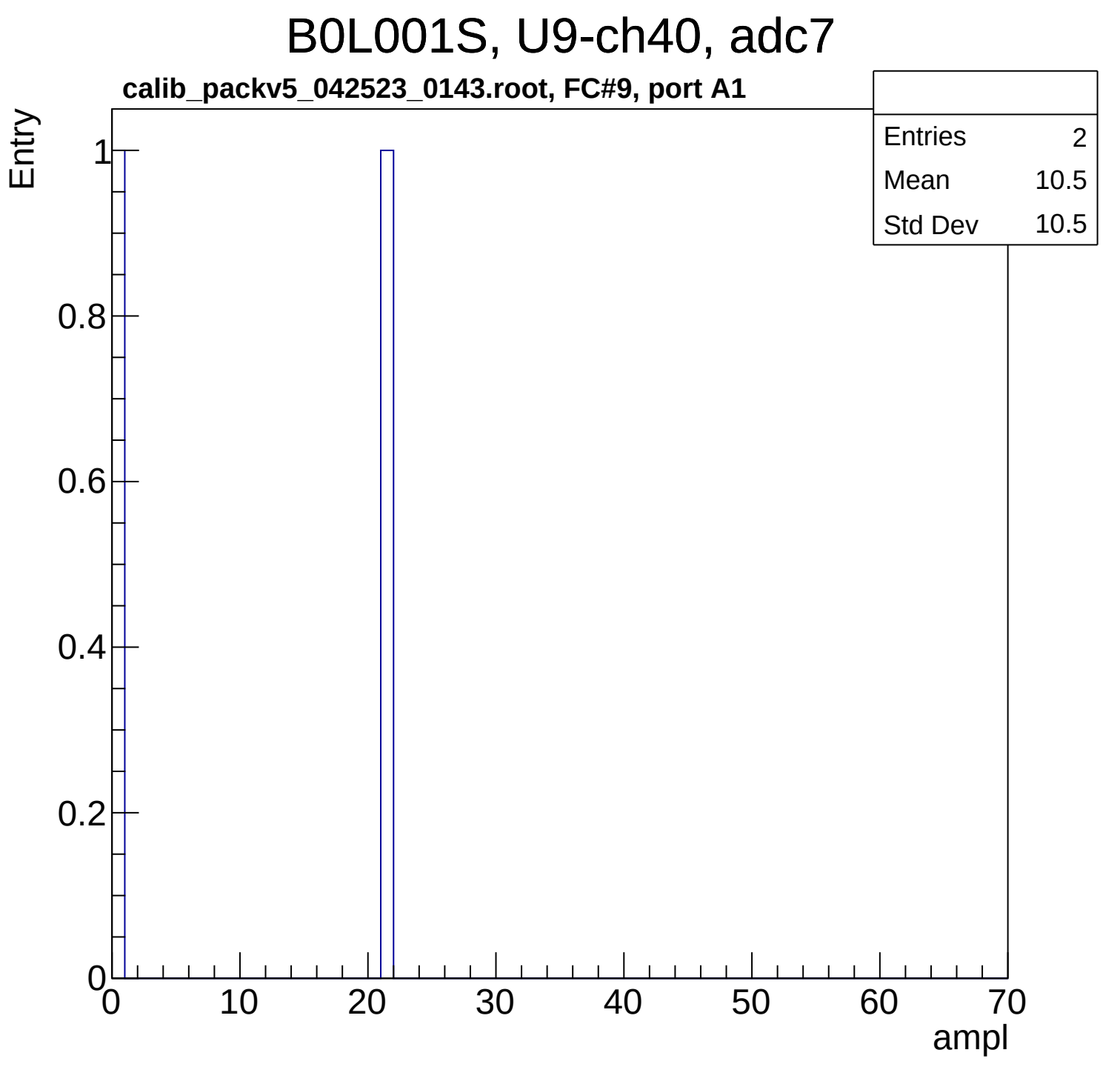
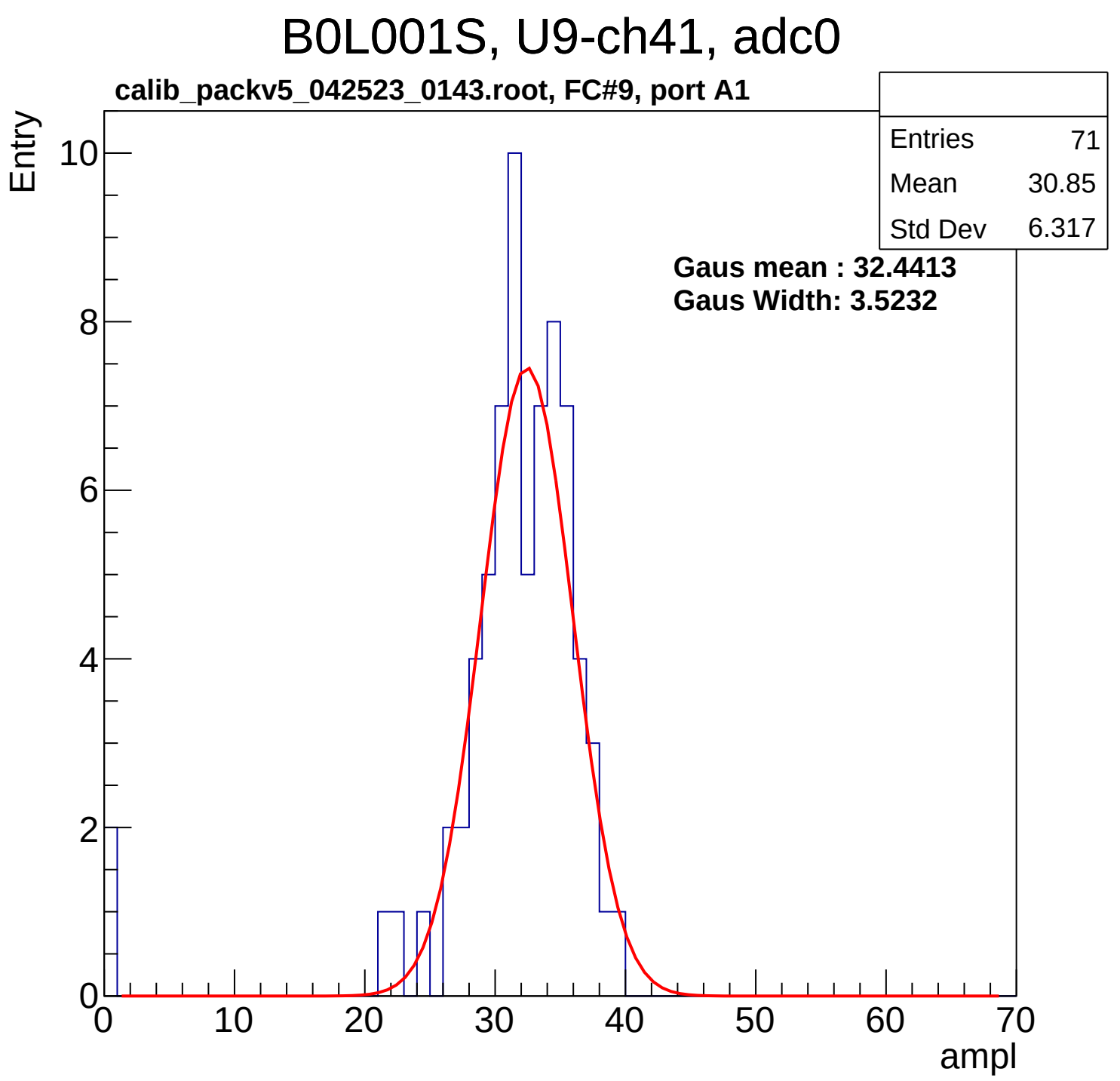
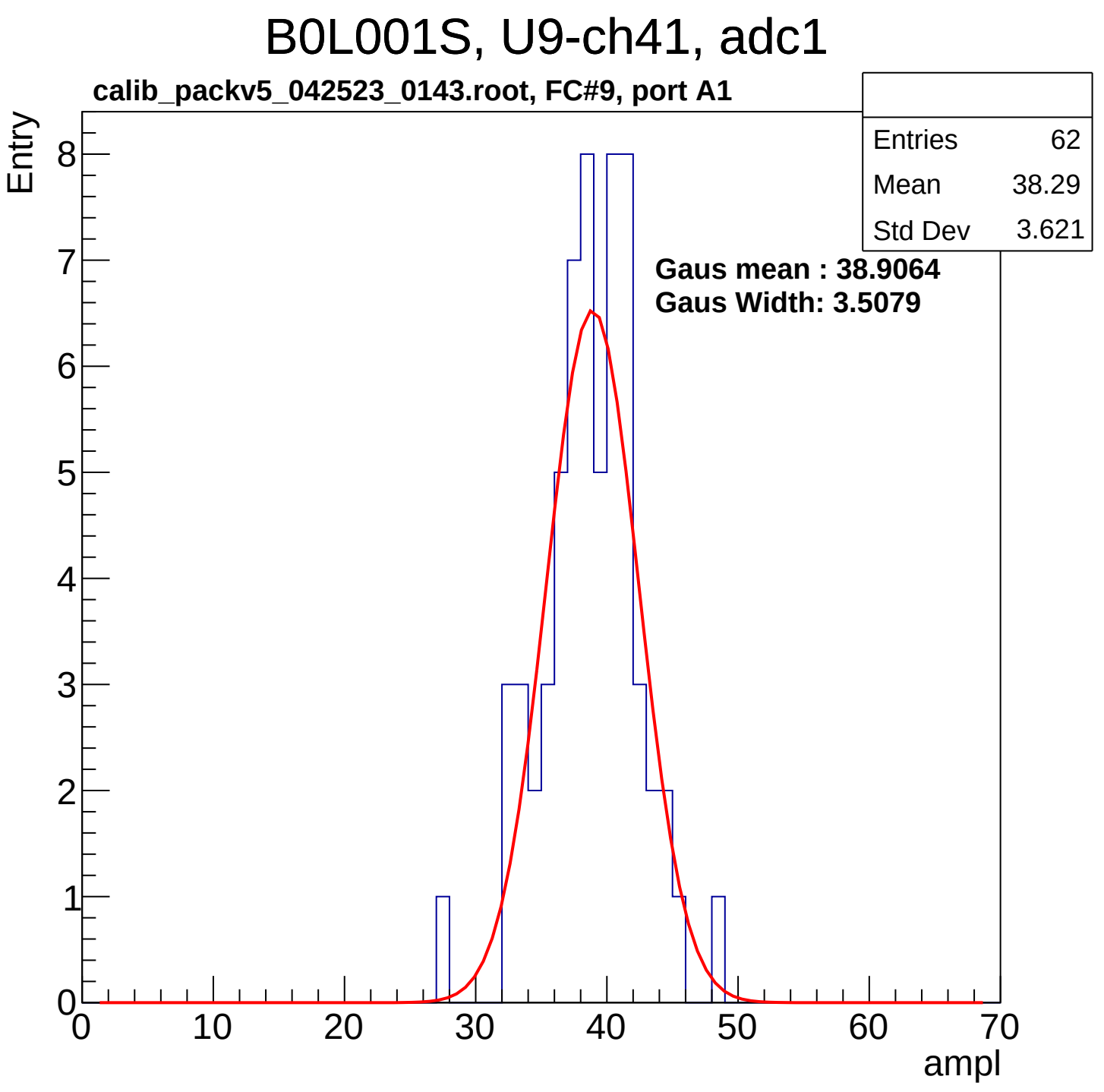
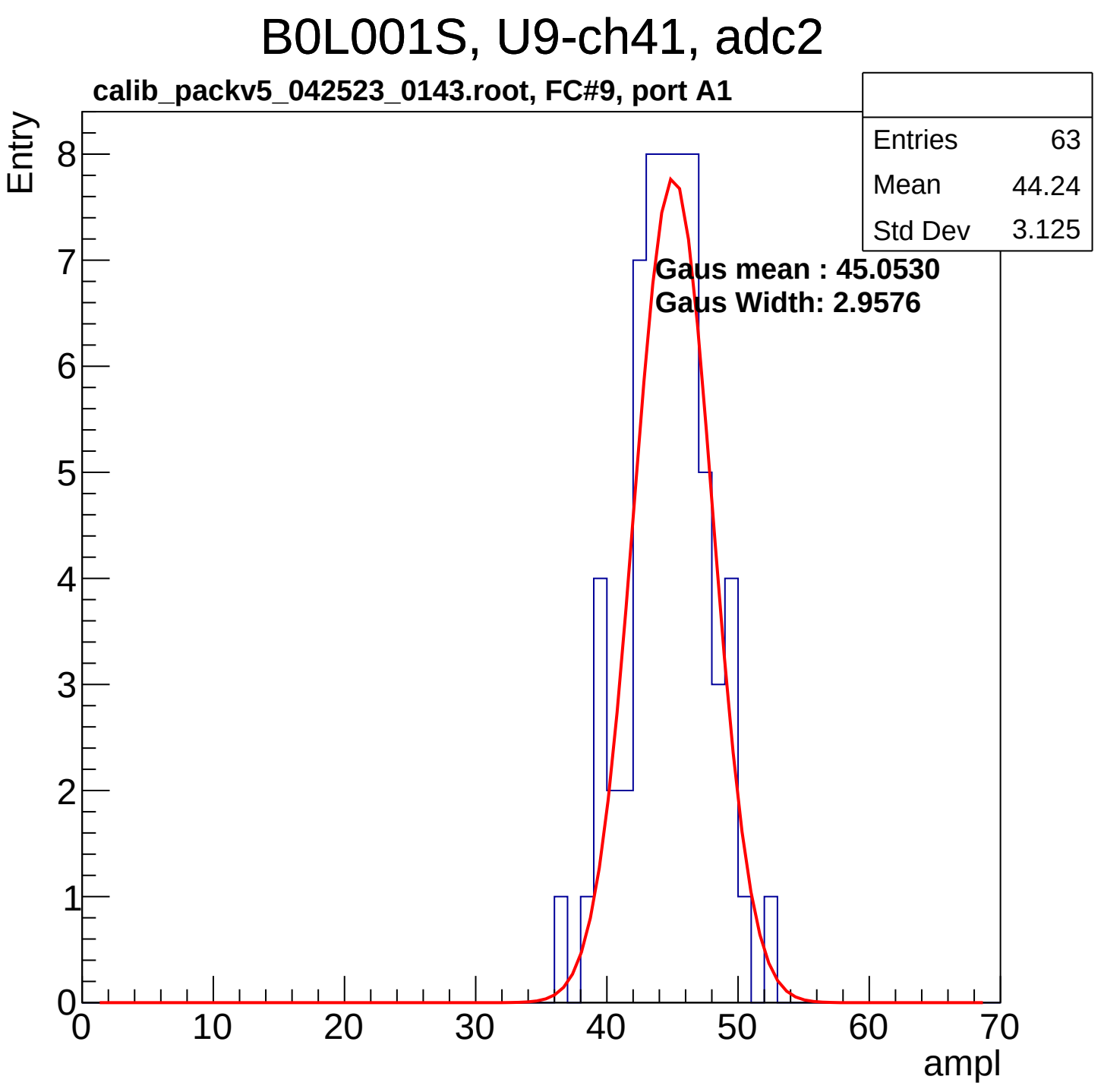
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

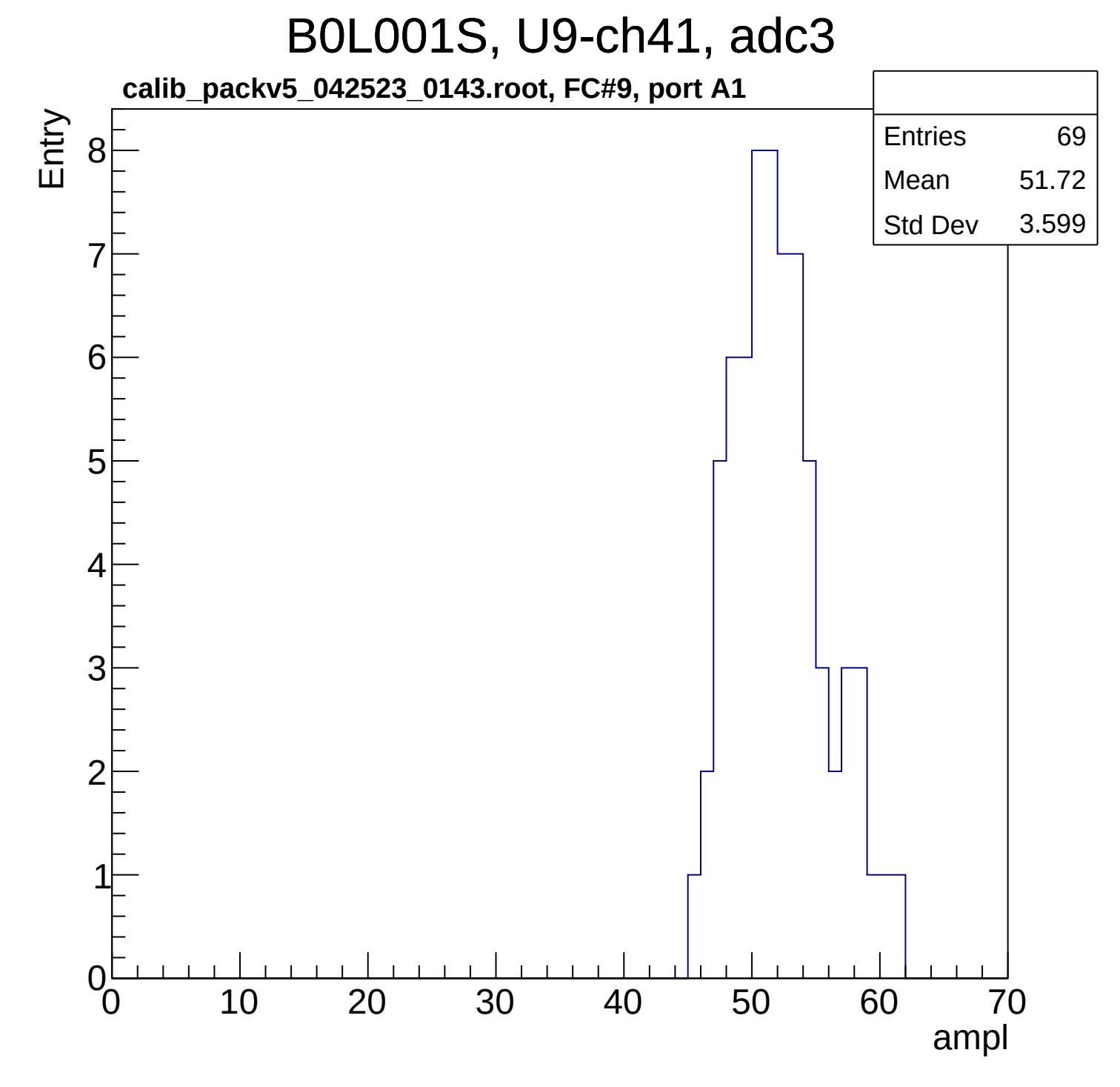

B0L001S, U9-ch40, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 60.5
Std Dev 1.607
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch40, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch41, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 30.85
Std Dev 6.317
Gaus mean : 32.4413
Gaus Width: 3.5232
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch41, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.29
Std Dev 3.621
Gaus mean : 38.9064
Gaus Width: 3.5079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch41, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.24
Std Dev 3.125
Gaus mean : 45.0530
Gaus Width: 2.9576
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch41, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.72
Std Dev 3.599
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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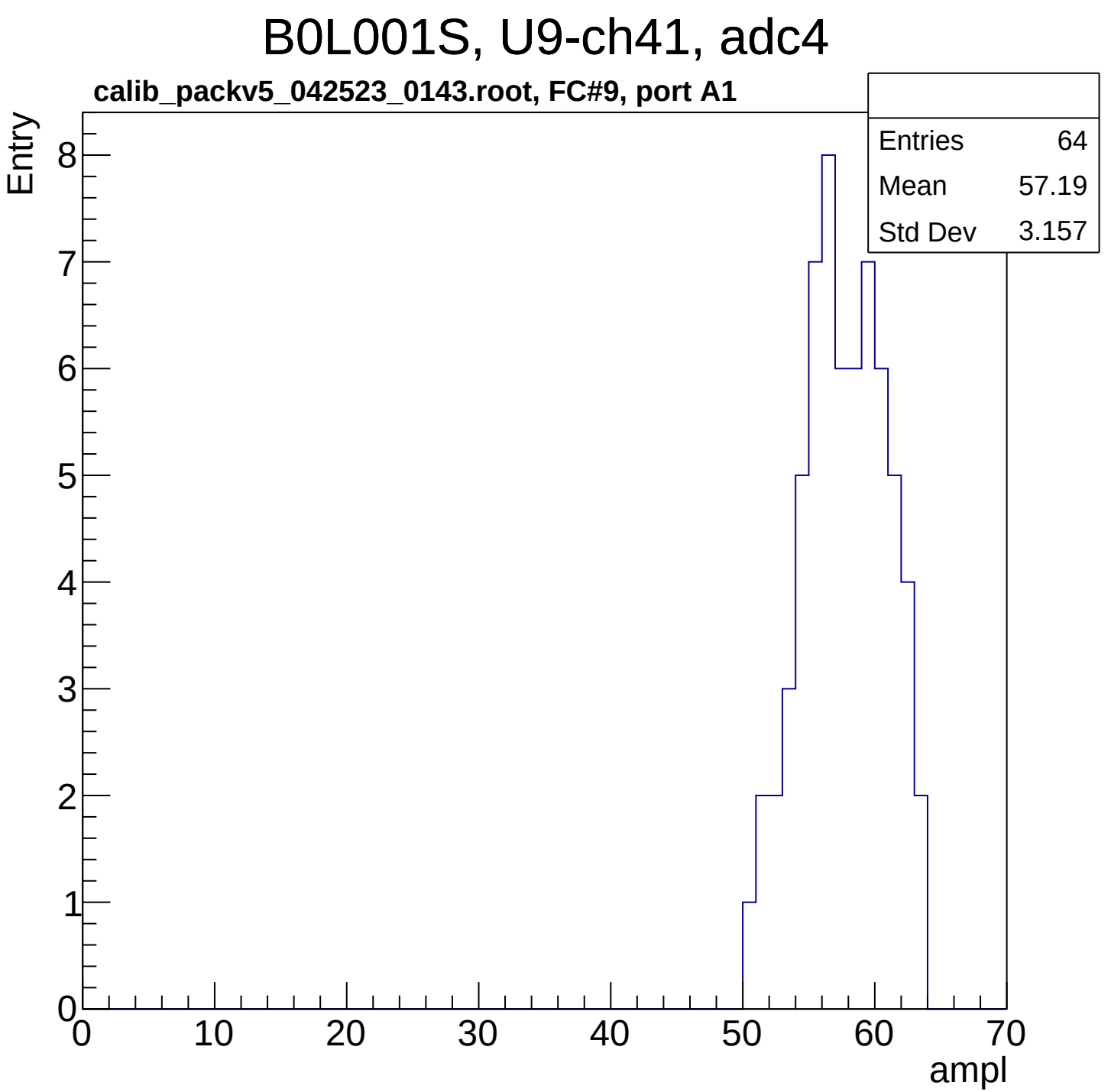
40
50
60
70


B0L001S, U9-ch41, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 57.19
Std Dev 3.157
Entry
0
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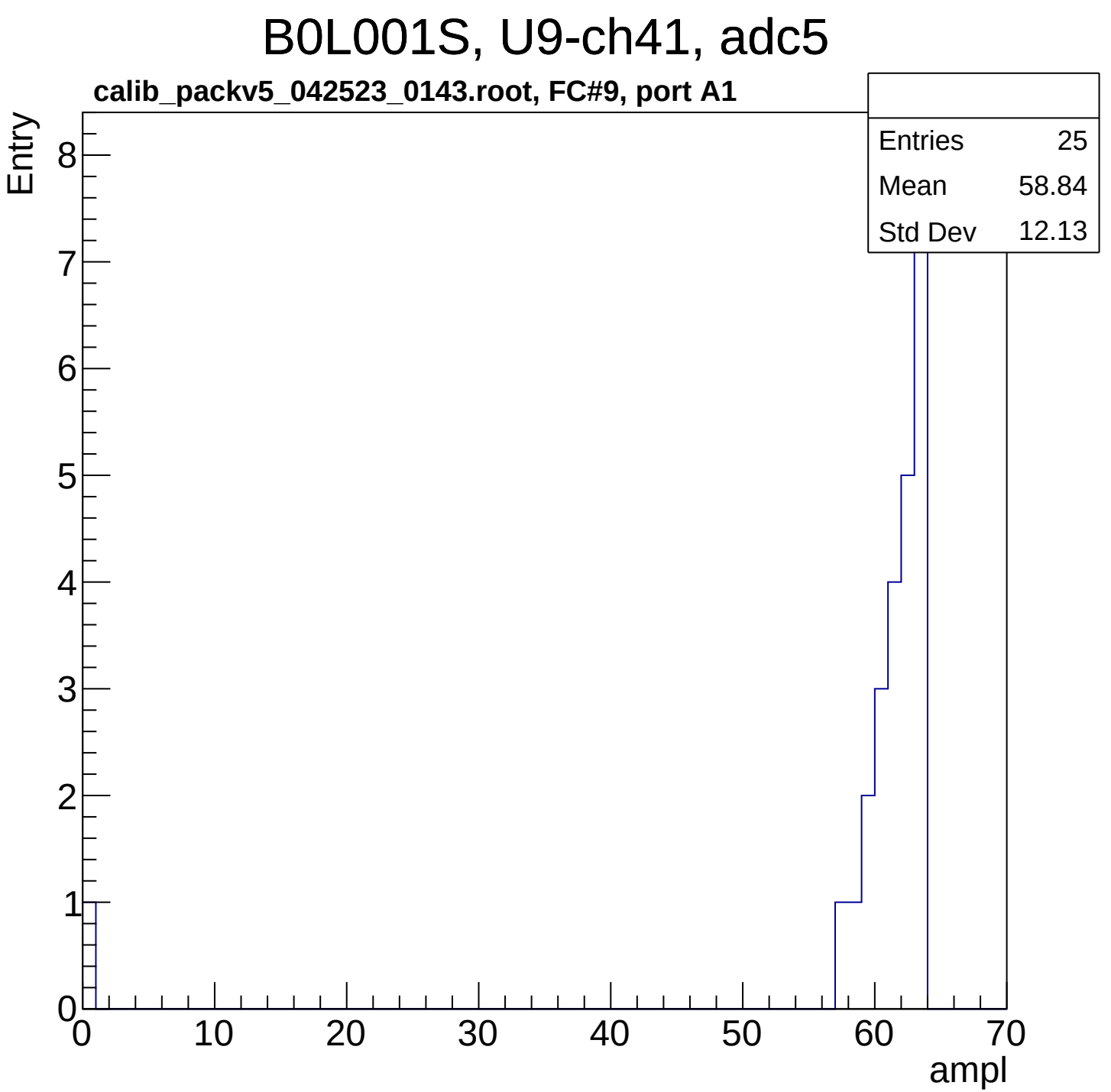
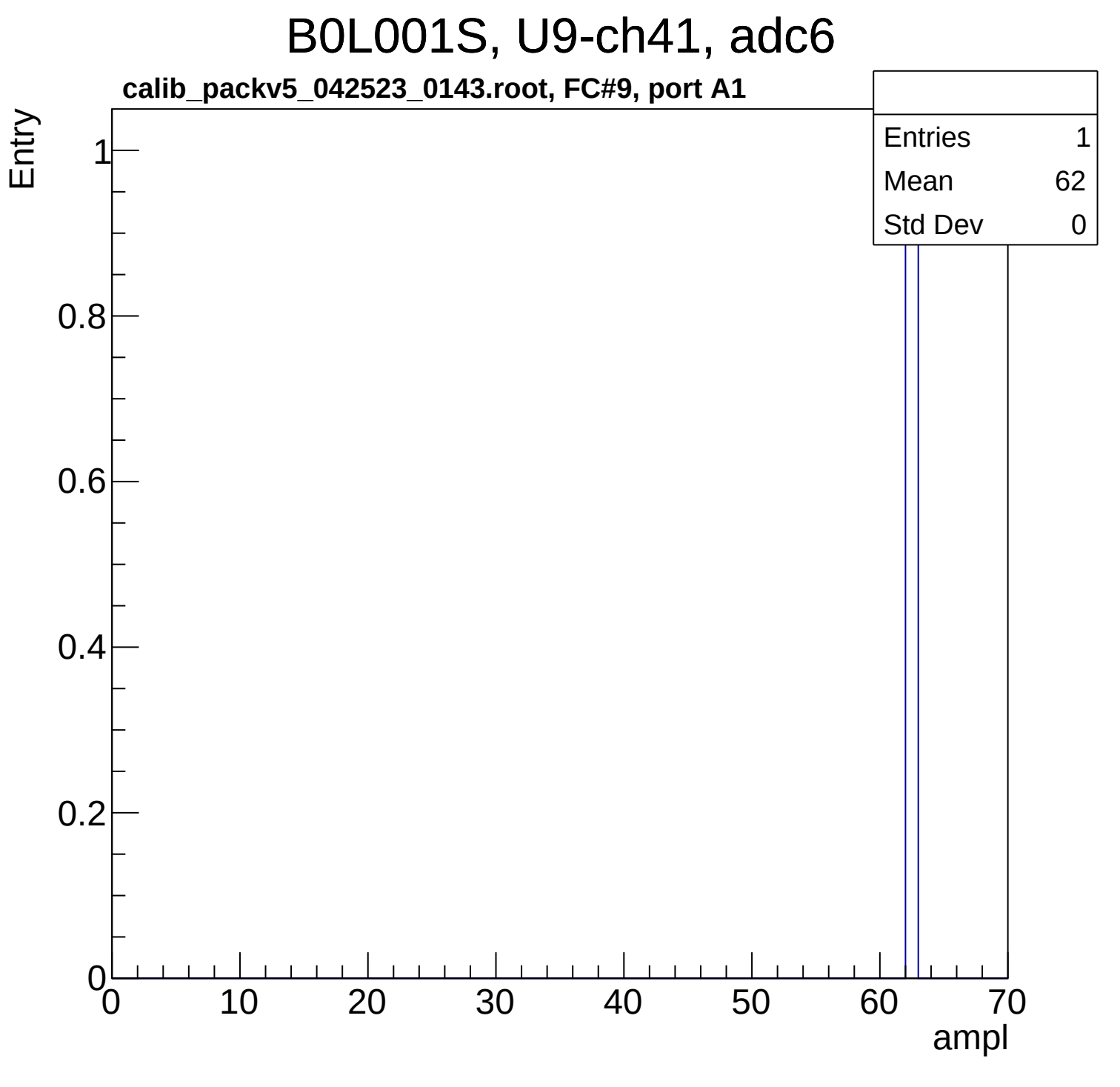
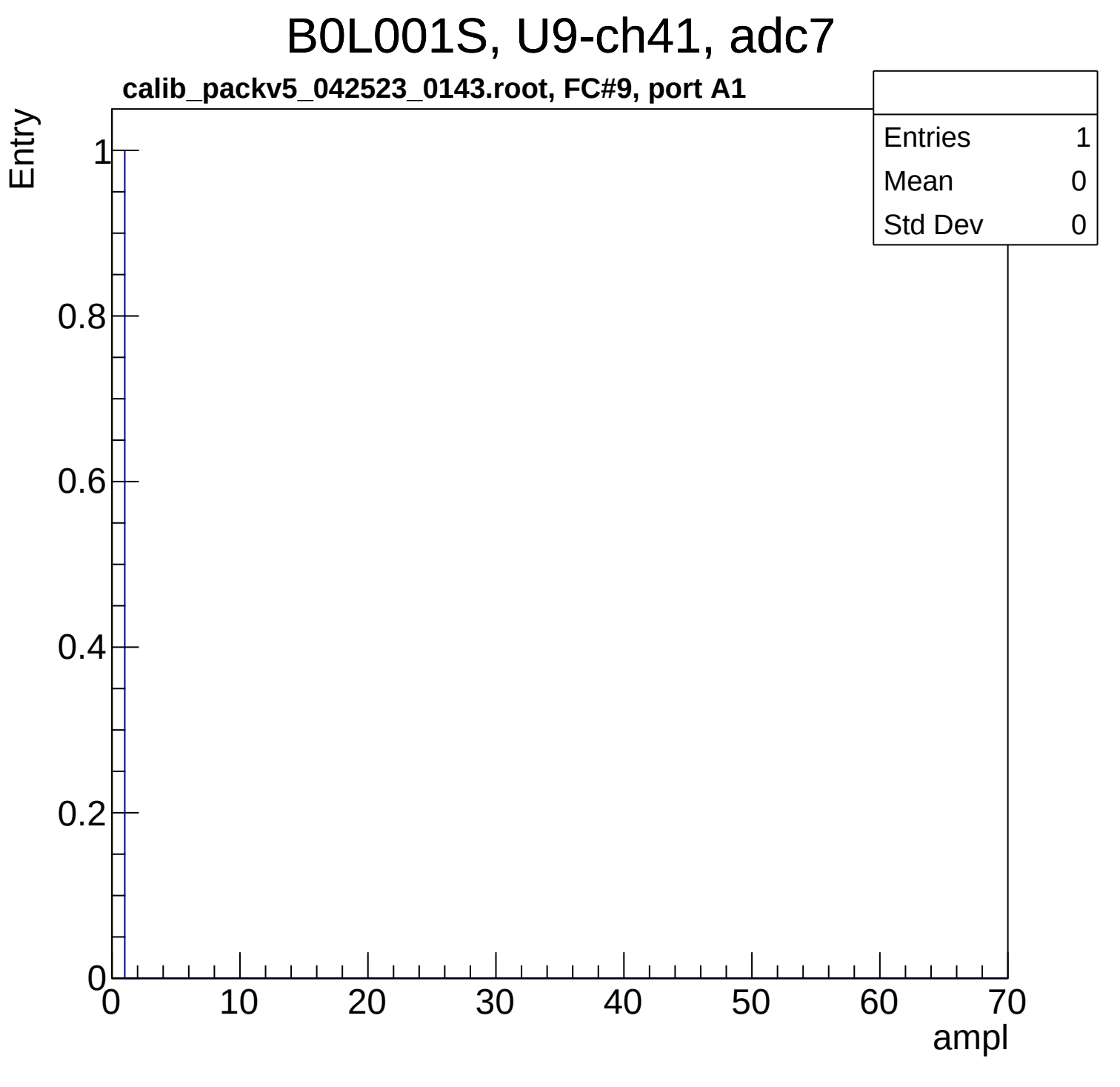
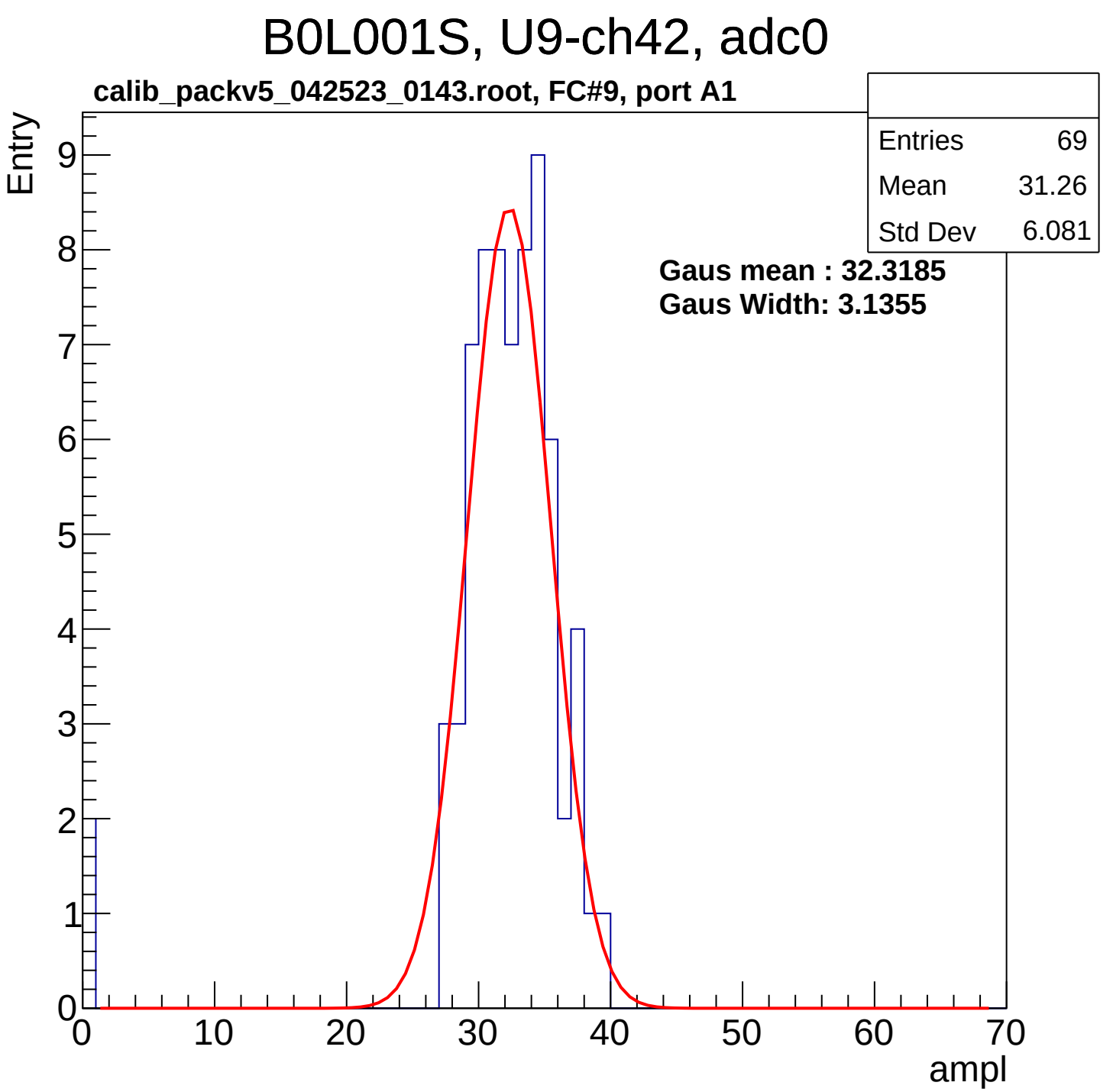
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch41, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.84
Std Dev 12.13
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch41, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch41, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch42, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 31.26
Std Dev 6.081
Gaus mean : 32.3185
Gaus Width: 3.1355
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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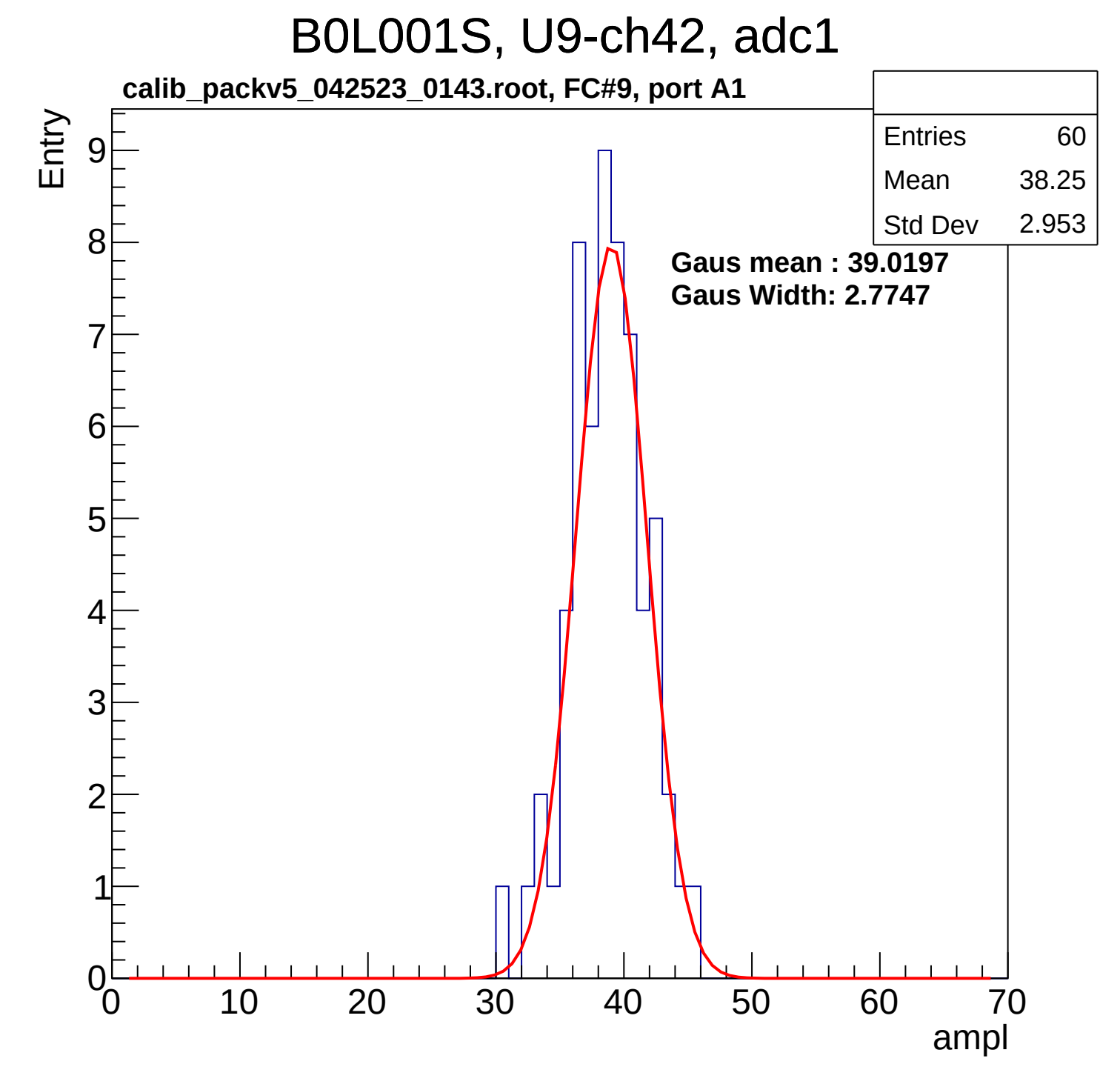
B0L001S, U9-ch42, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 38.25
Std Dev 2.953
Gaus mean : 39.0197
Gaus Width: 2.7747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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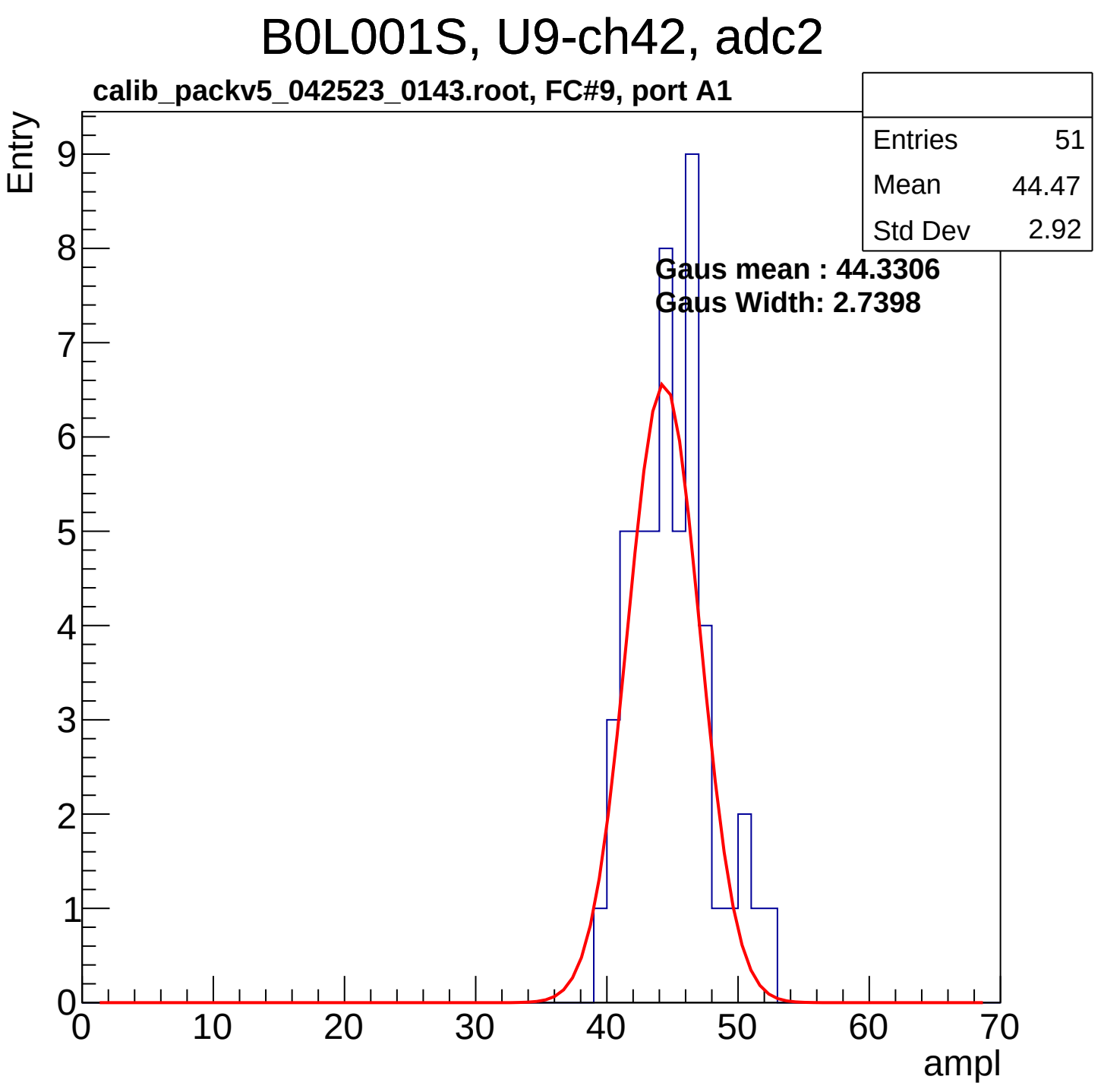
B0L001S, U9-ch42, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 44.47
Std Dev 2.92
Gaus mean : 44.3306
Gaus Width: 2.7398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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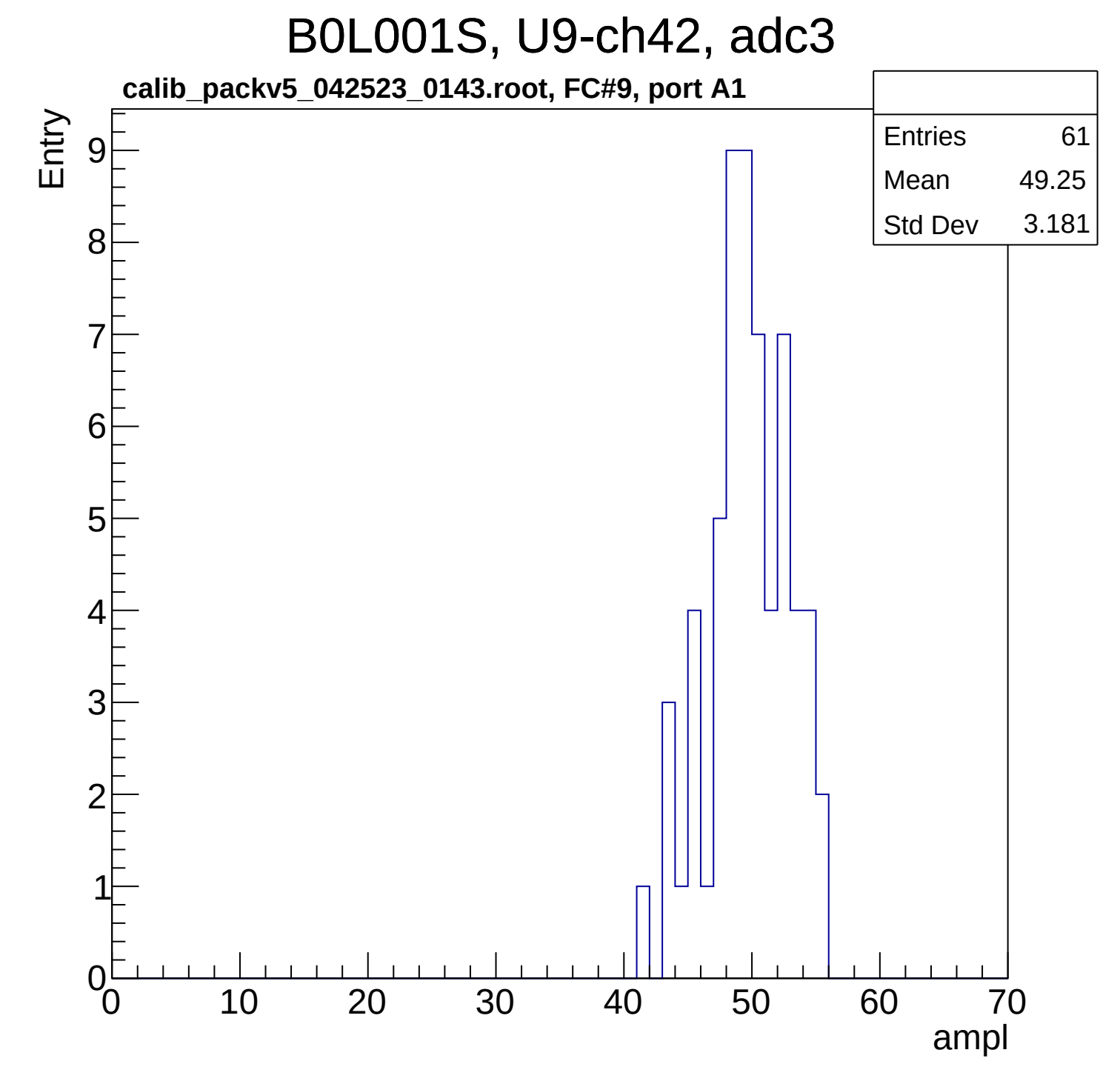
40
50
60
70


B0L001S, U9-ch42, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 49.25
Std Dev 3.181
Entry
ampl
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1
2
3
4
5
6
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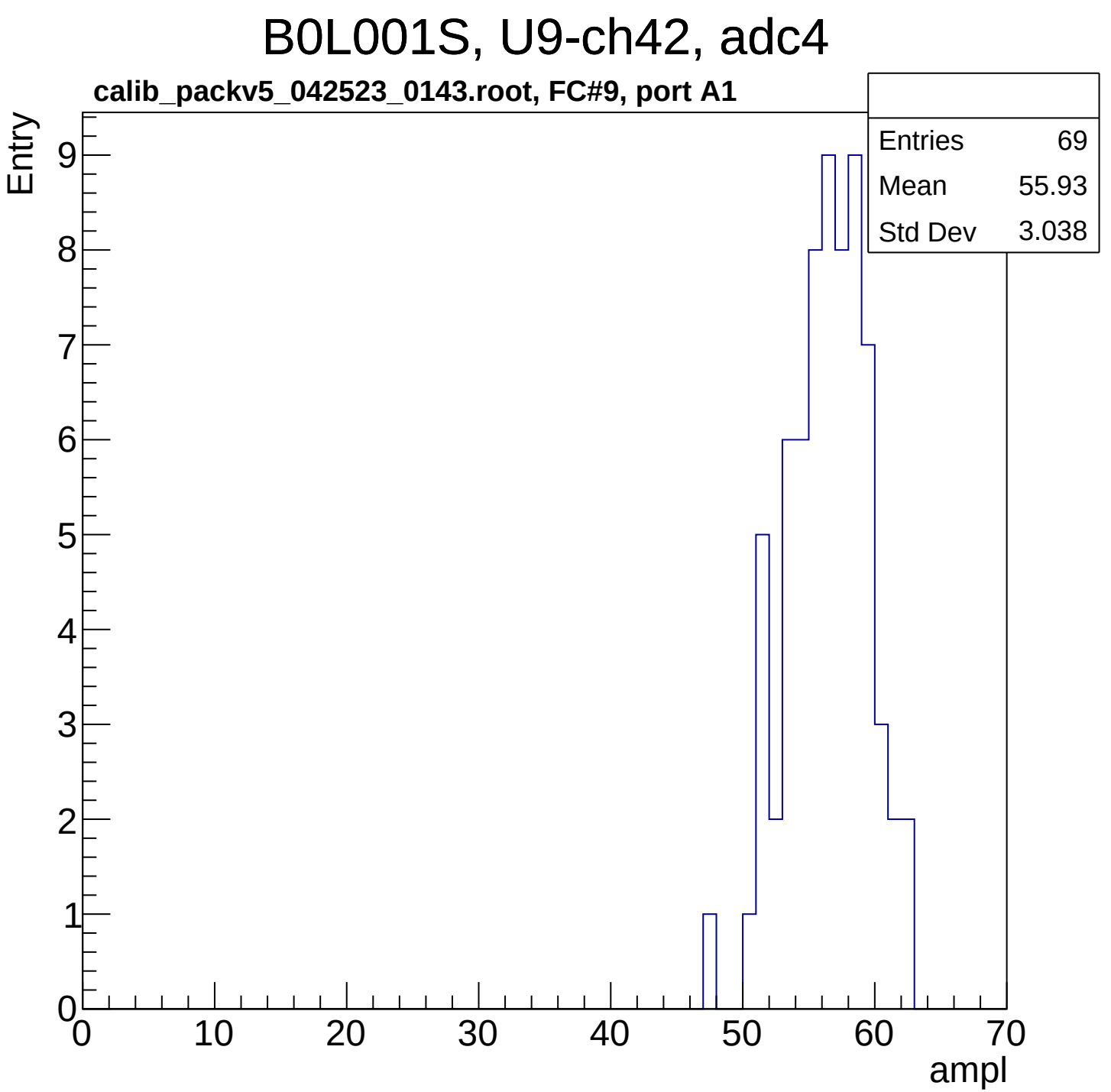
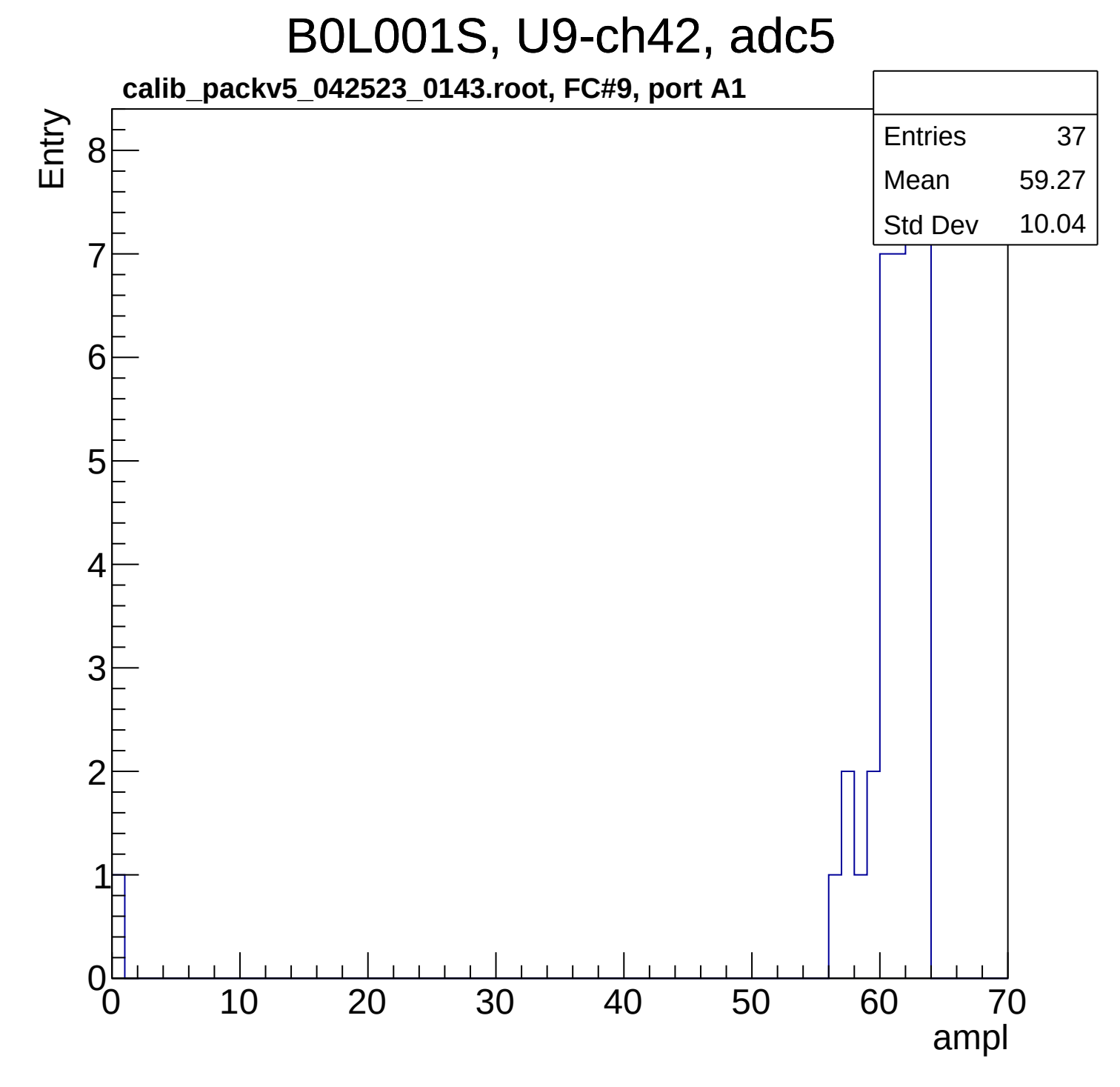

B0L001S, U9-ch42, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 55.93
Std Dev 3.038
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch42, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.27
Std Dev 10.04
Entry
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1
2
3
4
5
6
7
8
0
10
20
30
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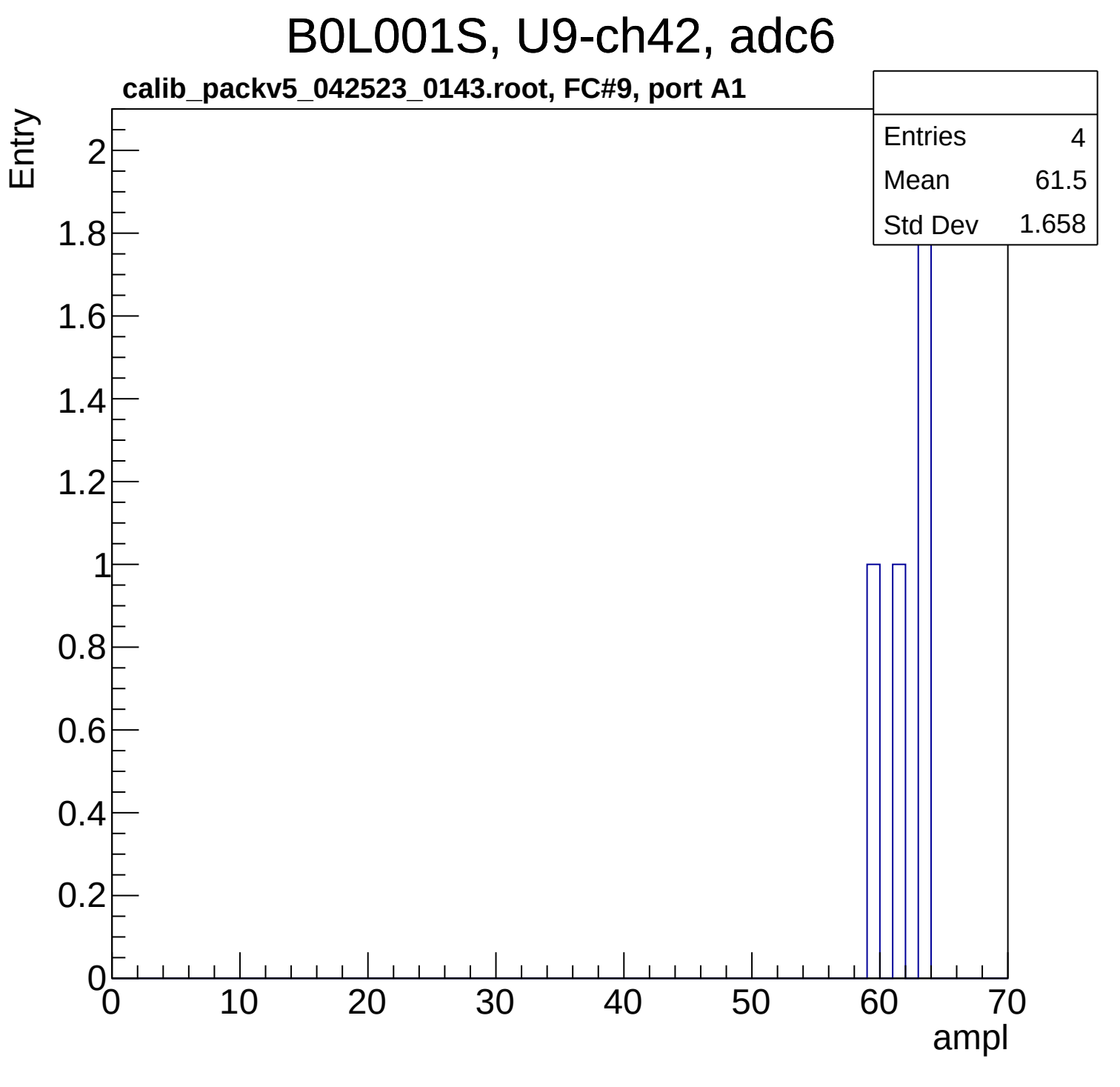
50
60
70
ampl


B0L001S, U9-ch42, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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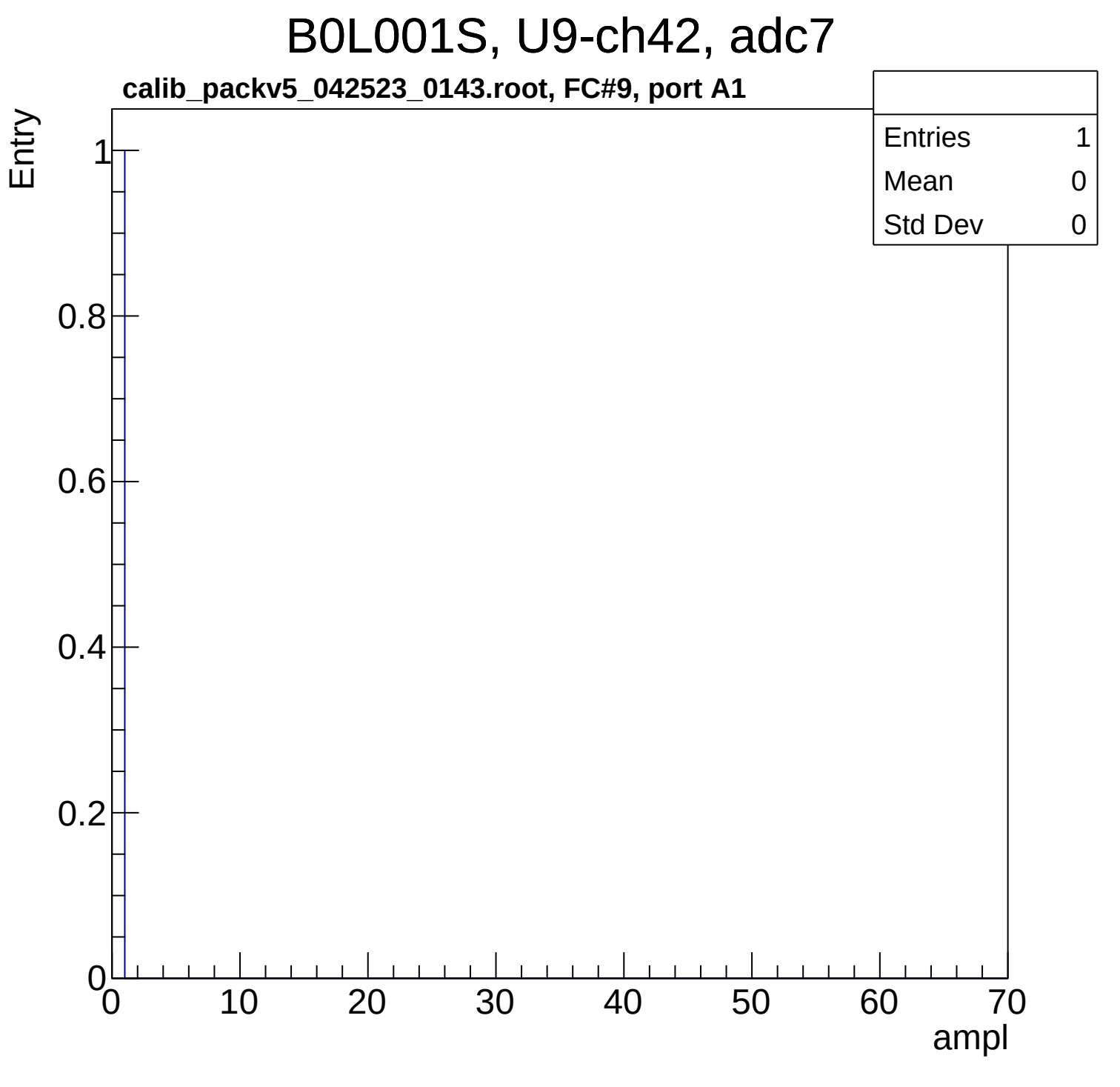
B0L001S, U9-ch42, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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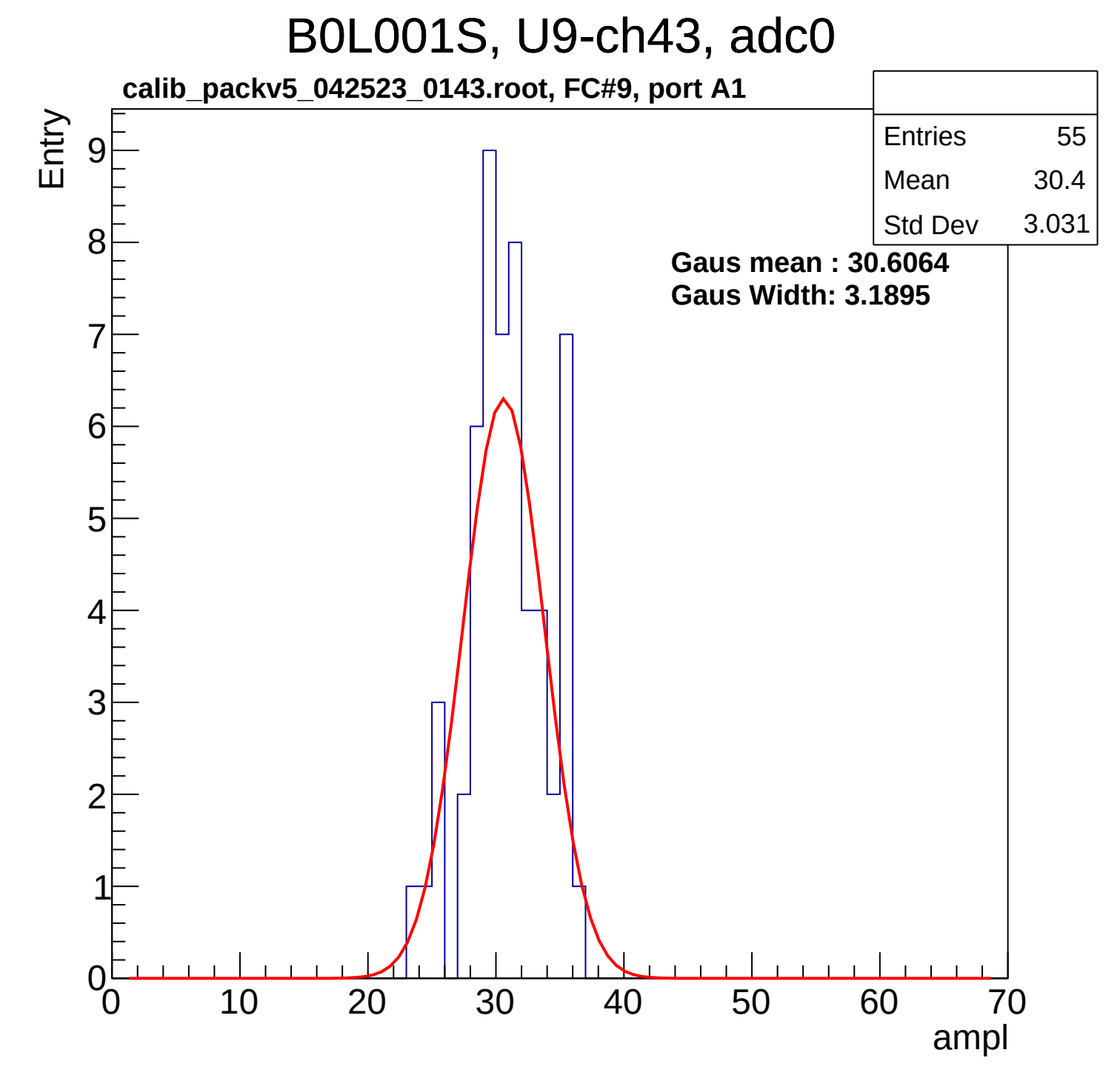
40
50
60
70
ampl


B0L001S, U9-ch43, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 30.4
Std Dev 3.031
Gaus mean : 30.6064
Gaus Width: 3.1895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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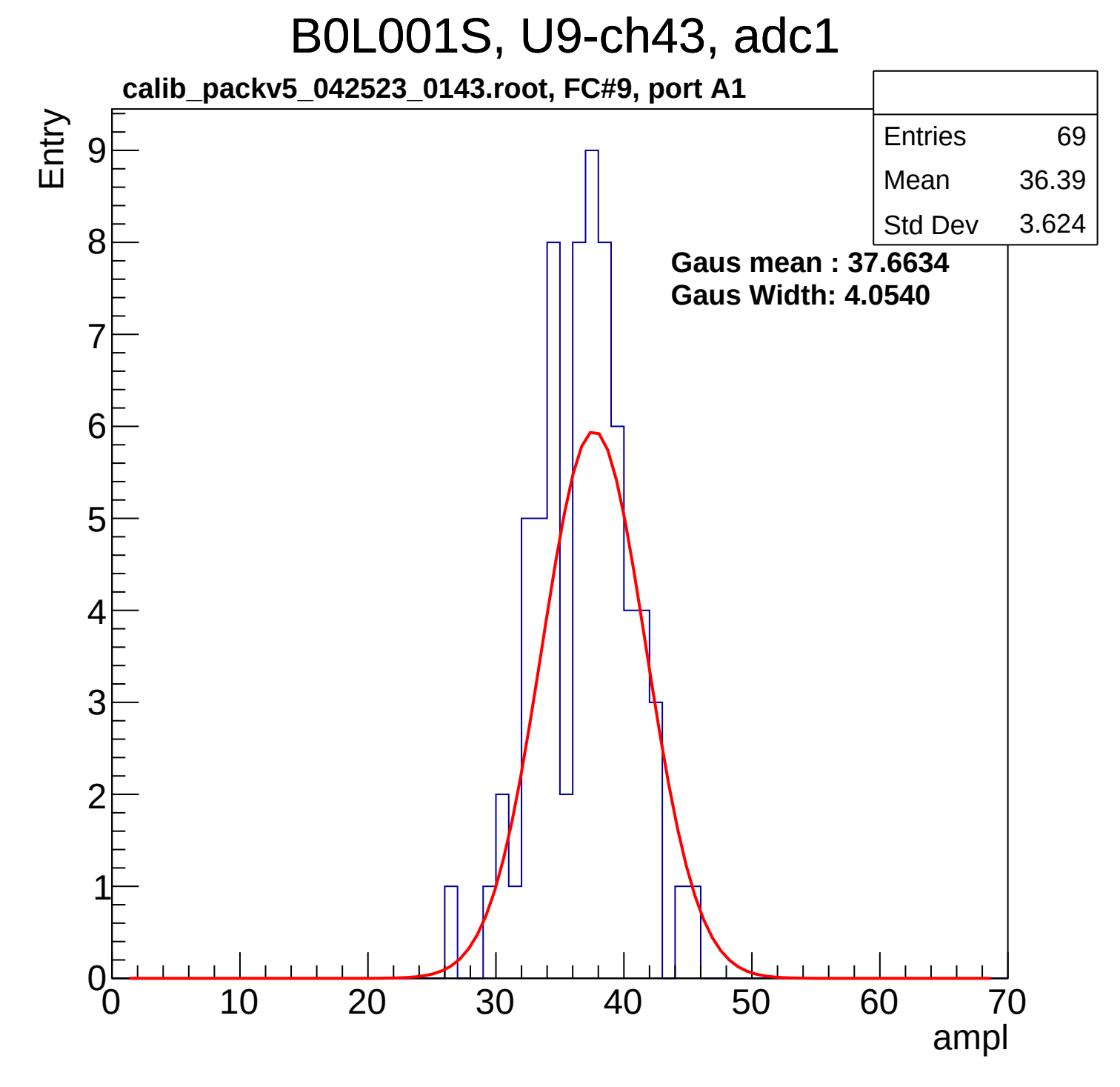
B0L001S, U9-ch43, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 36.39
Std Dev 3.624
Gaus mean : 37.6634
Gaus Width: 4.0540
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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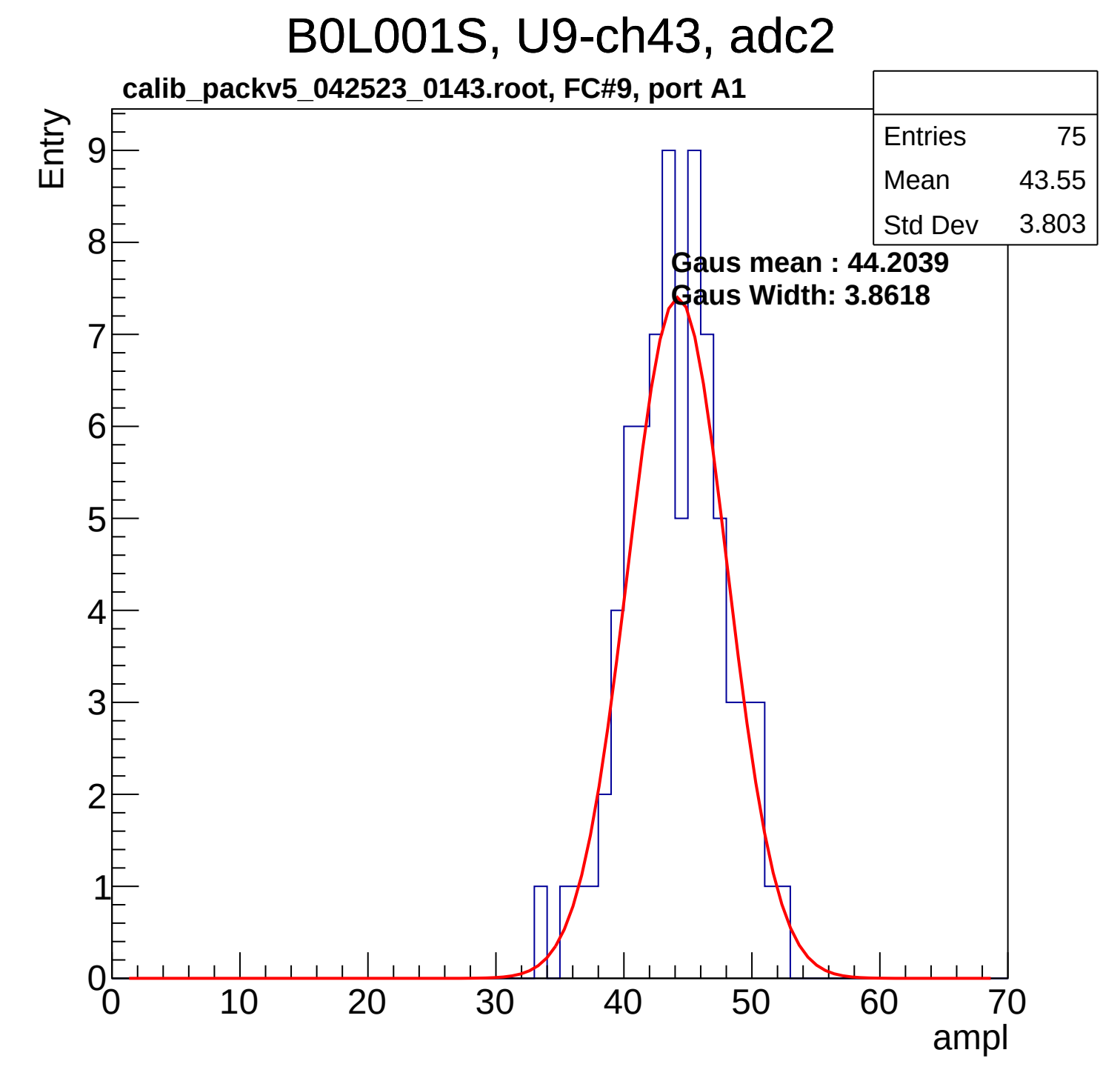
B0L001S, U9-ch43, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 43.55
Std Dev 3.803
Gaus mean : 44.2039
Gaus Width: 3.8618
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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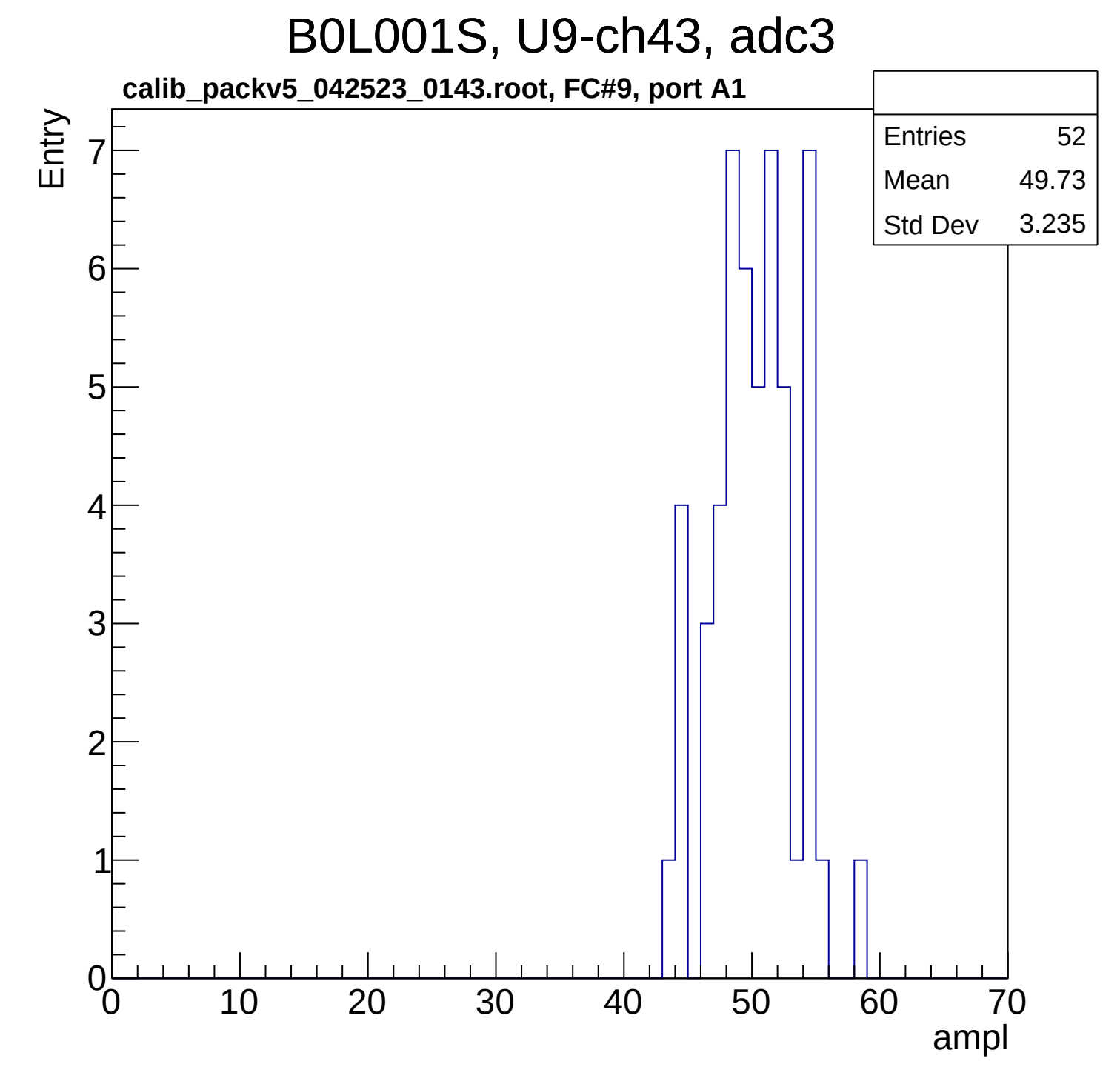
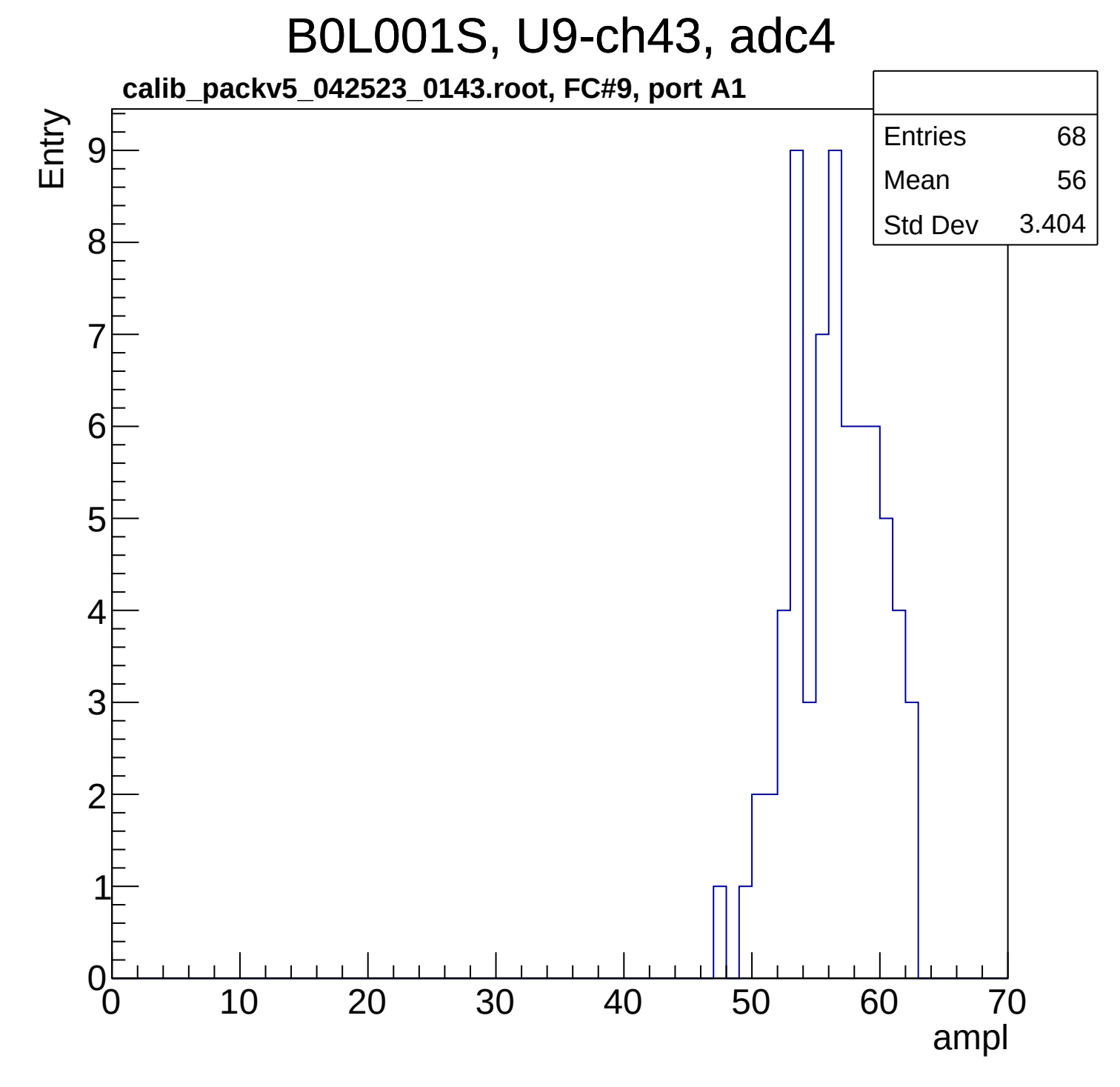
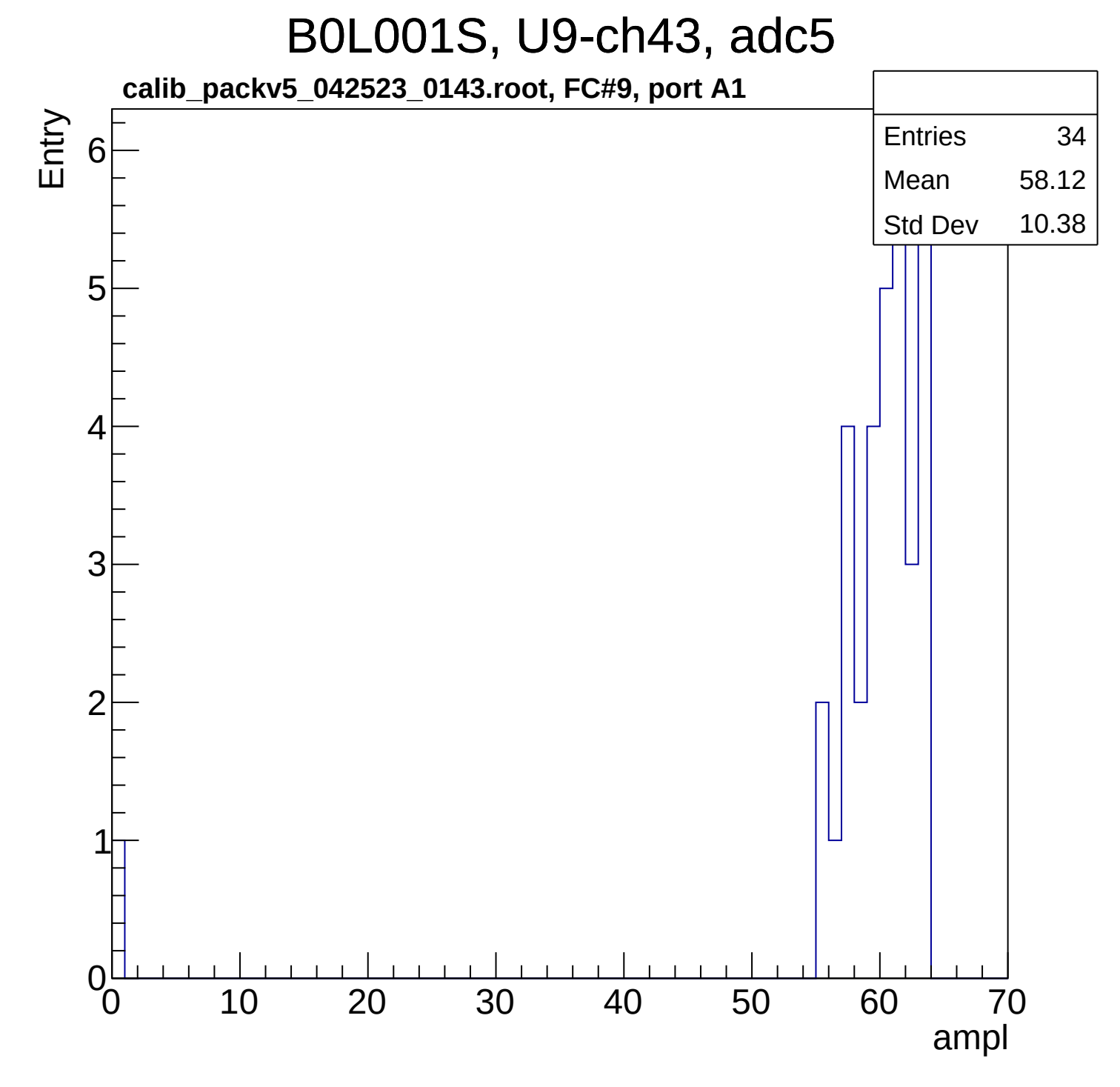
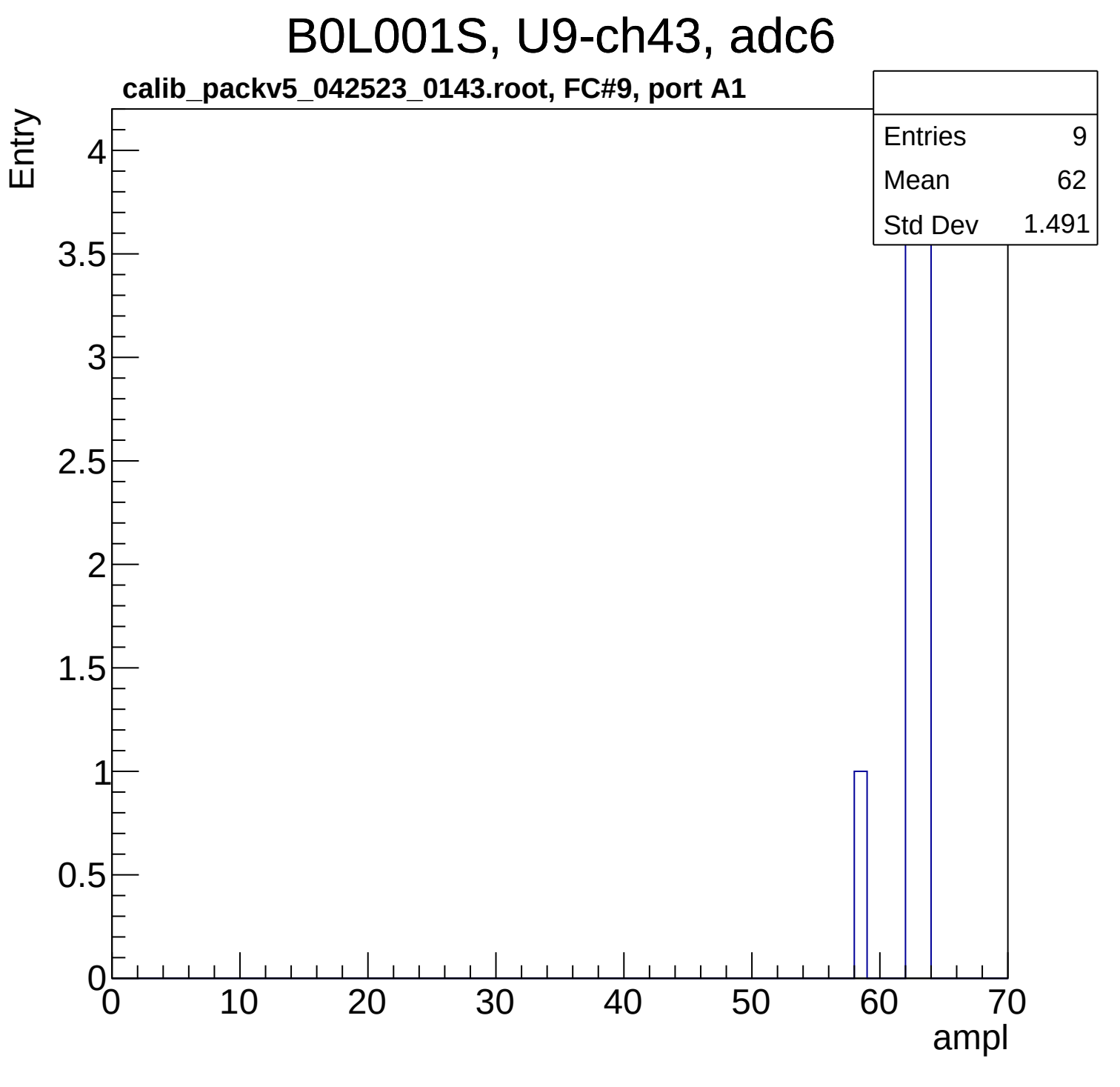
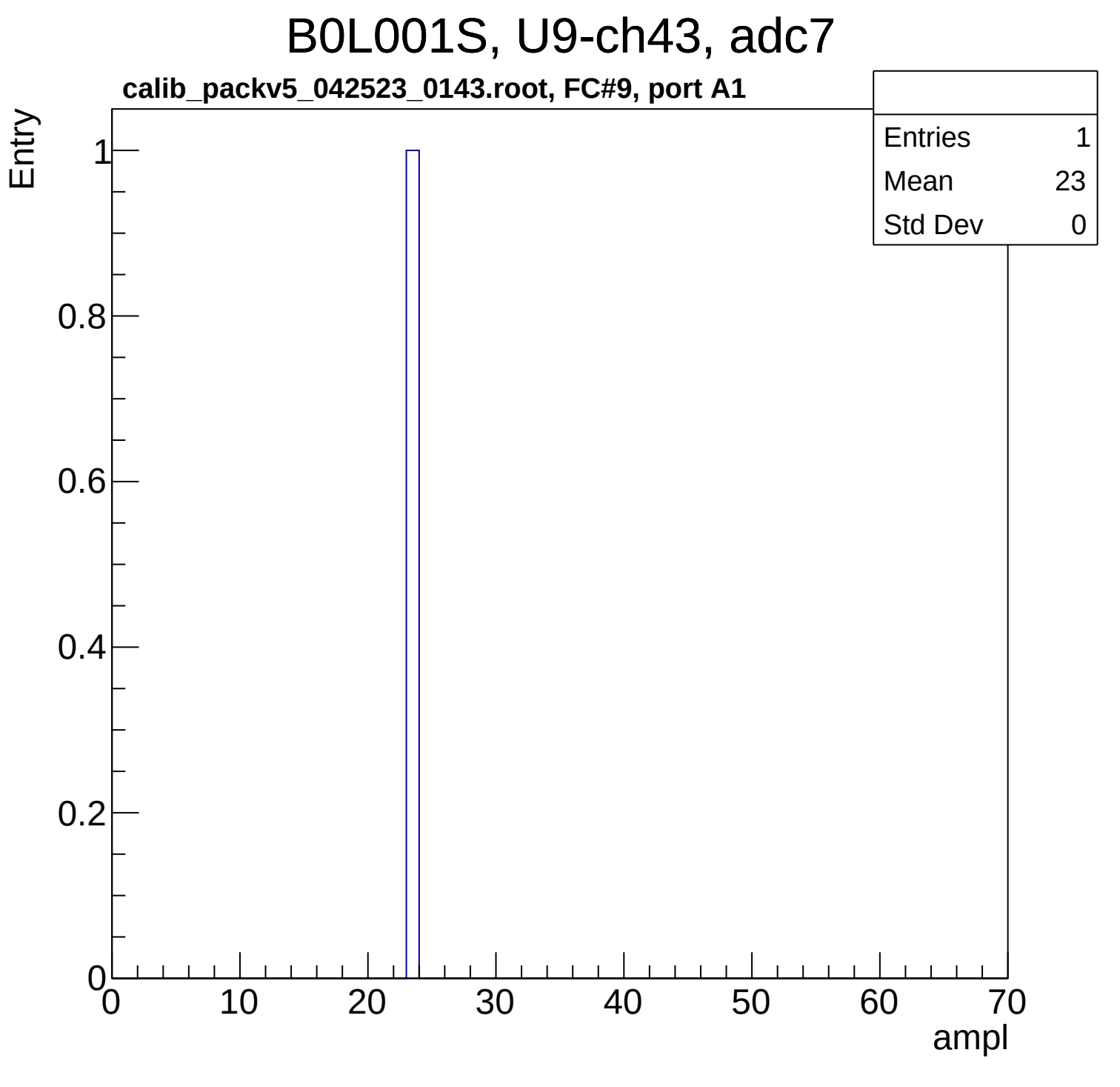

B0L001S, U9-ch43, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 49.73
Std Dev 3.235
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch43, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 56
Std Dev 3.404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch43, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.12
Std Dev 10.38
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch43, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 62
Std Dev 1.491
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U9-ch43, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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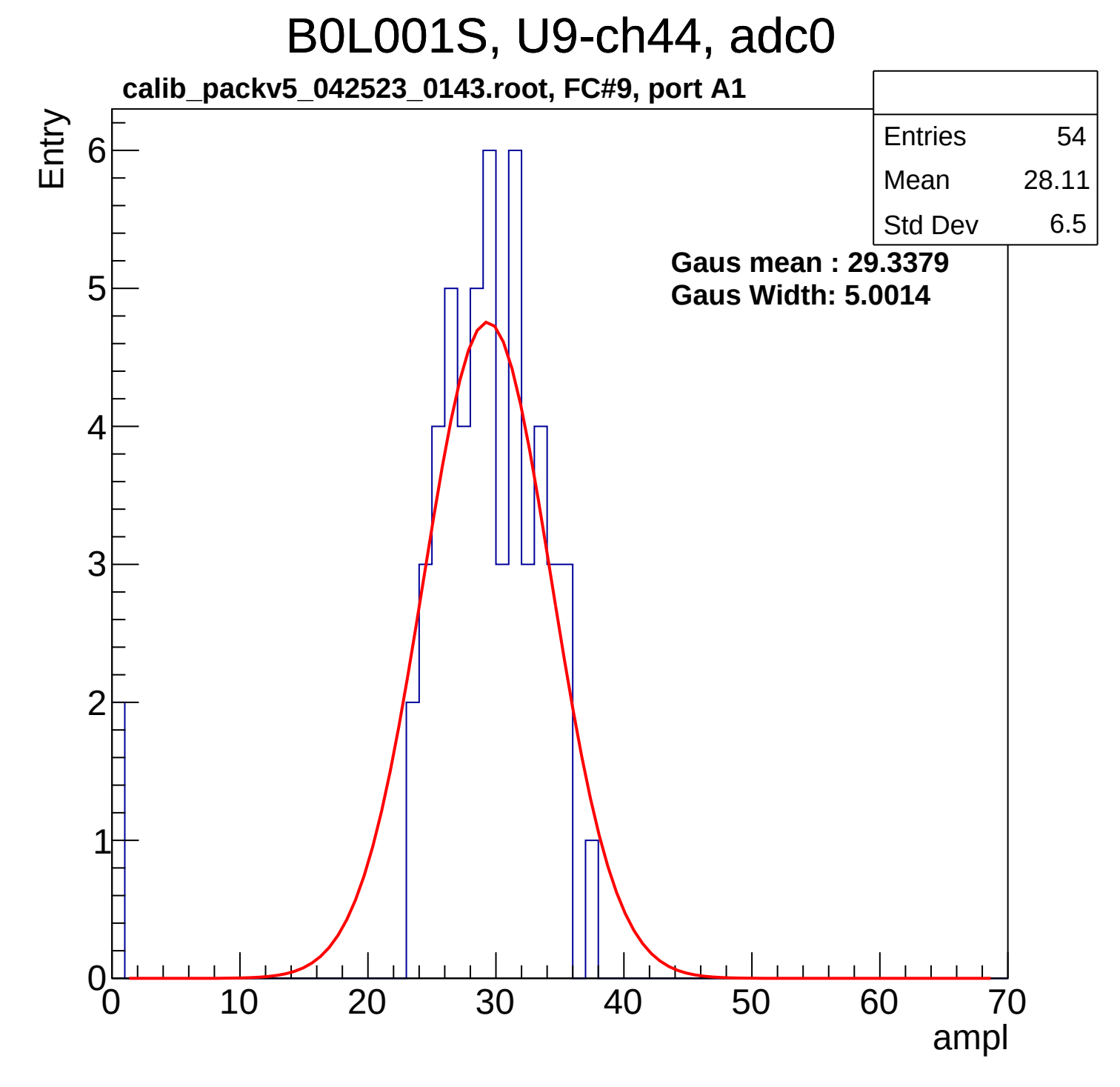
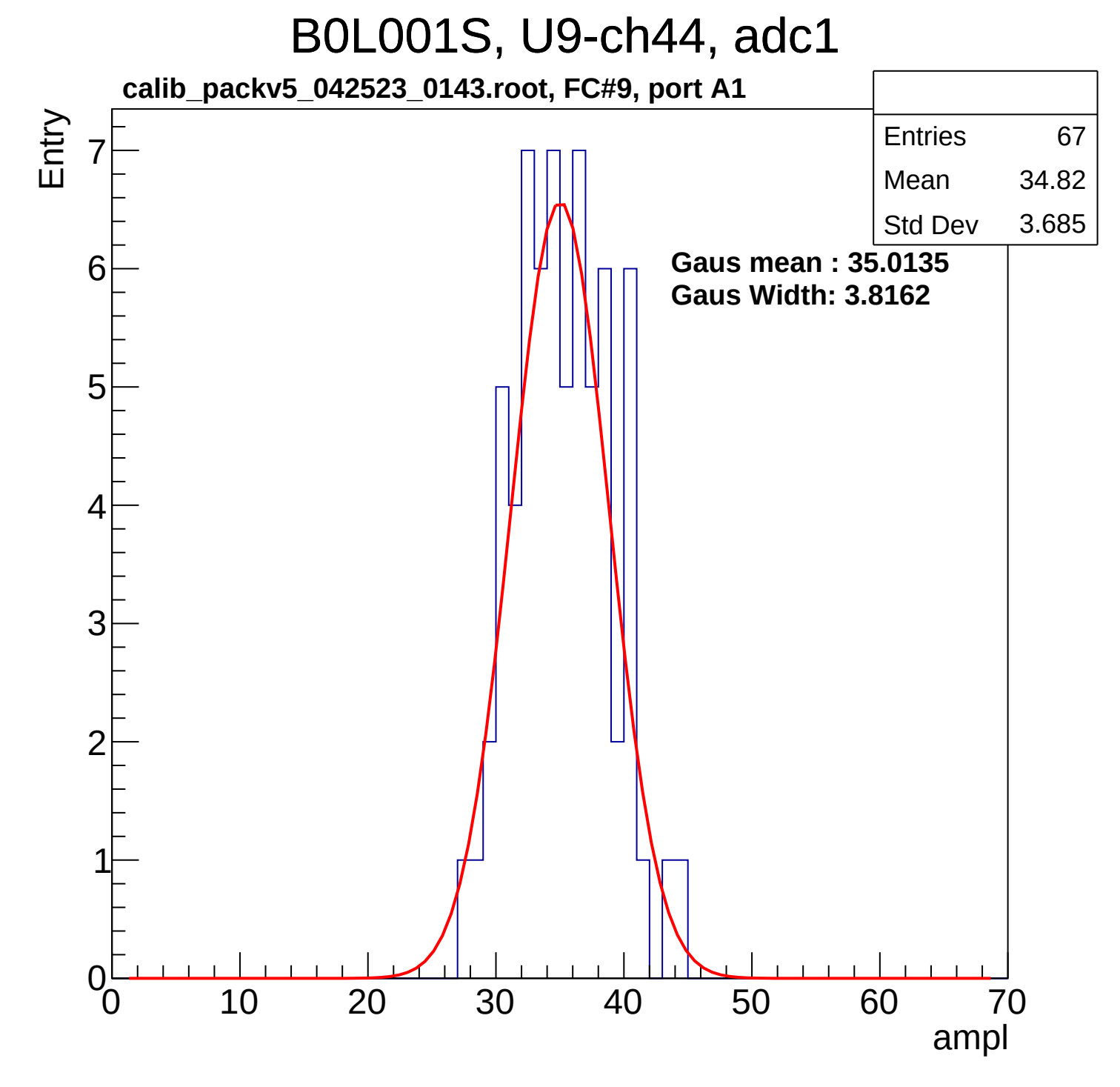
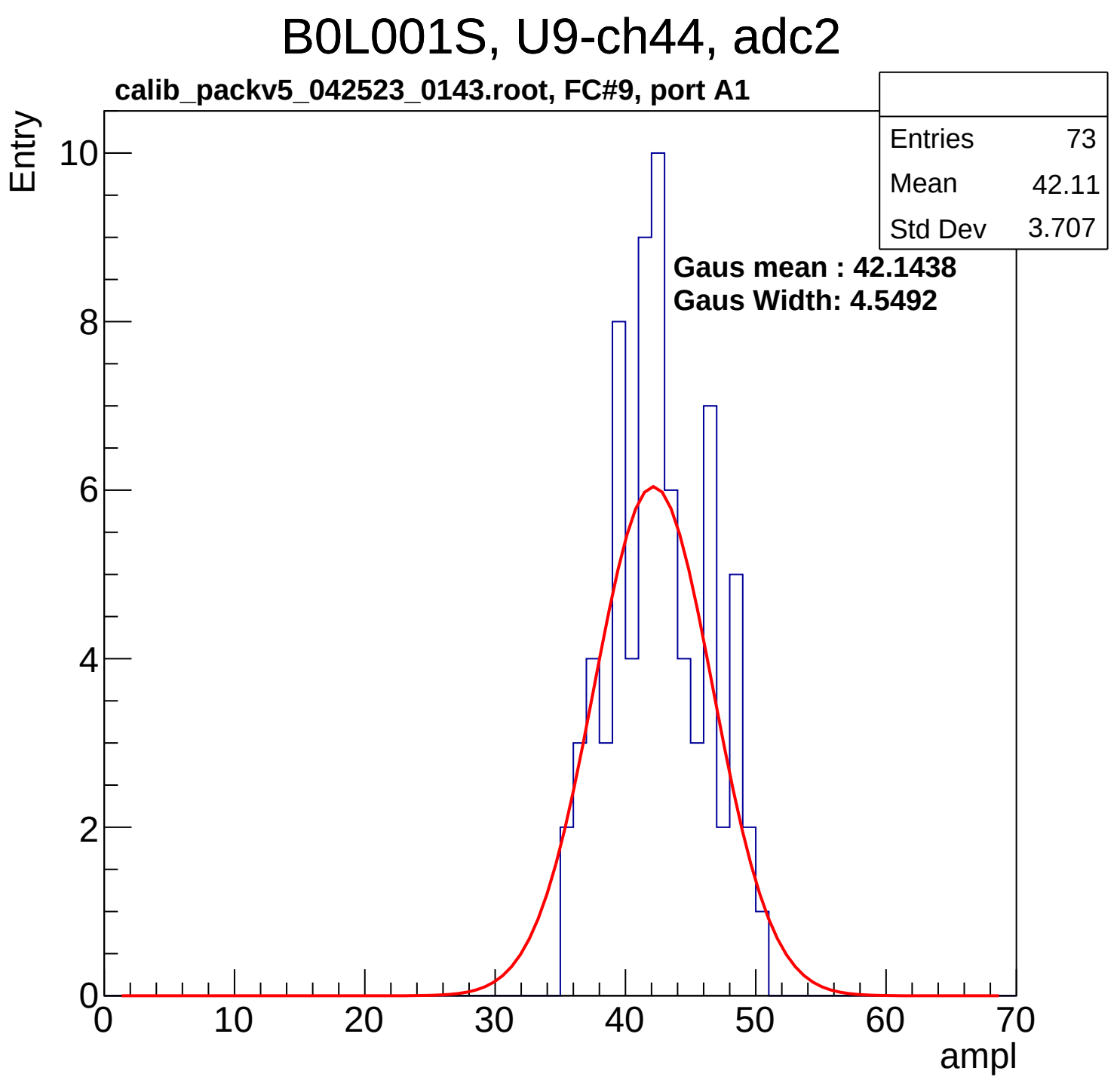
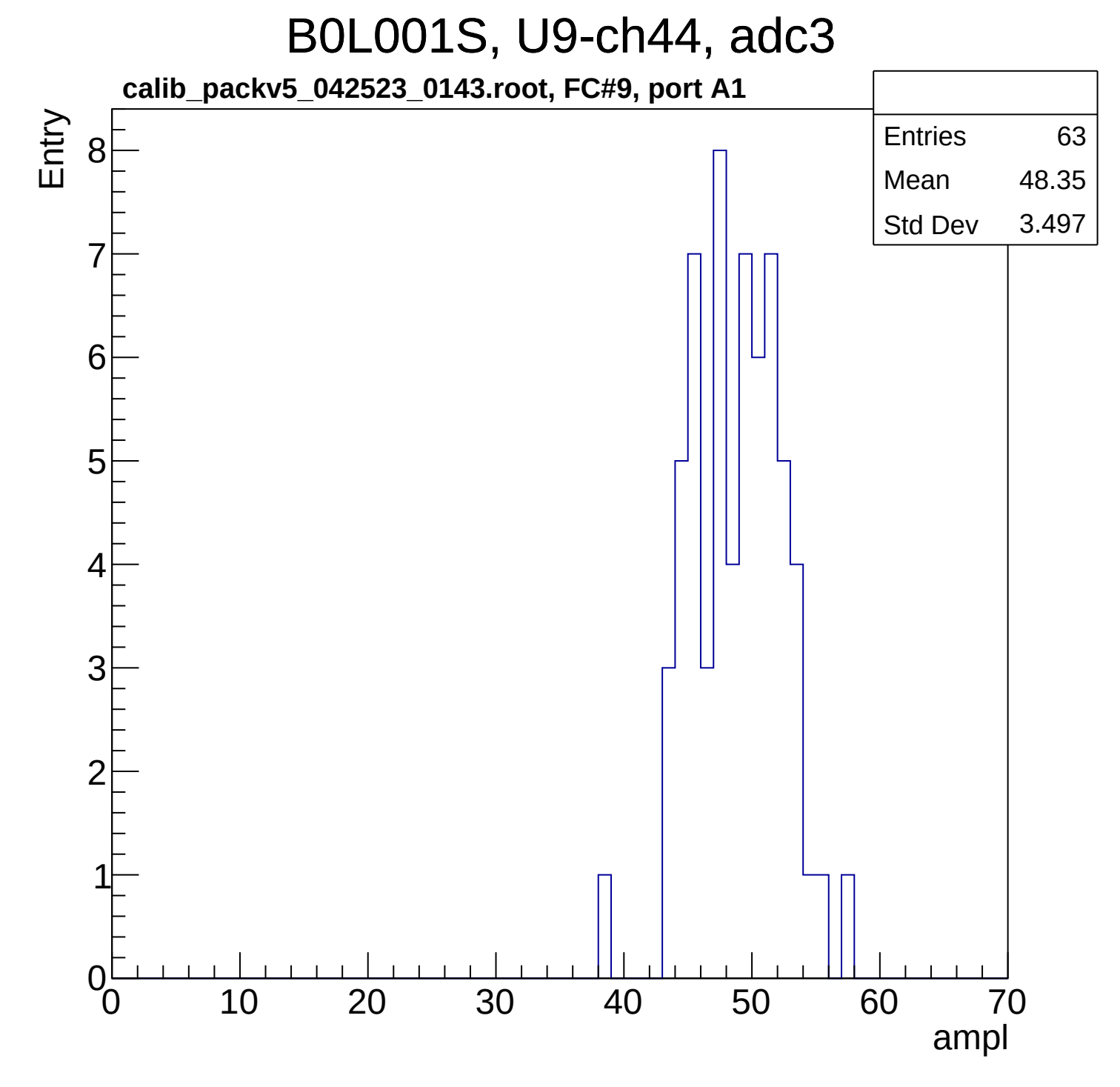
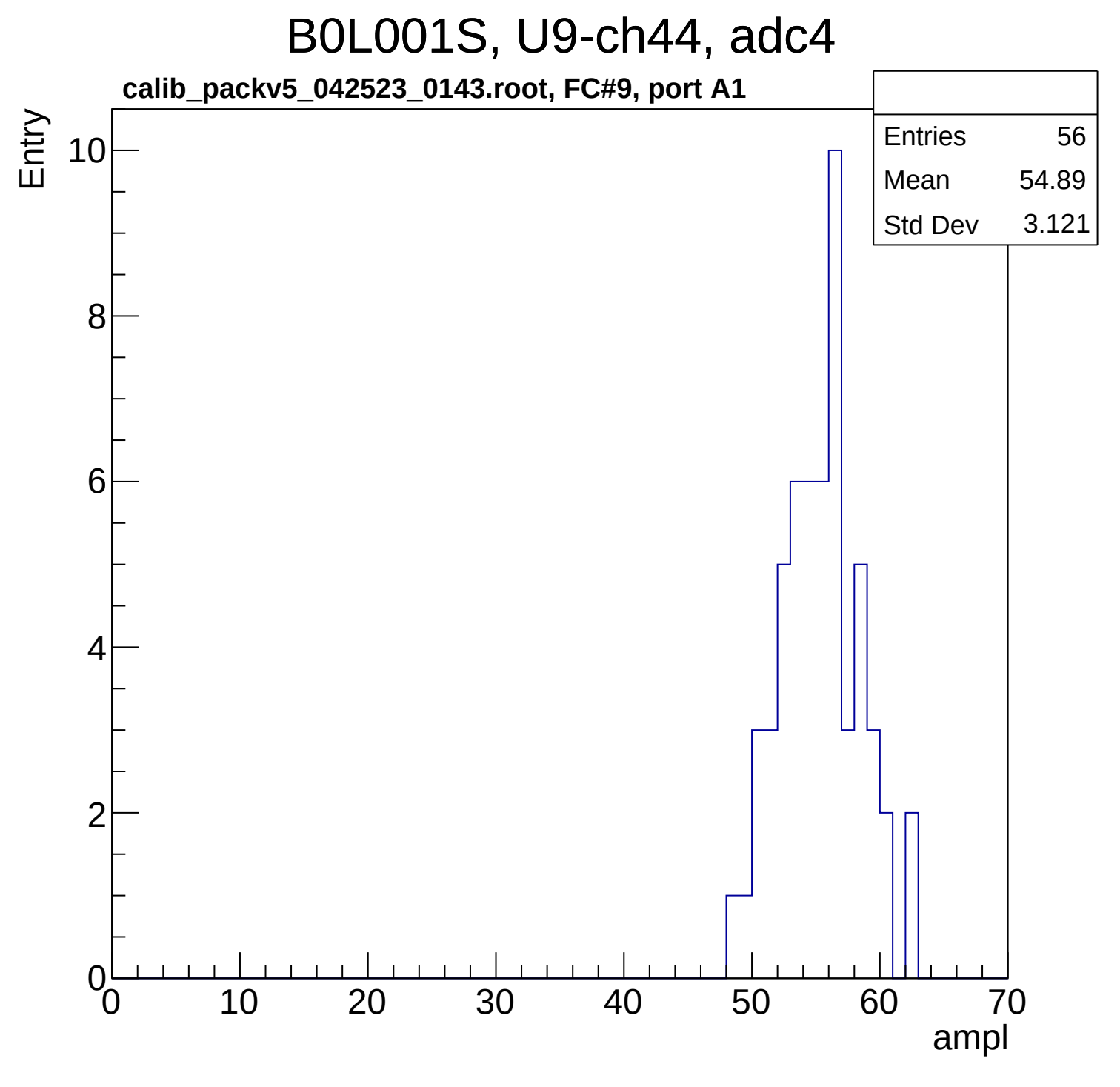
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

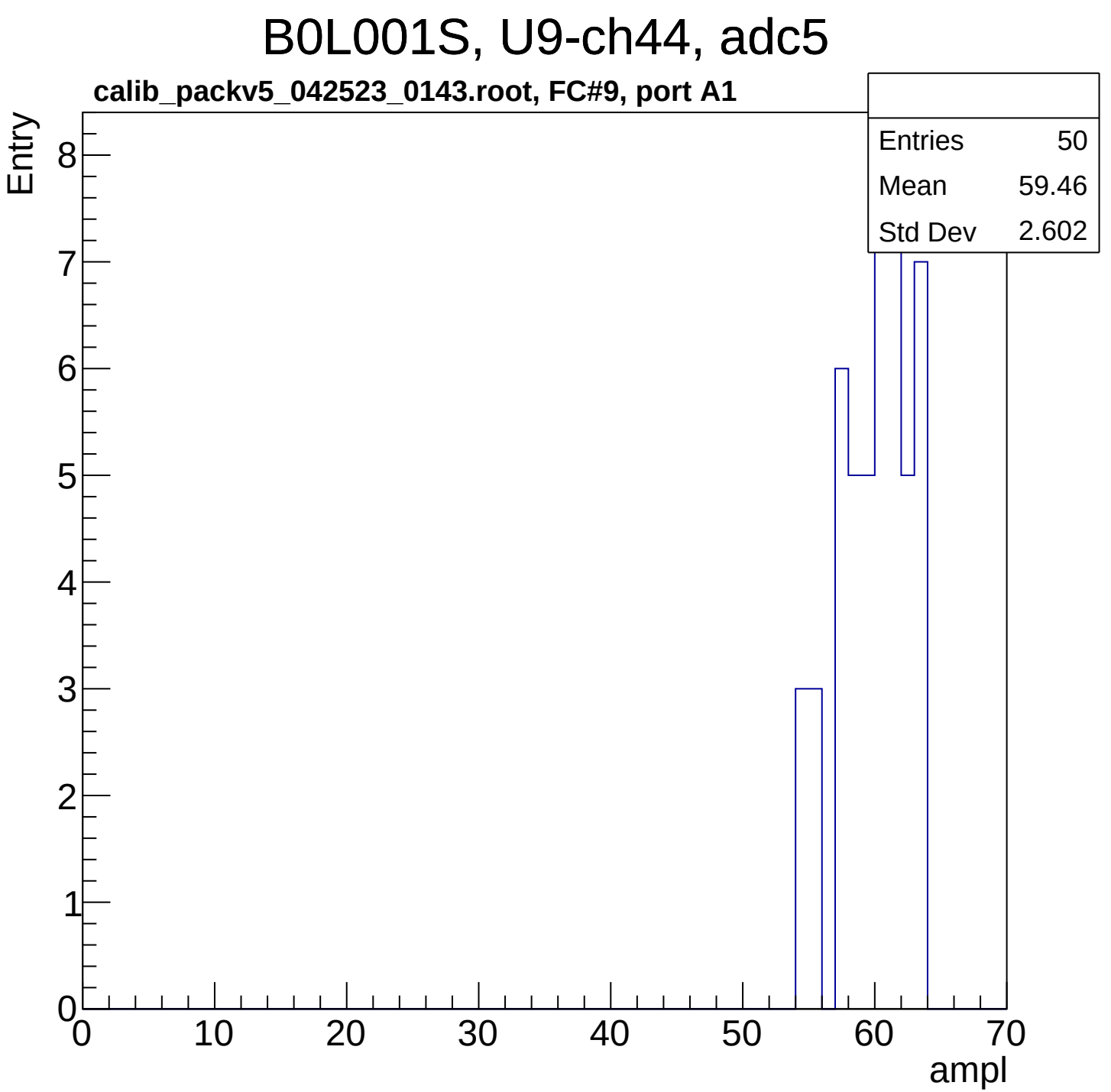

B0L001S, U9-ch44, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 28.11
Std Dev 6.5
Gaus mean : 29.3379
Gaus Width: 5.0014
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U9-ch44, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 34.82
Std Dev 3.685
Gaus mean : 35.0135
Gaus Width: 3.8162
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch44, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 42.11
Std Dev 3.707
Gaus mean : 42.1438
Gaus Width: 4.5492
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch44, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 48.35
Std Dev 3.497
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch44, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 54.89
Std Dev 3.121
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch44, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 59.46
Std Dev 2.602
Entry
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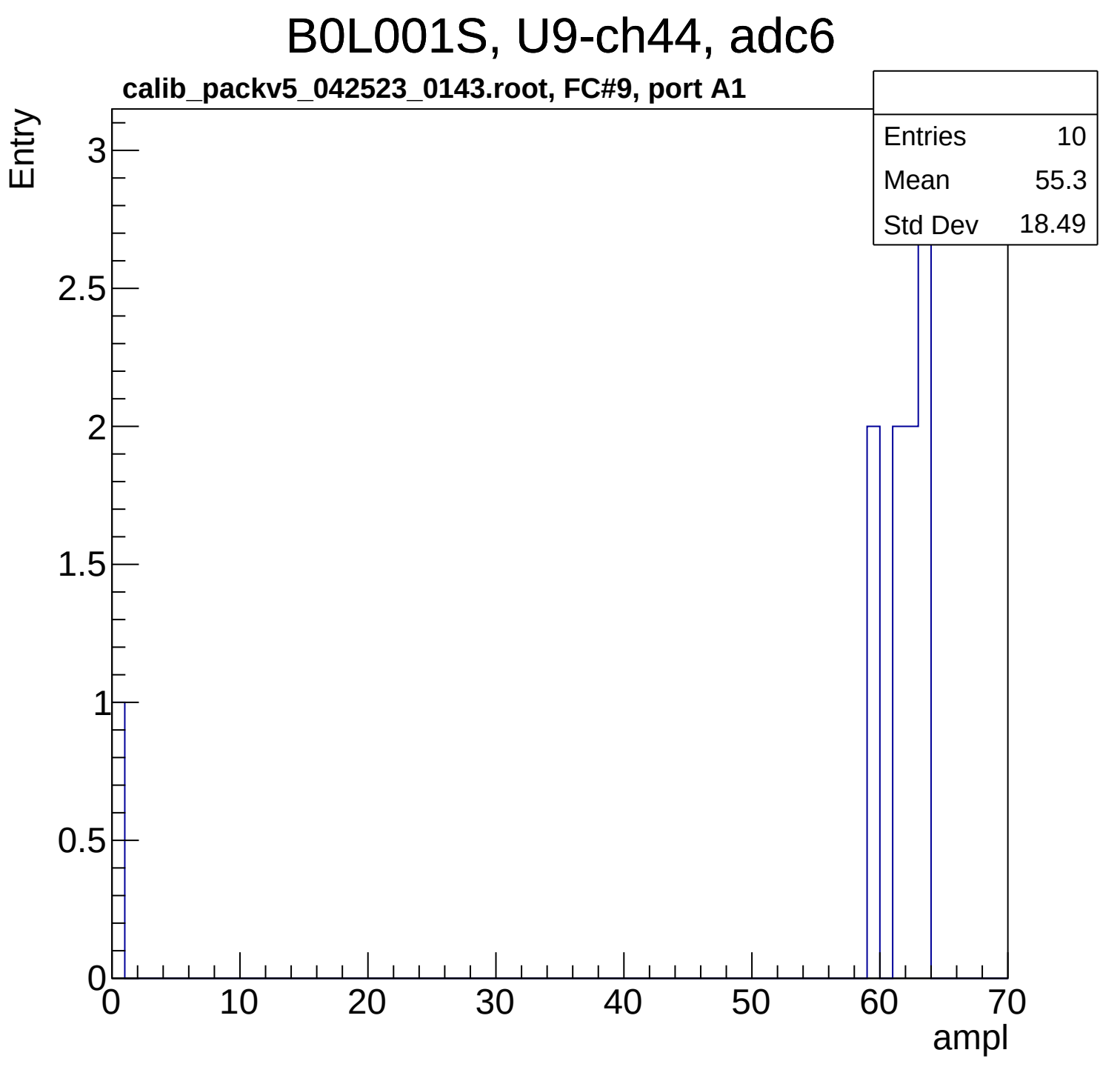
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch44, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 55.3
Std Dev 18.49
Entry
ampl
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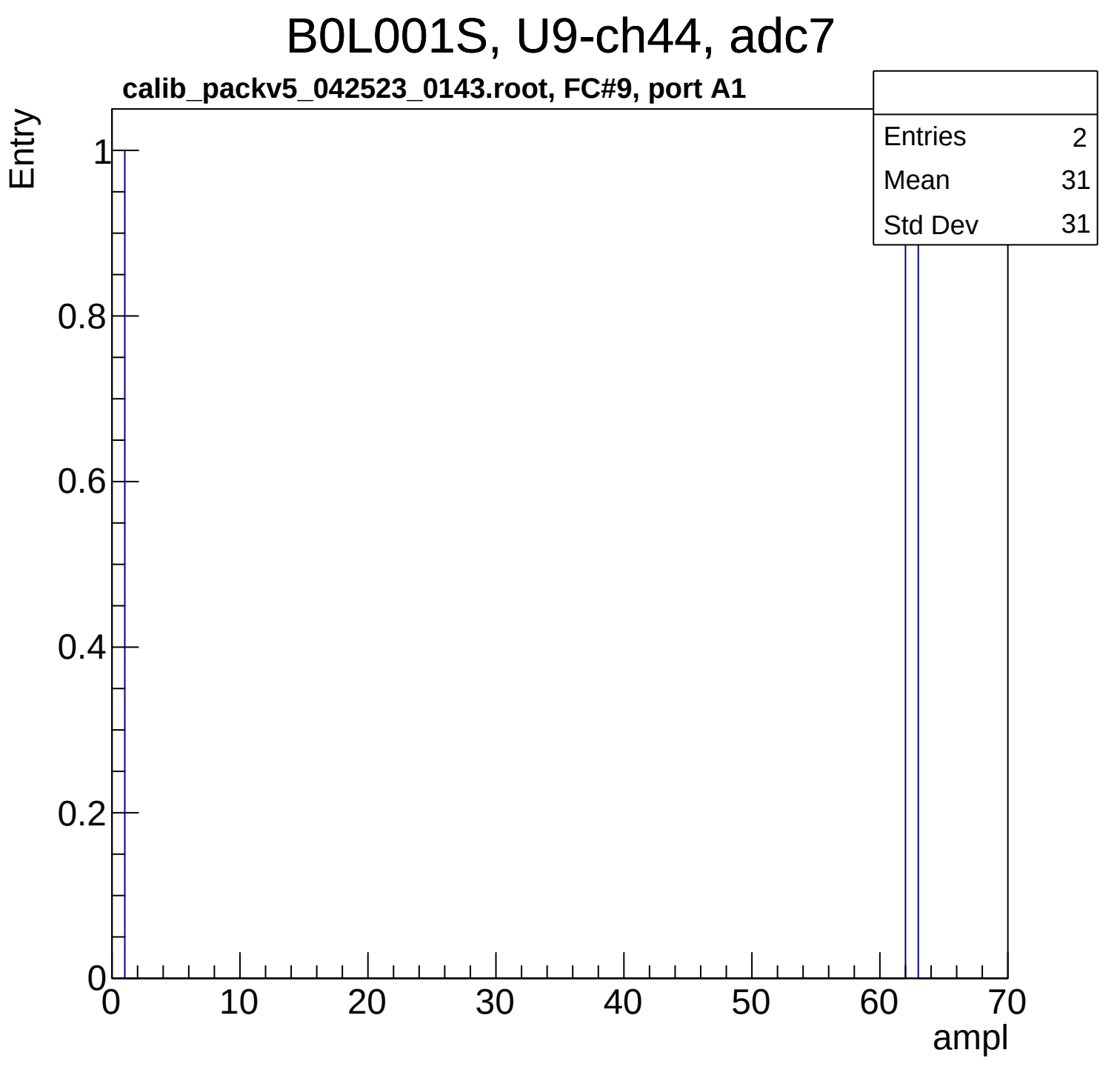
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch44, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
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0.6
0.4
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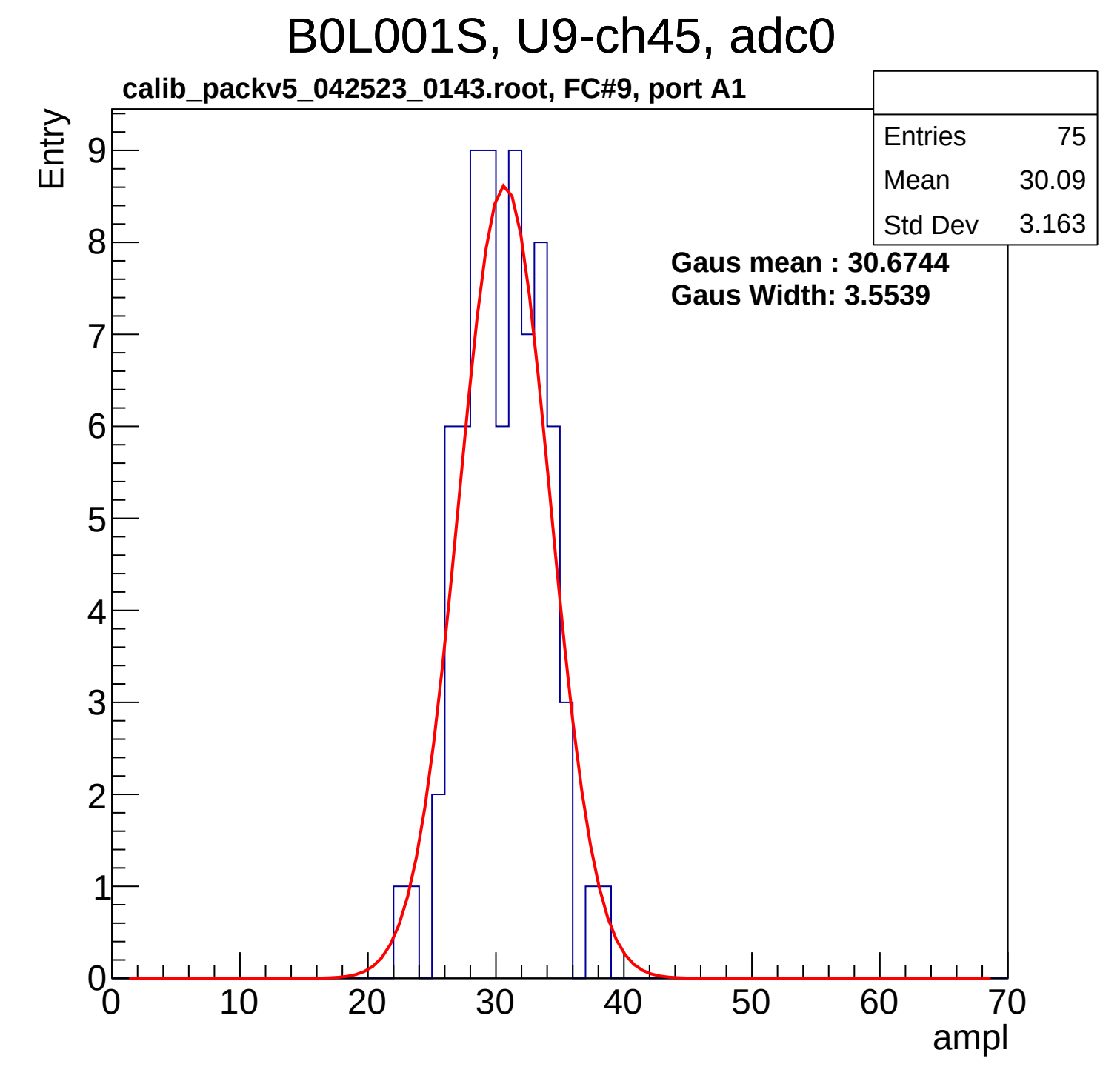
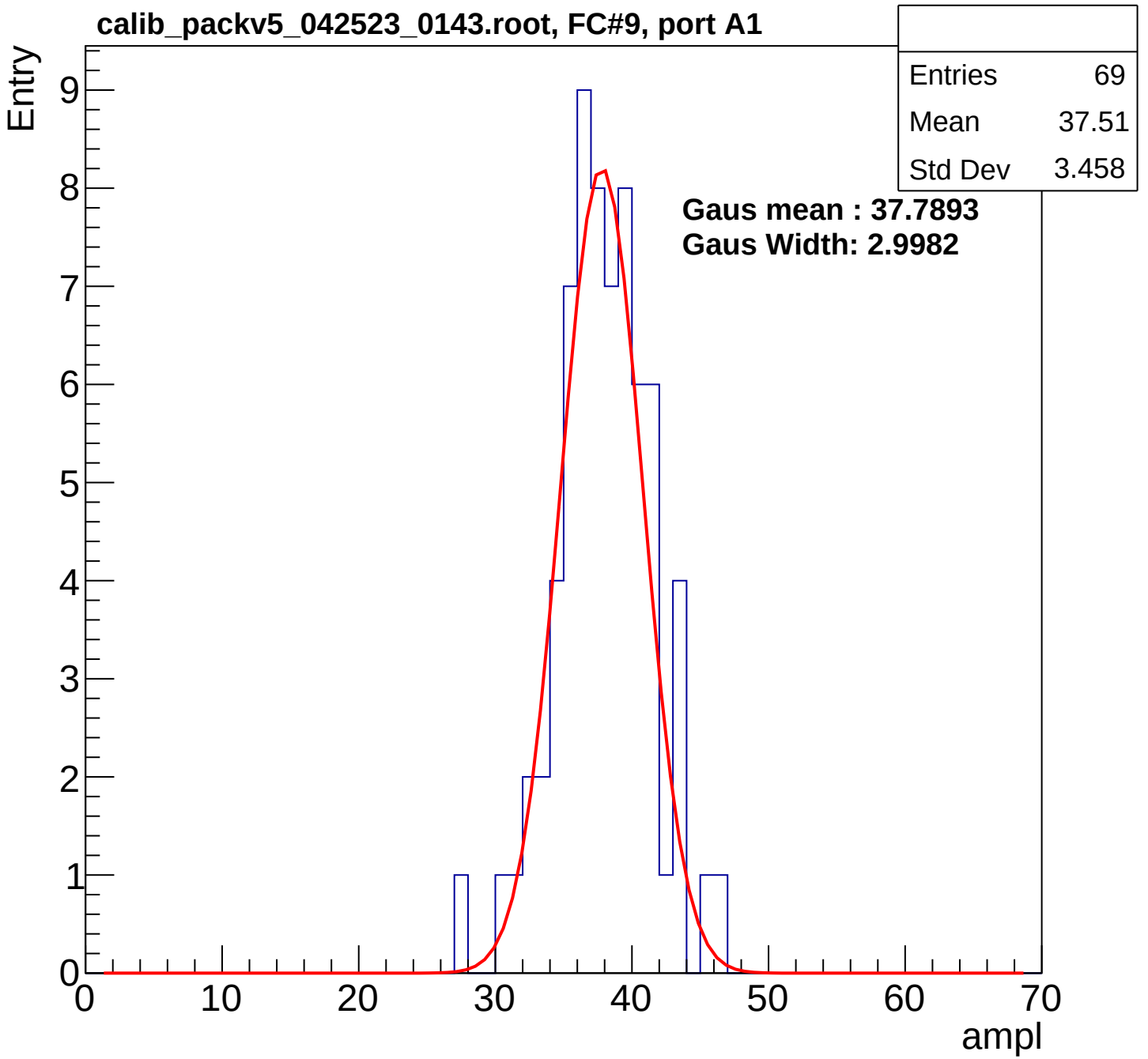
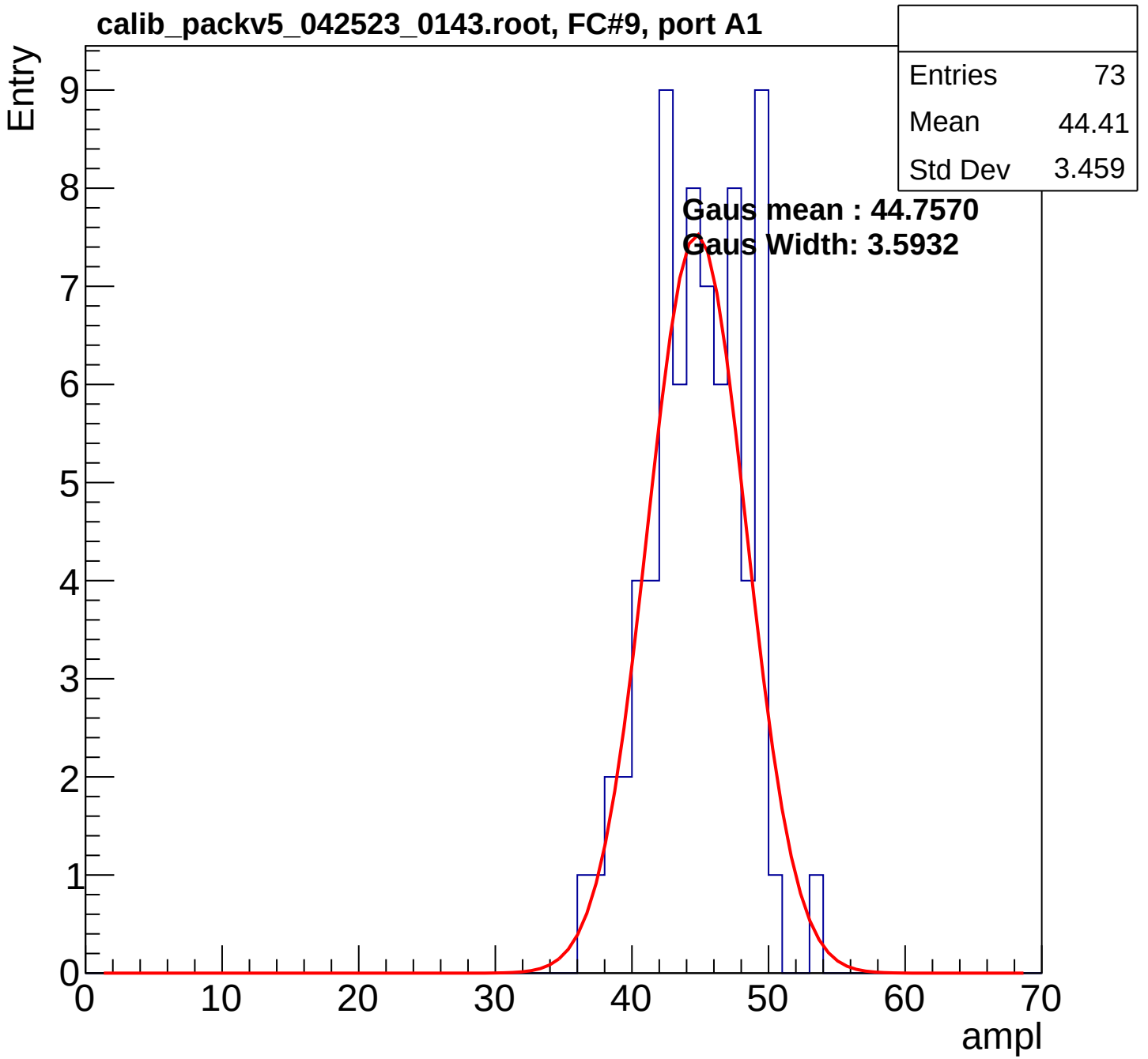
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch45, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 30.09
Std Dev 3.163
Gaus mean : 30.6744
Gaus Width: 3.5539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch45, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.51
Std Dev 3.458
Gaus mean : 37.7893
Gaus Width: 2.9982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch45, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 44.41
Std Dev 3.459
Gaus mean : 44.7570
Gaus Width: 3.5932
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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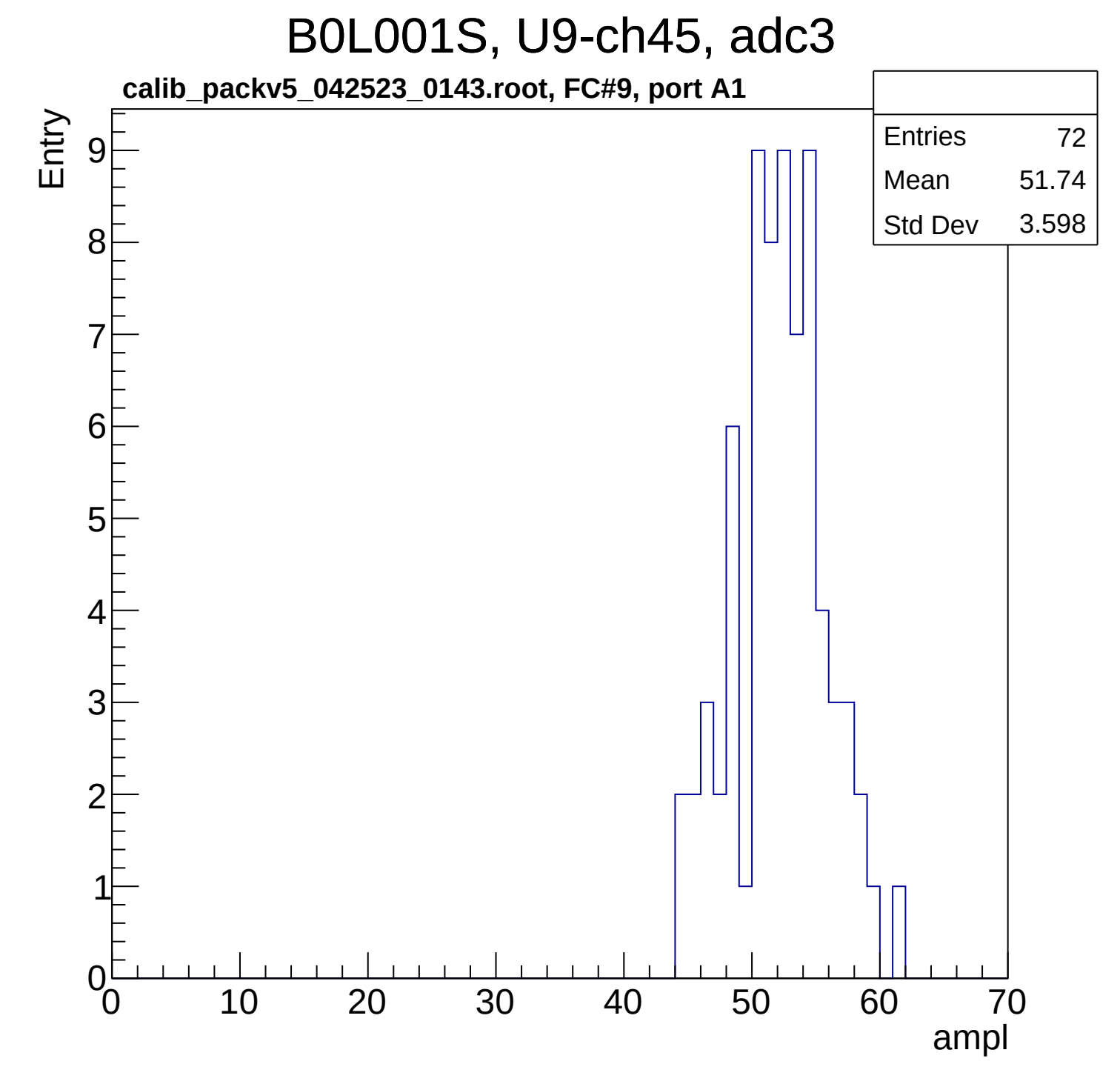
B0L001S, U9-ch45, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 51.74
Std Dev 3.598
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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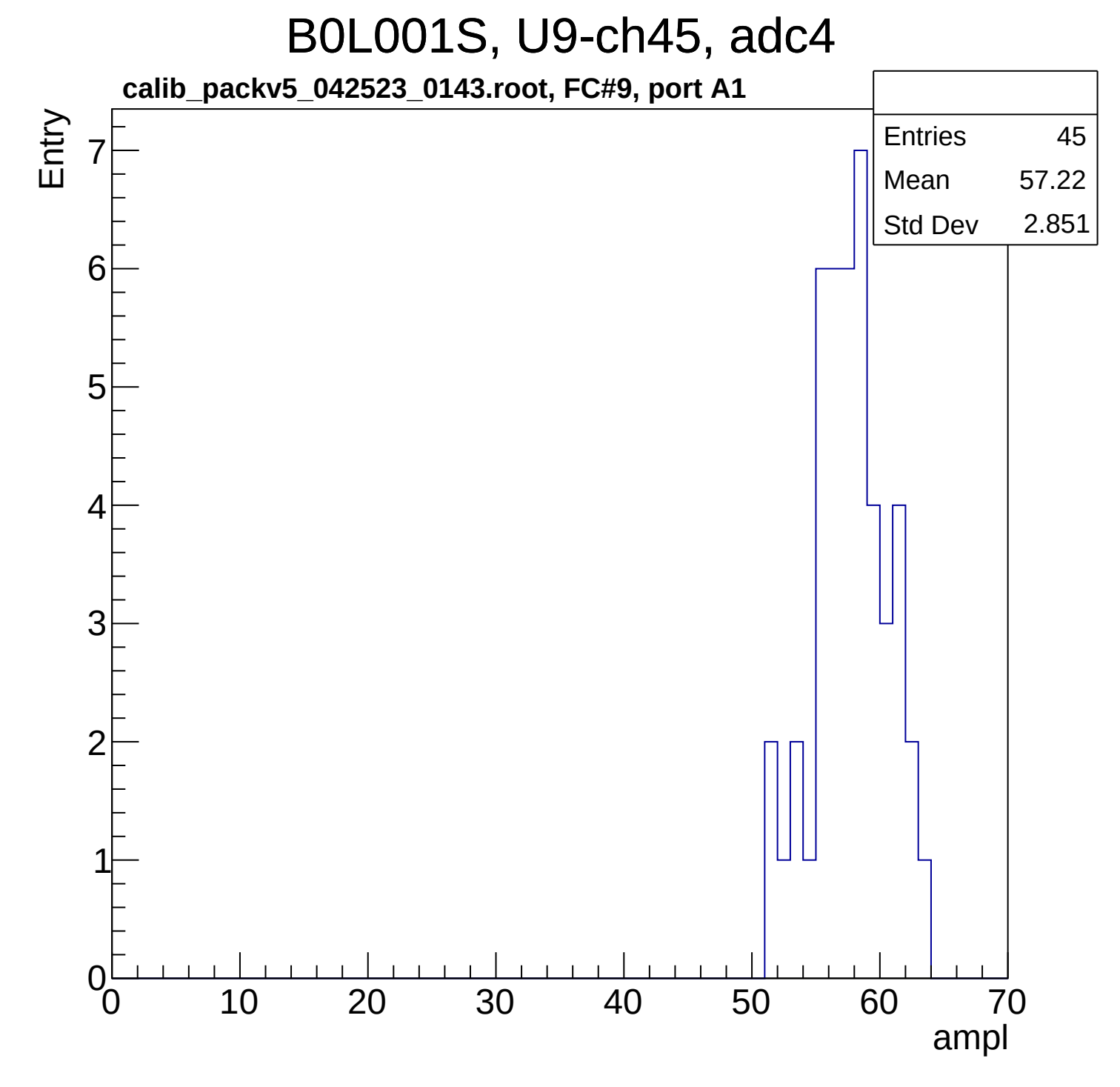
B0L001S, U9-ch45, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 57.22
Std Dev 2.851
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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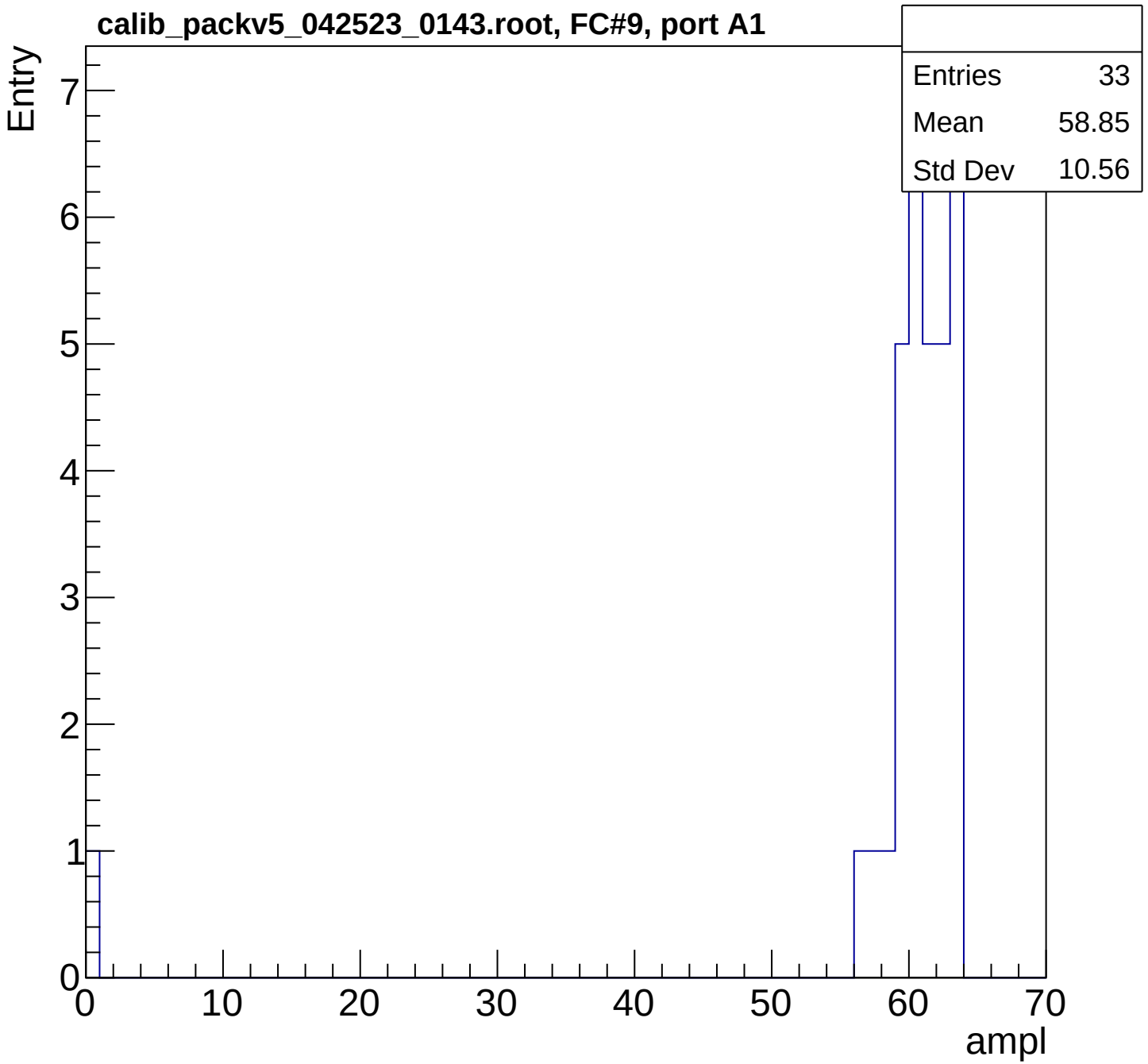
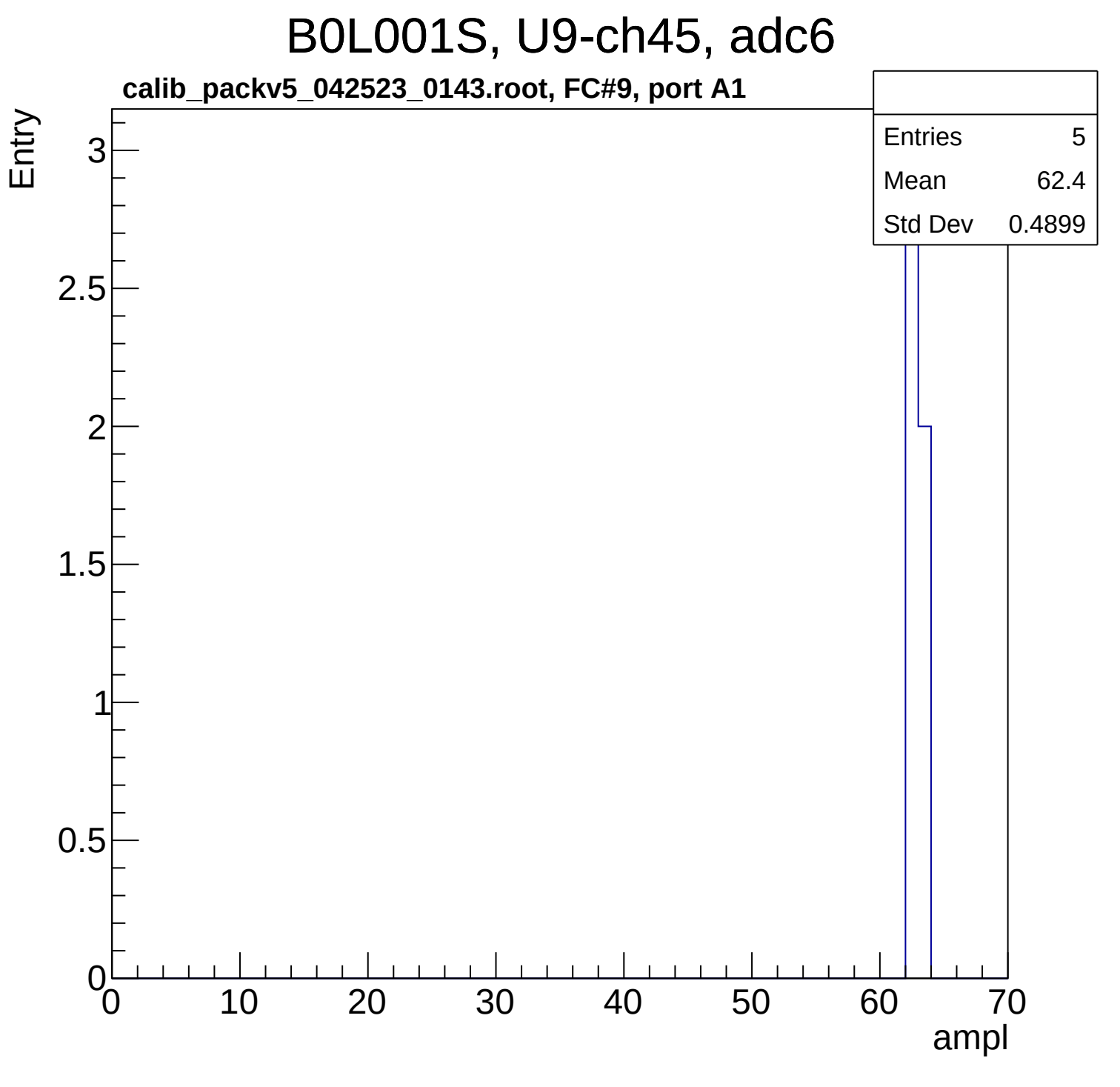
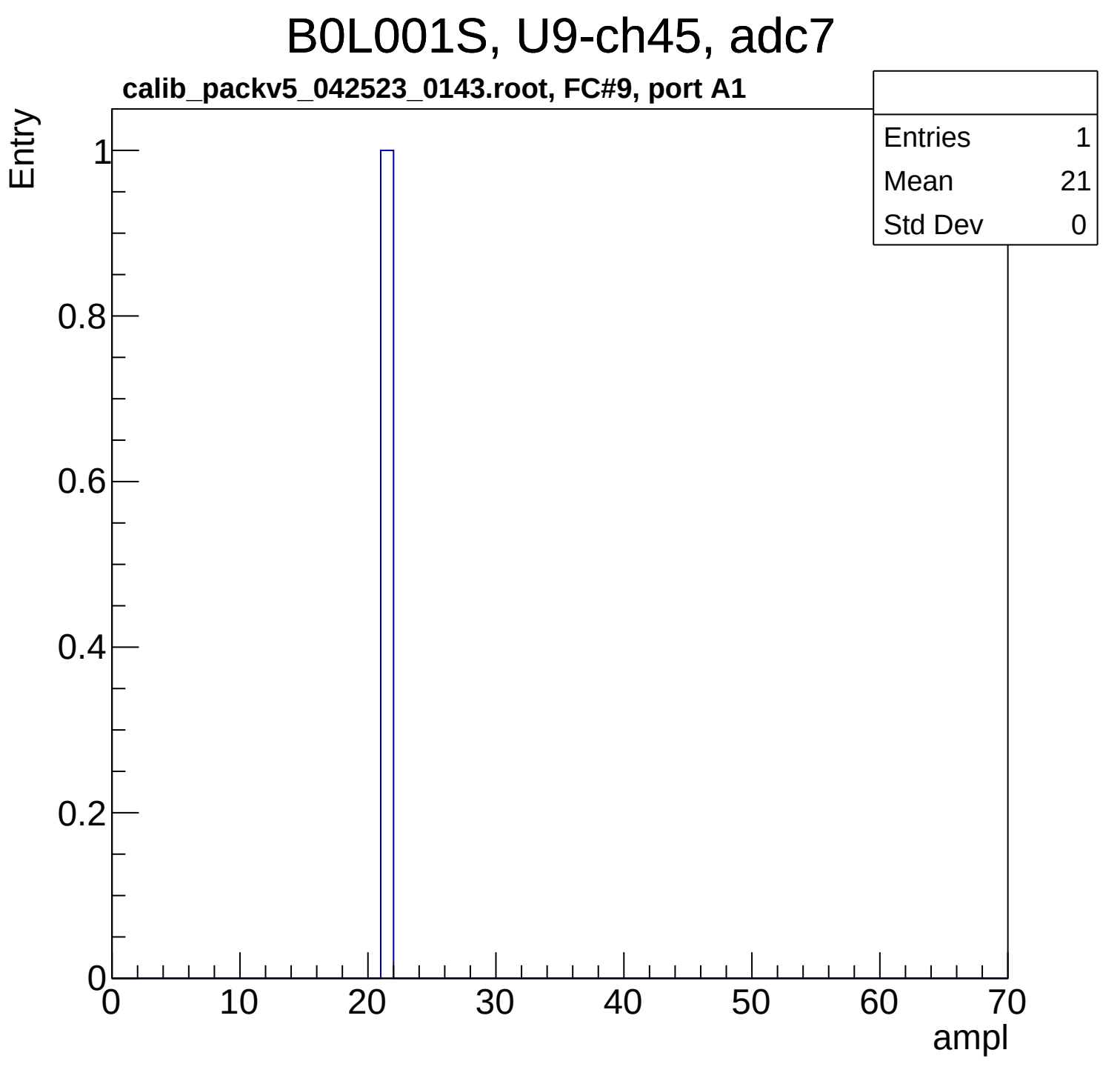

B0L001S, U9-ch45, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.85
Std Dev 10.56
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch45, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch45, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 21
Std Dev 0
Entry
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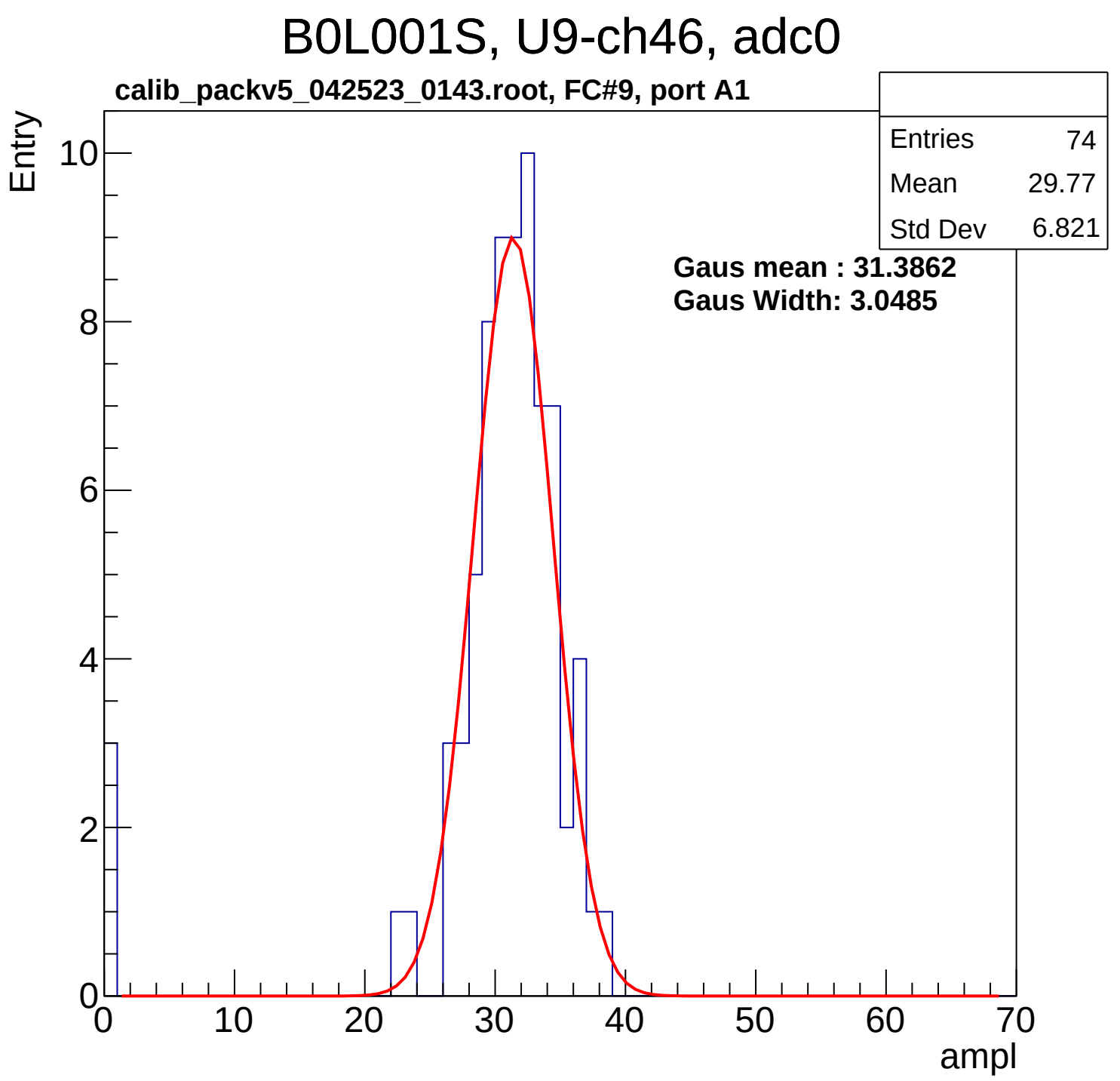
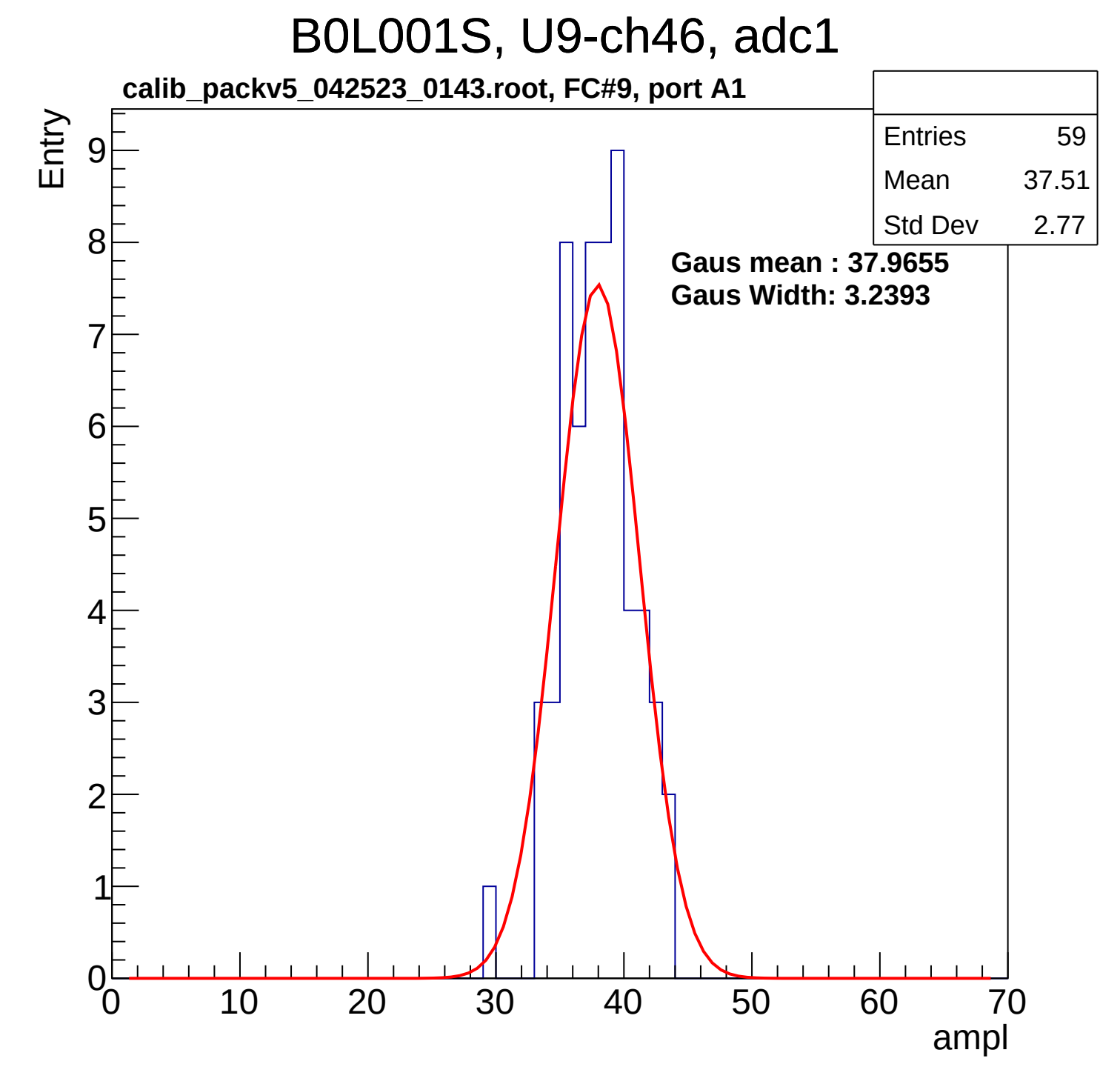
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch46, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 29.77
Std Dev 6.821
Gaus mean : 31.3862
Gaus Width: 3.0485
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch46, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.51
Std Dev 2.77
Gaus mean : 37.9655
Gaus Width: 3.2393
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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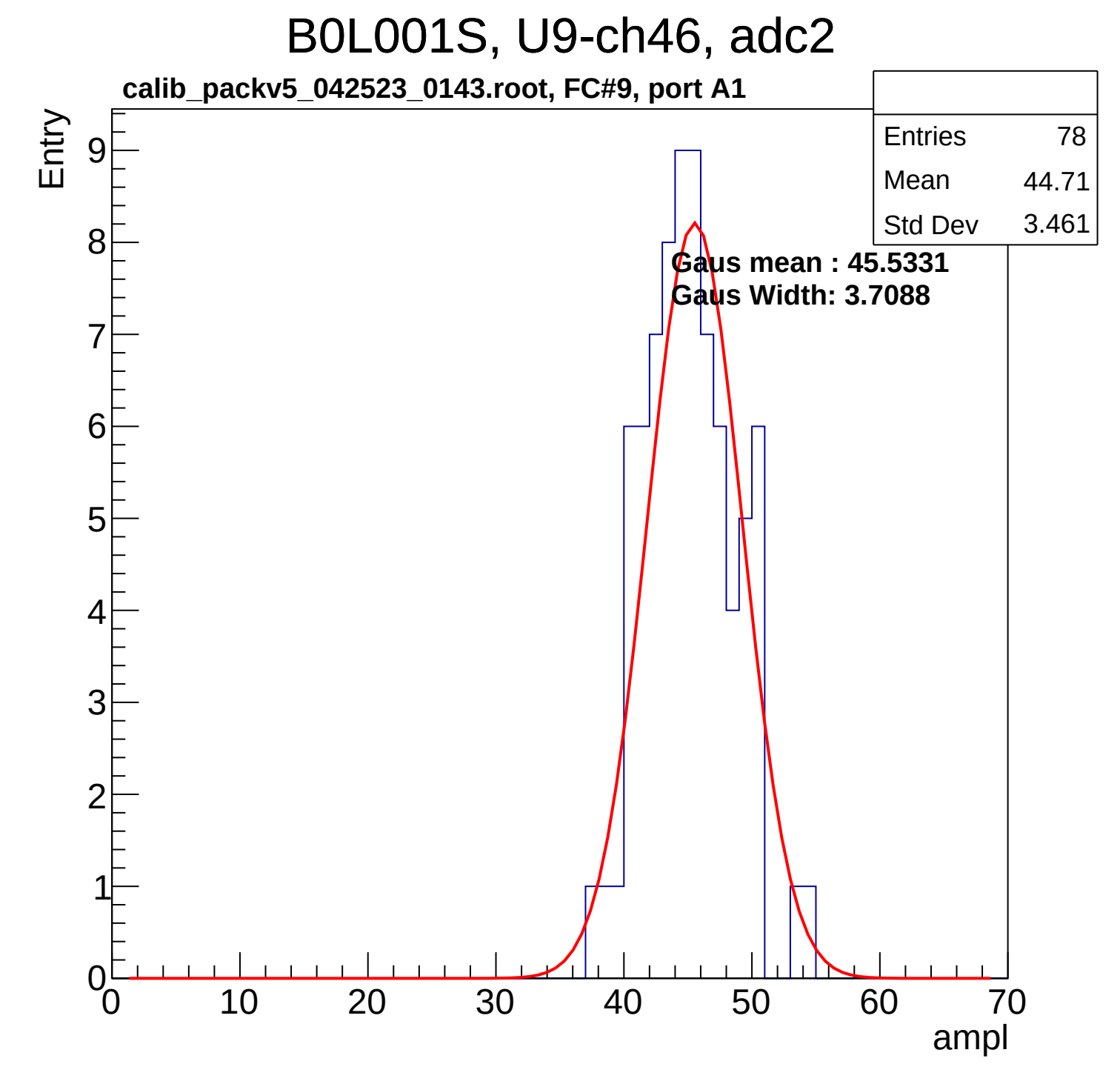
B0L001S, U9-ch46, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 44.71
Std Dev 3.461
Gaus mean : 45.5331
Gaus Width: 3.7088
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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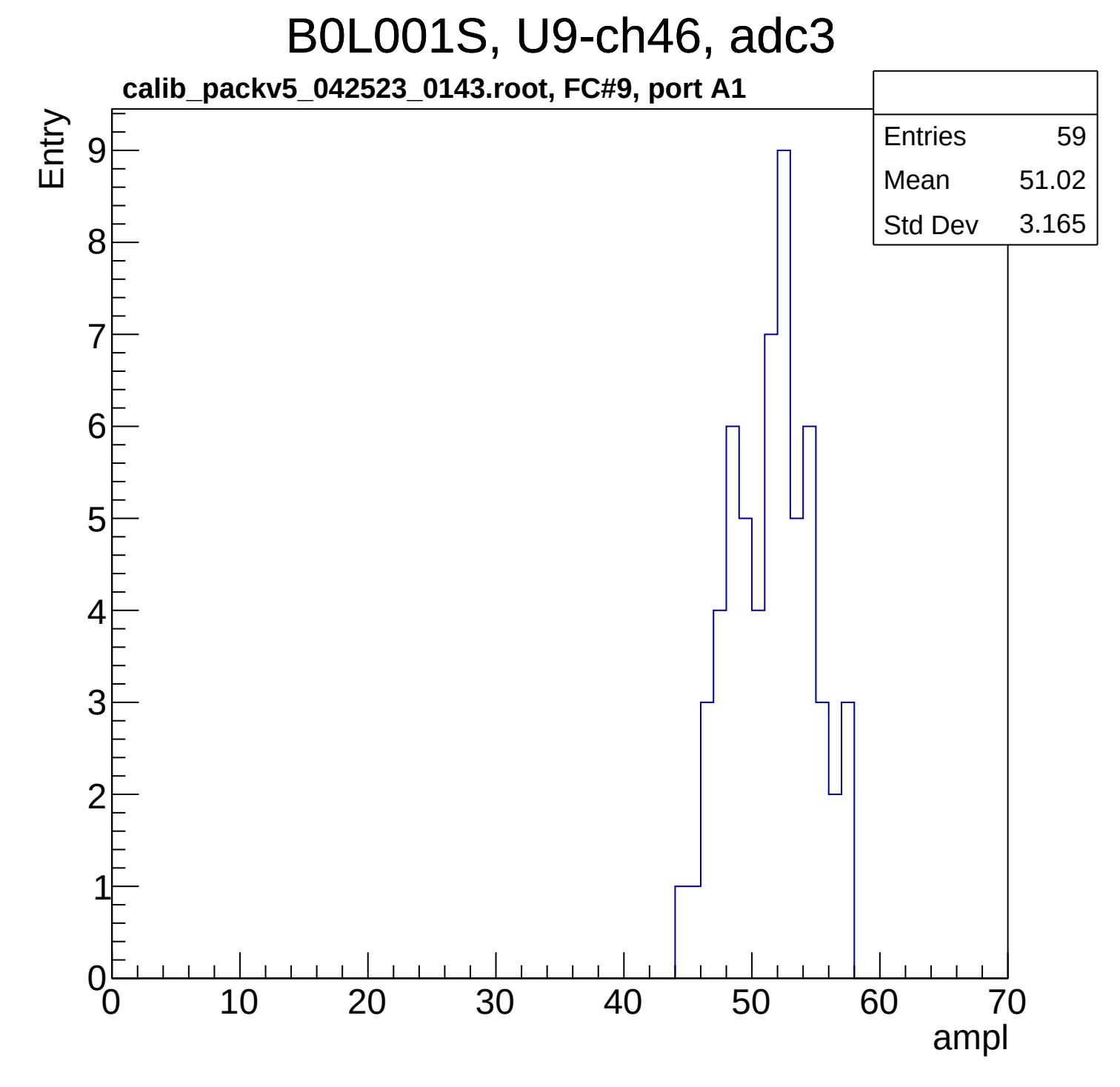
B0L001S, U9-ch46, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.02
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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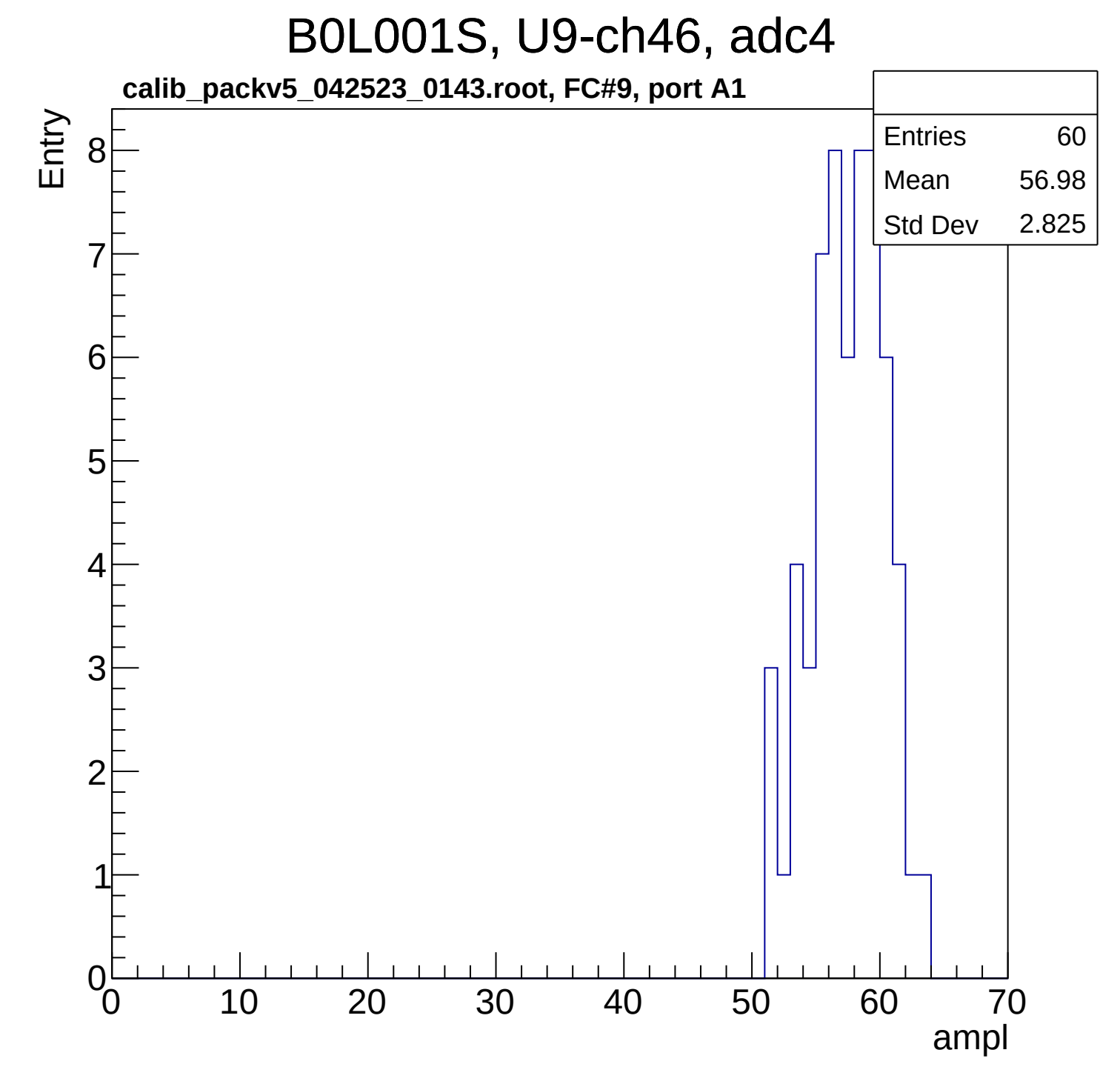
B0L001S, U9-ch46, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.98
Std Dev 2.825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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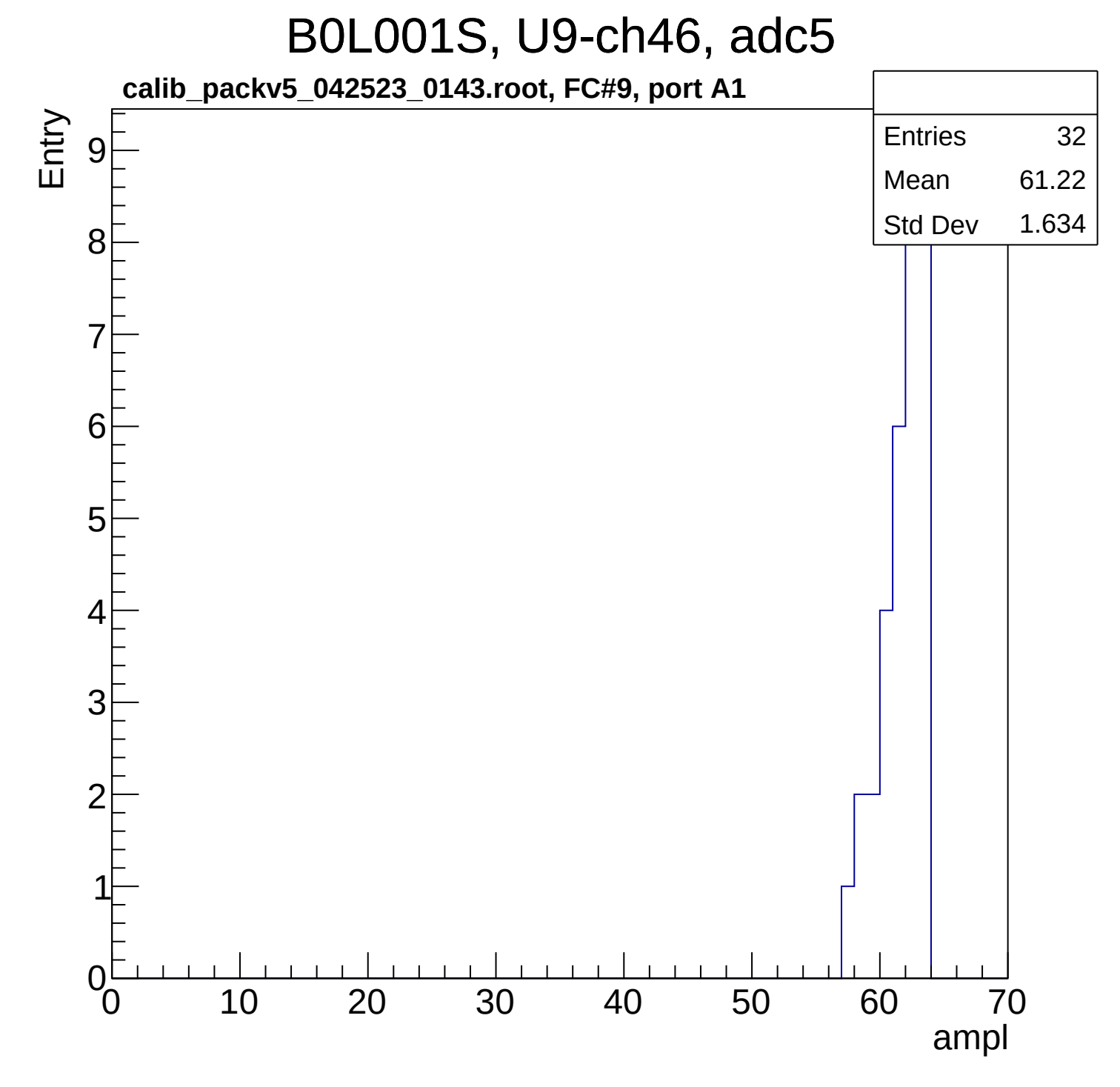
B0L001S, U9-ch46, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 61.22
Std Dev 1.634
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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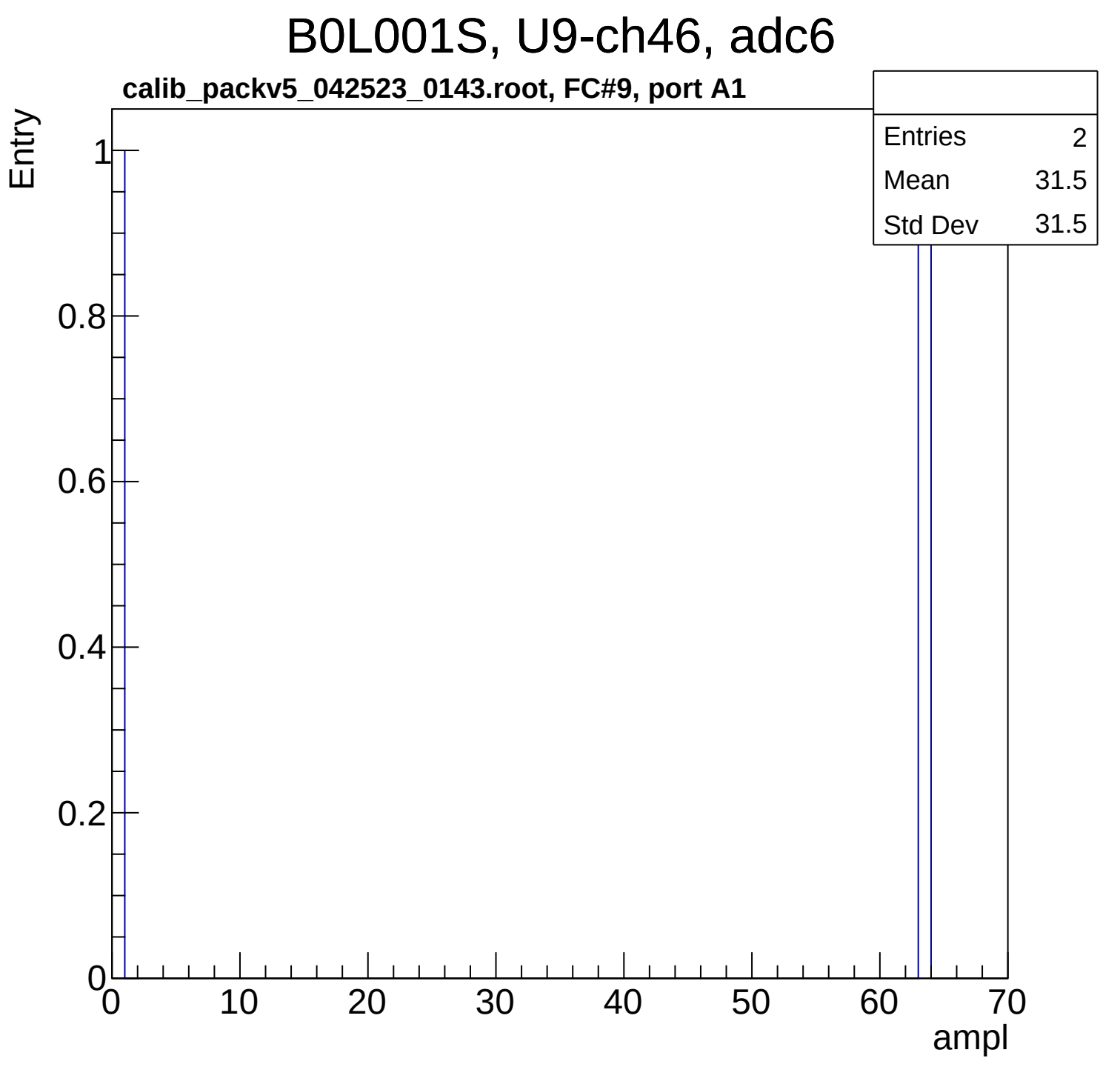
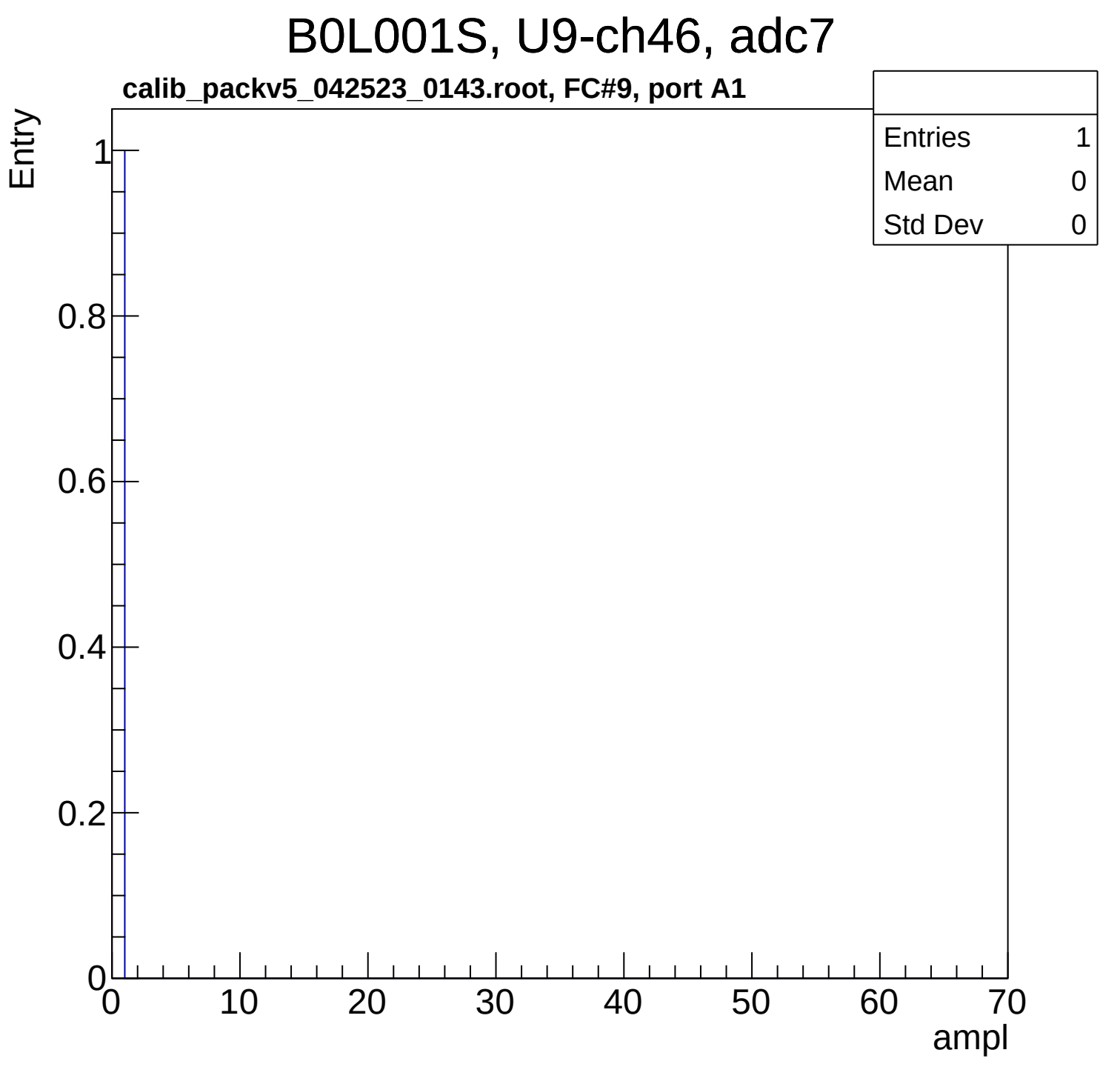
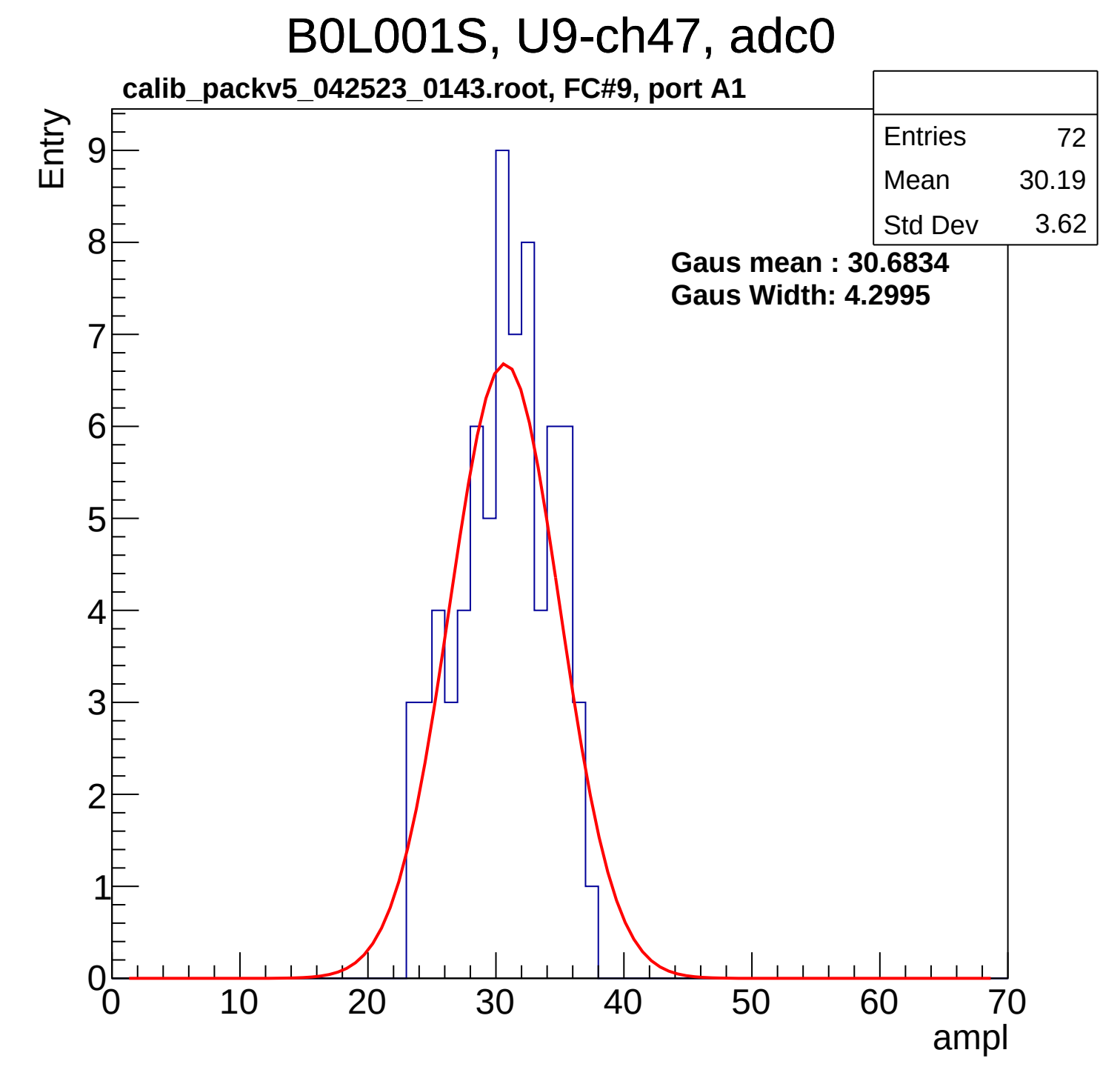
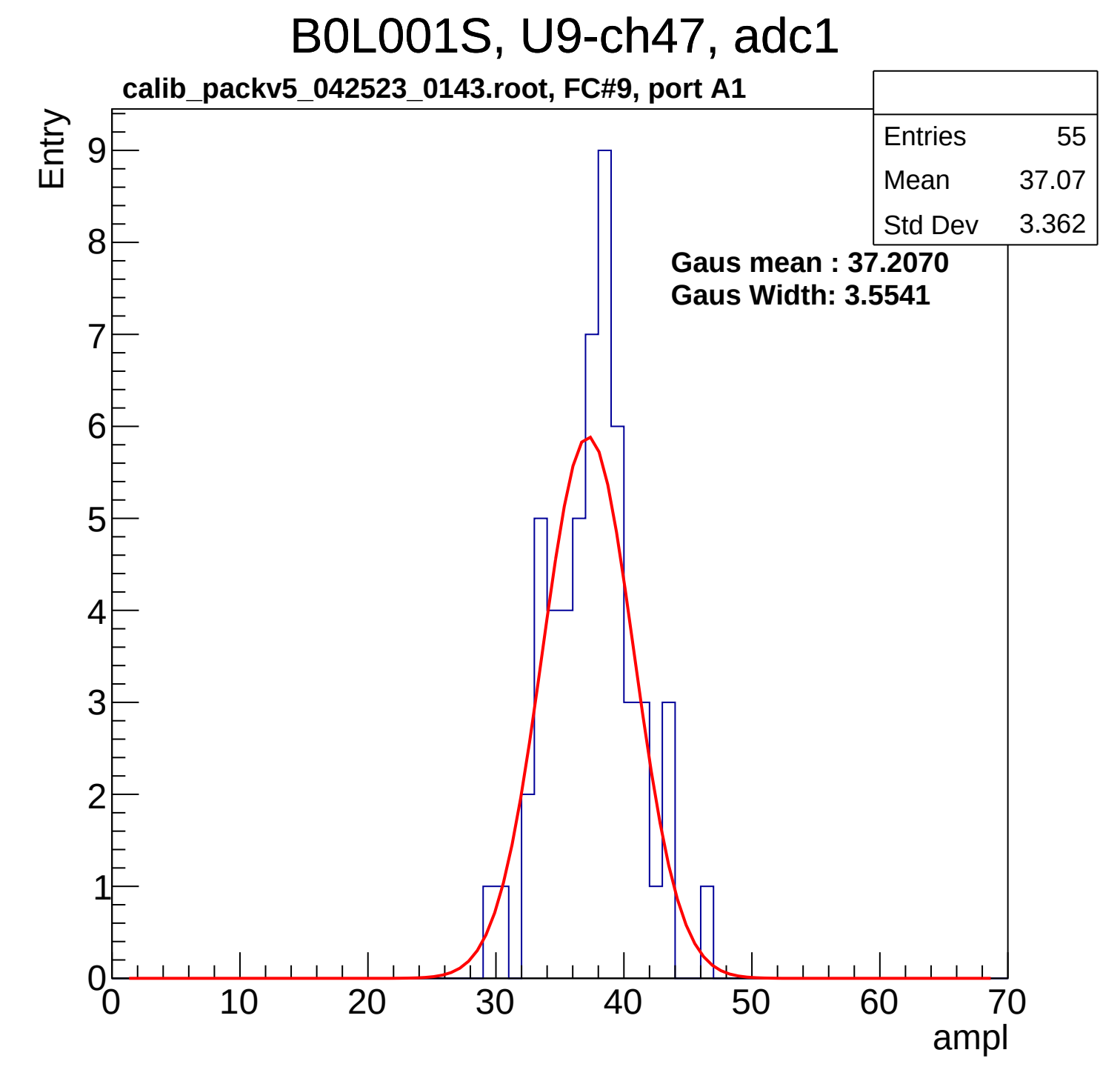

B0L001S, U9-ch46, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch46, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch47, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.19
Std Dev 3.62
Gaus mean : 30.6834
Gaus Width: 4.2995
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch47, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 37.07
Std Dev 3.362
Gaus mean : 37.2070
Gaus Width: 3.5541
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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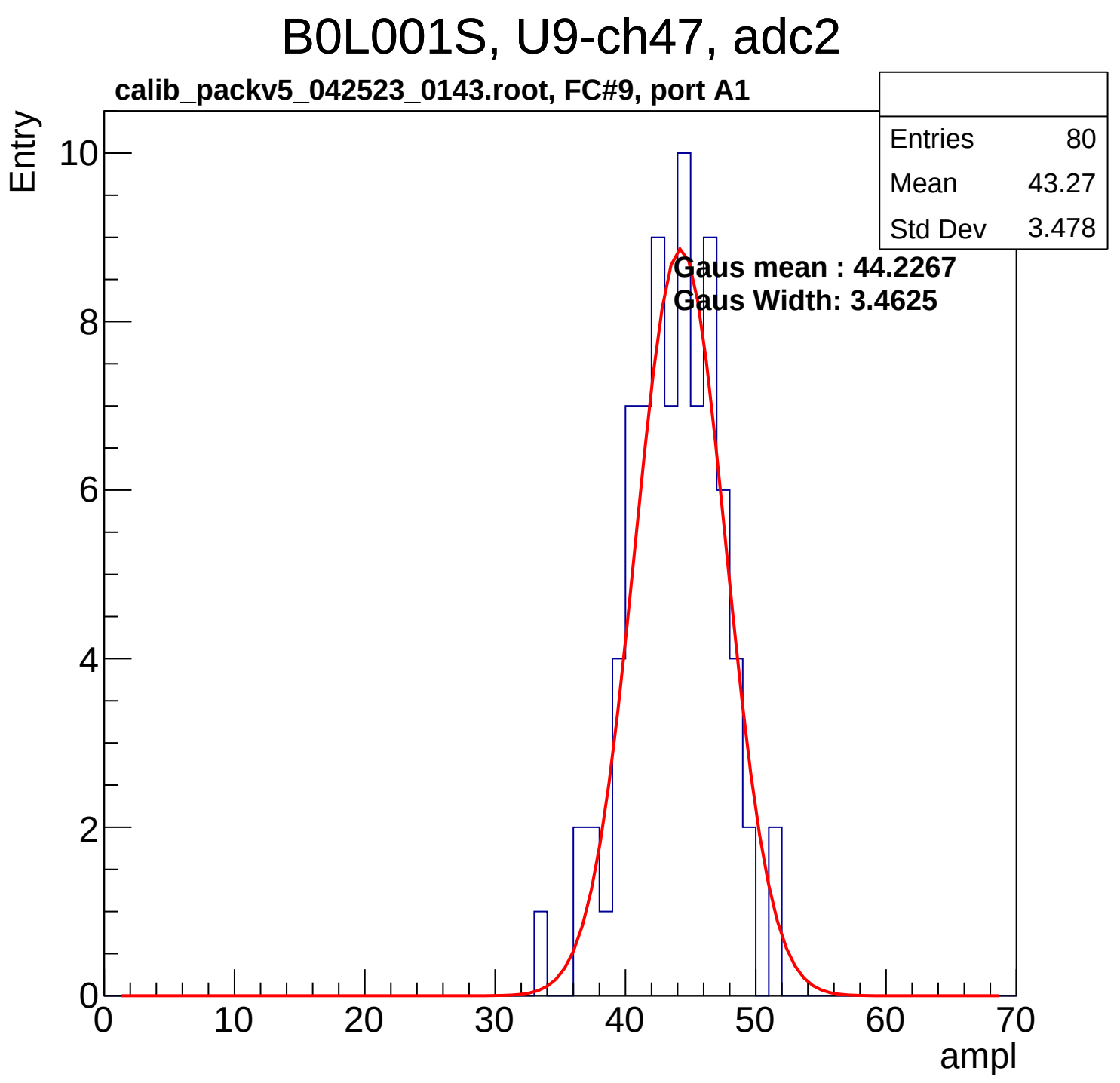
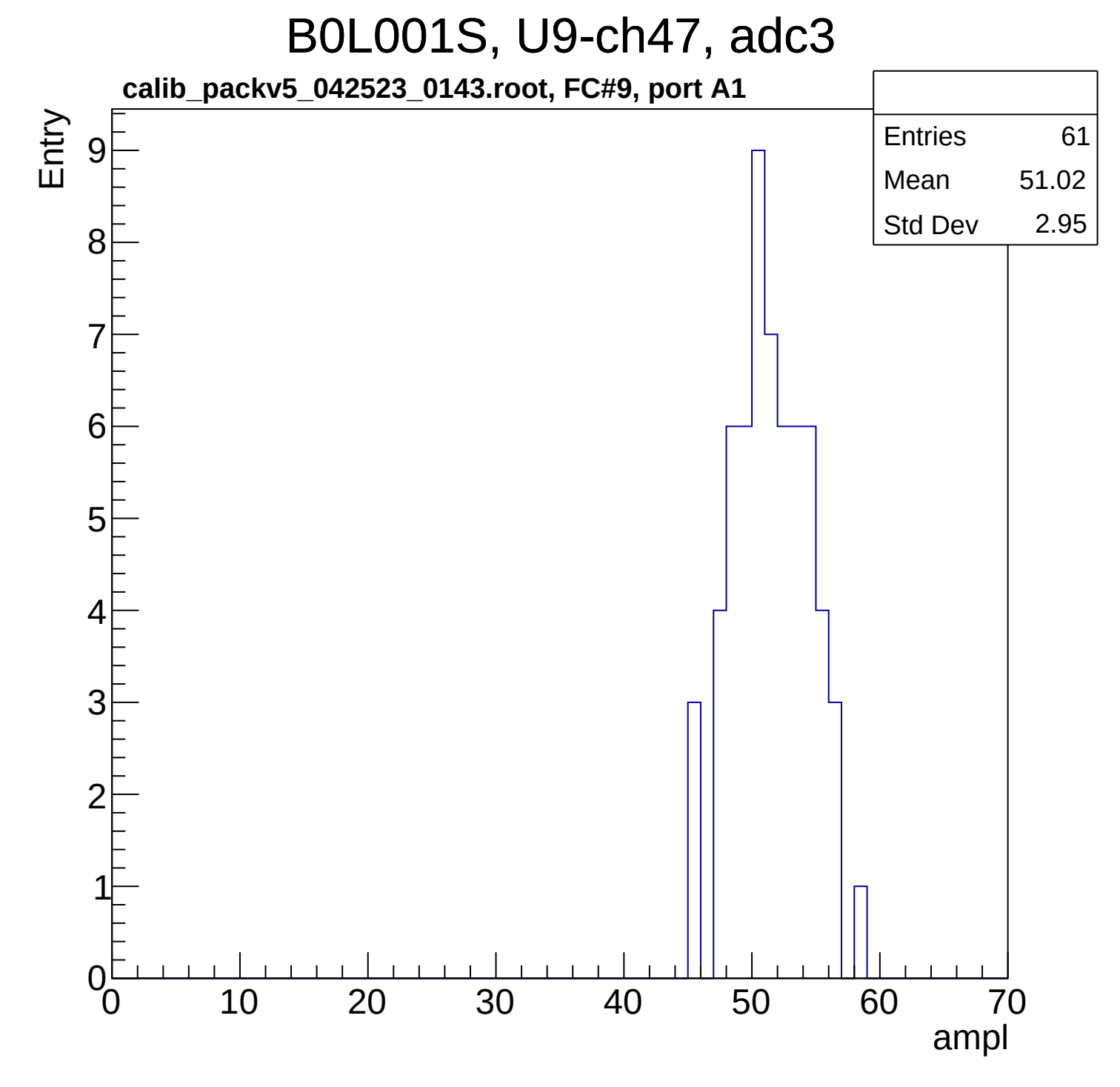
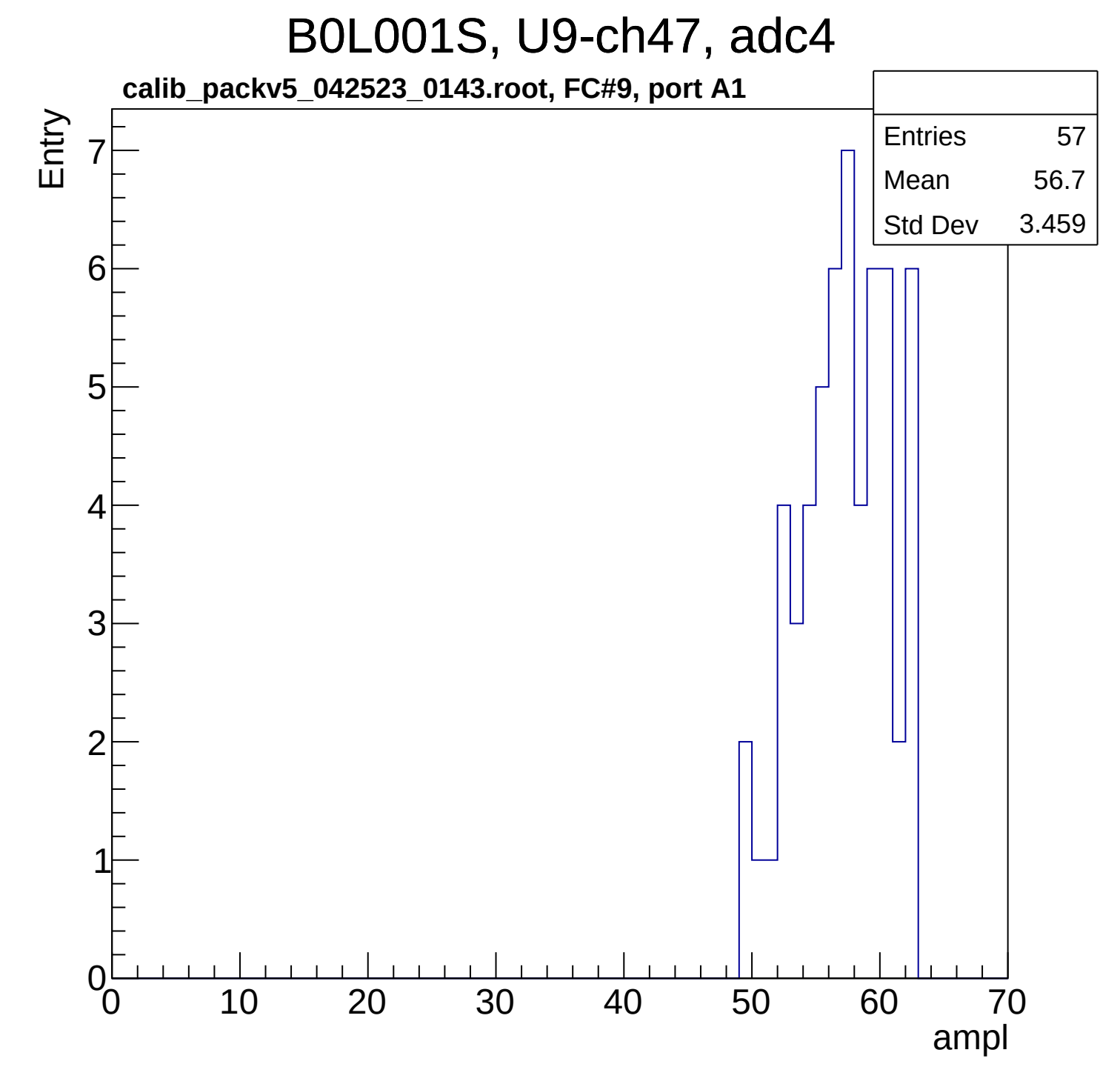

B0L001S, U9-ch47, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 43.27
Std Dev 3.478
Gaus mean : 44.2267
Gaus Width: 3.4625
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch47, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.02
Std Dev 2.95
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch47, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.7
Std Dev 3.459
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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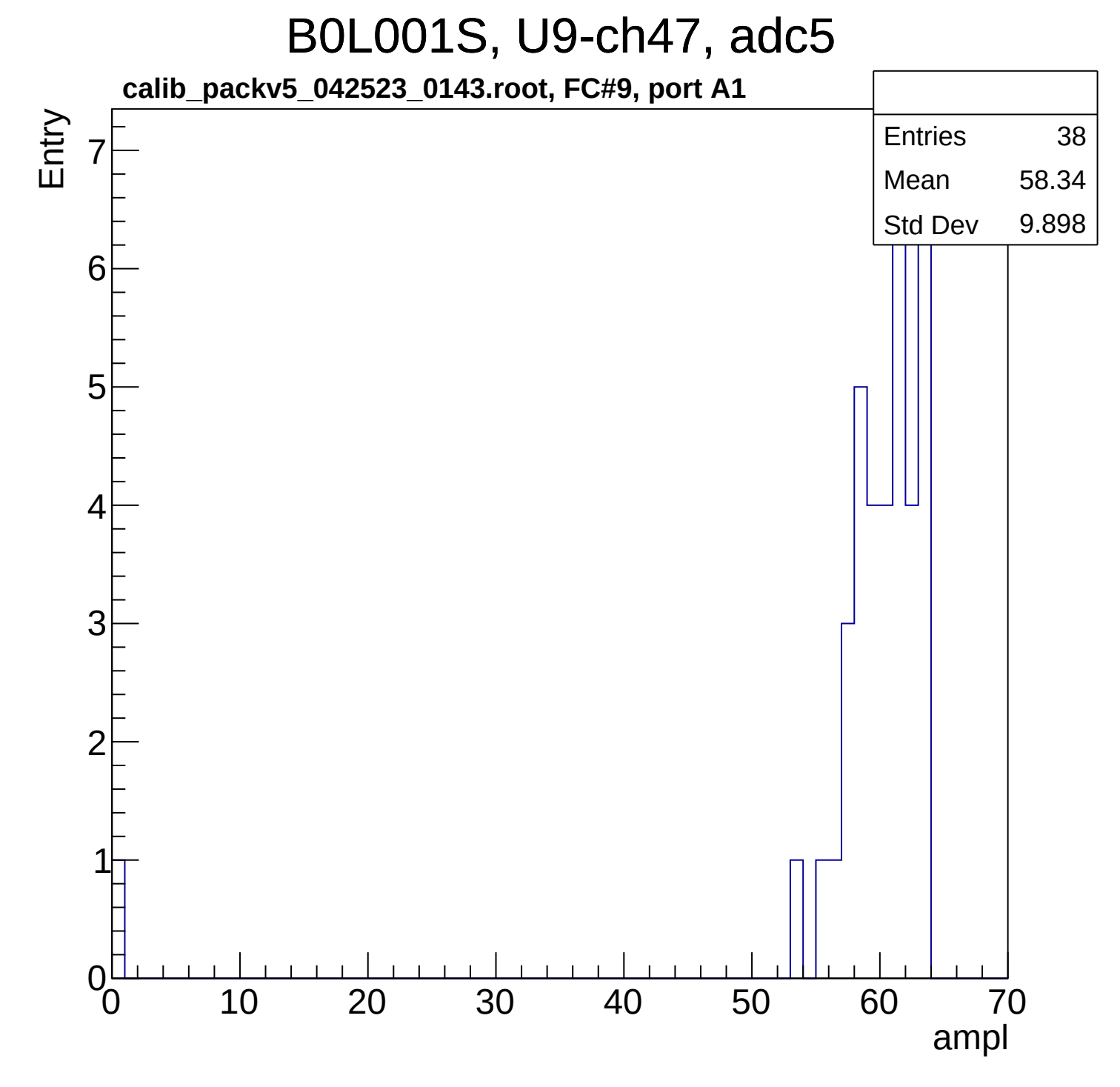
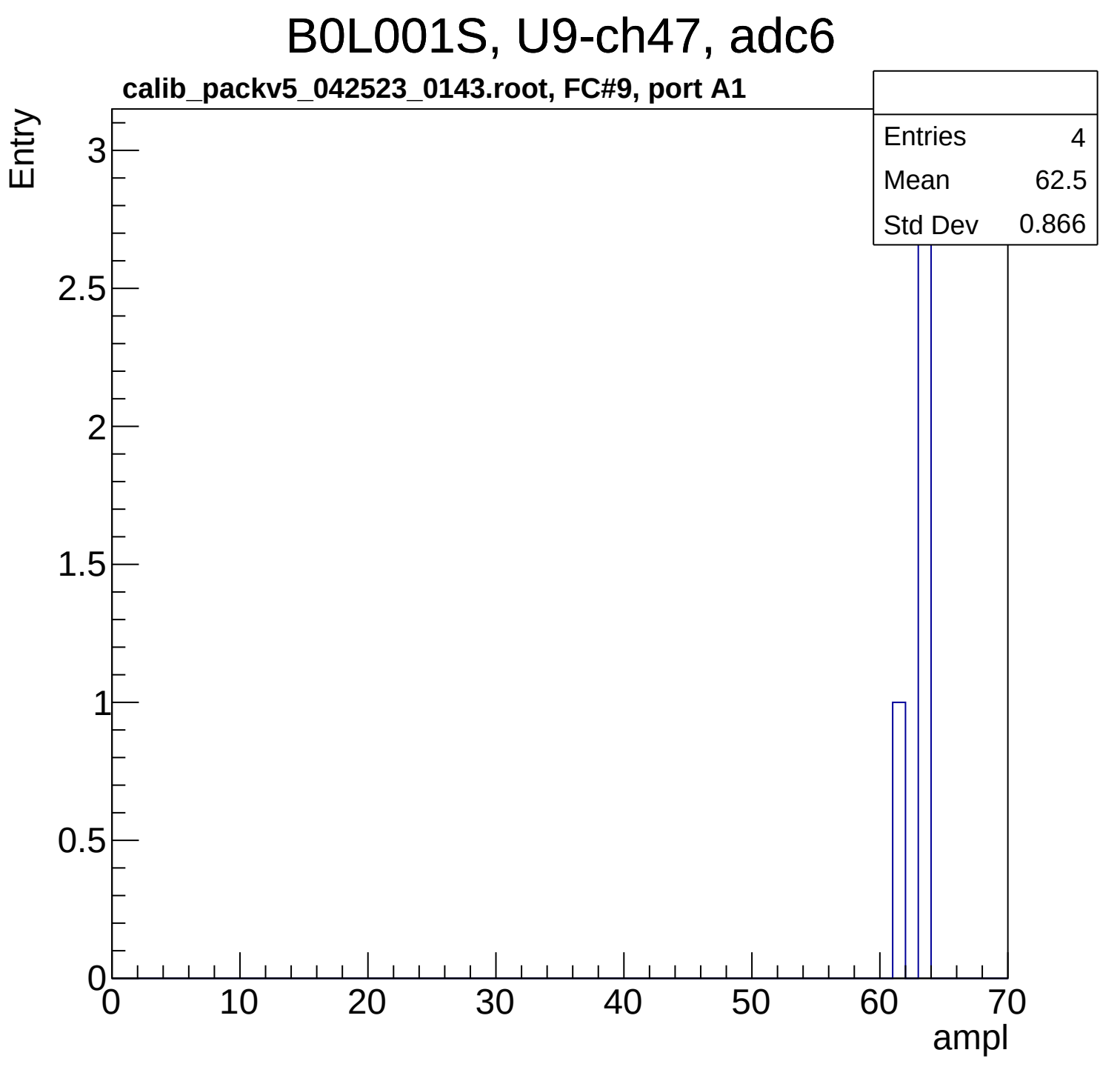
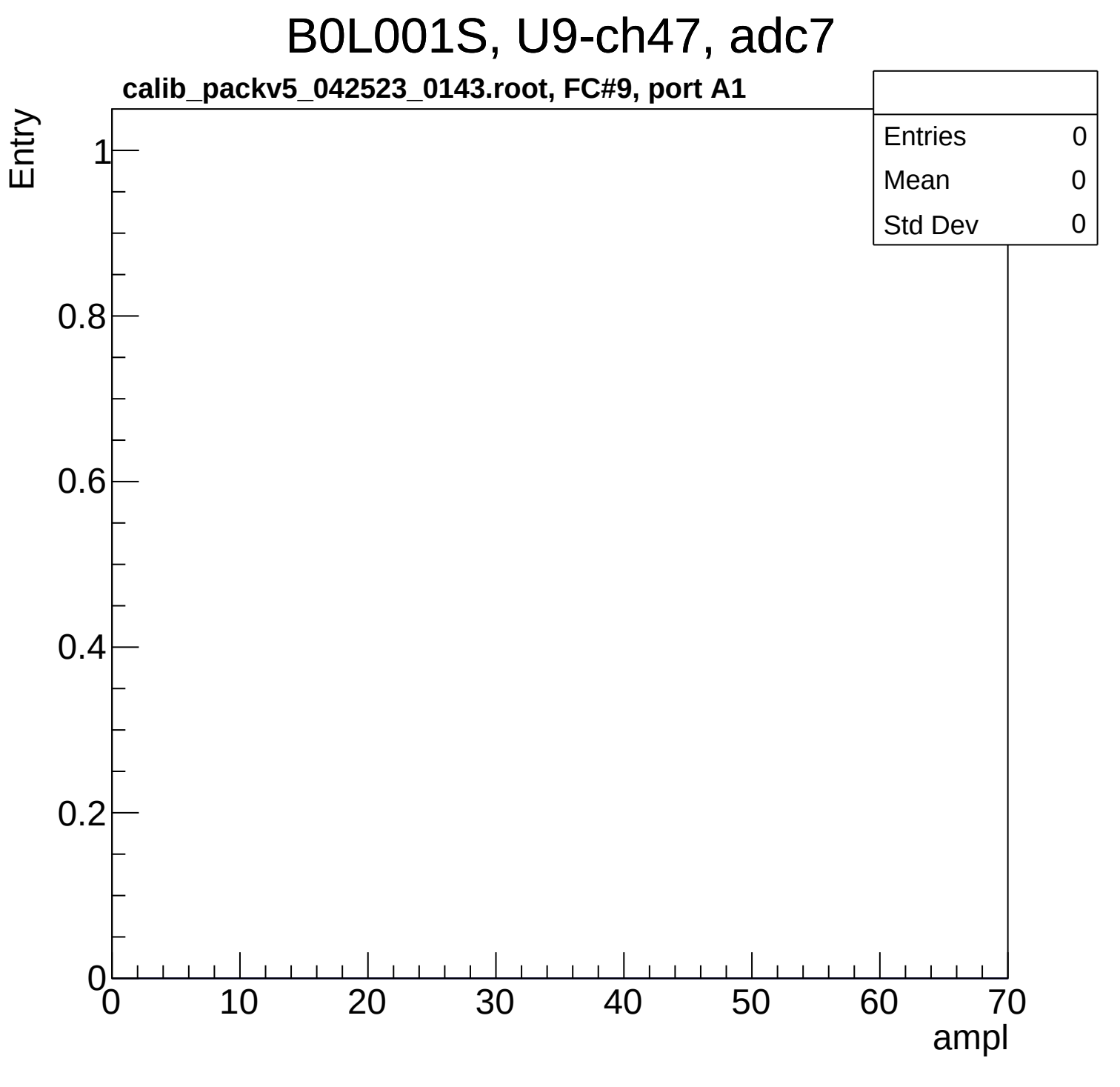
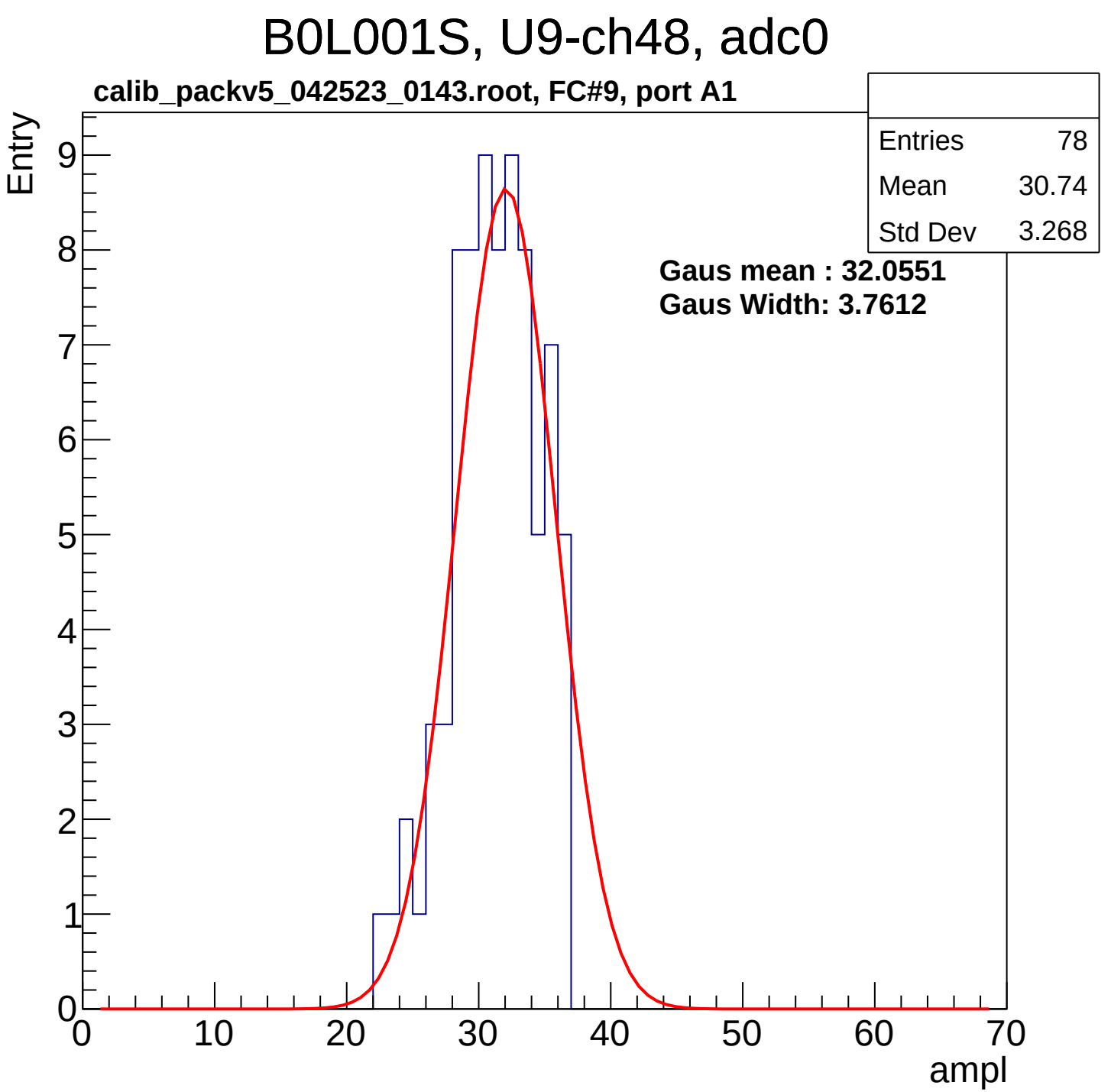
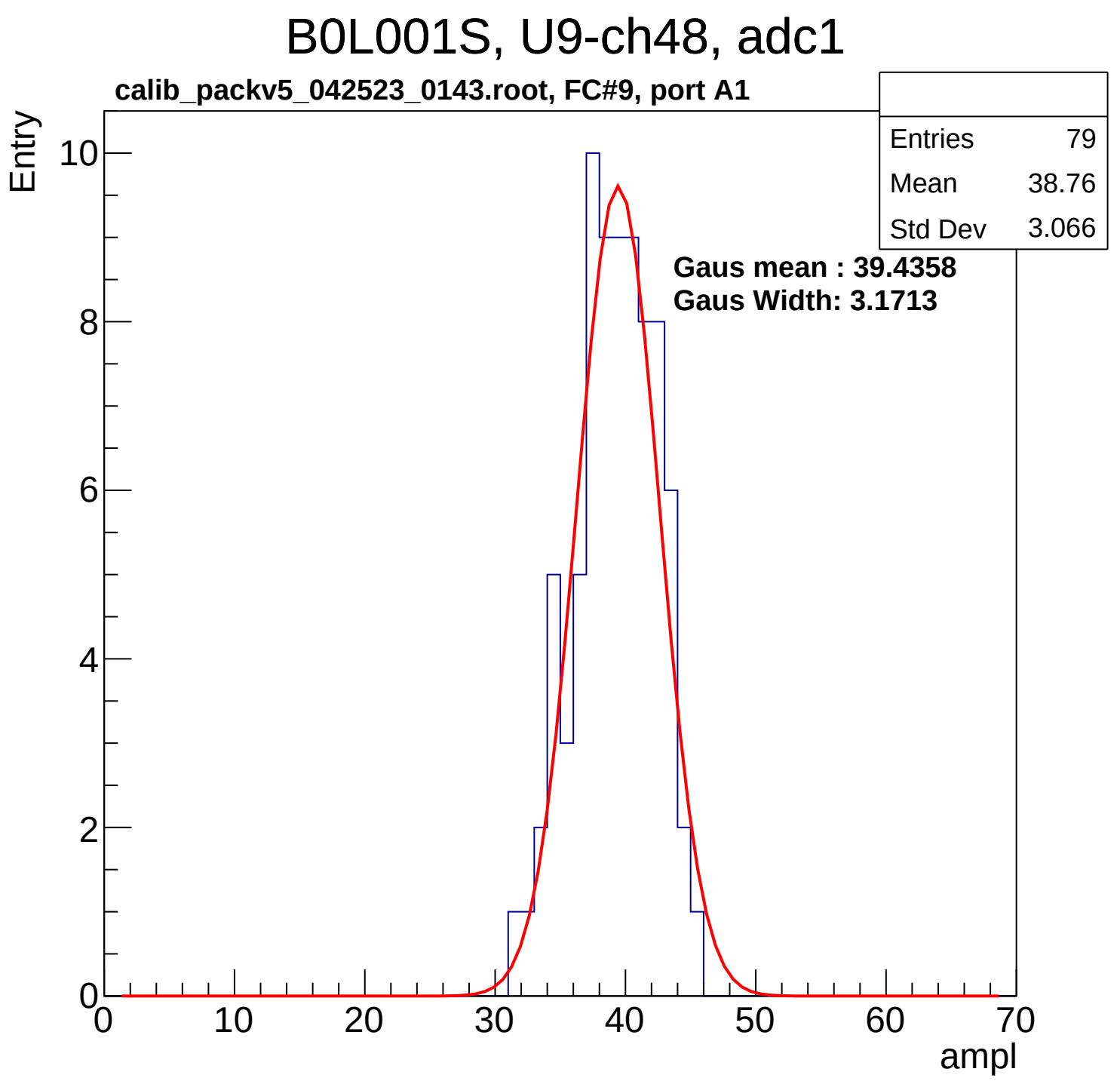
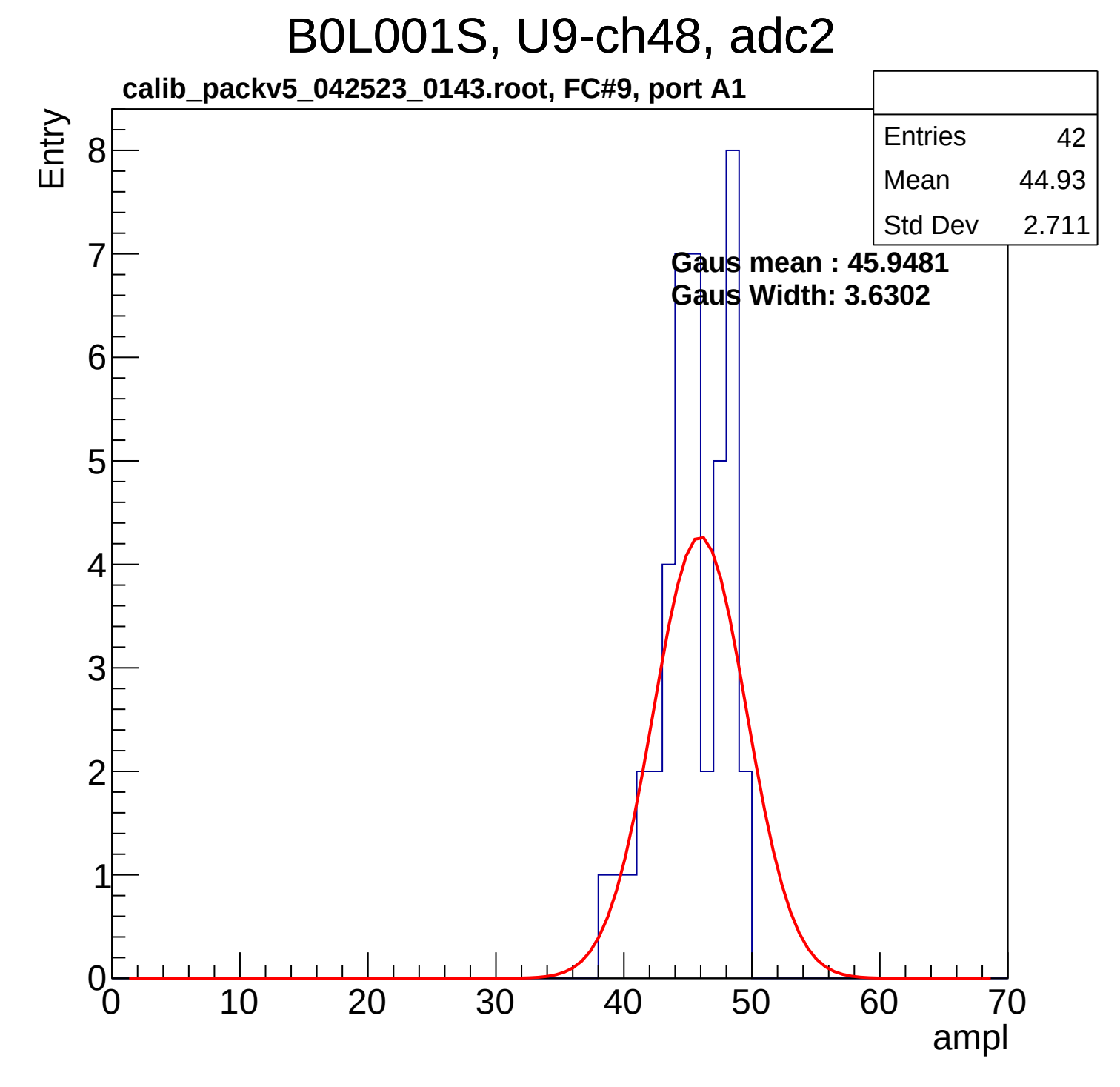

B0L001S, U9-ch47, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.34
Std Dev 9.898
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

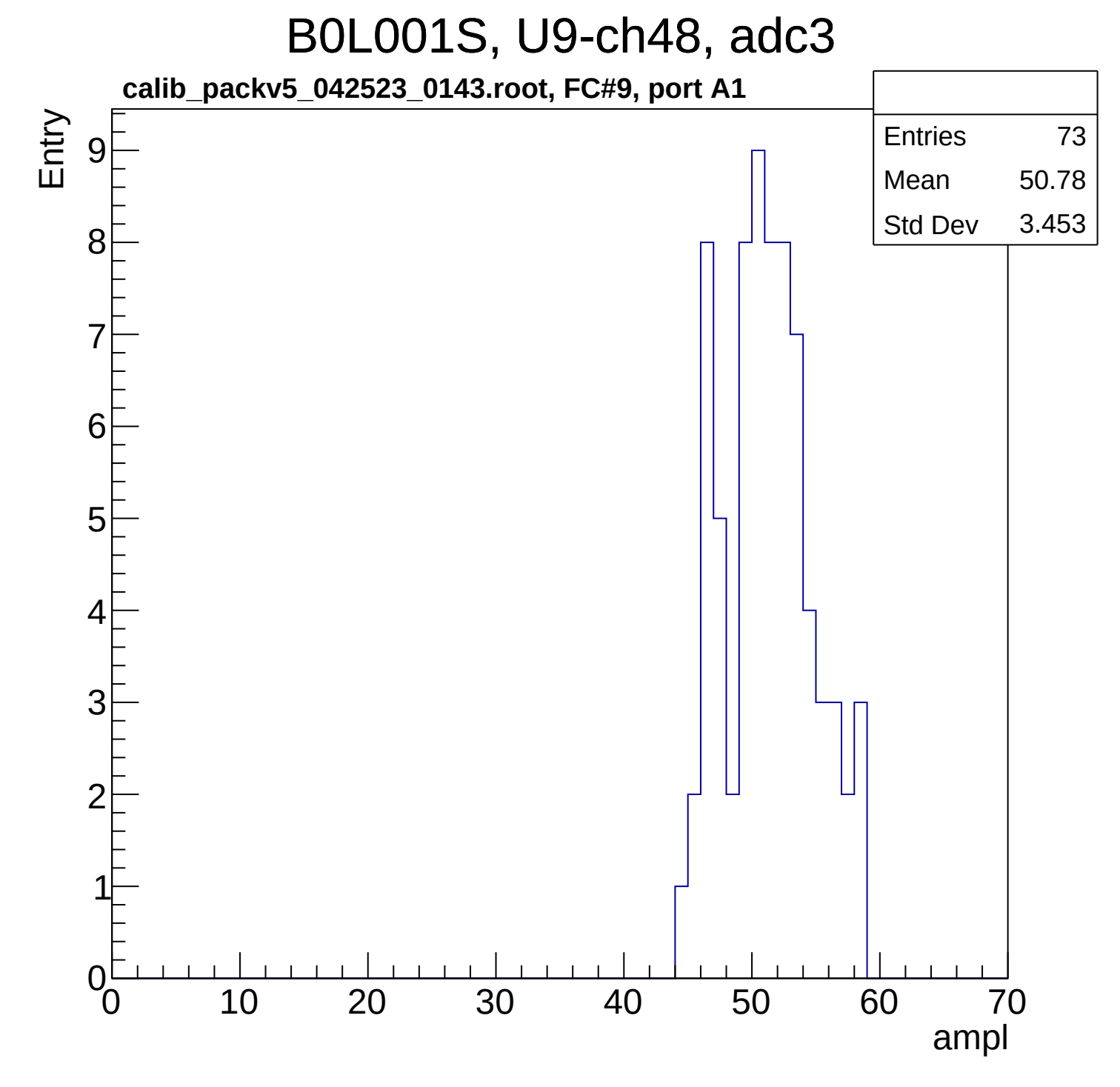

B0L001S, U9-ch47, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch47, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch48, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.74
Std Dev 3.268
Gaus mean : 32.0551
Gaus Width: 3.7612
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch48, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 38.76
Std Dev 3.066
Gaus mean : 39.4358
Gaus Width: 3.1713
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch48, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 44.93
Std Dev 2.711
Gaus mean : 45.9481
Gaus Width: 3.6302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch48, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 50.78
Std Dev 3.453
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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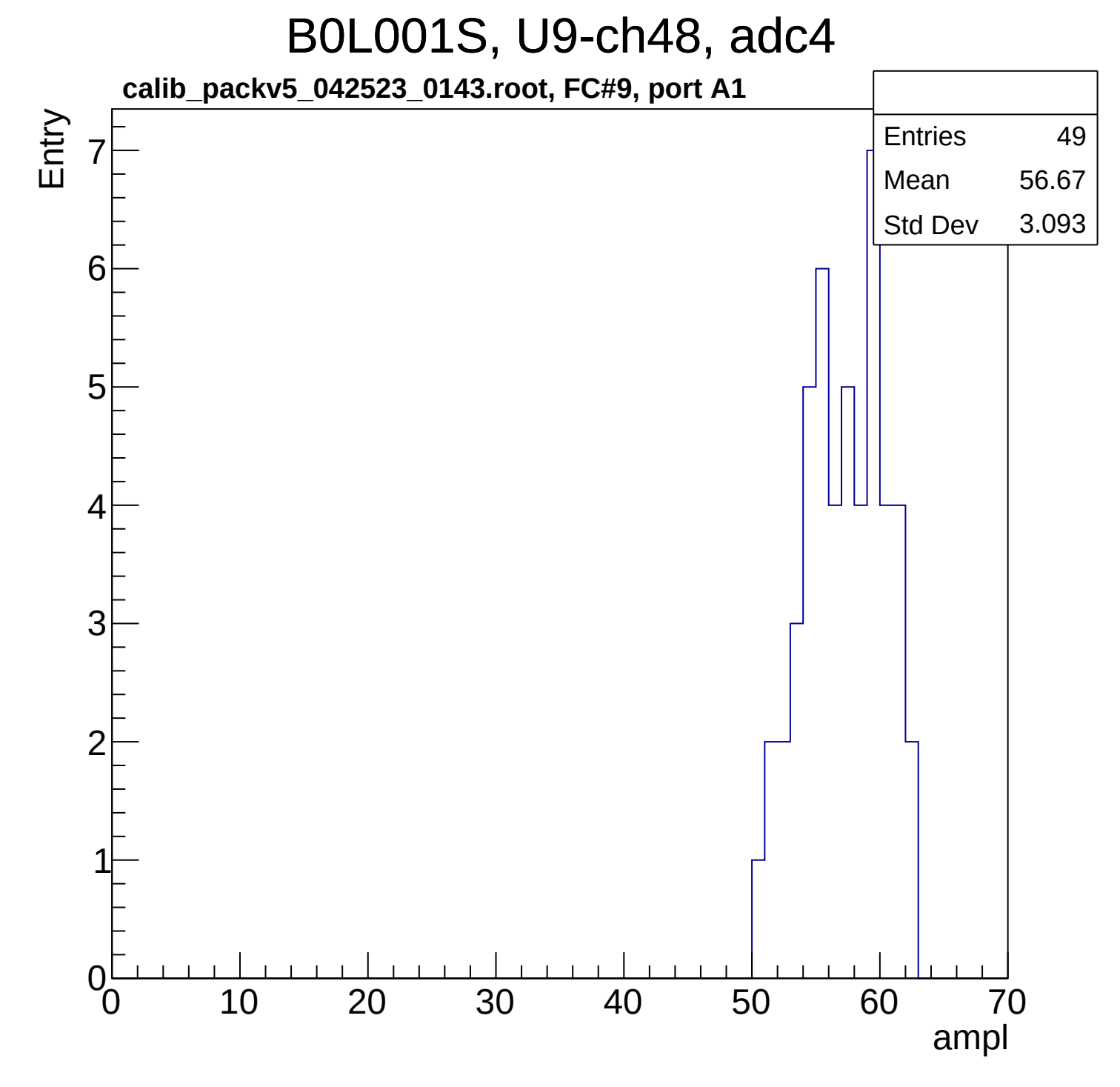
B0L001S, U9-ch48, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 56.67
Std Dev 3.093
Entry
ampl
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1
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3
4
5
6
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0
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60
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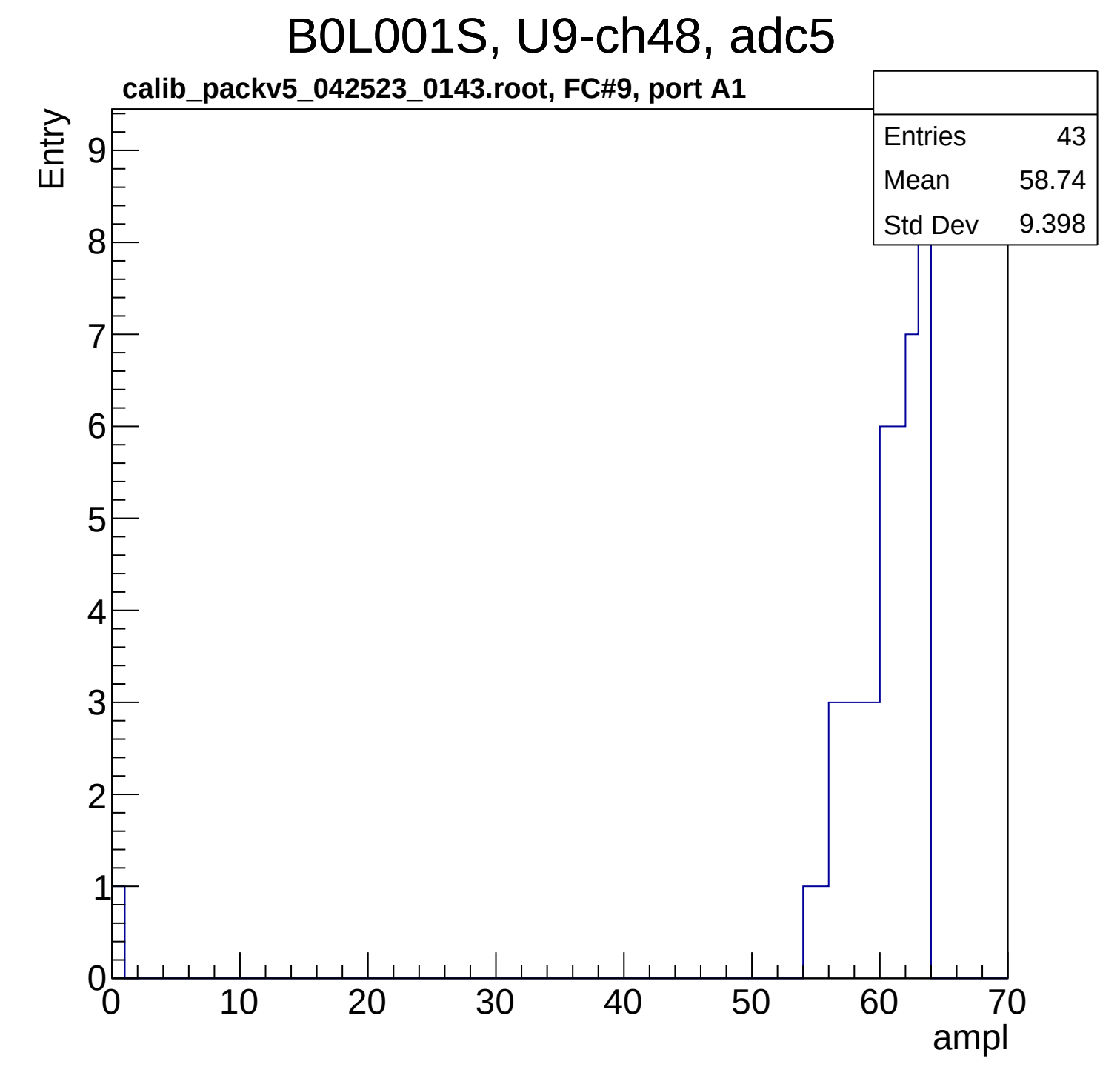
B0L001S, U9-ch48, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.74
Std Dev 9.398
Entry
ampl
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2
3
4
5
6
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0
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60
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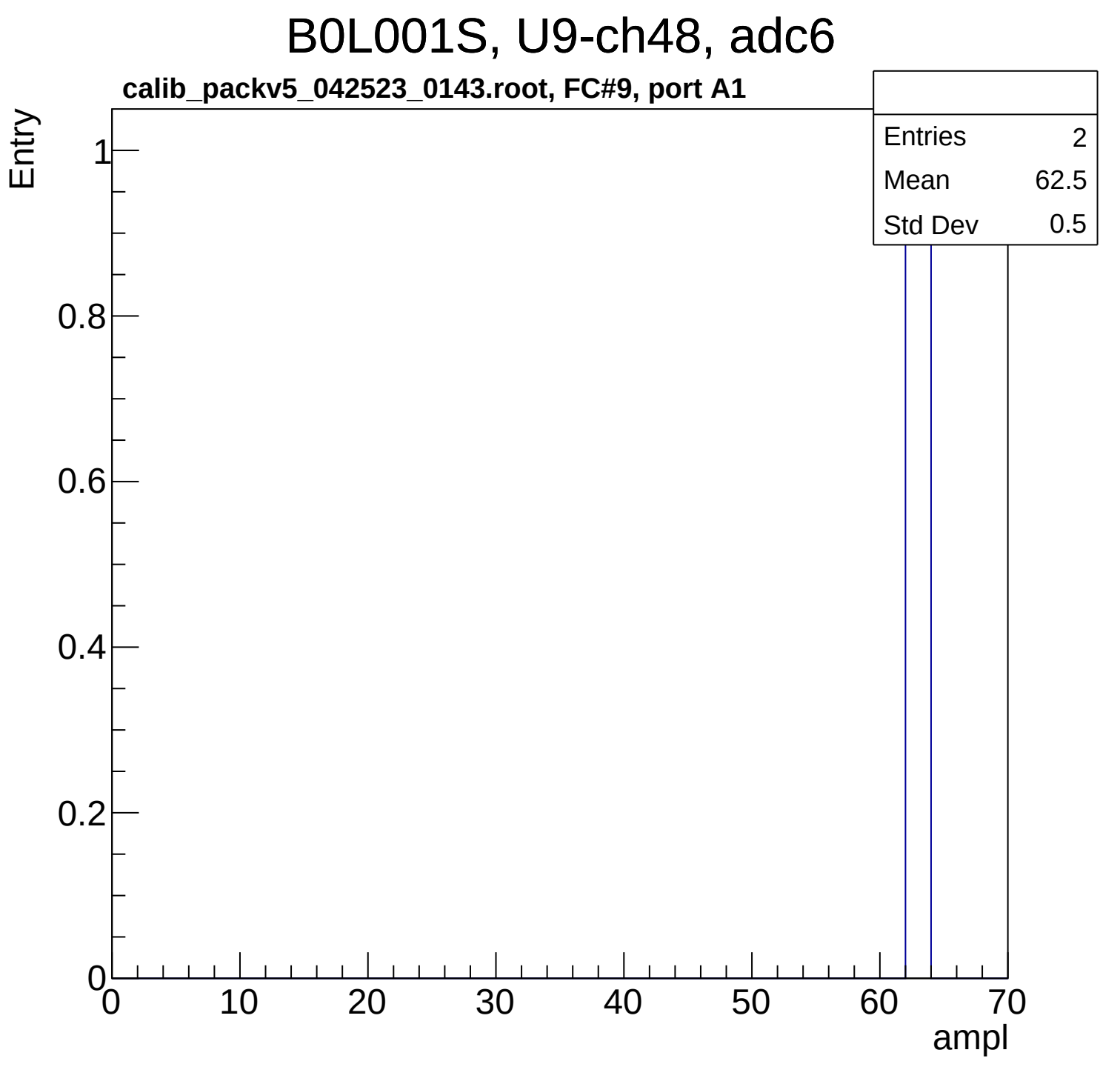
B0L001S, U9-ch48, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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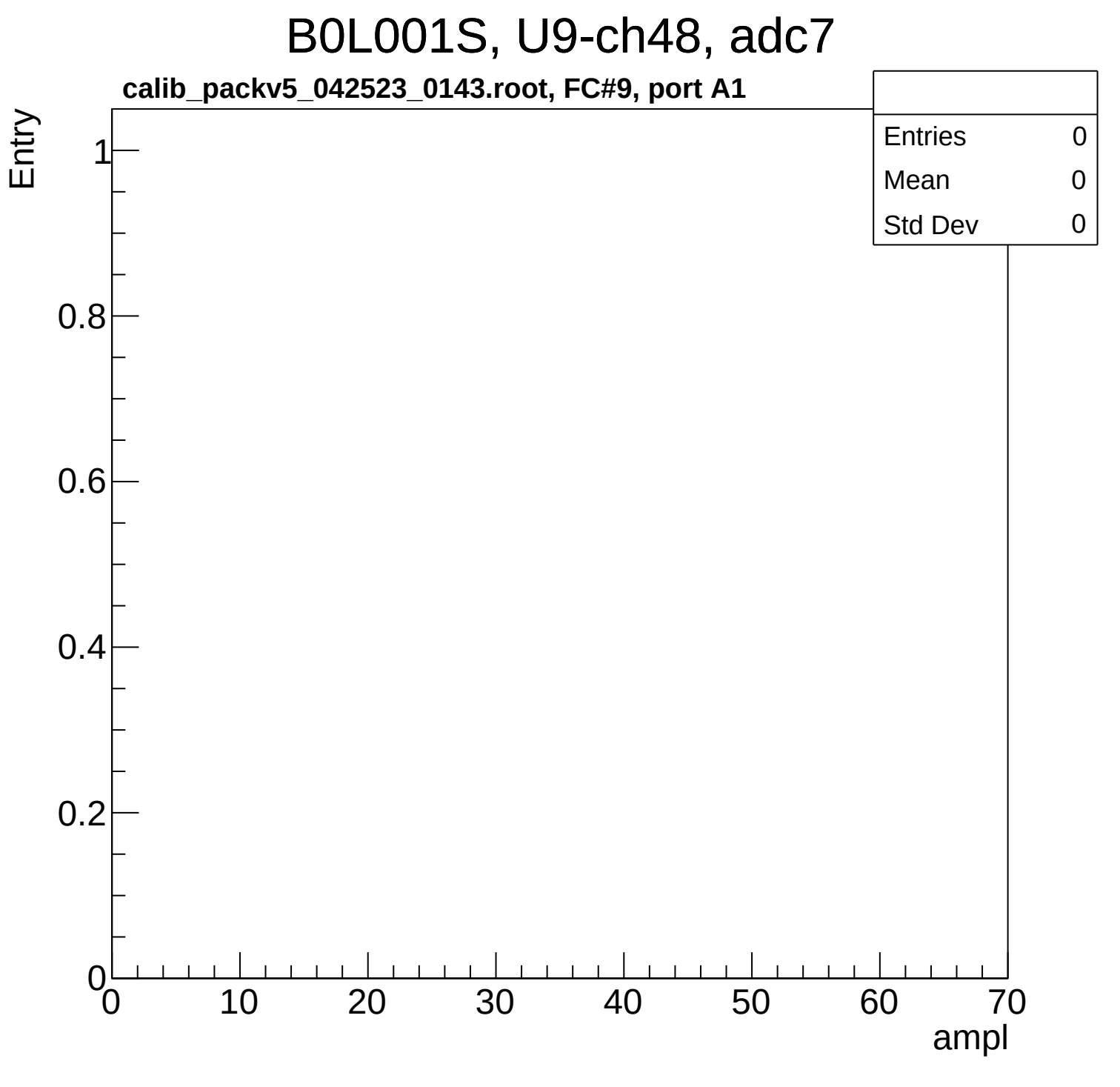
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch48, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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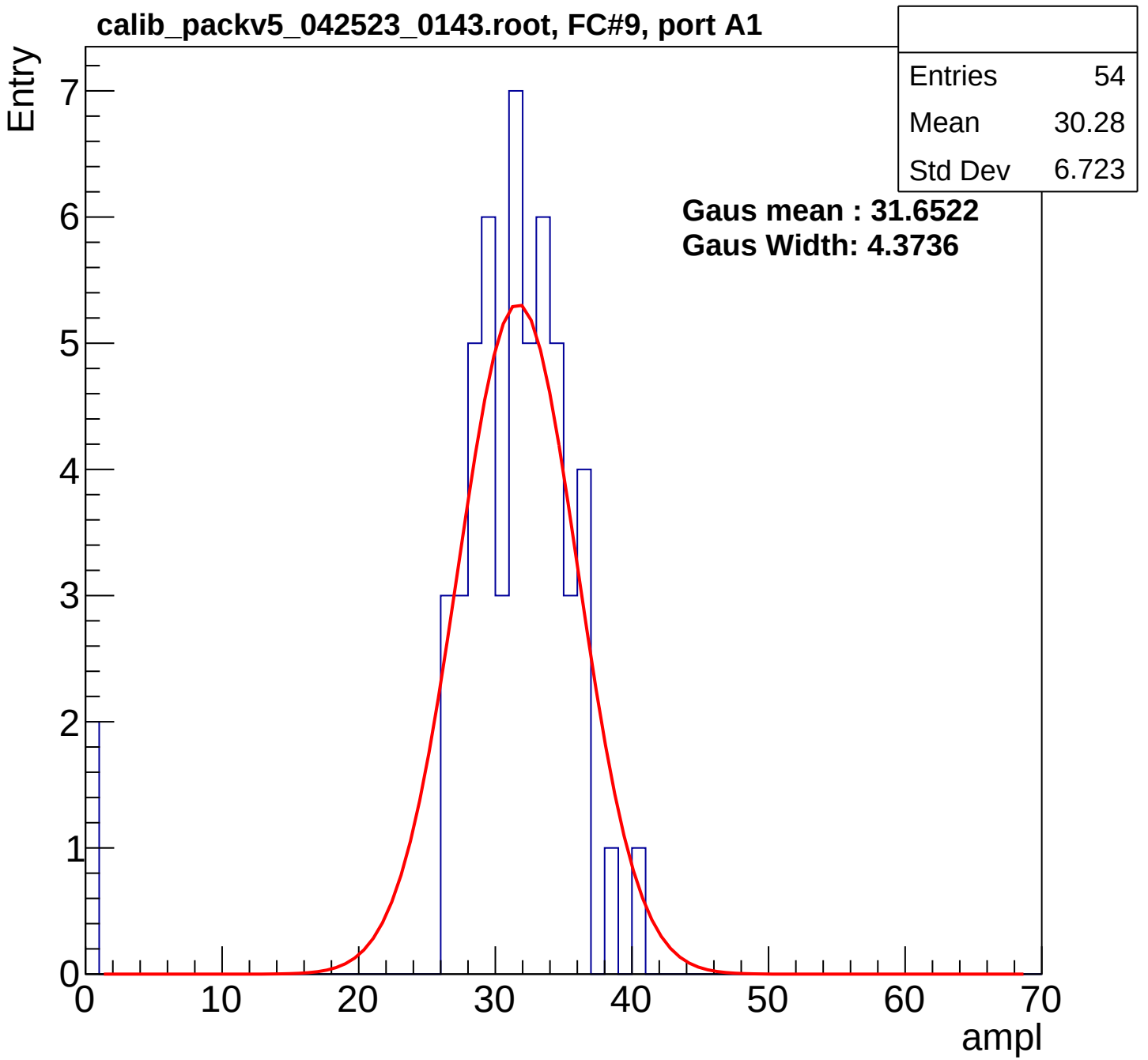
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch49, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 30.28
Std Dev 6.723
Gaus mean : 31.6522
Gaus Width: 4.3736
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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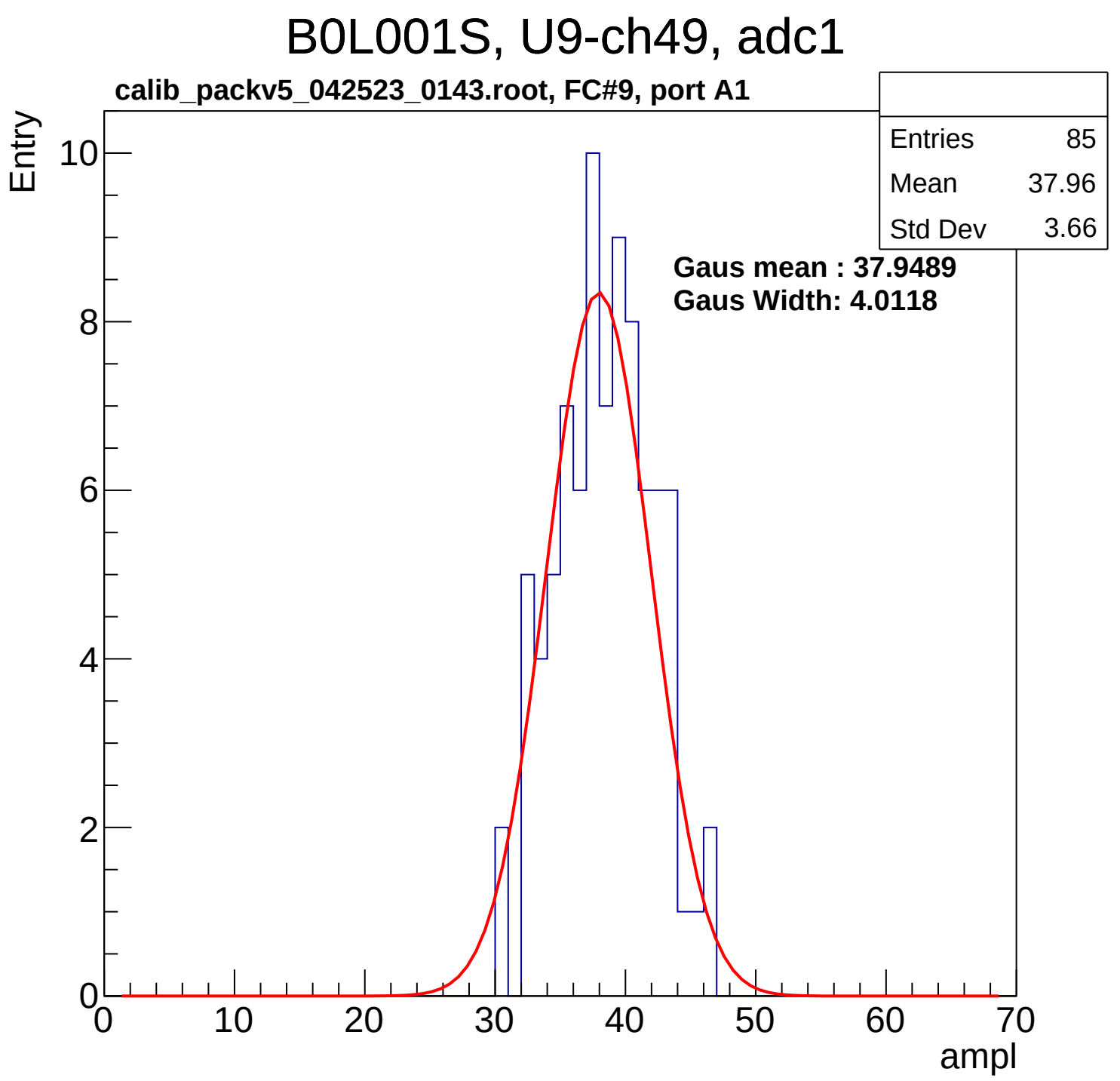
40
50
60
70


B0L001S, U9-ch49, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 37.96
Std Dev 3.66
Gaus mean : 37.9489
Gaus Width: 4.0118
Entry
ampl
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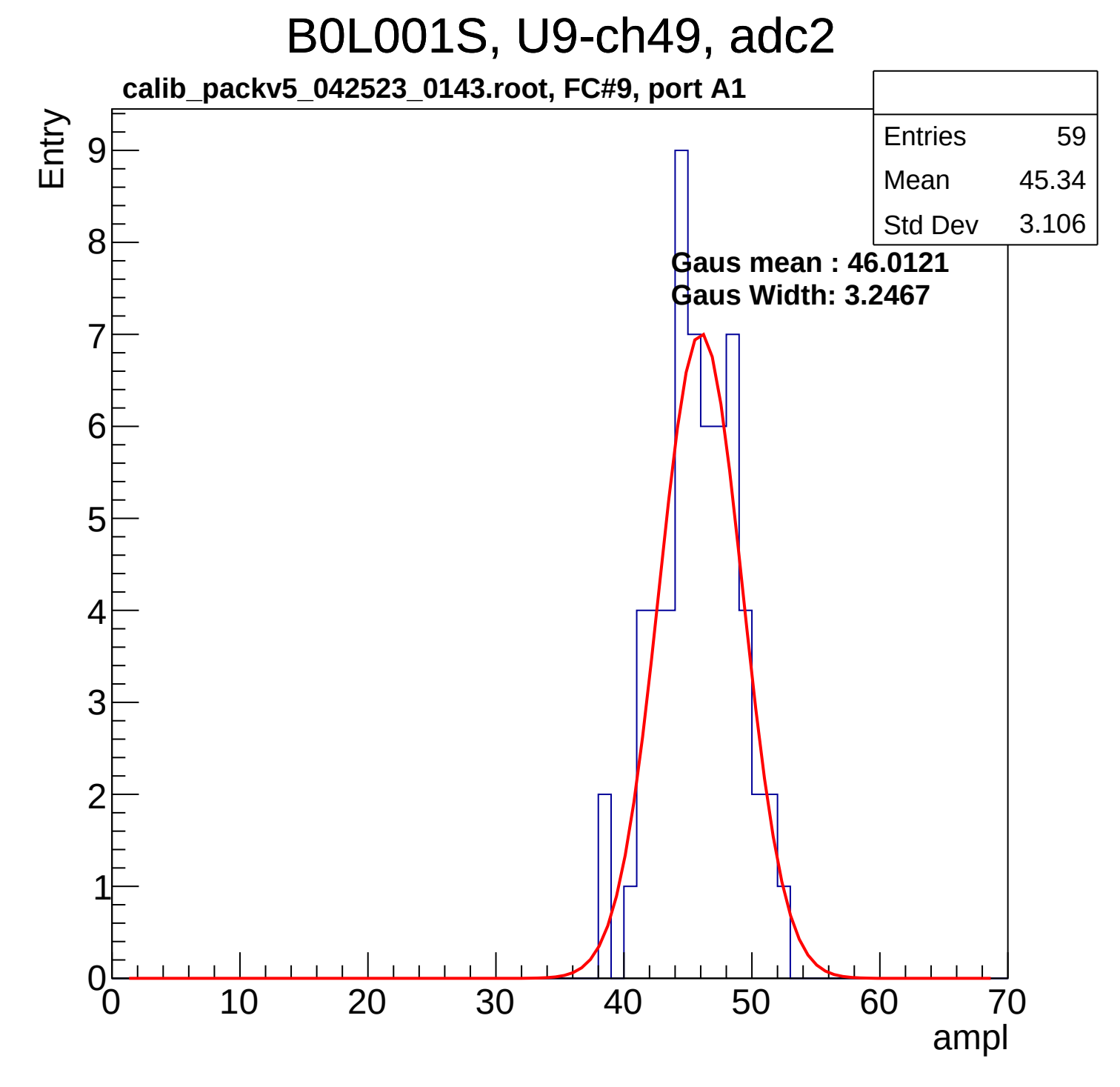
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch49, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 45.34
Std Dev 3.106
Gaus mean : 46.0121
Gaus Width: 3.2467
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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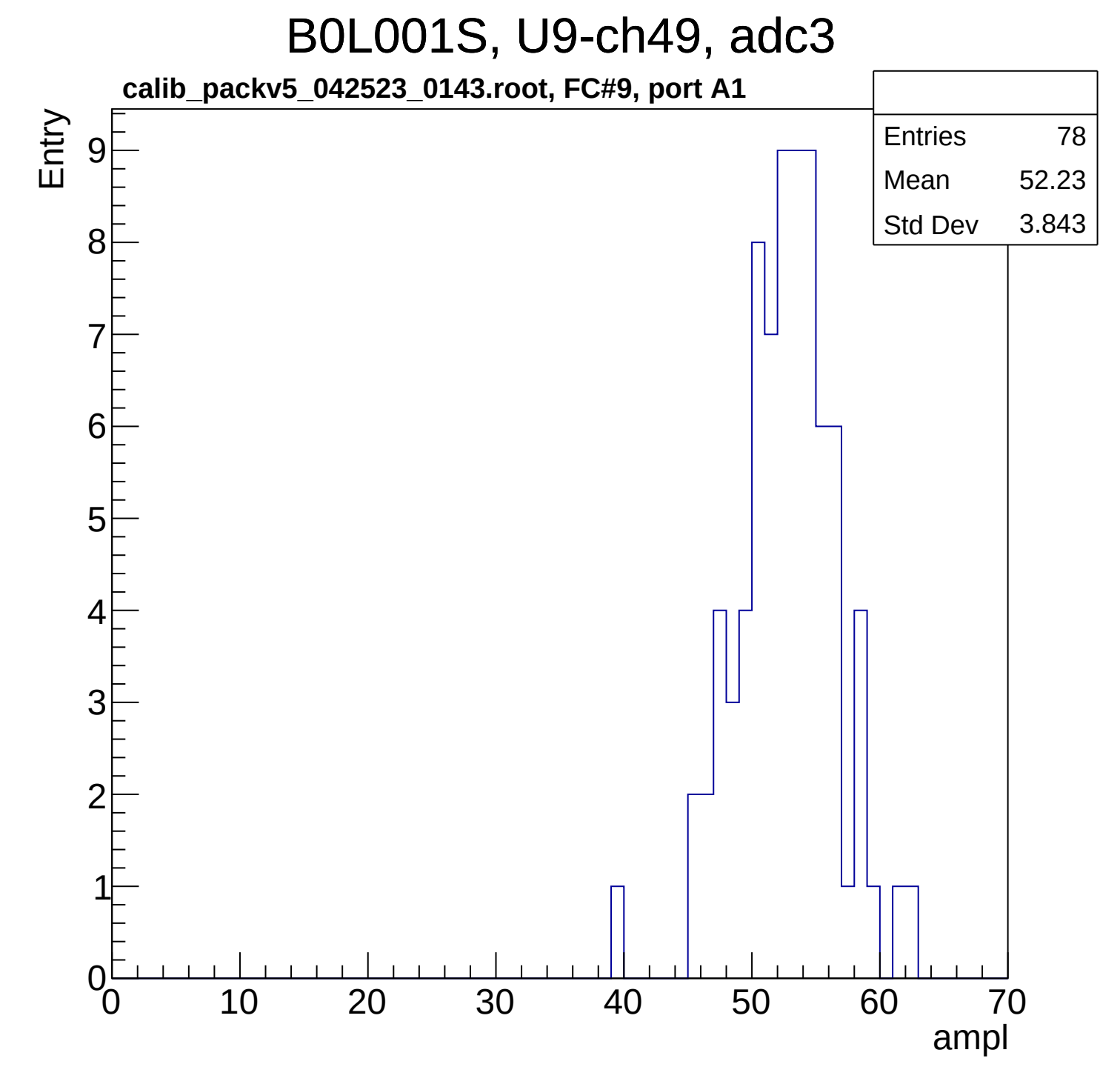
40
50
60
70


B0L001S, U9-ch49, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 52.23
Std Dev 3.843
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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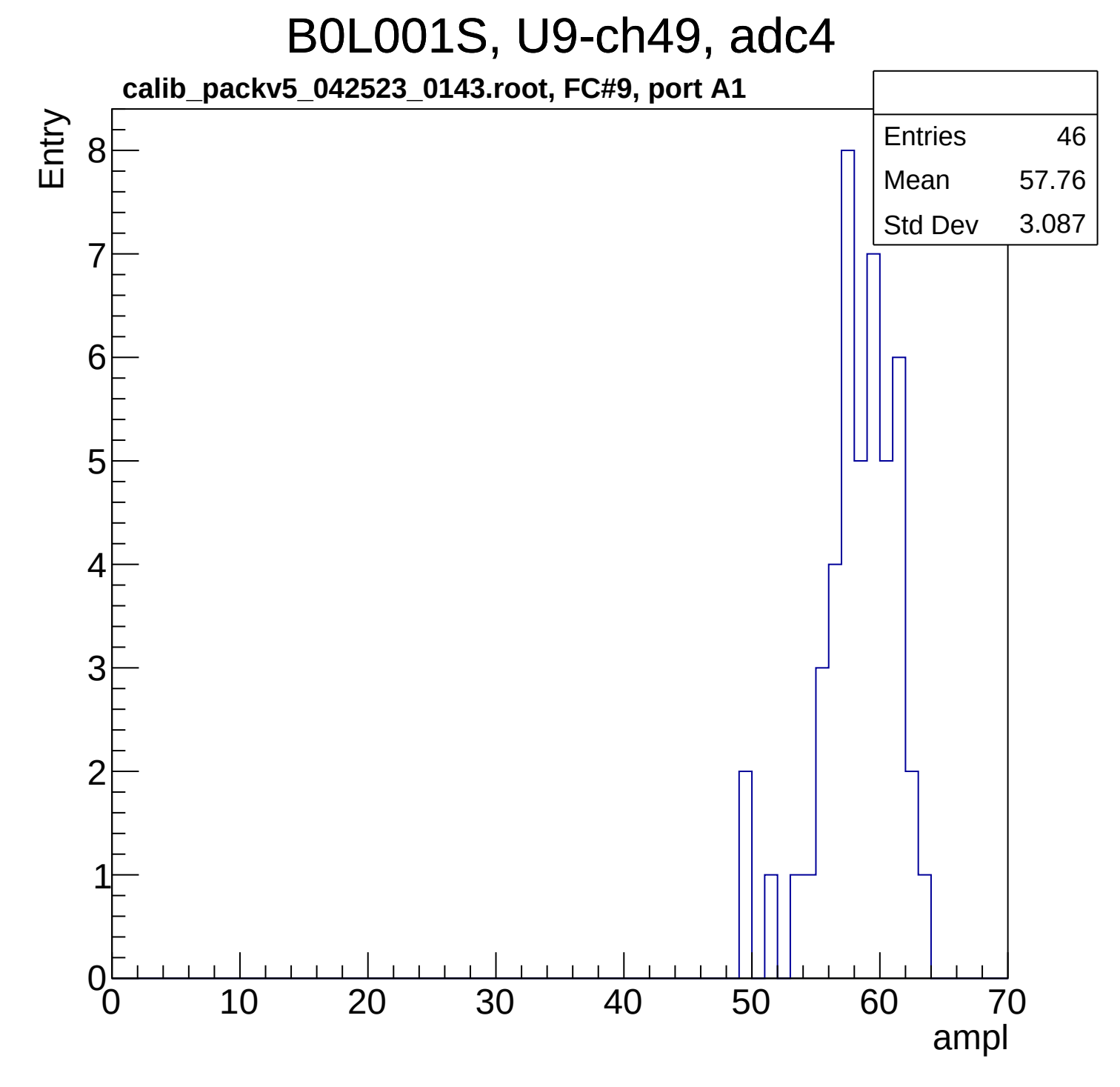
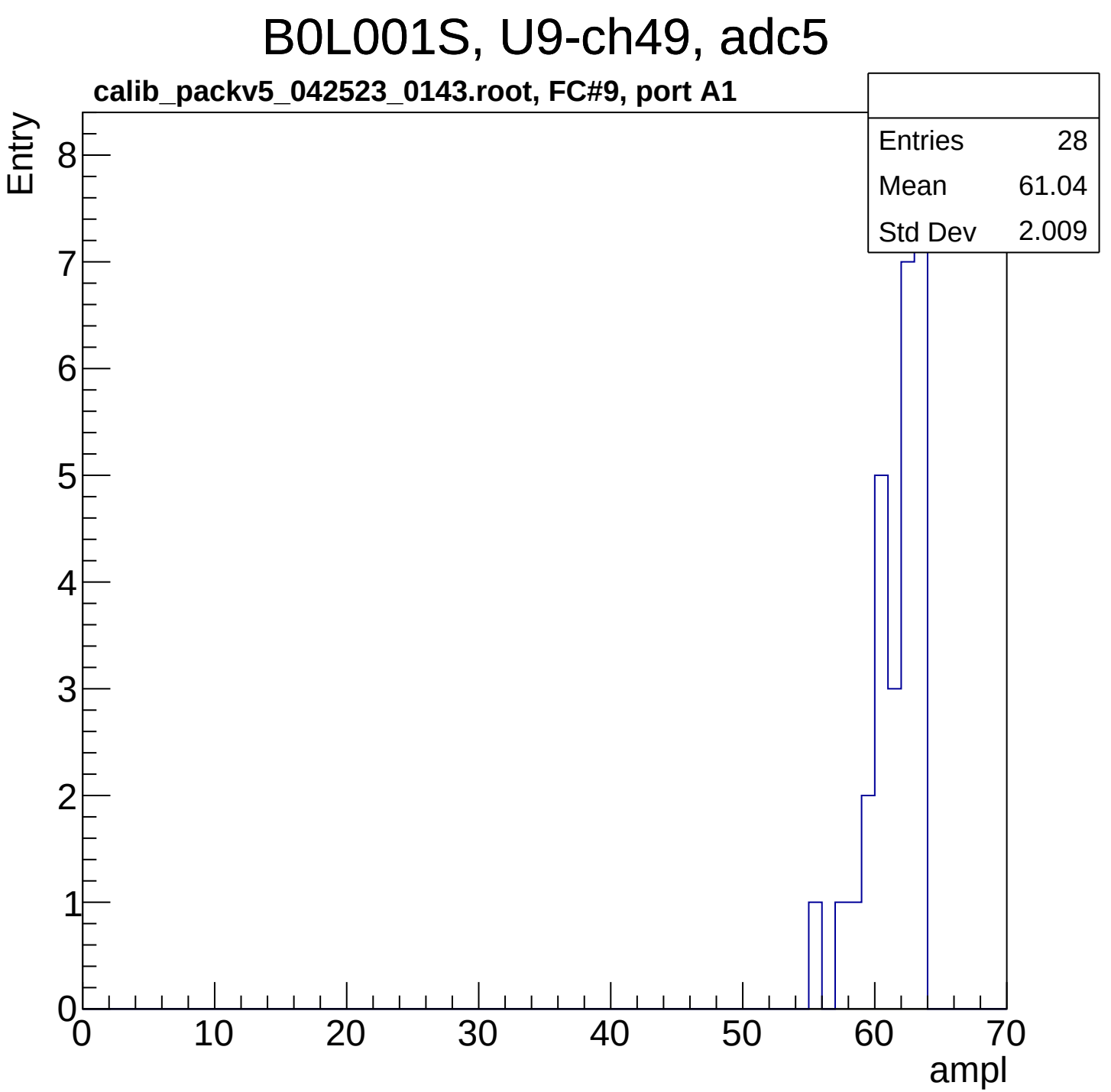

B0L001S, U9-ch49, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 57.76
Std Dev 3.087
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch49, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 61.04
Std Dev 2.009
Entry
0
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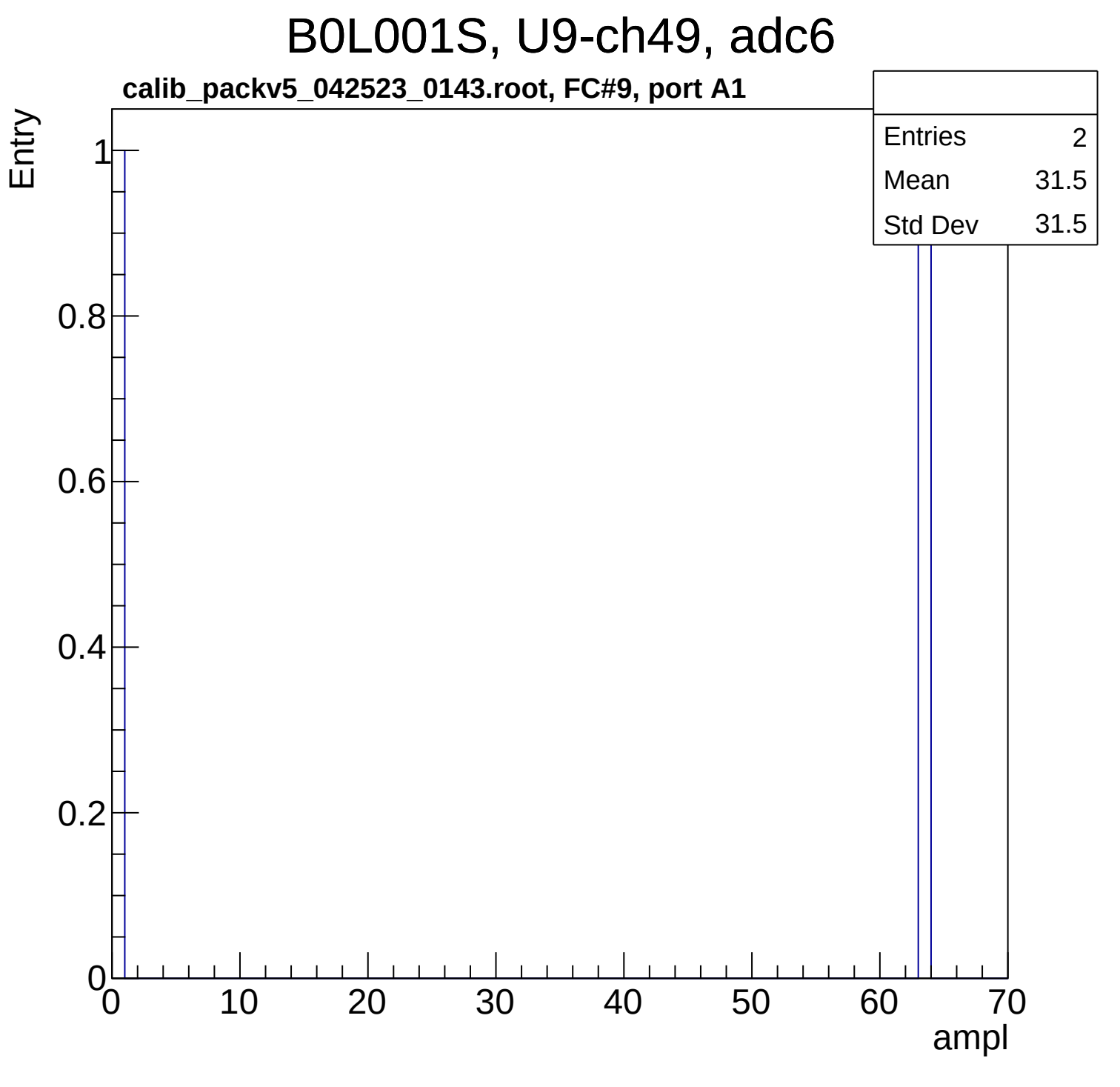
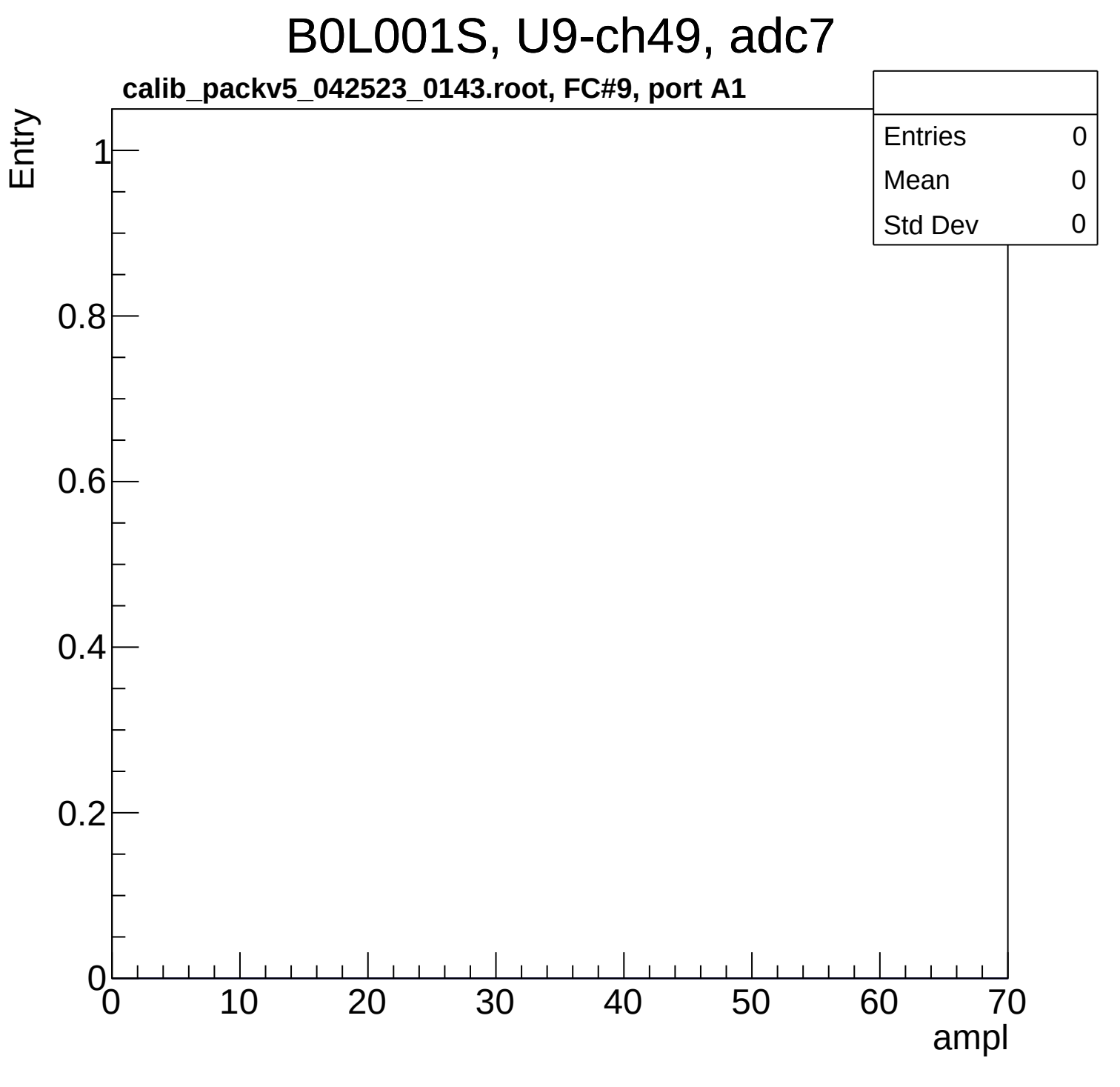
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch49, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch49, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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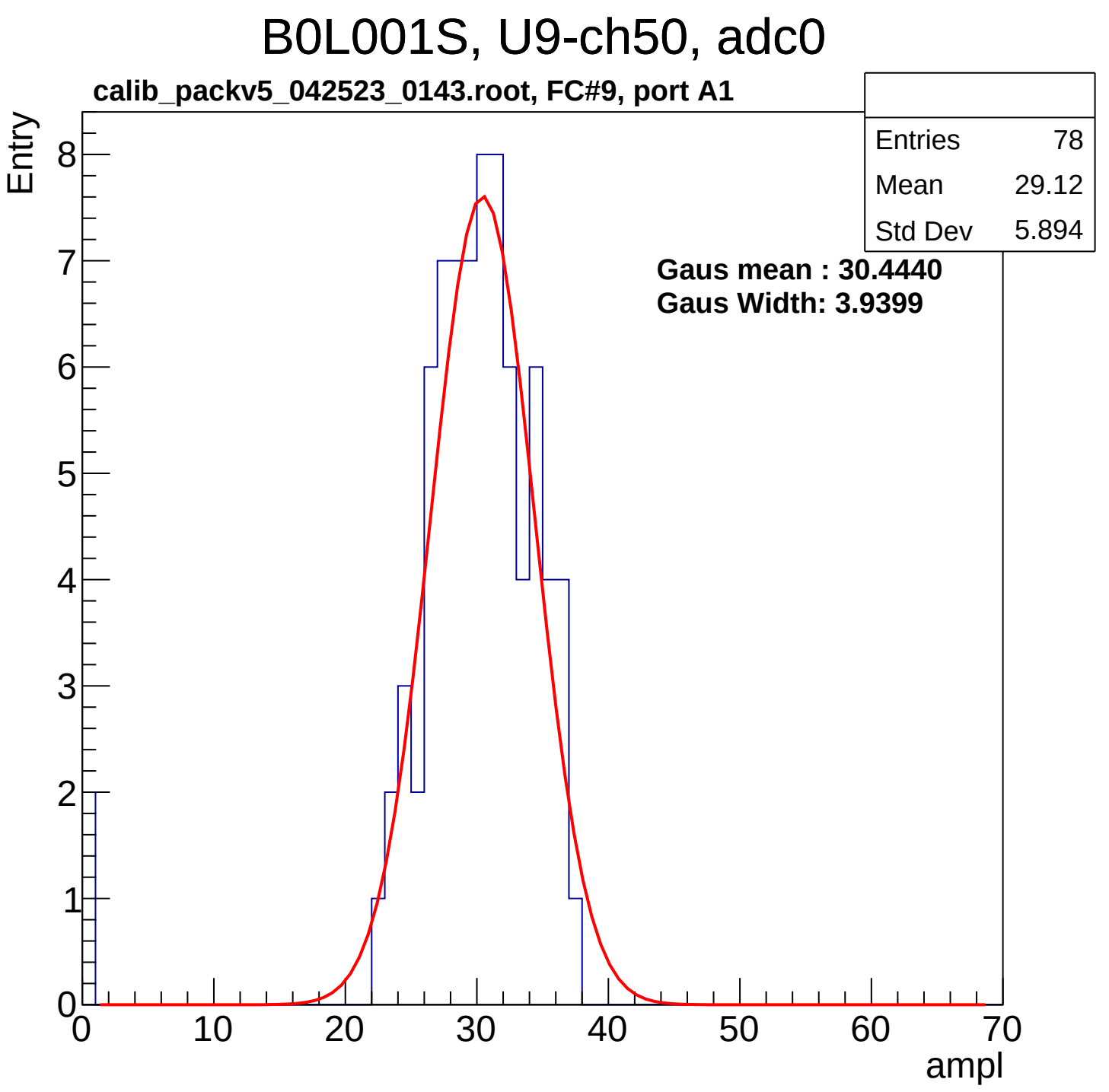
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch50, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.12
Std Dev 5.894
Gaus mean : 30.4440
Gaus Width: 3.9399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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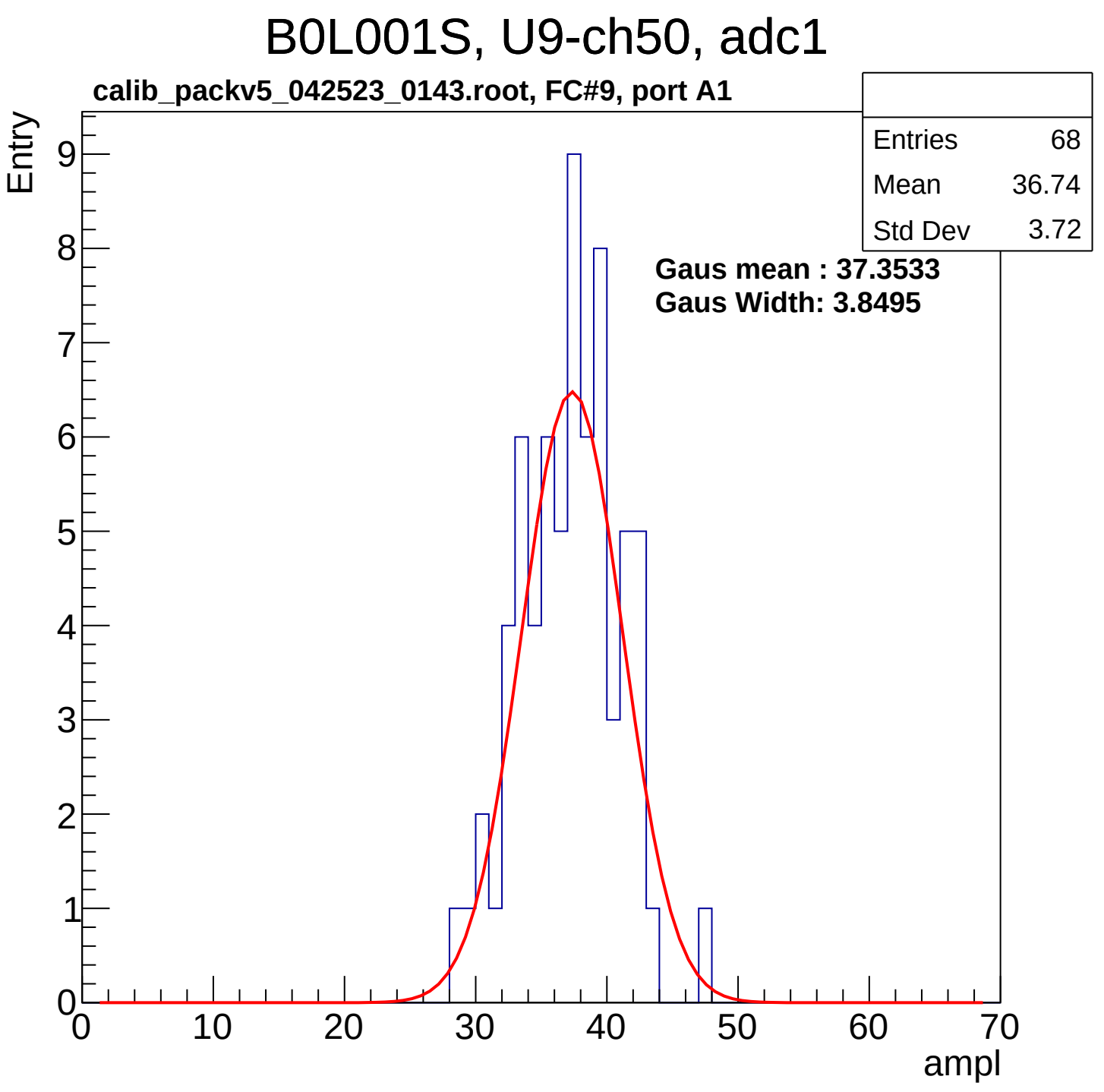
B0L001S, U9-ch50, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.74
Std Dev 3.72
Gaus mean : 37.3533
Gaus Width: 3.8495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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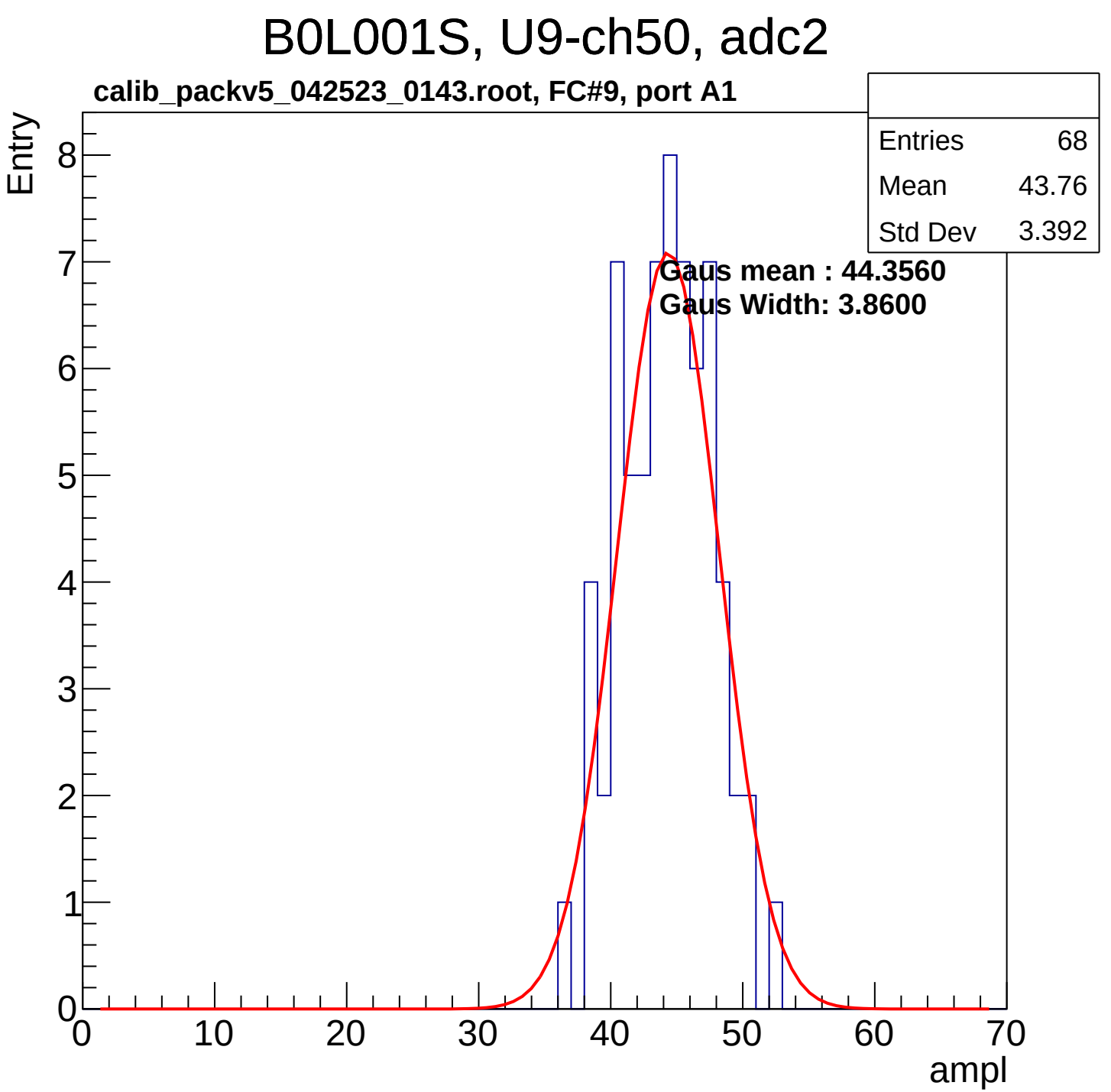
B0L001S, U9-ch50, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.76
Std Dev 3.392
Gaus mean : 44.3560
Gaus Width: 3.8600
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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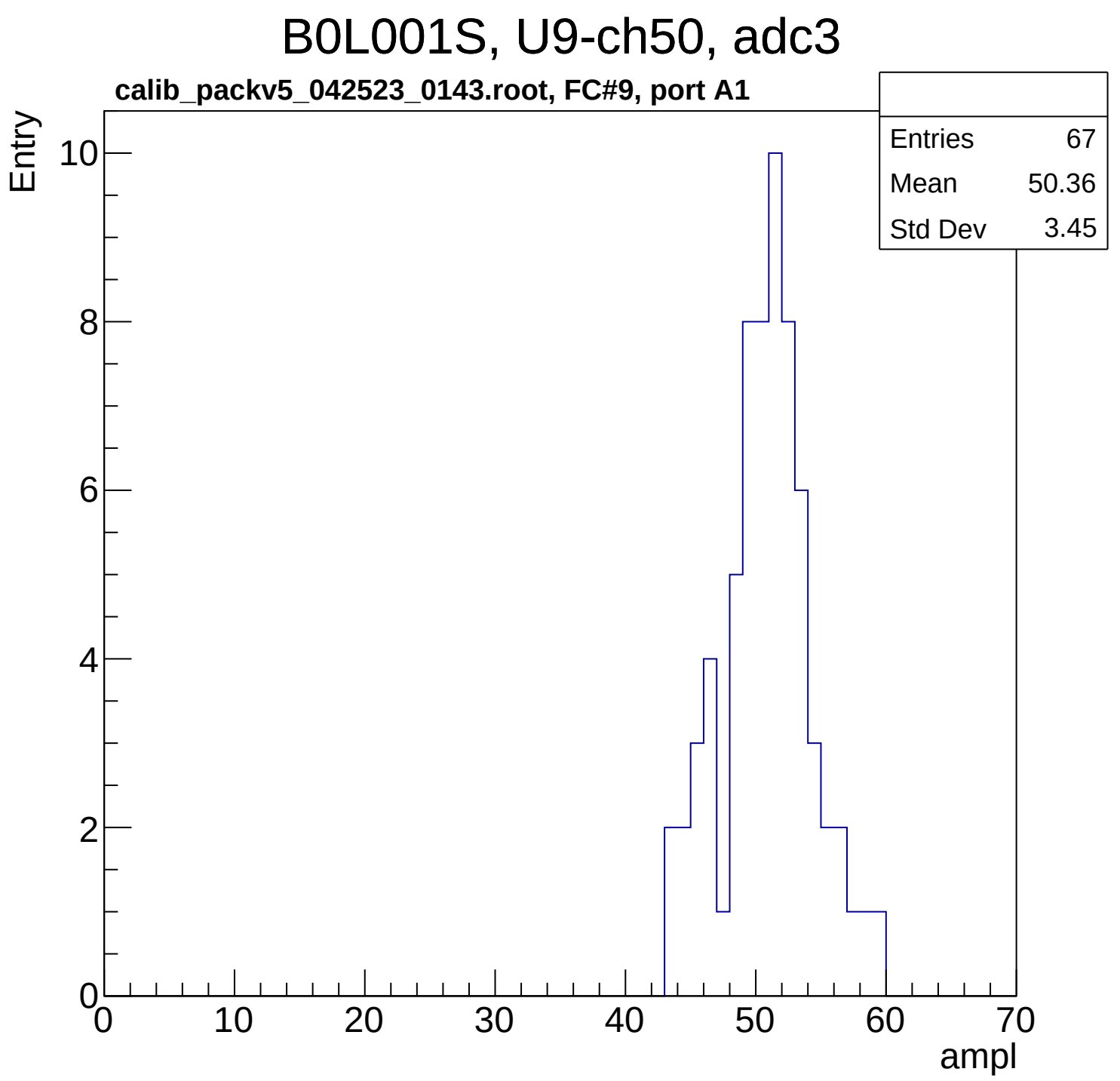
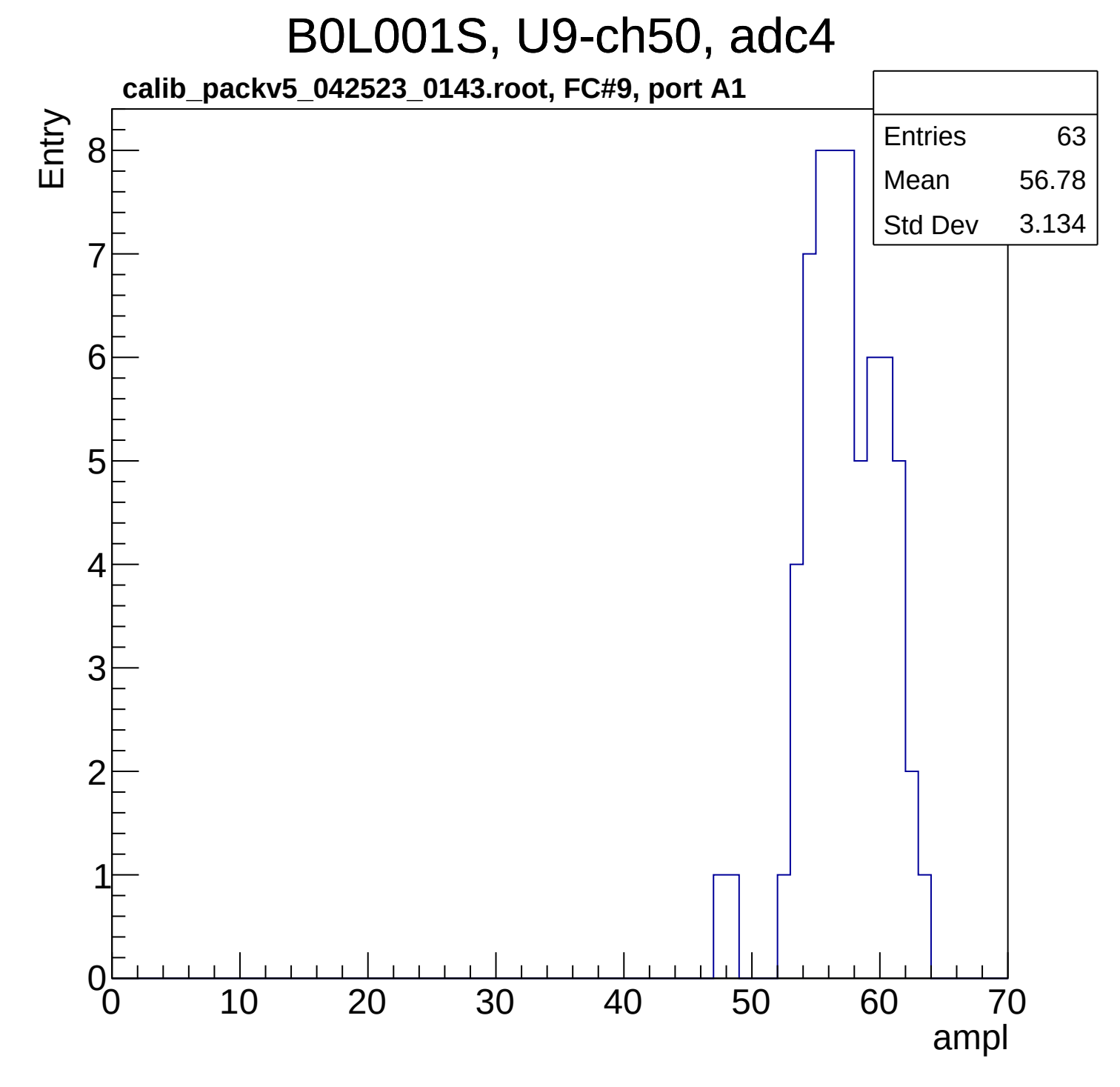

B0L001S, U9-ch50, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.36
Std Dev 3.45
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch50, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.78
Std Dev 3.134
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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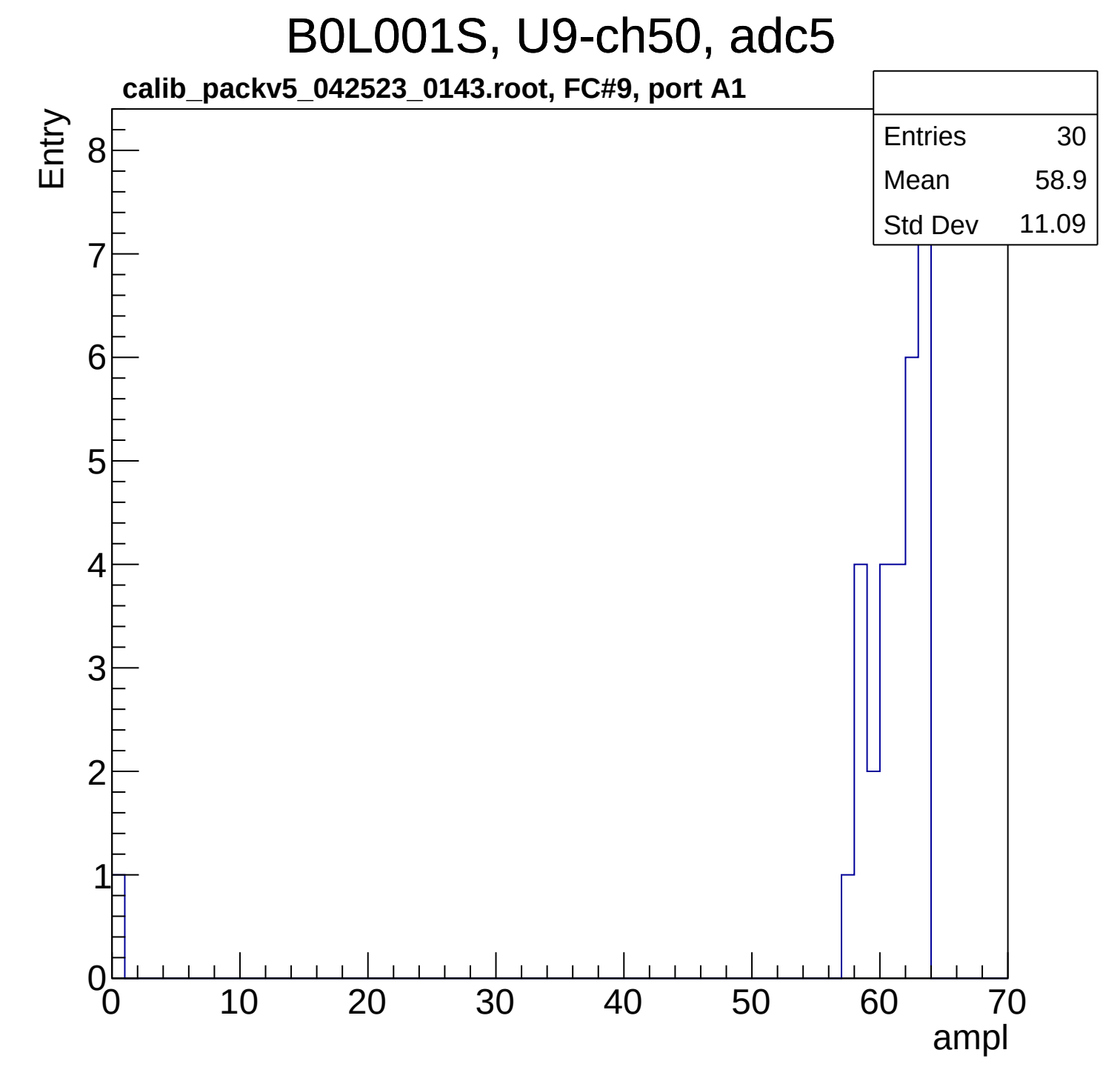
B0L001S, U9-ch50, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 58.9
Std Dev 11.09
Entry
0
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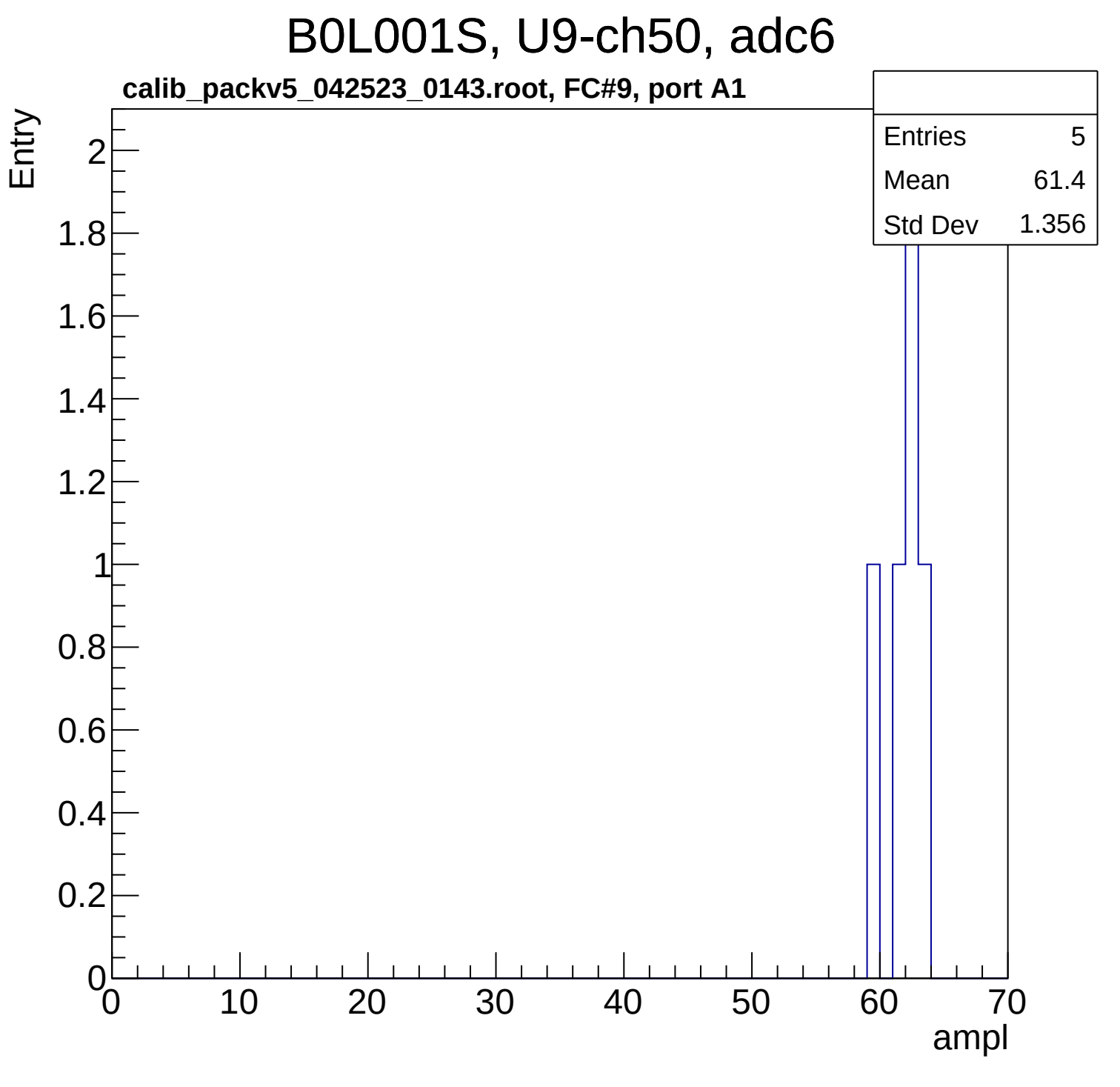
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch50, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.4
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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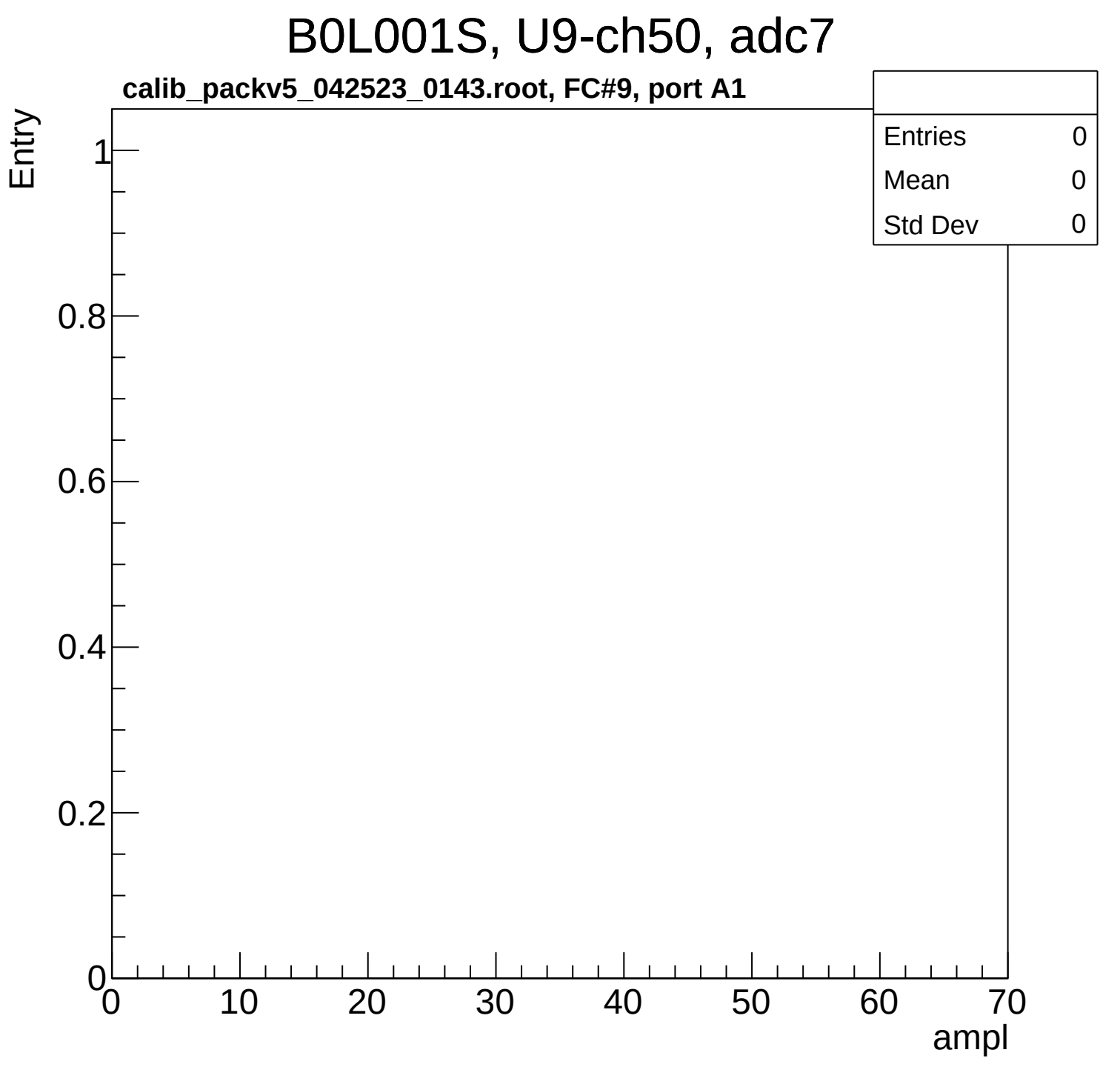
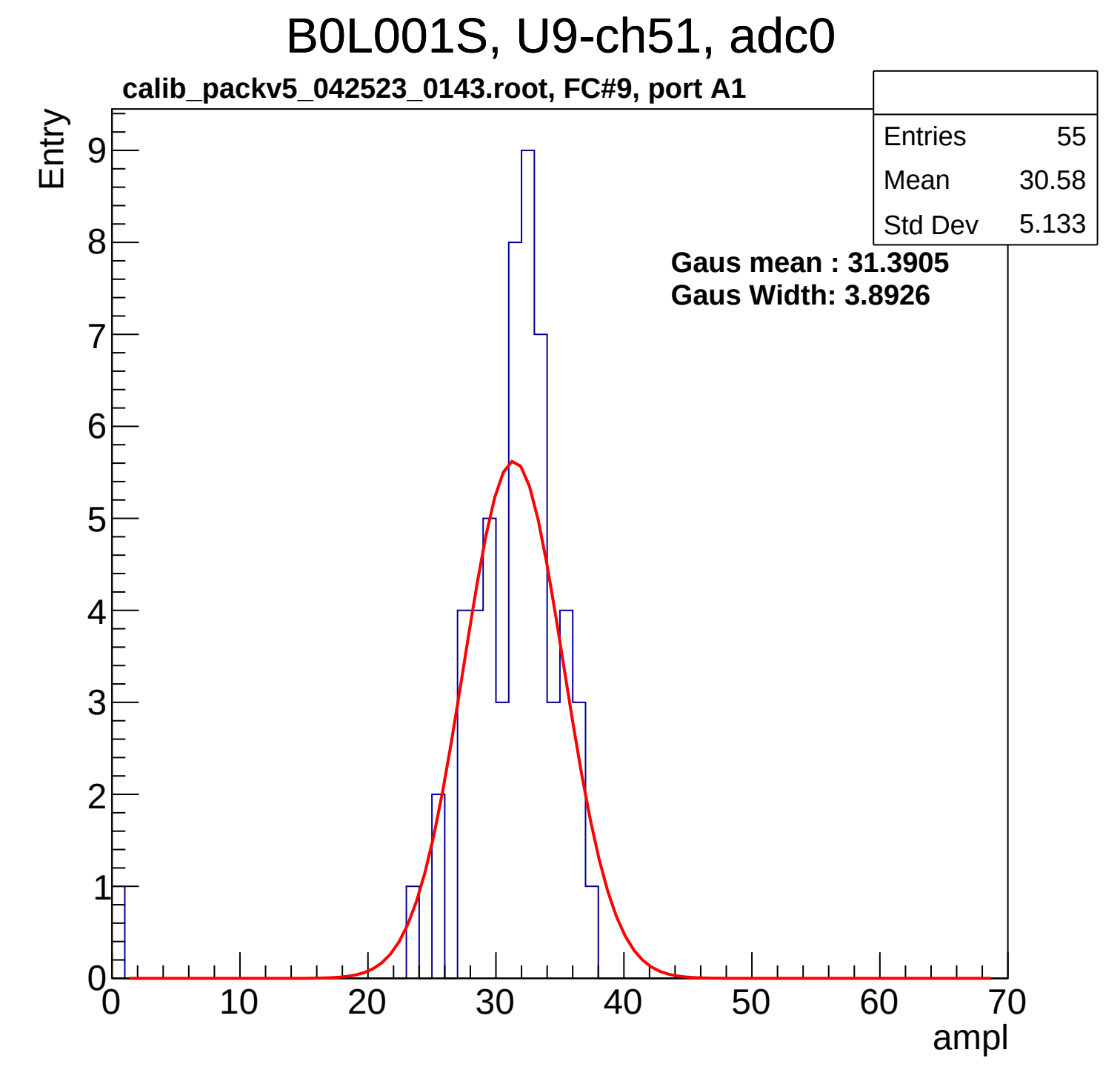
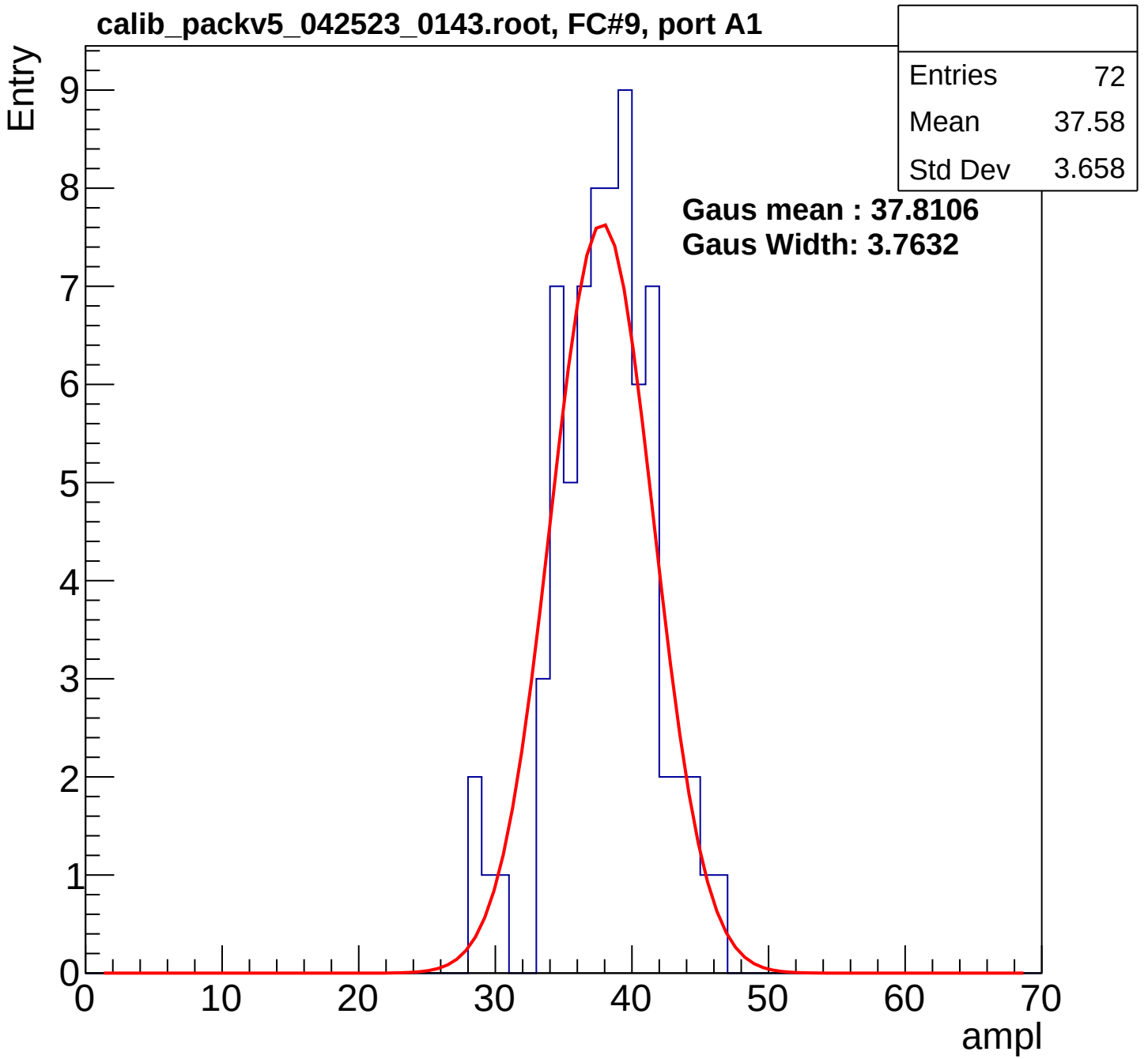
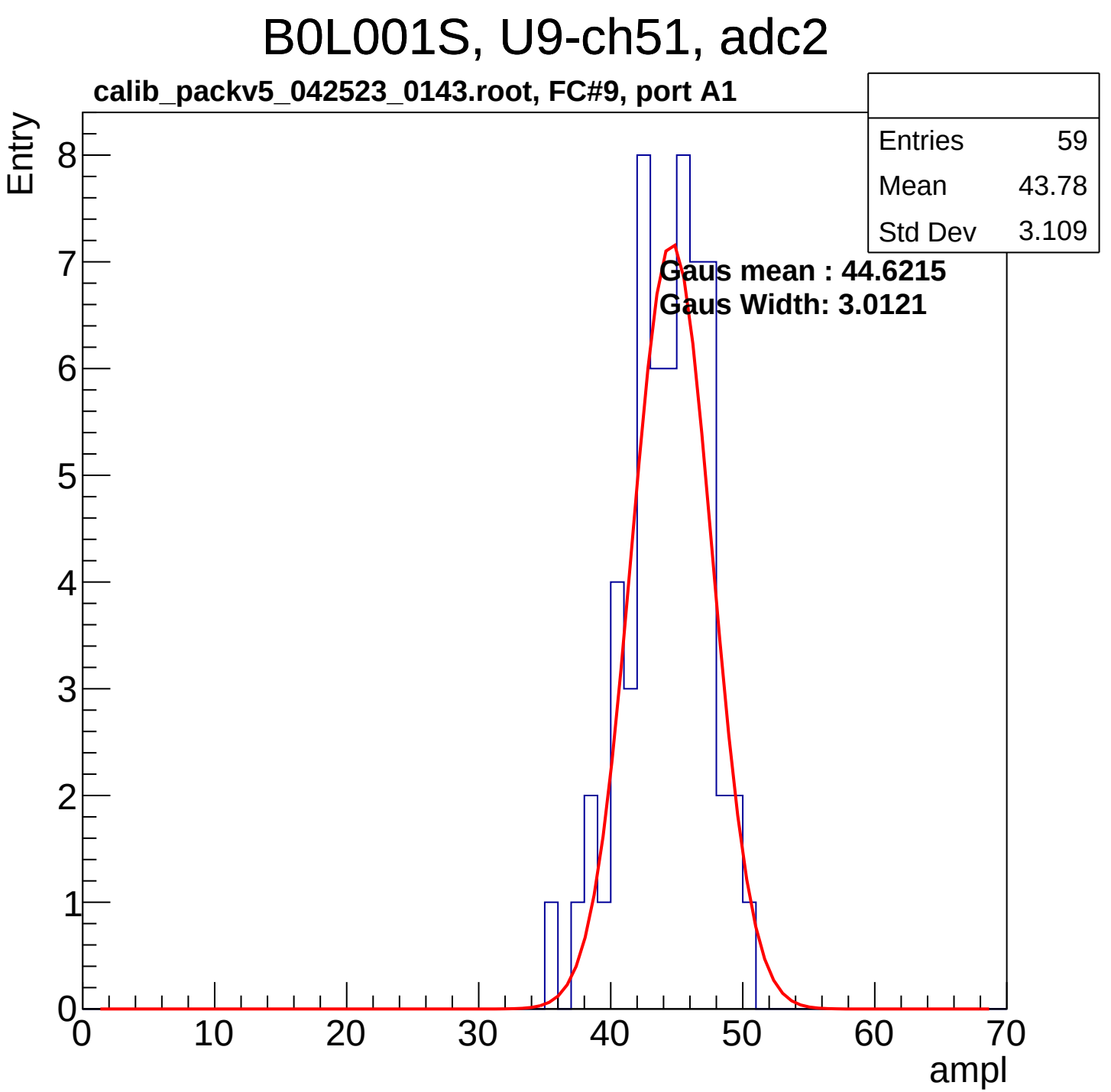

B0L001S, U9-ch50, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch51, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 30.58
Std Dev 5.133
Gaus mean : 31.3905
Gaus Width: 3.8926
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch51, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.58
Std Dev 3.658
Gaus mean : 37.8106
Gaus Width: 3.7632
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch51, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.78
Std Dev 3.109
Gaus mean : 44.6215
Gaus Width: 3.0121
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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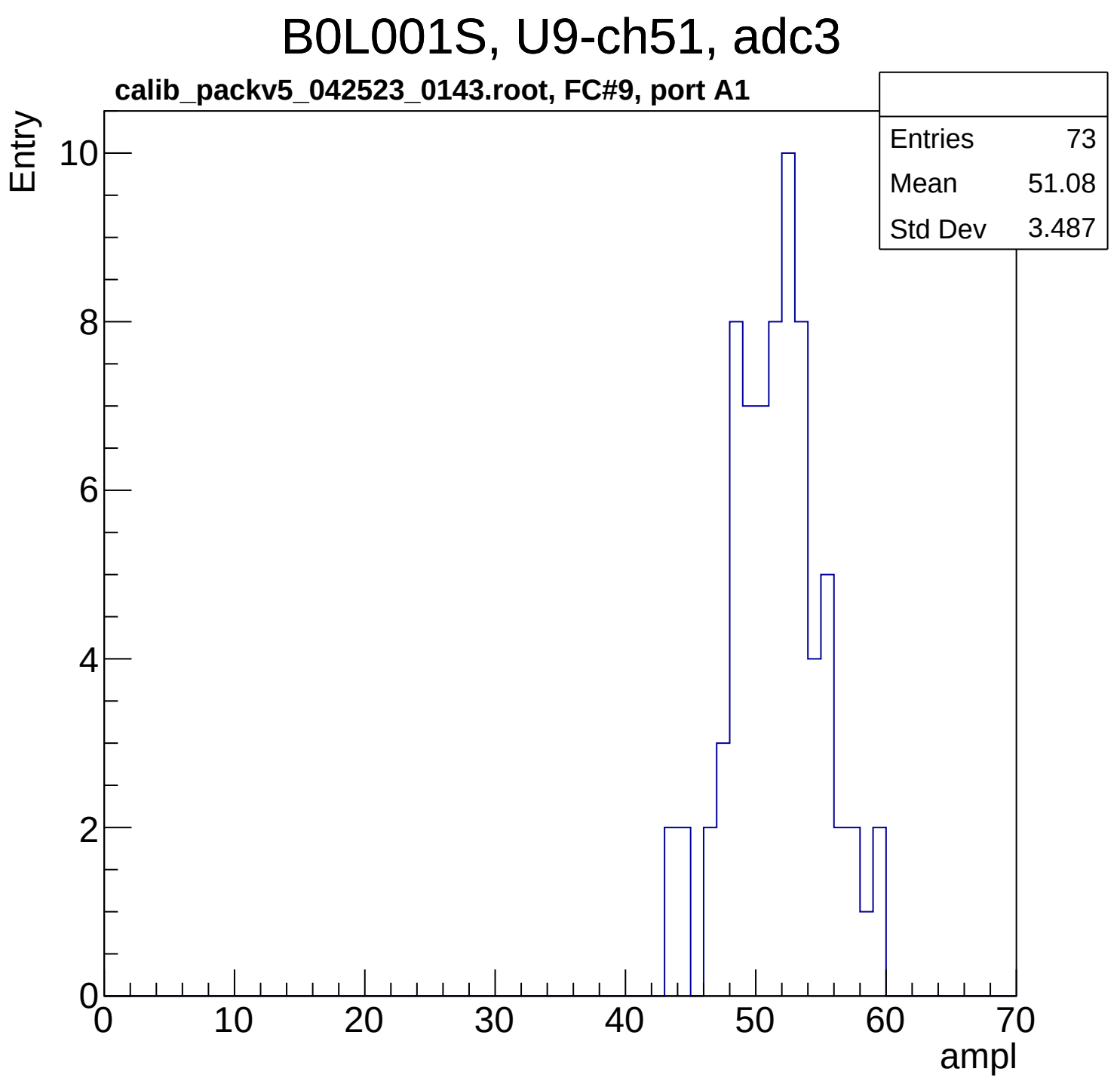
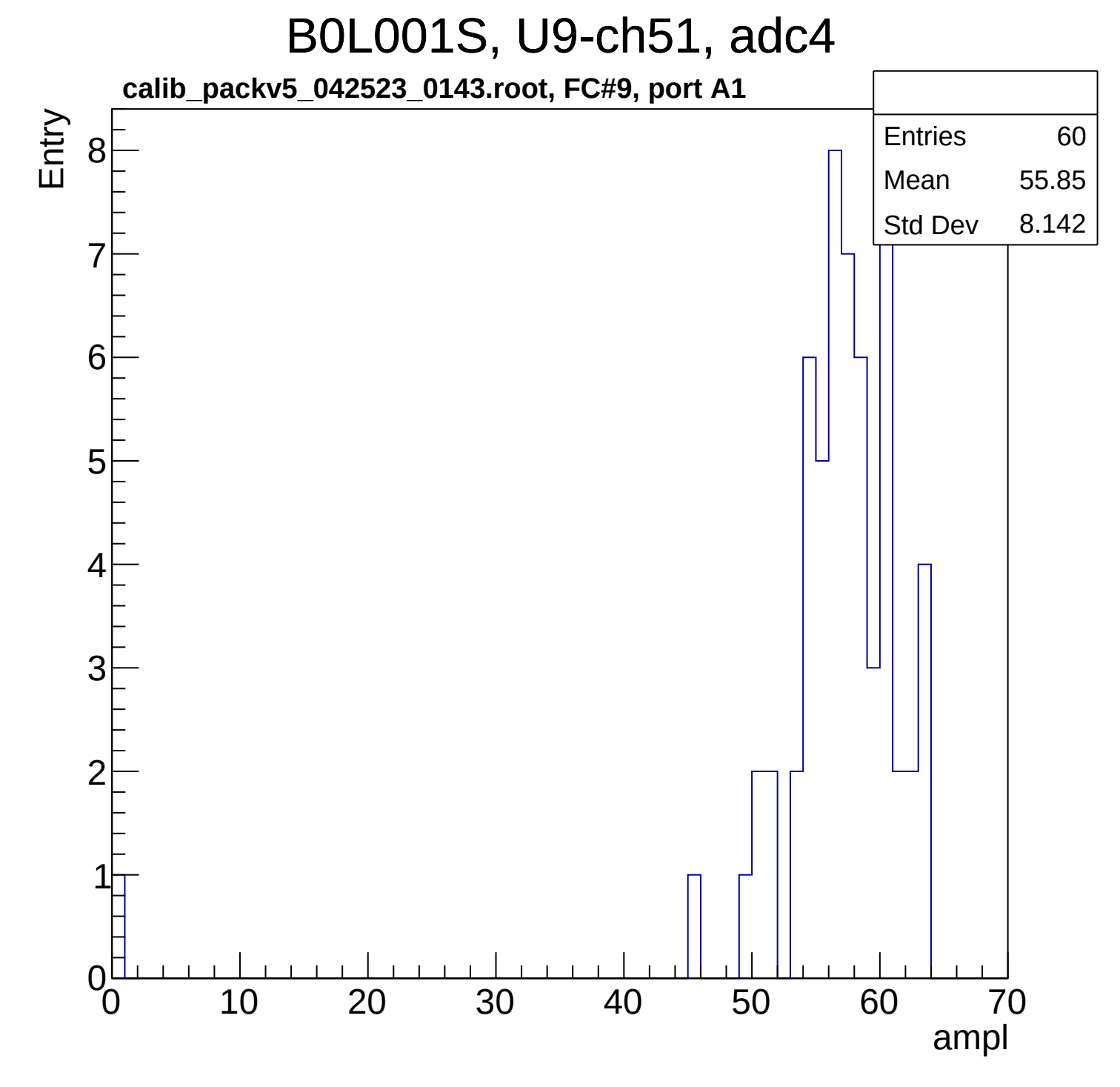
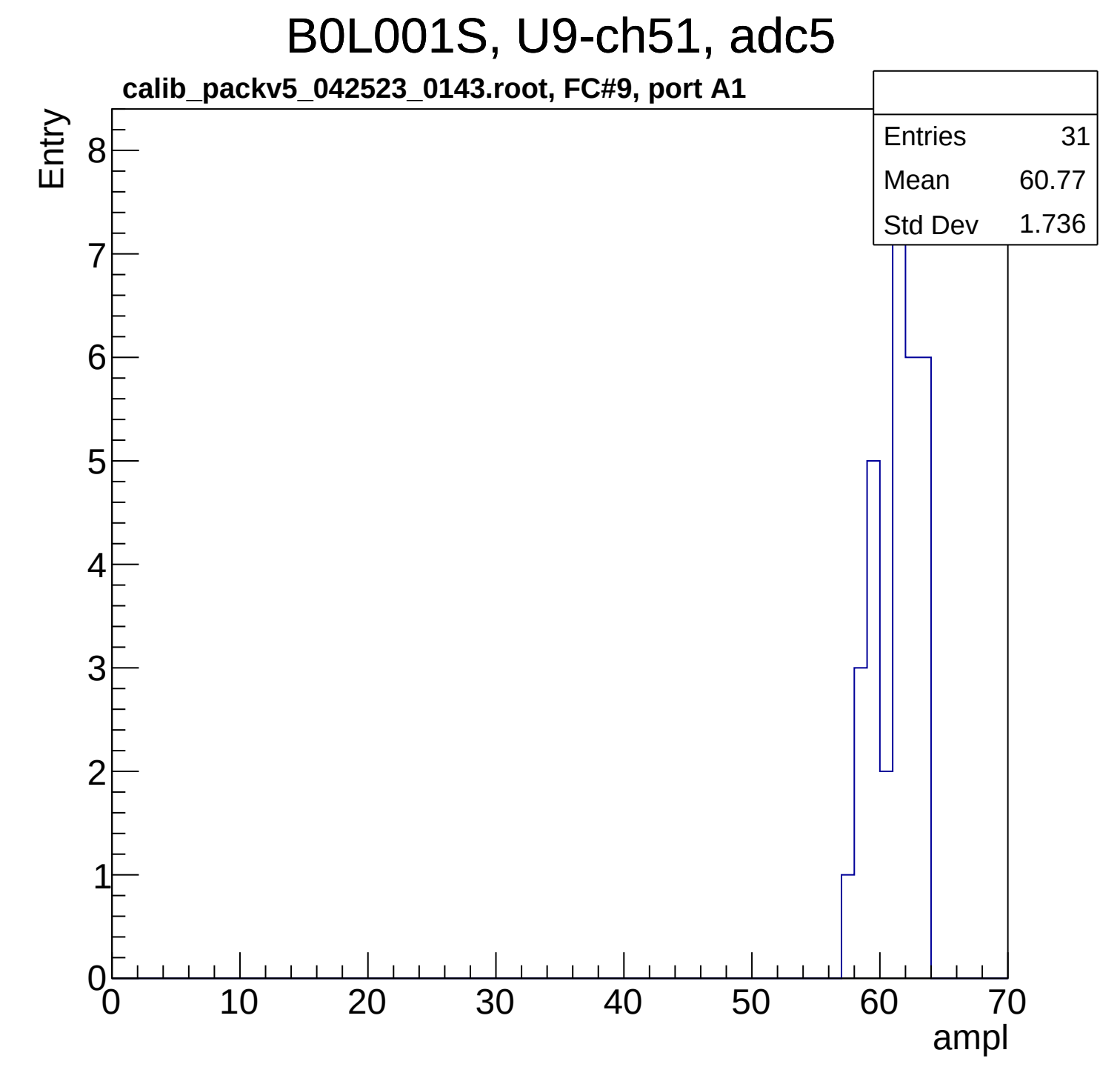
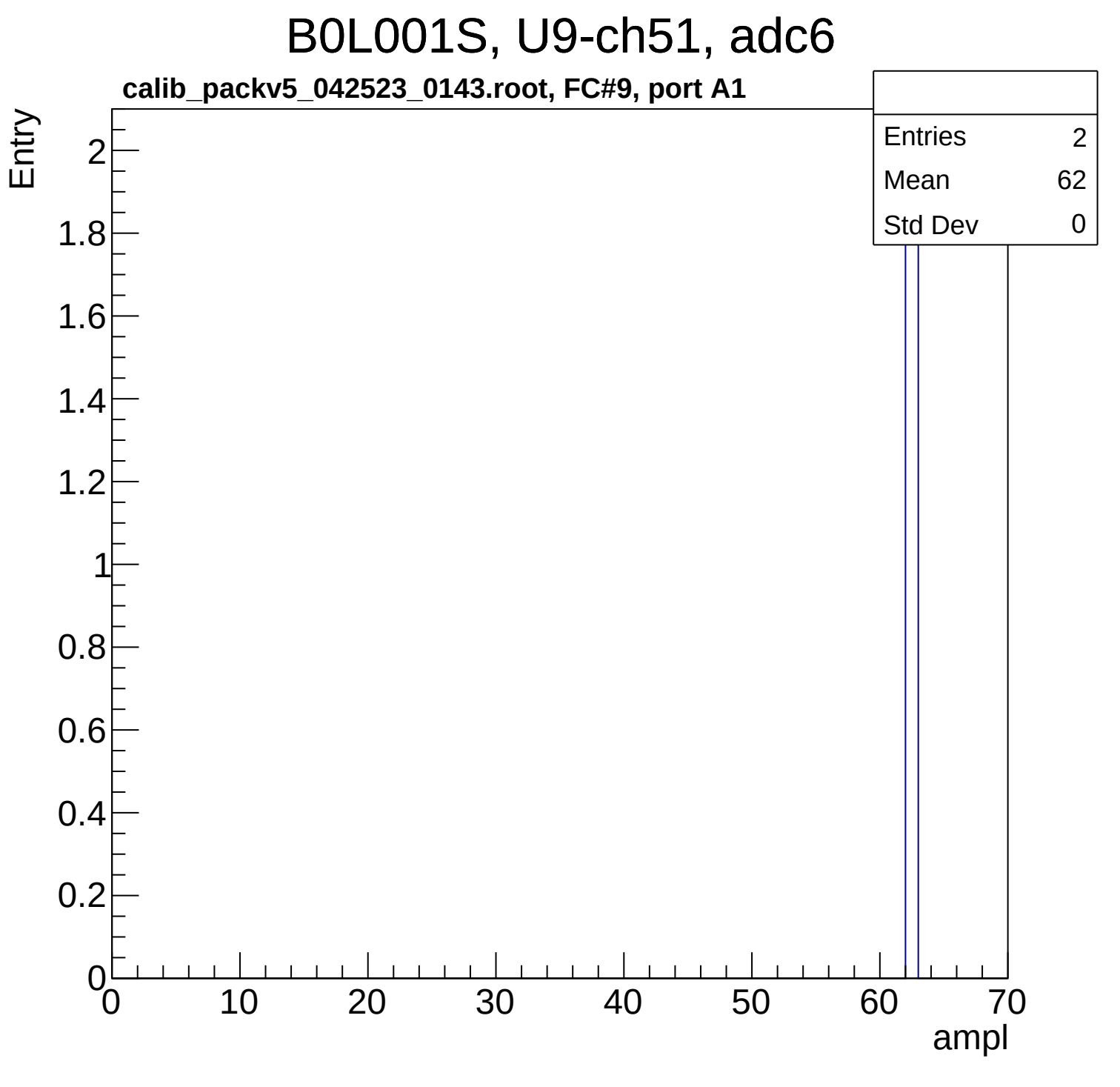
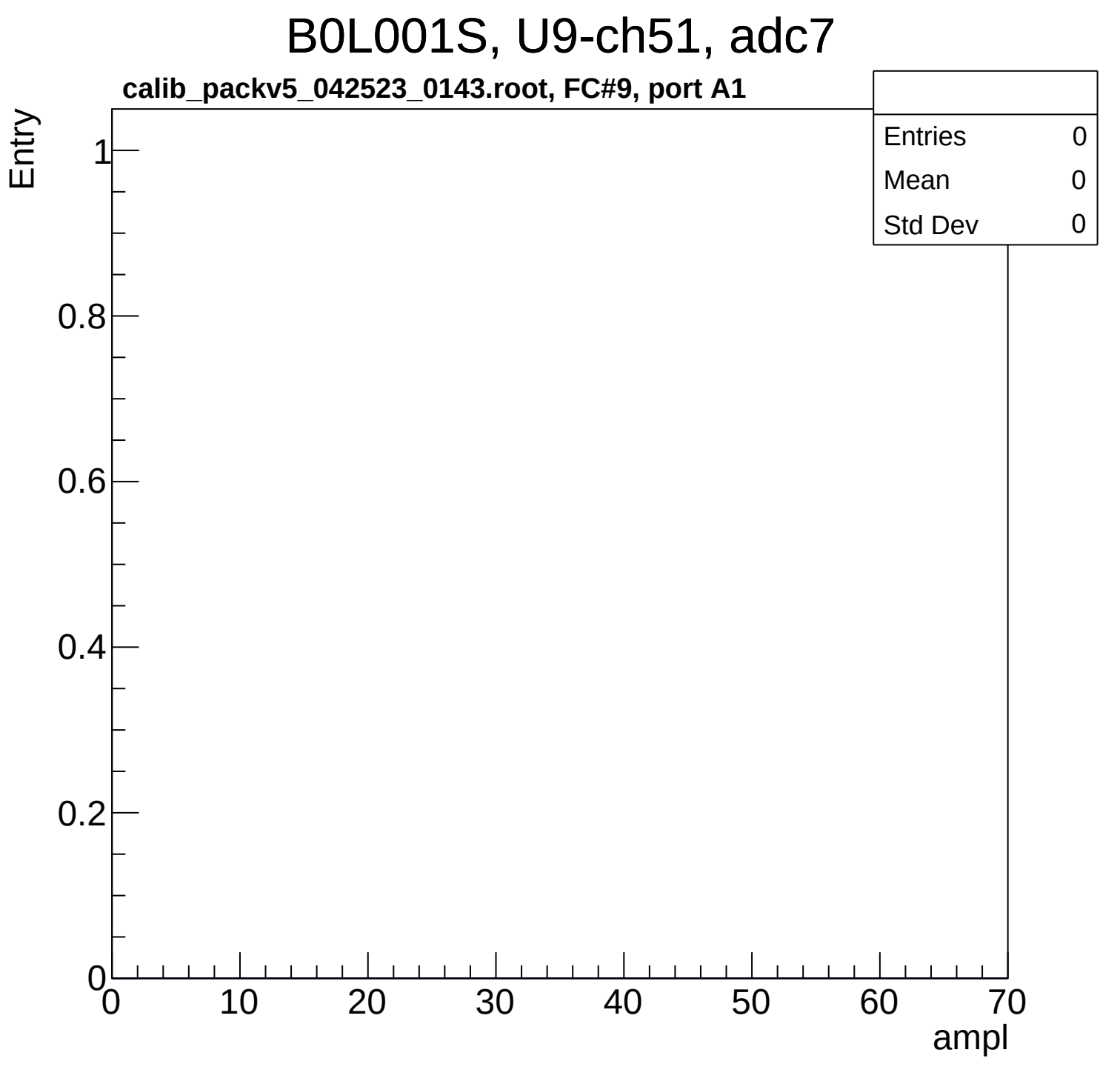
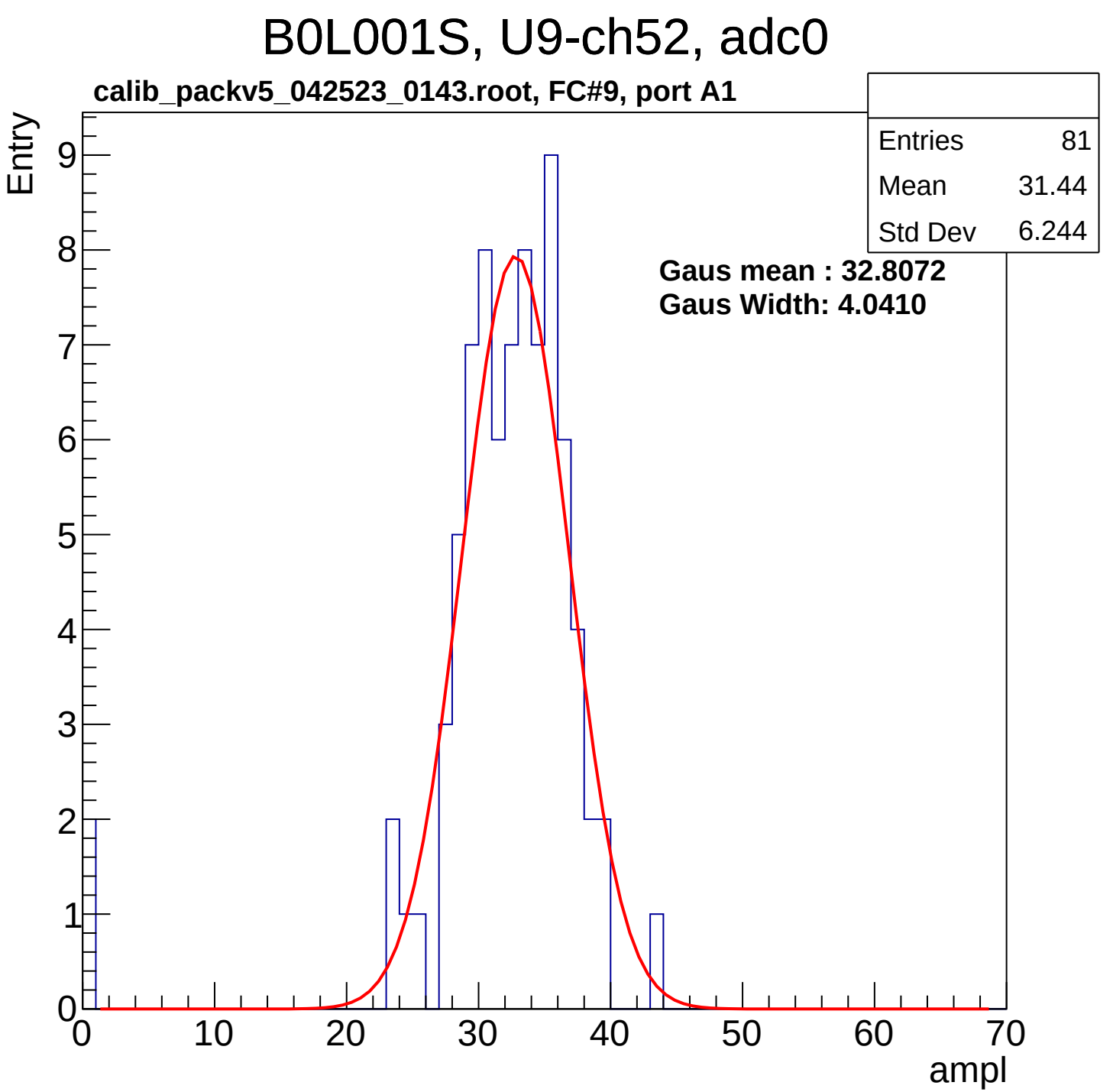

B0L001S, U9-ch51, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 51.08
Std Dev 3.487
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

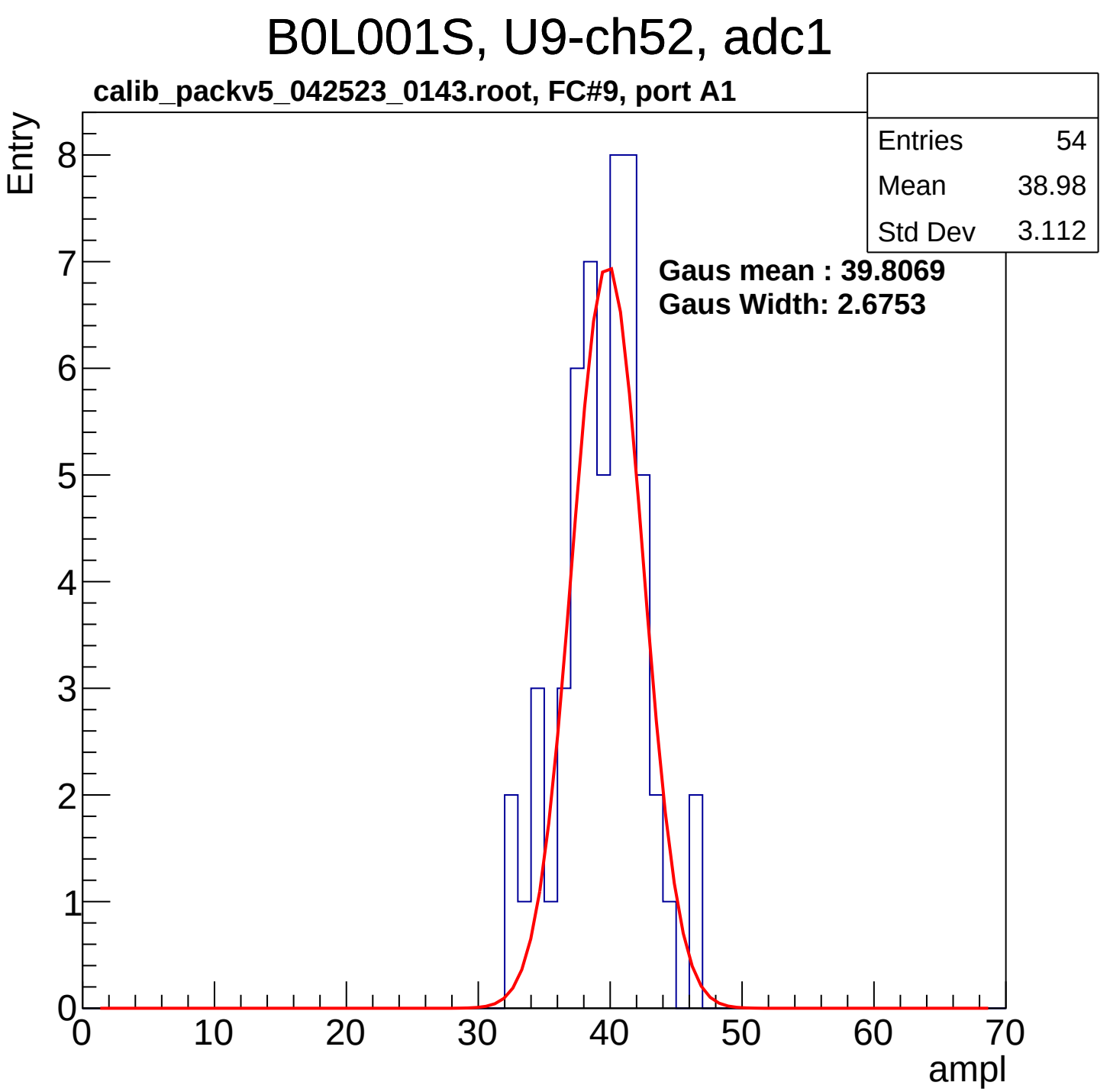

B0L001S, U9-ch51, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 55.85
Std Dev 8.142
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch51, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 60.77
Std Dev 1.736
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch51, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch51, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch52, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 31.44
Std Dev 6.244
Gaus mean : 32.8072
Gaus Width: 4.0410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch52, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 38.98
Std Dev 3.112
Gaus mean : 39.8069
Gaus Width: 2.6753
Entry
ampl
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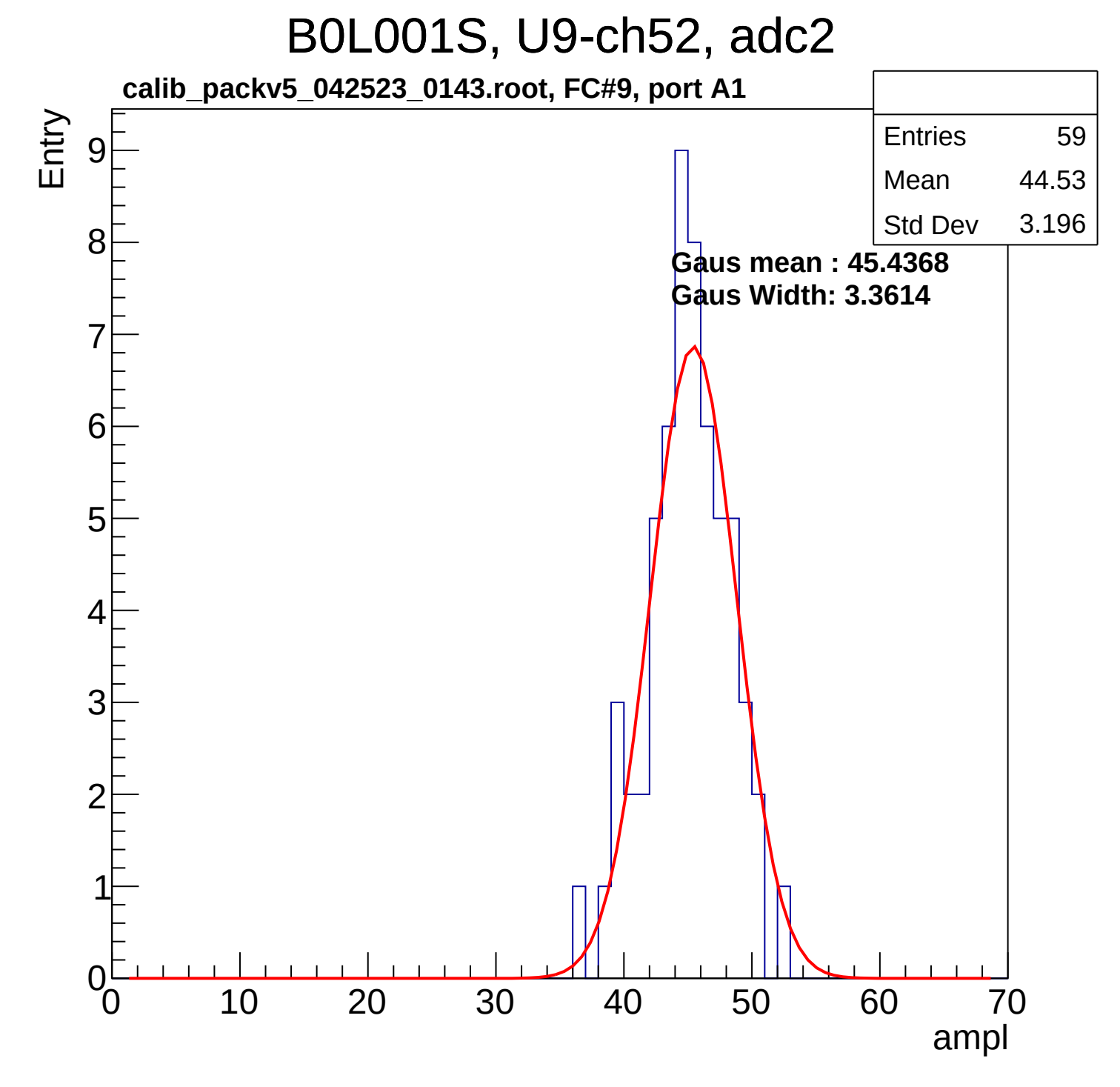
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch52, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.53
Std Dev 3.196
Gaus mean : 45.4368
Gaus Width: 3.3614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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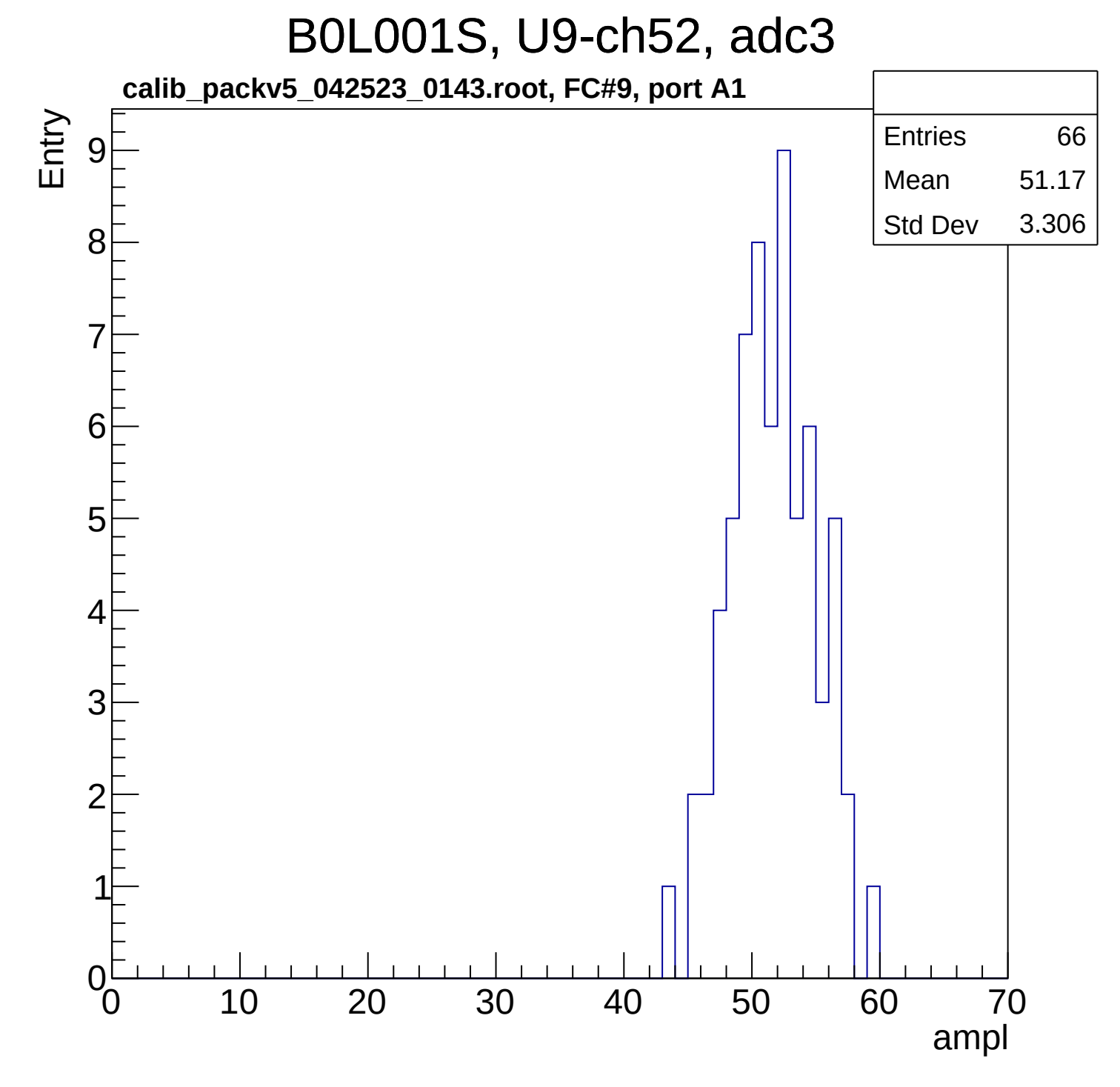
B0L001S, U9-ch52, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.17
Std Dev 3.306
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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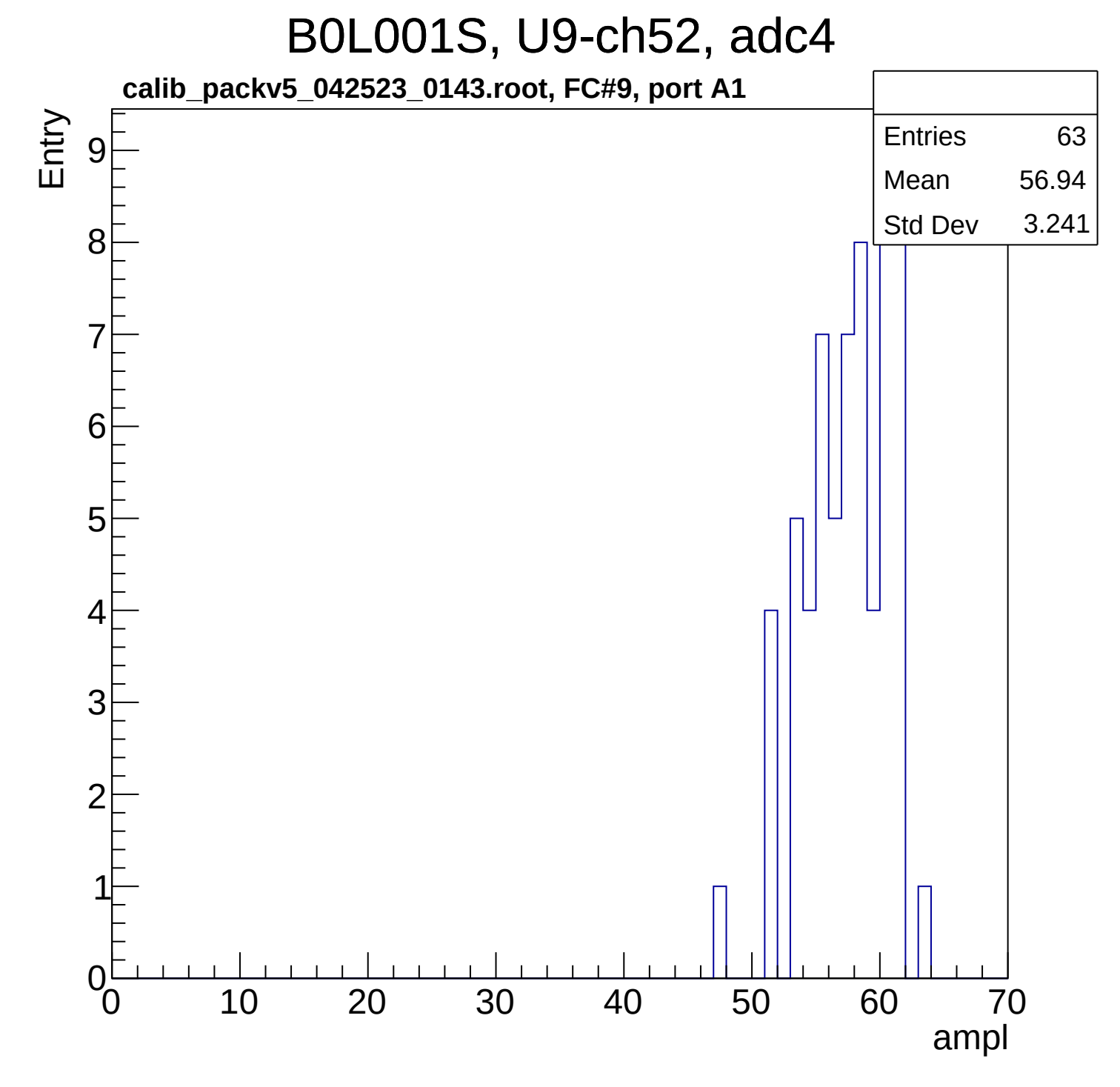
B0L001S, U9-ch52, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.94
Std Dev 3.241
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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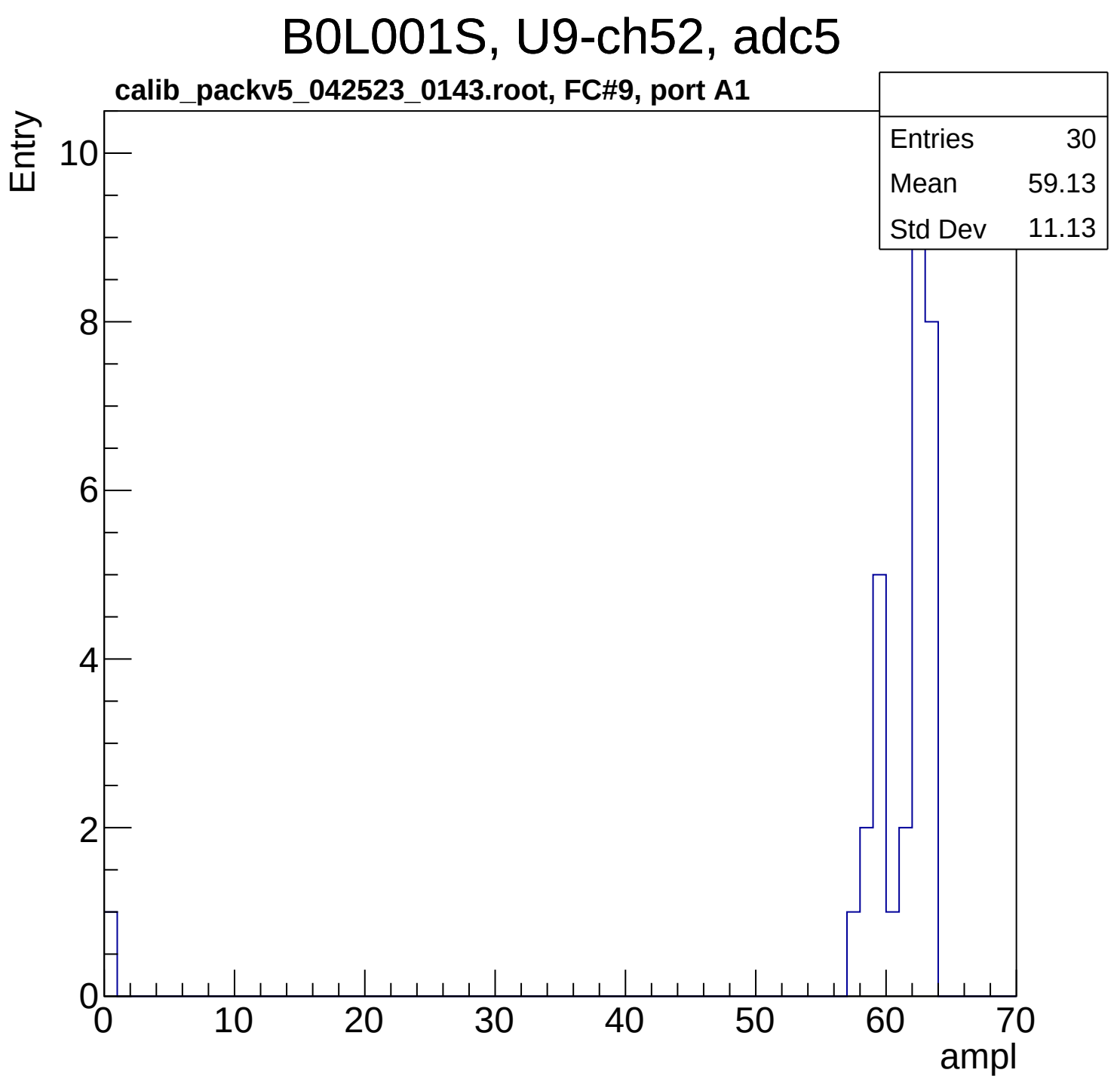
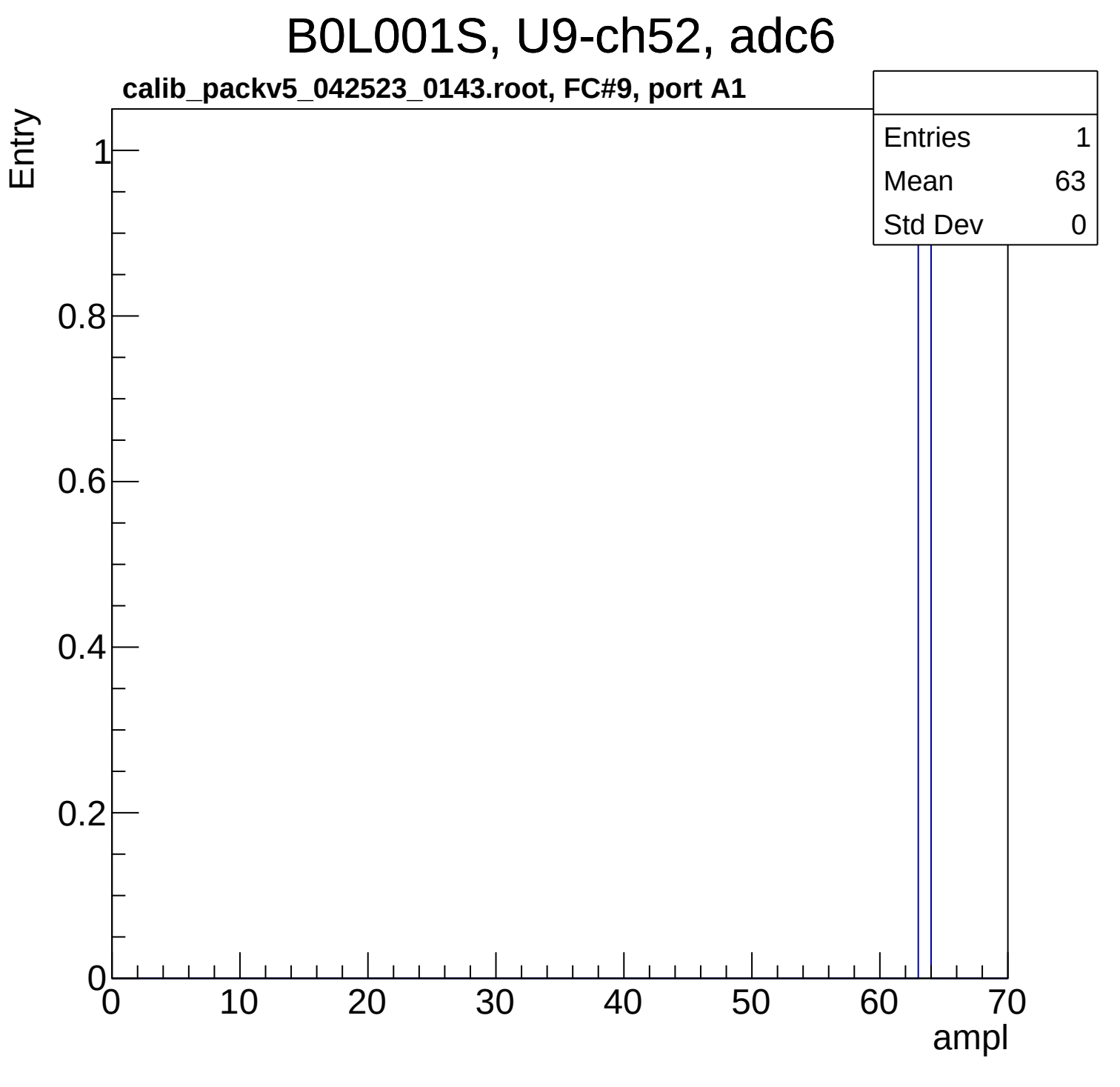

B0L001S, U9-ch52, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 59.13
Std Dev 11.13
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch52, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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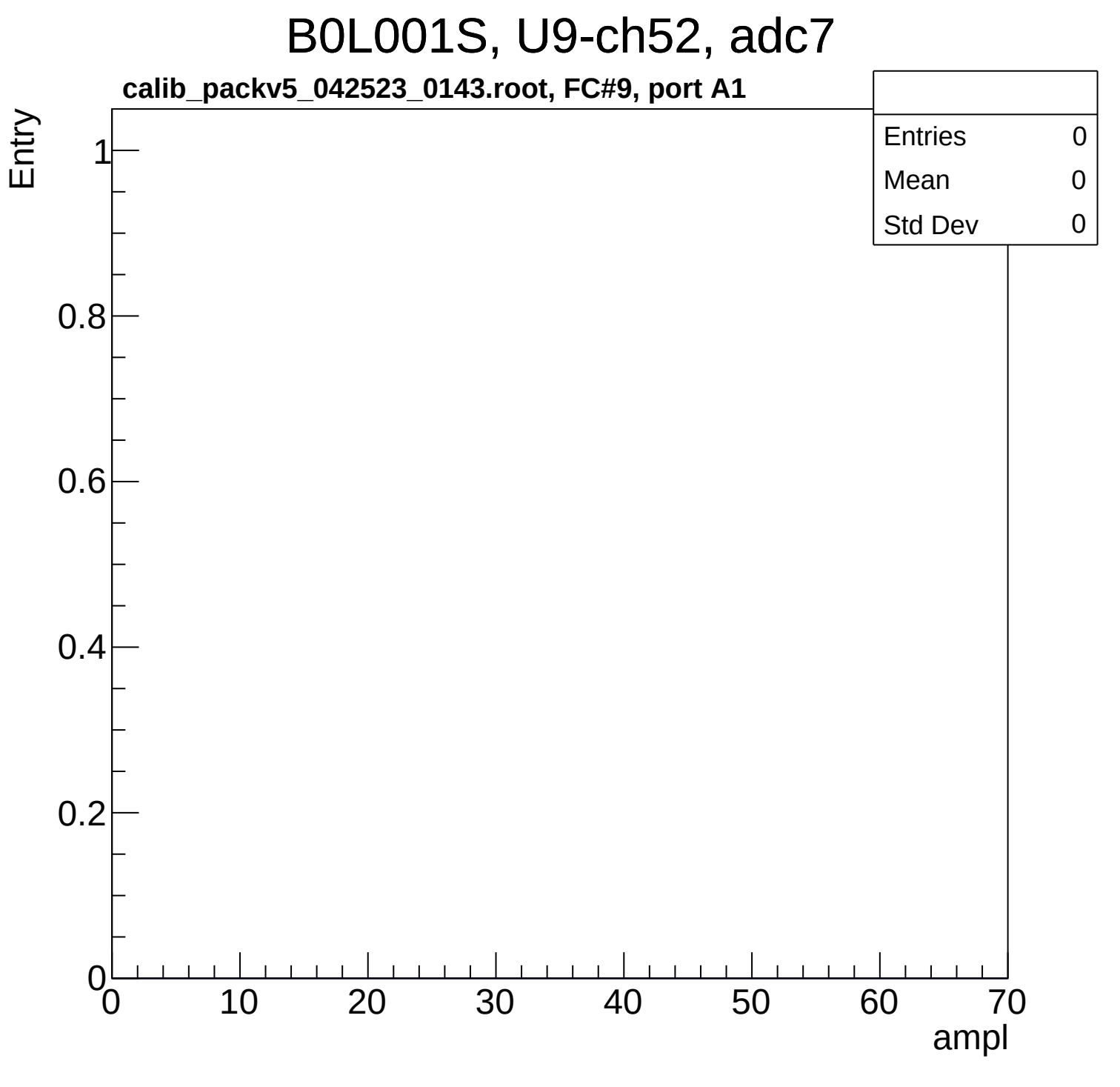
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch52, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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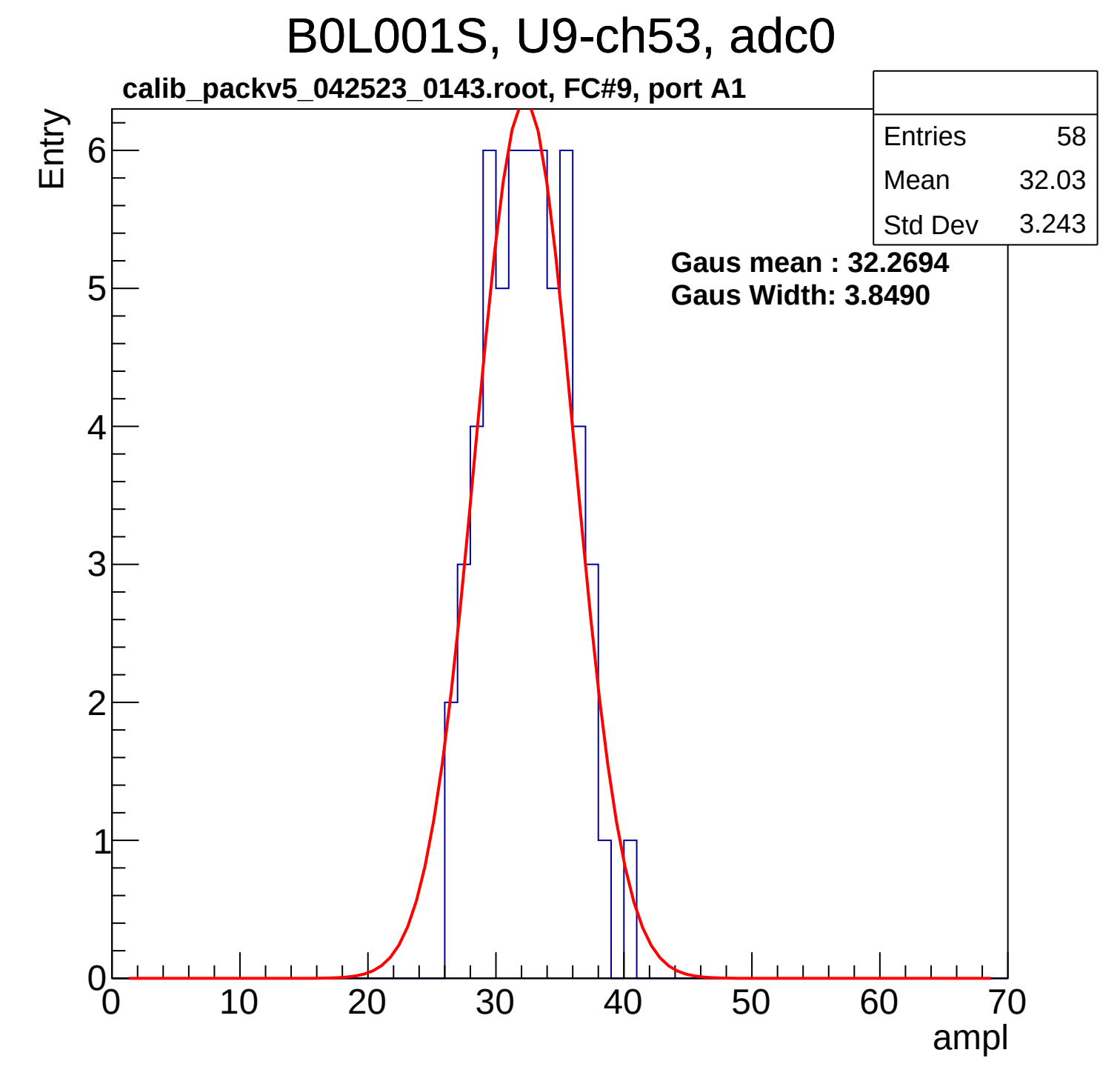
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch53, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 32.03
Std Dev 3.243
Gaus mean : 32.2694
Gaus Width: 3.8490
Entry
ampl
0
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2
3
4
5
6
0
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20
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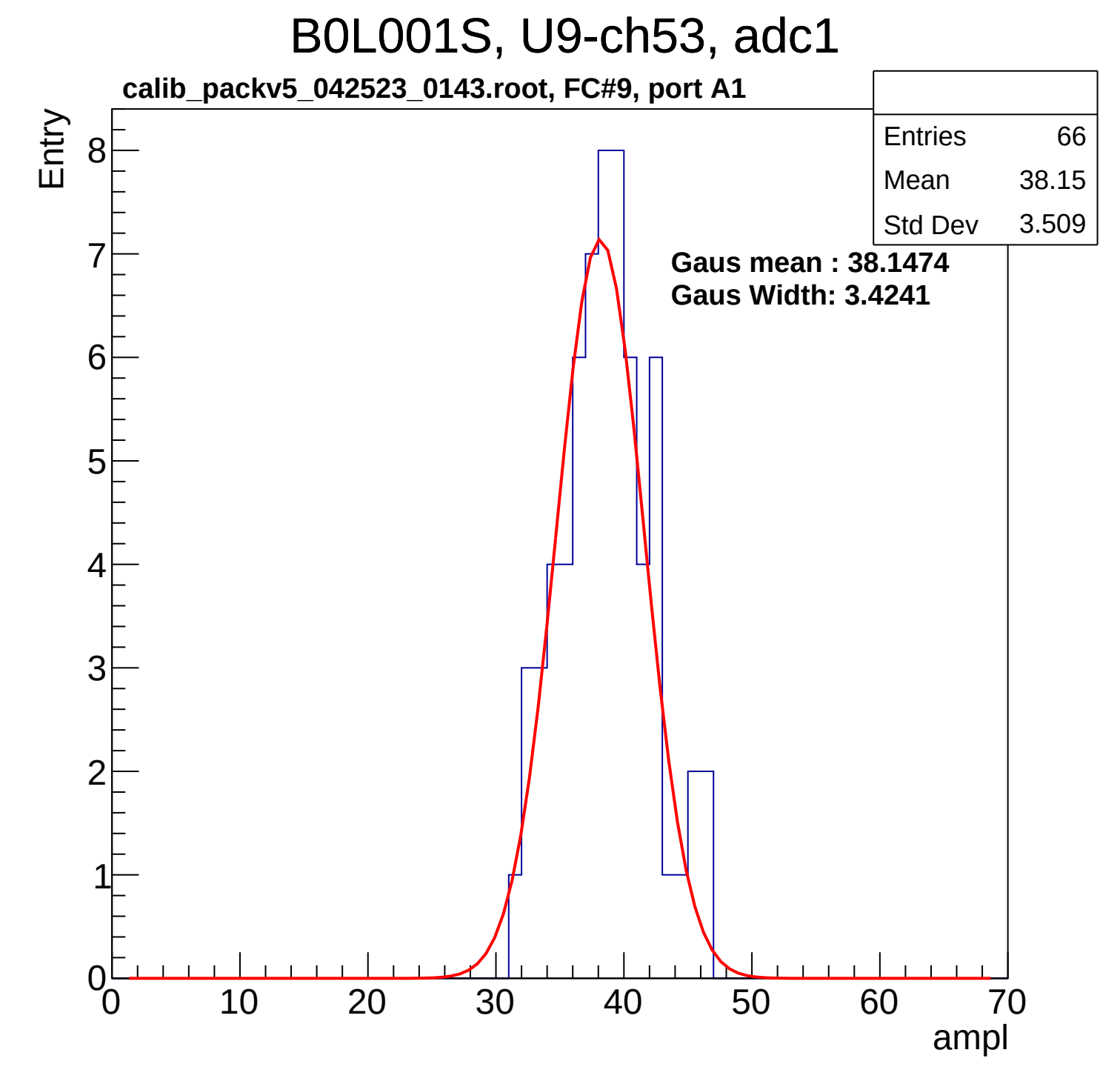
40
50
60
70


B0L001S, U9-ch53, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 38.15
Std Dev 3.509
Gaus mean : 38.1474
Gaus Width: 3.4241
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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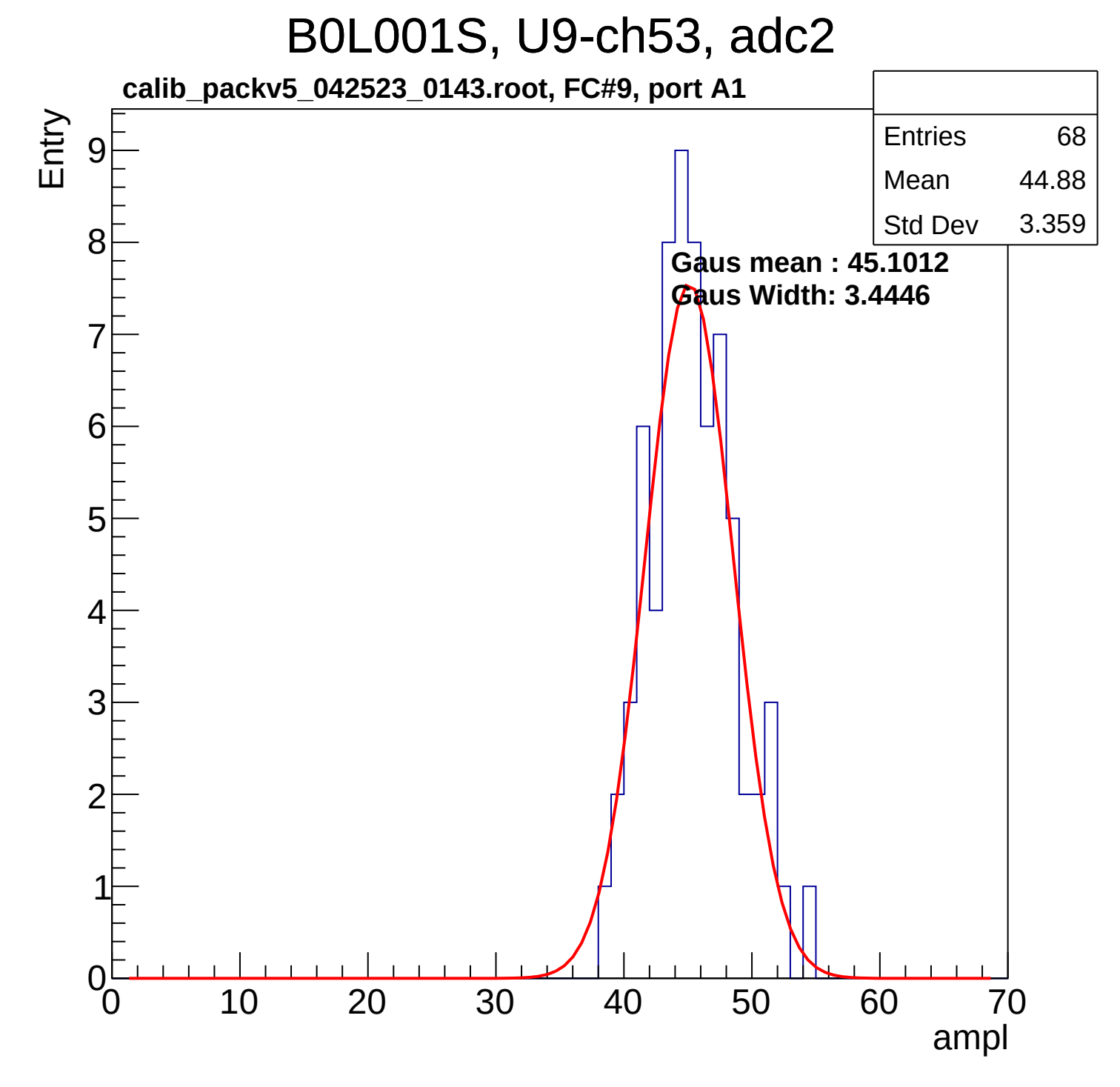
B0L001S, U9-ch53, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.88
Std Dev 3.359
Gaus mean : 45.1012
Gaus Width: 3.4446
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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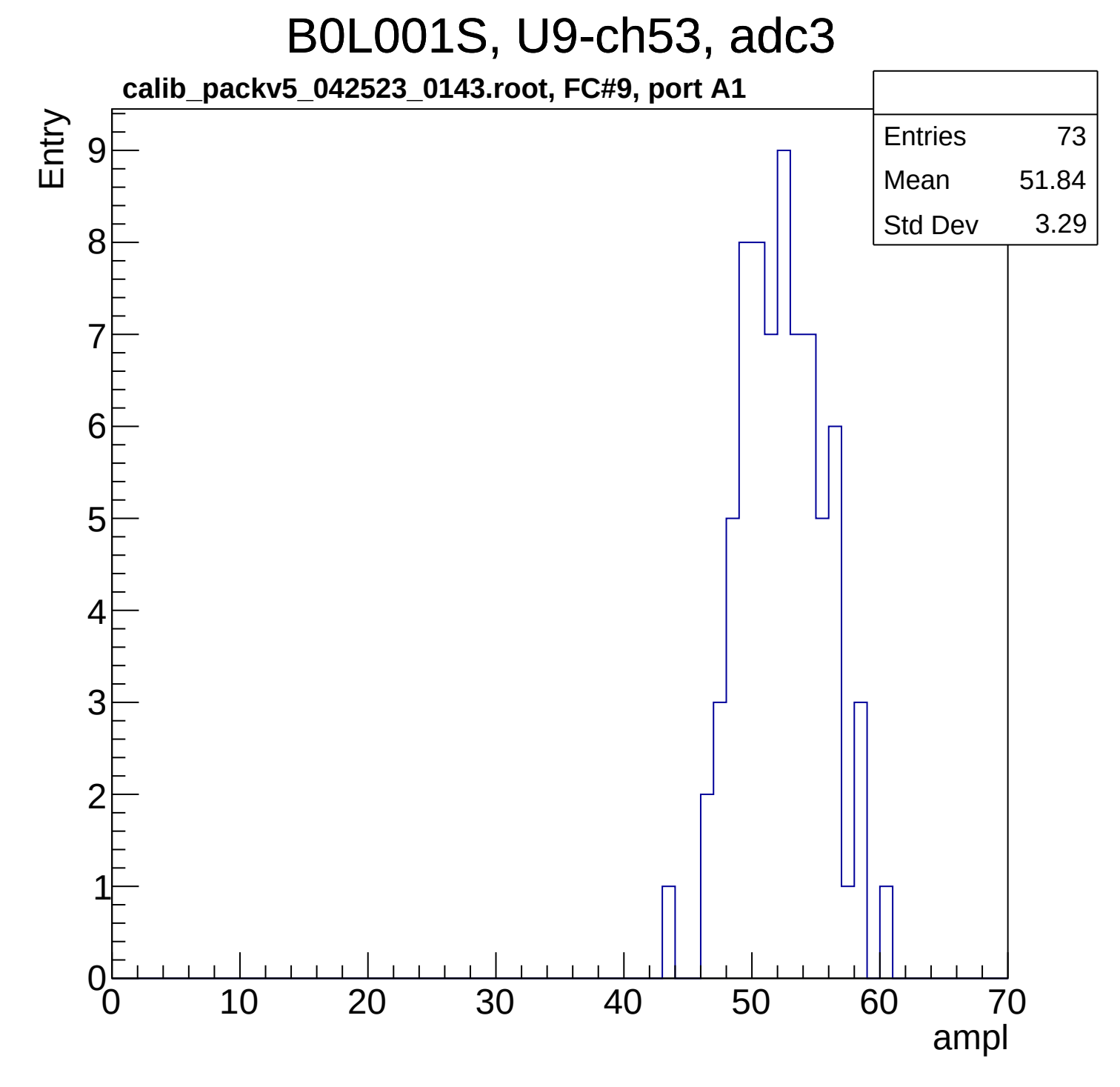
B0L001S, U9-ch53, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 51.84
Std Dev 3.29
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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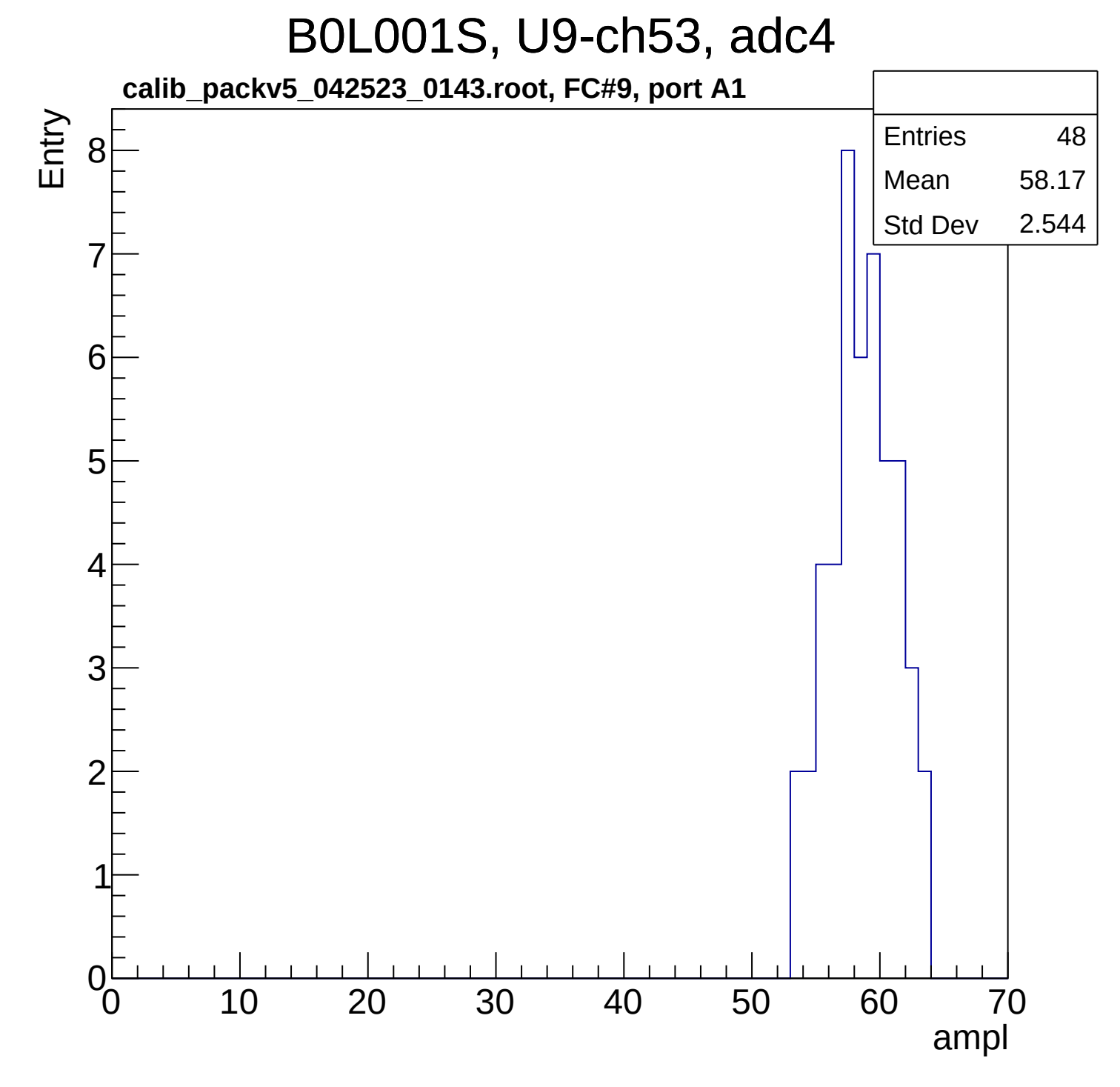
B0L001S, U9-ch53, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.17
Std Dev 2.544
Entry
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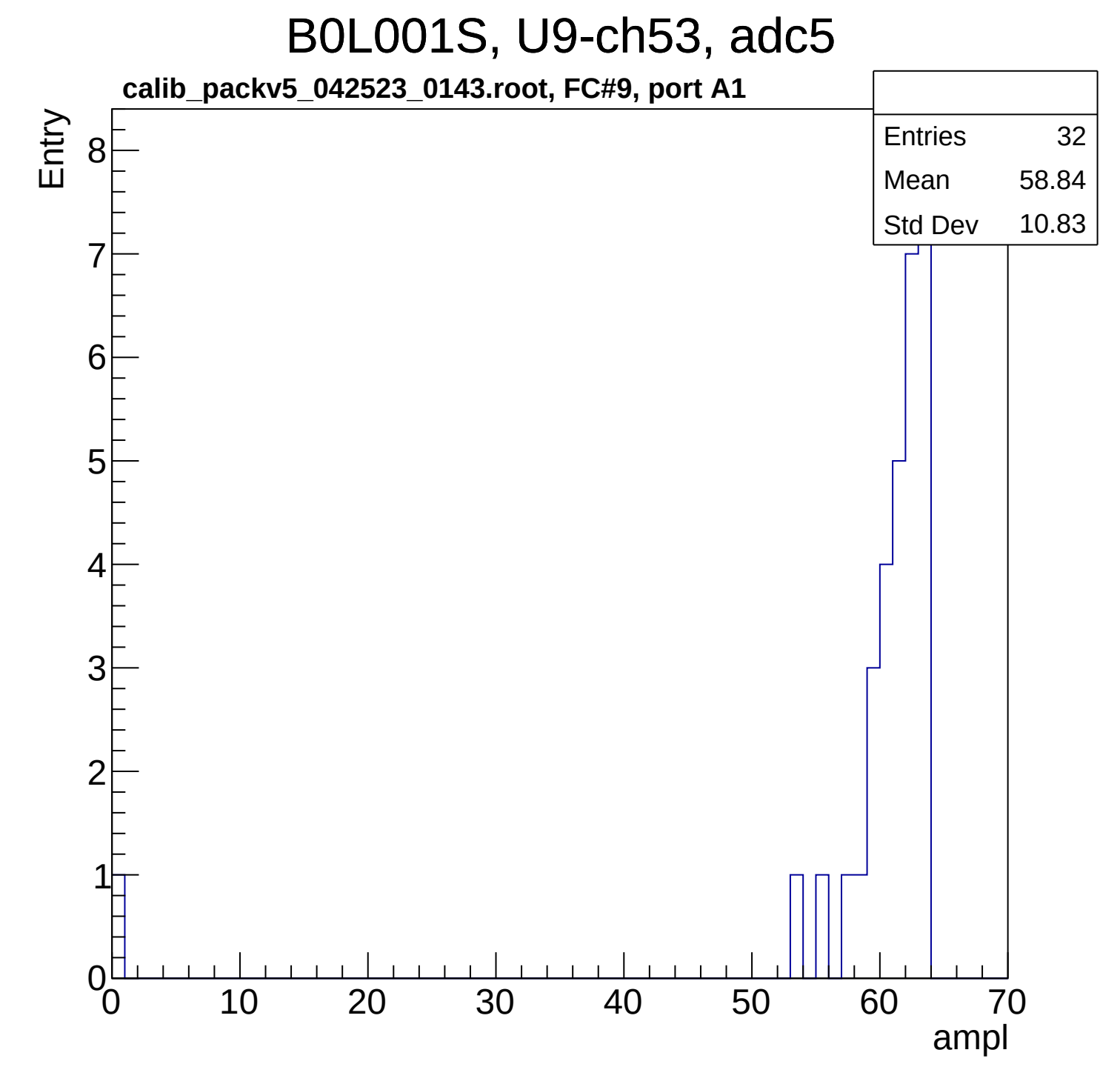
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch53, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.84
Std Dev 10.83
Entry
ampl
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1
2
3
4
5
6
7
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0
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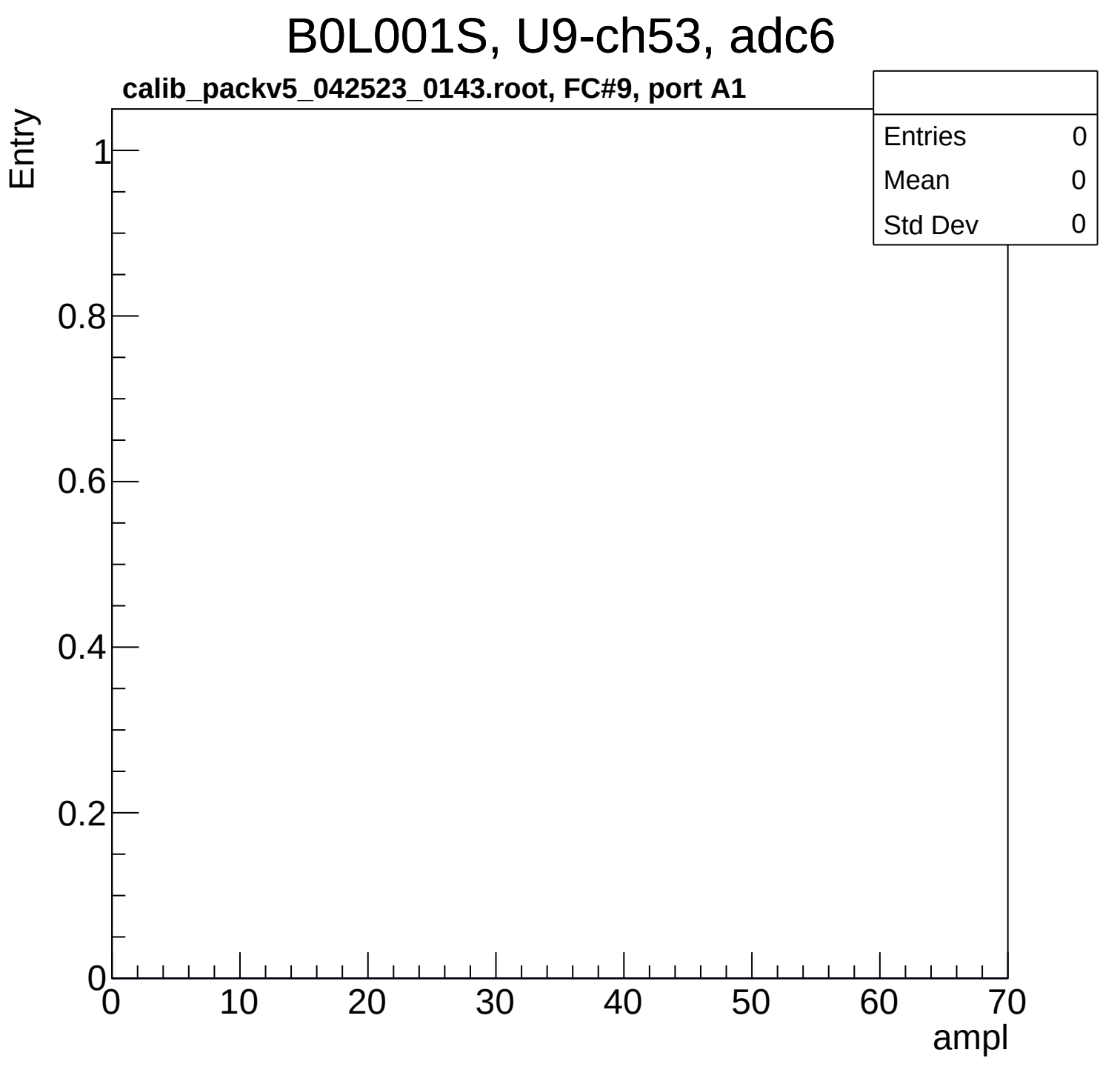
B0L001S, U9-ch53, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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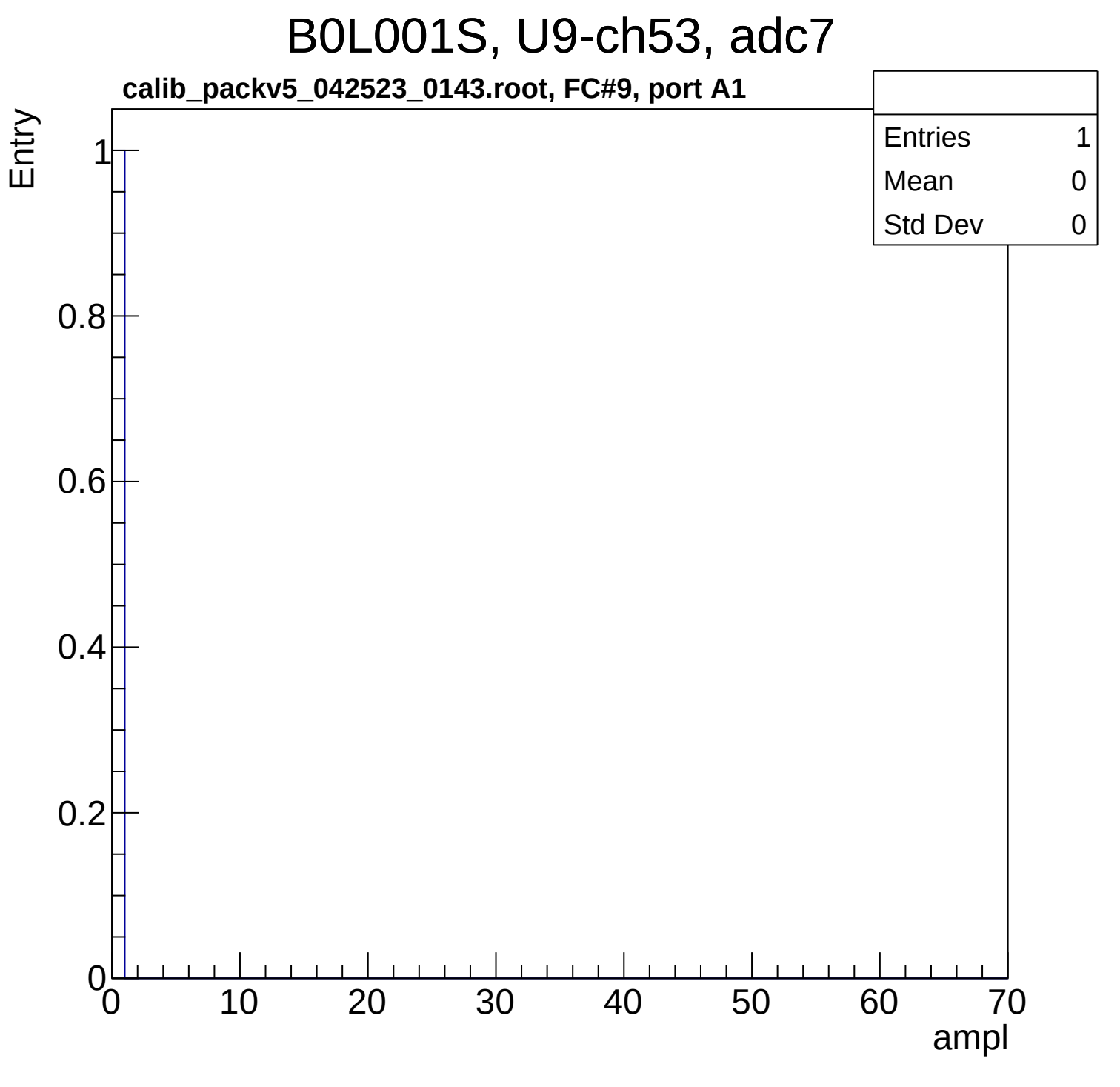
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch53, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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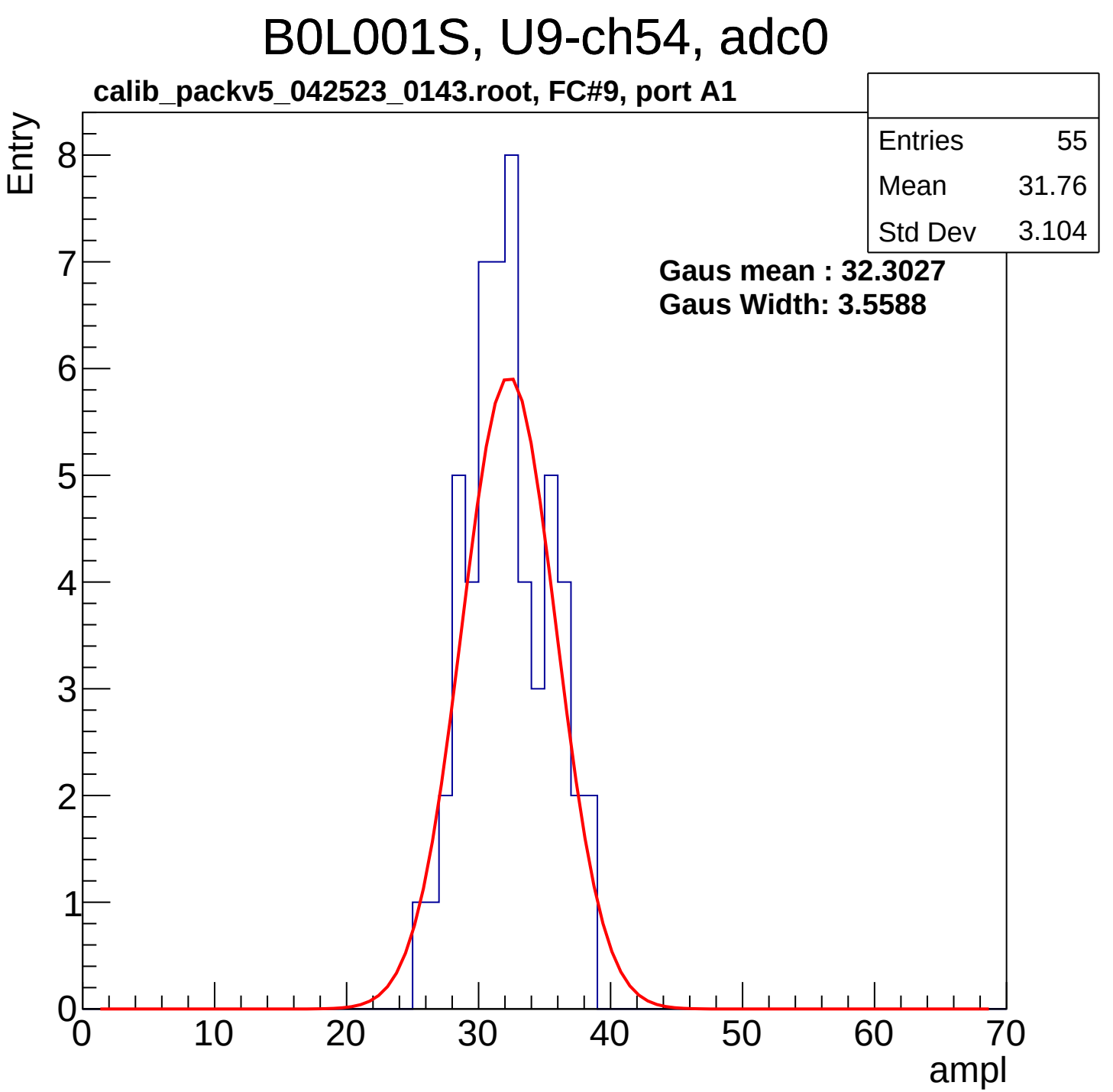
70
ampl


B0L001S, U9-ch54, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 31.76
Std Dev 3.104
Gaus mean : 32.3027
Gaus Width: 3.5588
Entry
ampl
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1
2
3
4
5
6
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0
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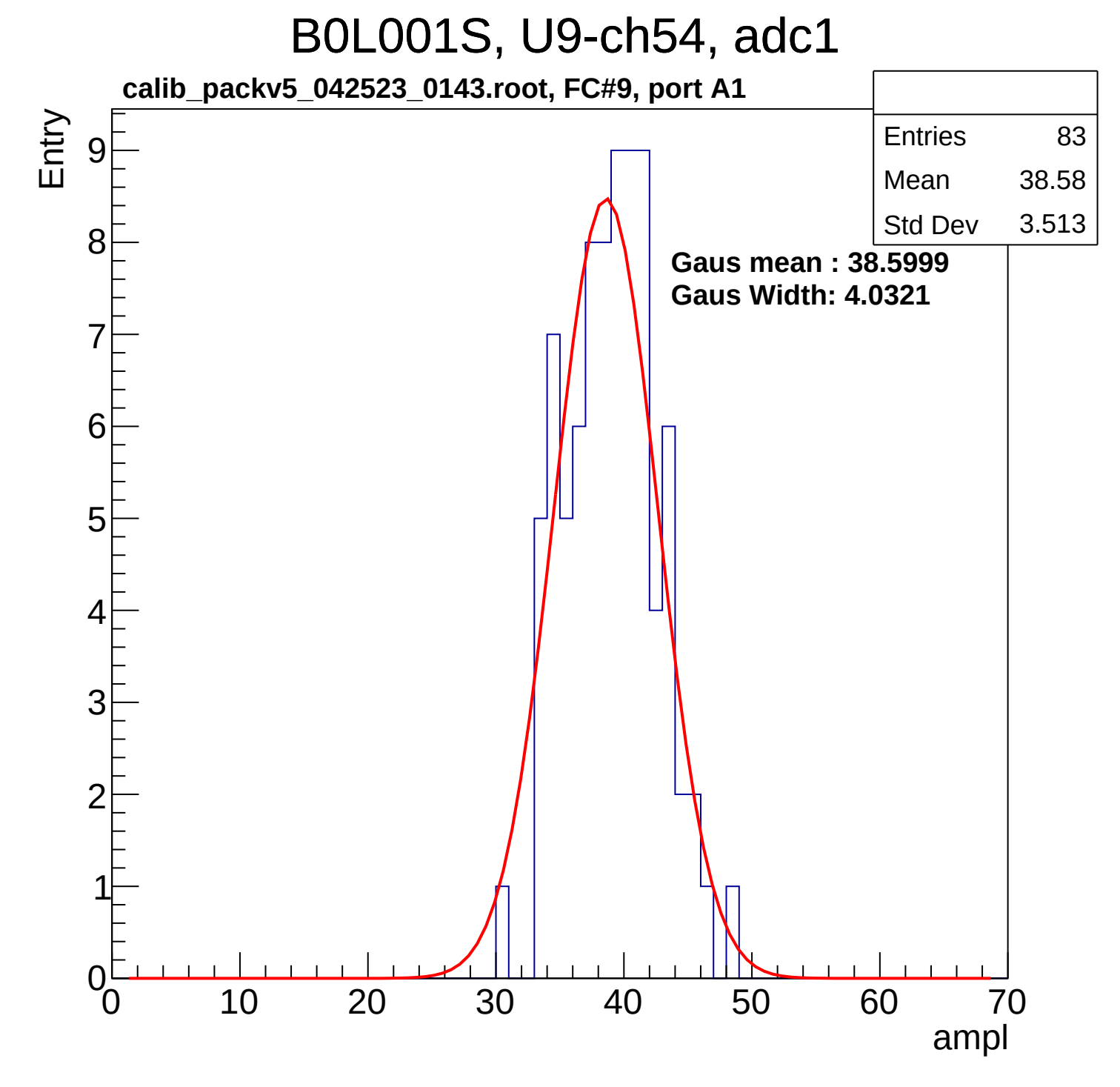
B0L001S, U9-ch54, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 38.58
Std Dev 3.513
Gaus mean : 38.5999
Gaus Width: 4.0321
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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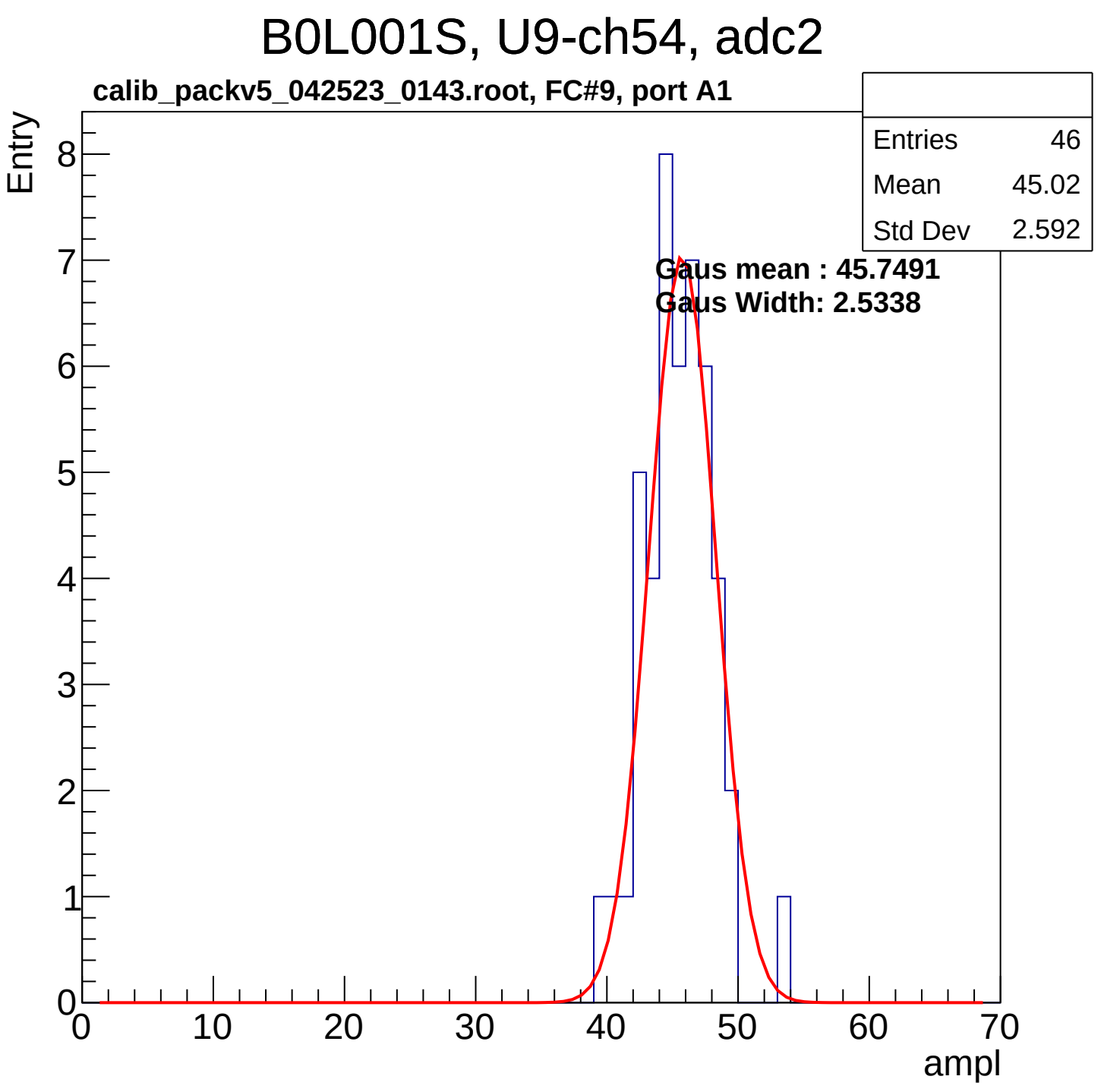
B0L001S, U9-ch54, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 45.02
Std Dev 2.592
Gaus mean : 45.7491
Gaus Width: 2.5338
Entry
ampl
0
1
2
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4
5
6
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8
0
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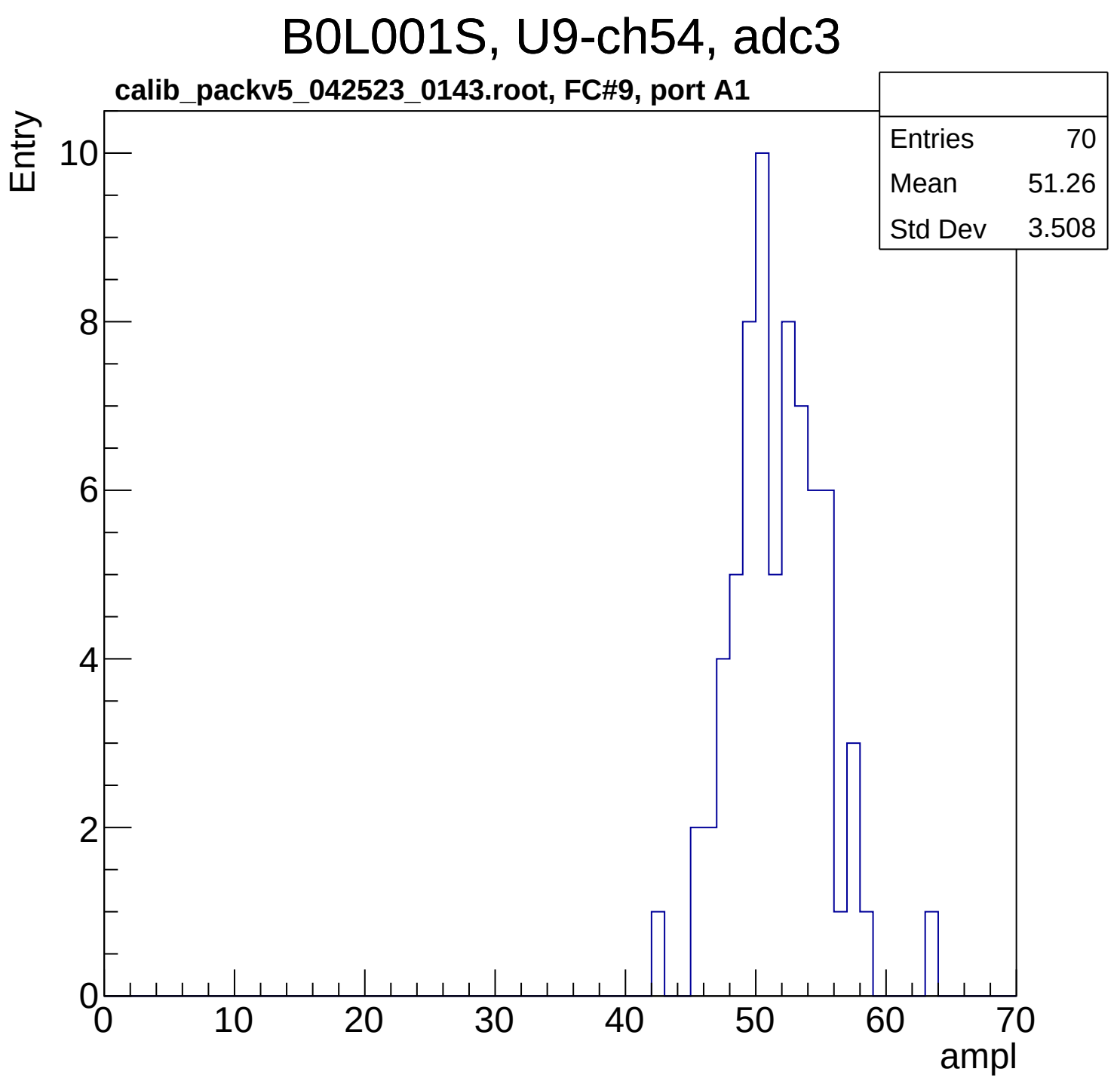
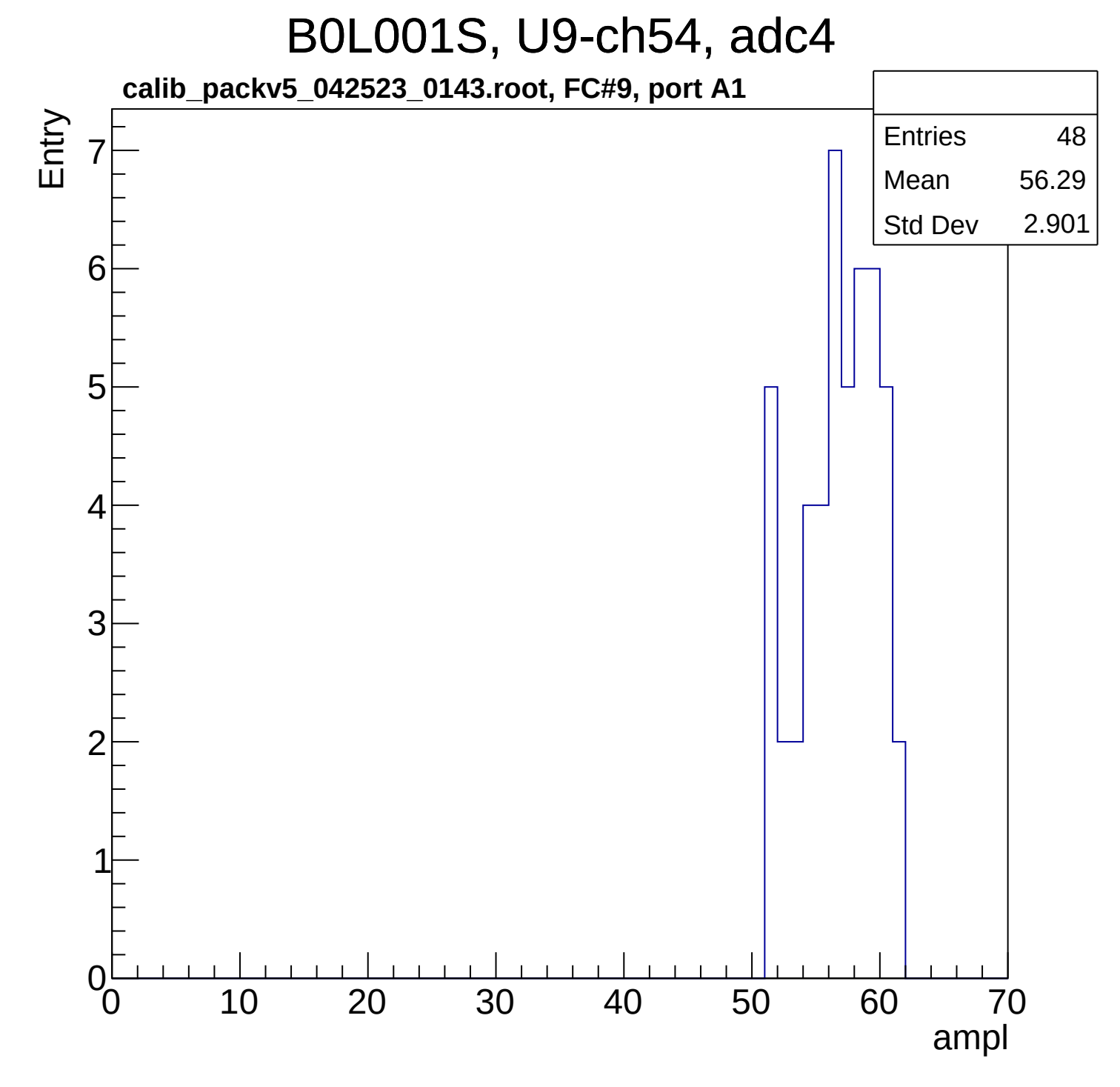

B0L001S, U9-ch54, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 51.26
Std Dev 3.508
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch54, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 56.29
Std Dev 2.901
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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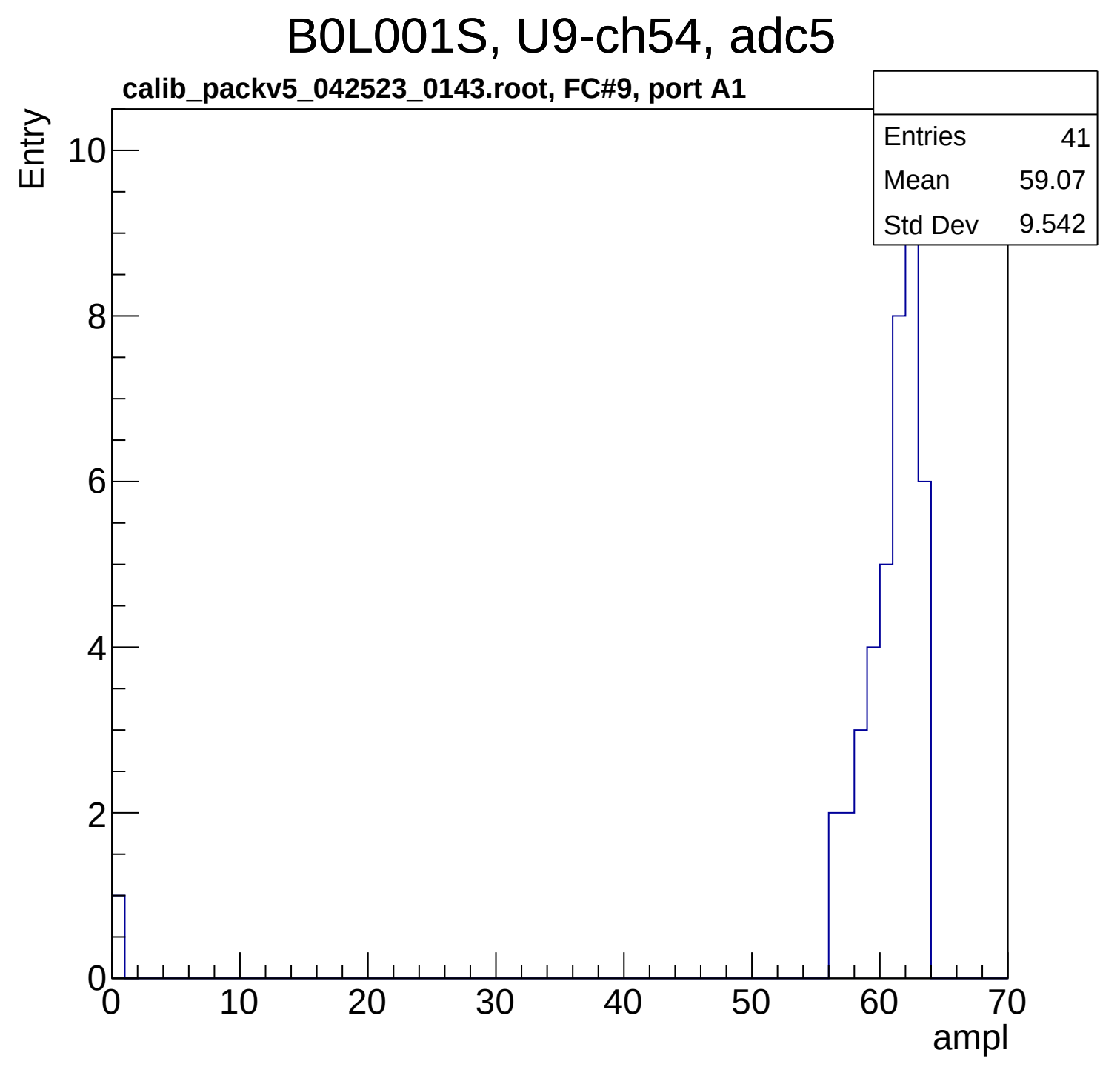
B0L001S, U9-ch54, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 59.07
Std Dev 9.542
Entry
0
2
4
6
8
10
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10
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30
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50
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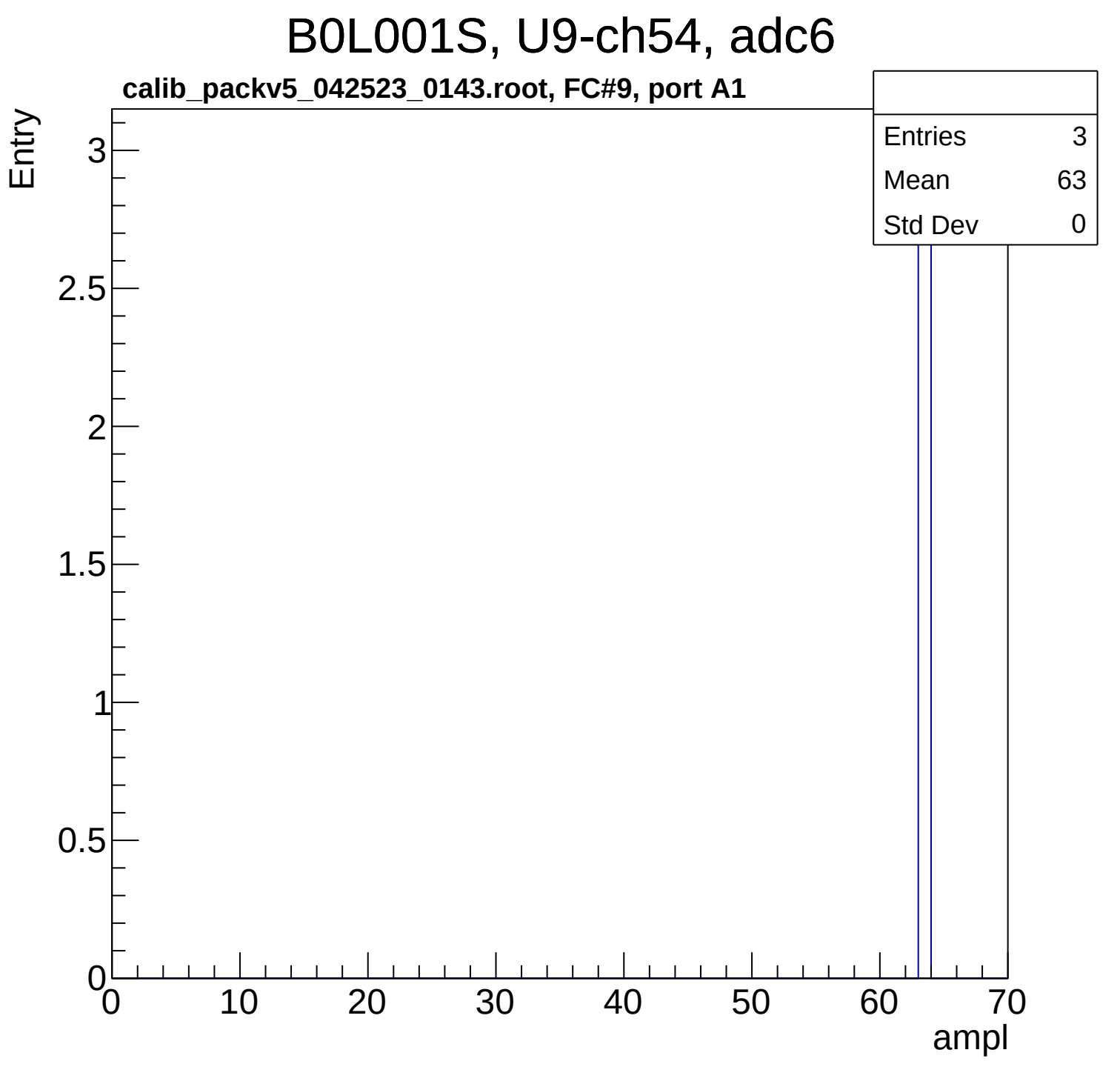
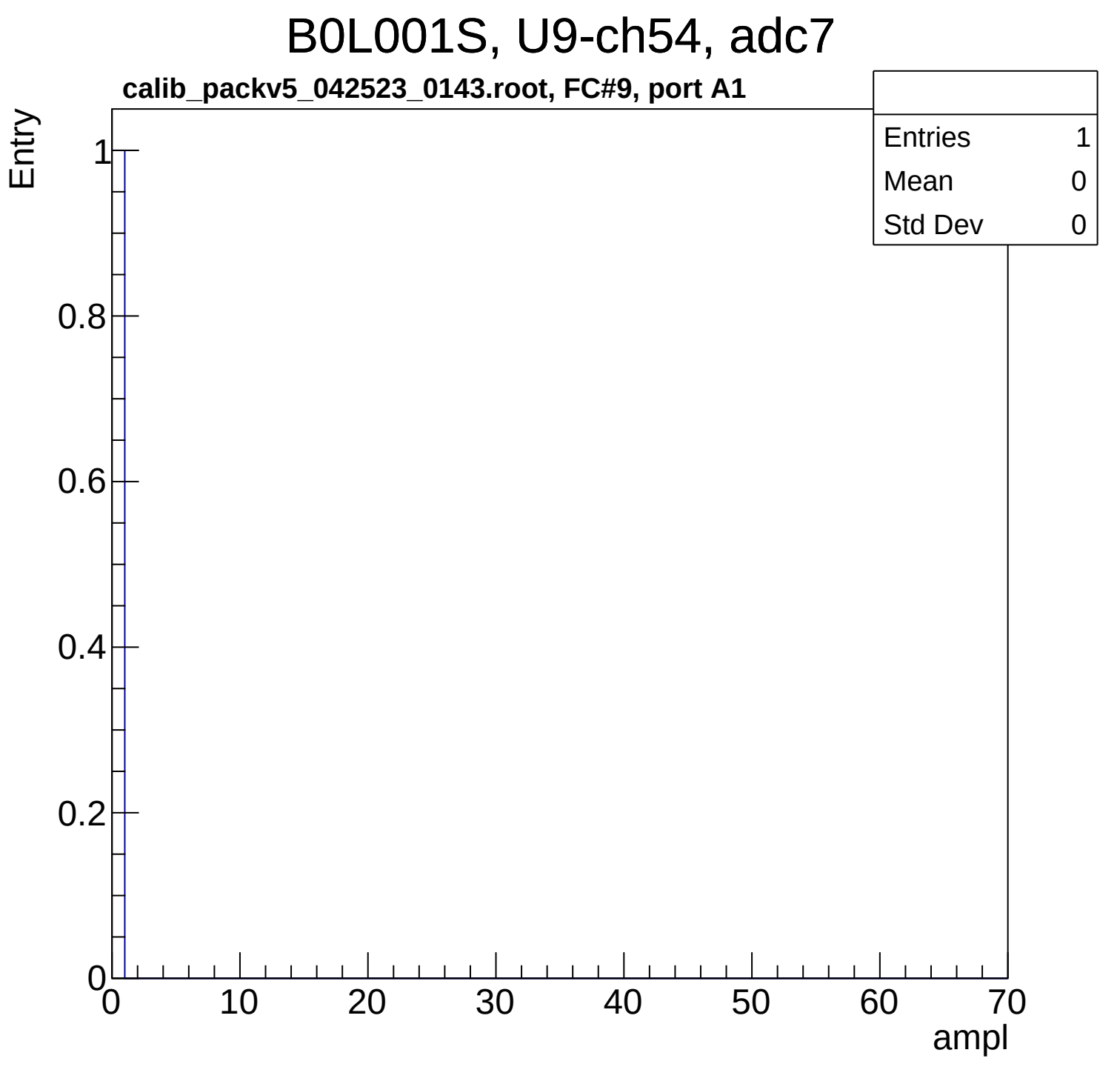
70
ampl


B0L001S, U9-ch54, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch54, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
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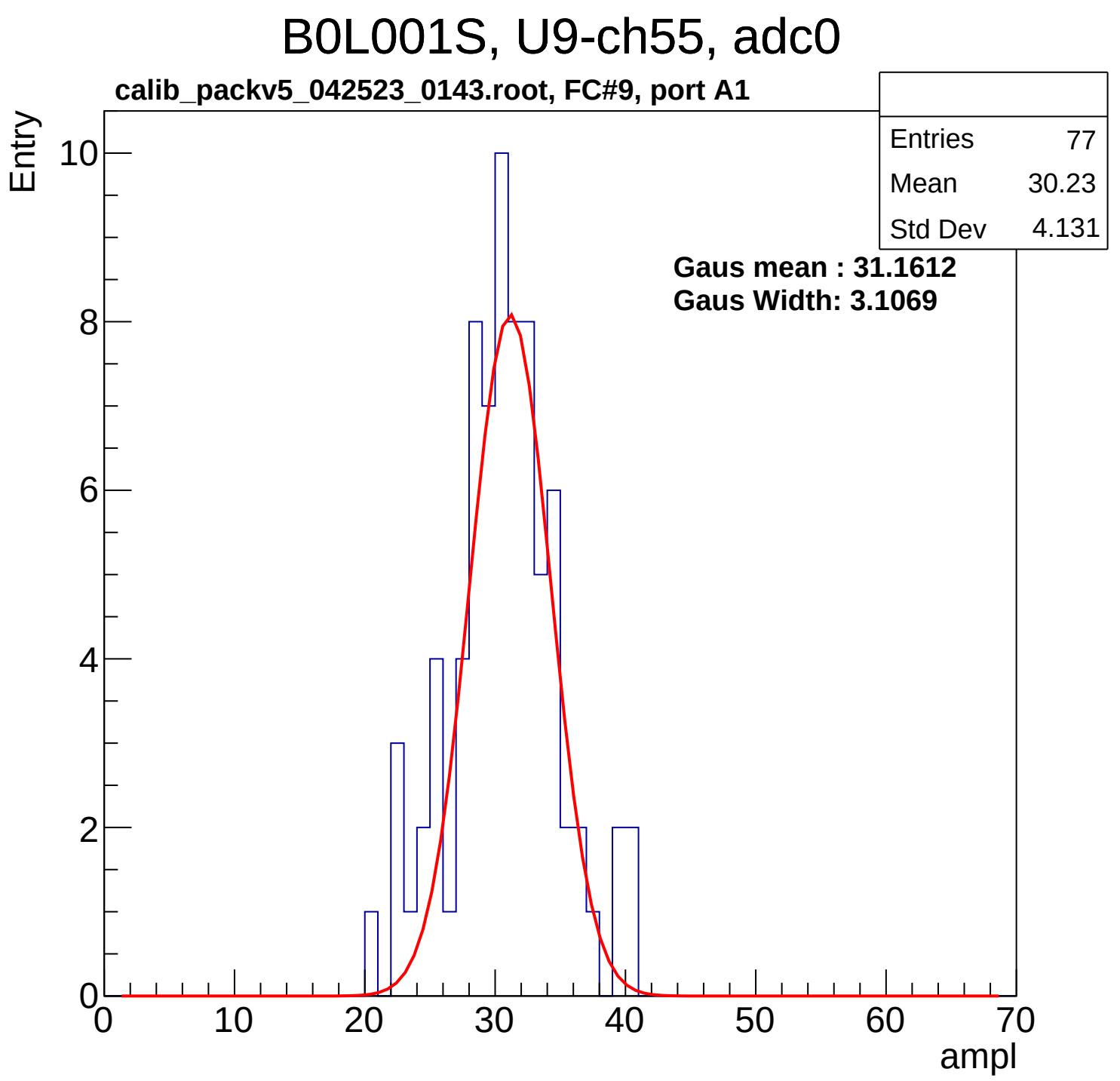
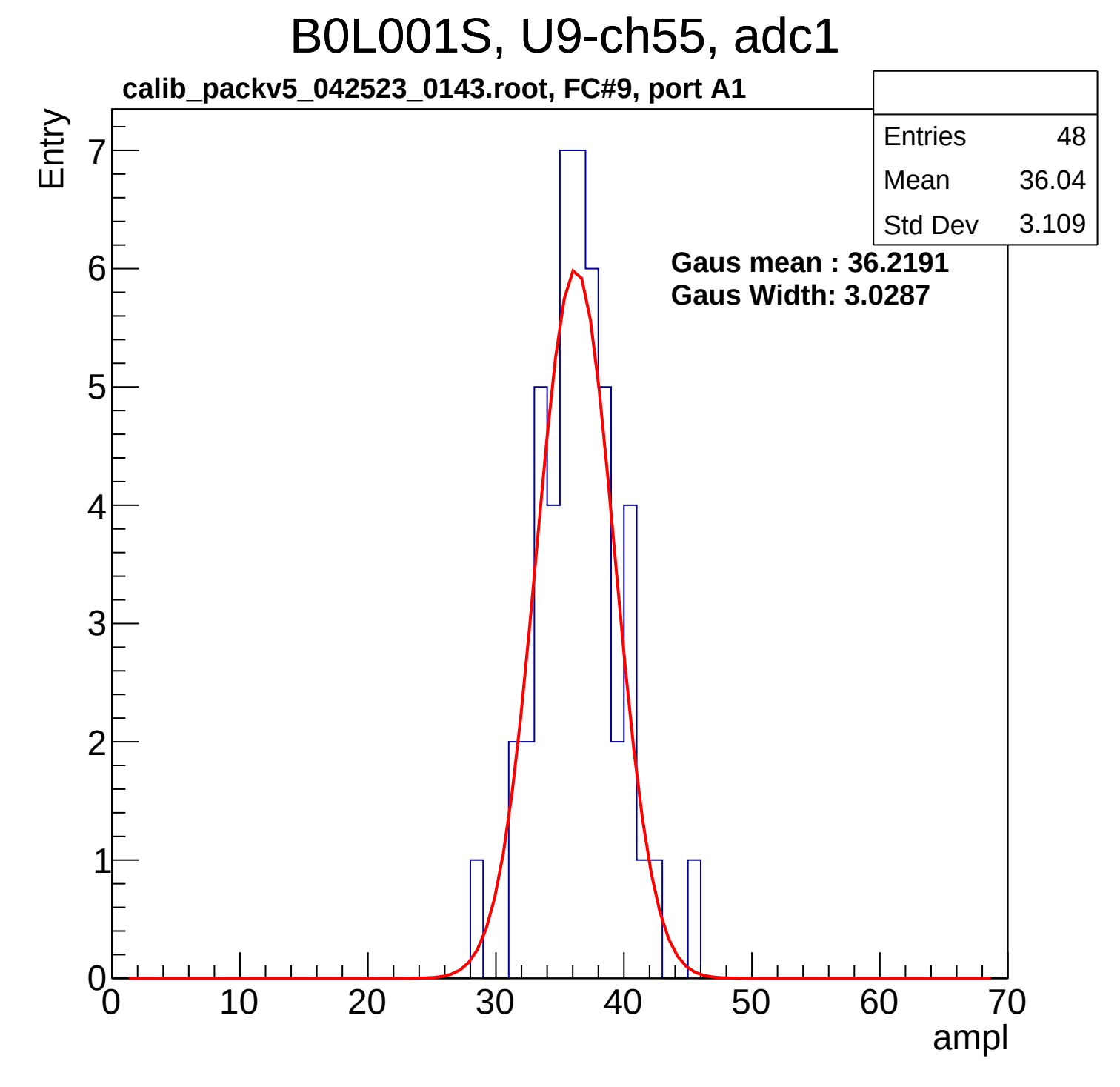
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch55, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 30.23
Std Dev 4.131
Gaus mean : 31.1612
Gaus Width: 3.1069
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch55, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 36.04
Std Dev 3.109
Gaus mean : 36.2191
Gaus Width: 3.0287
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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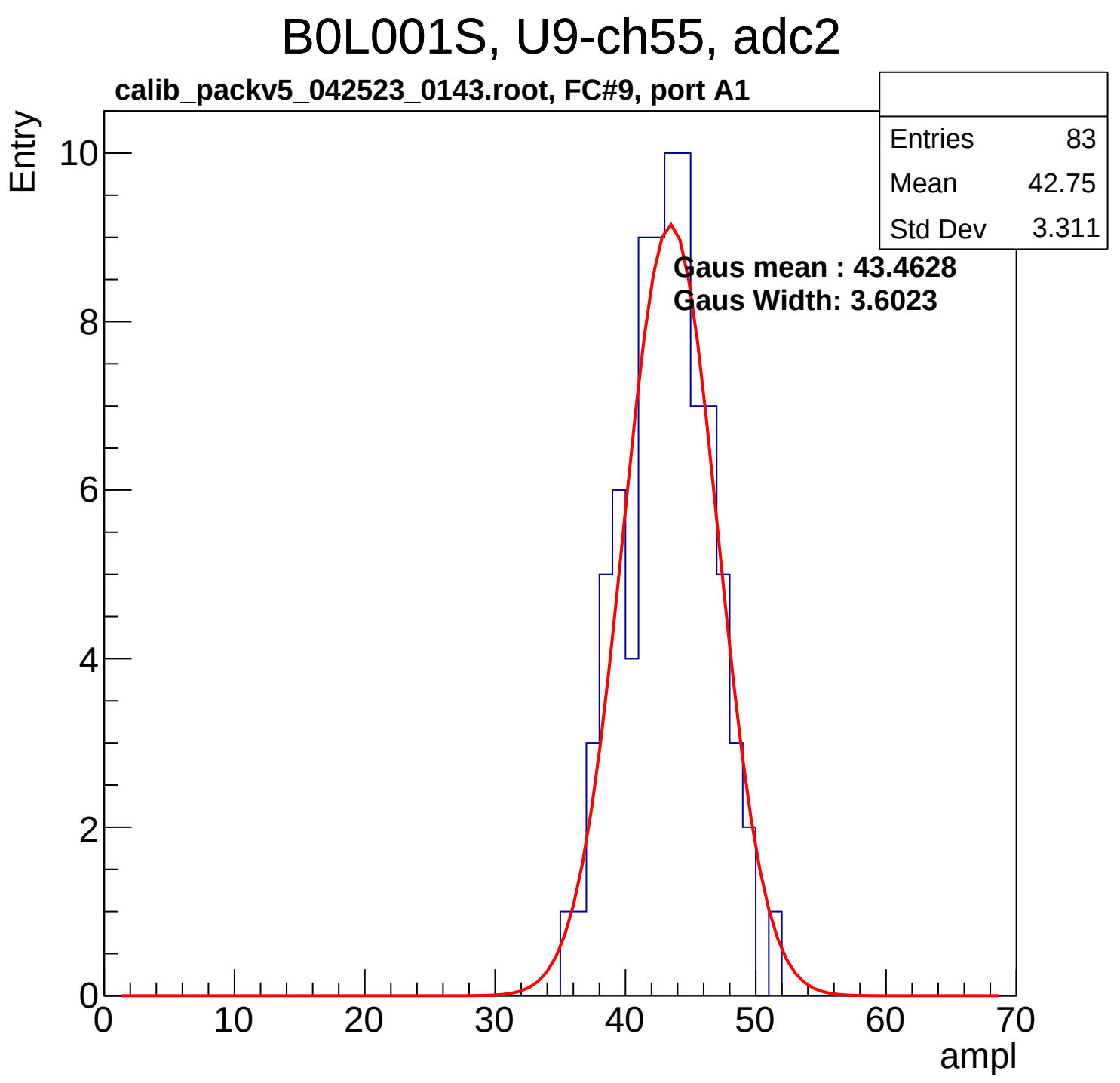
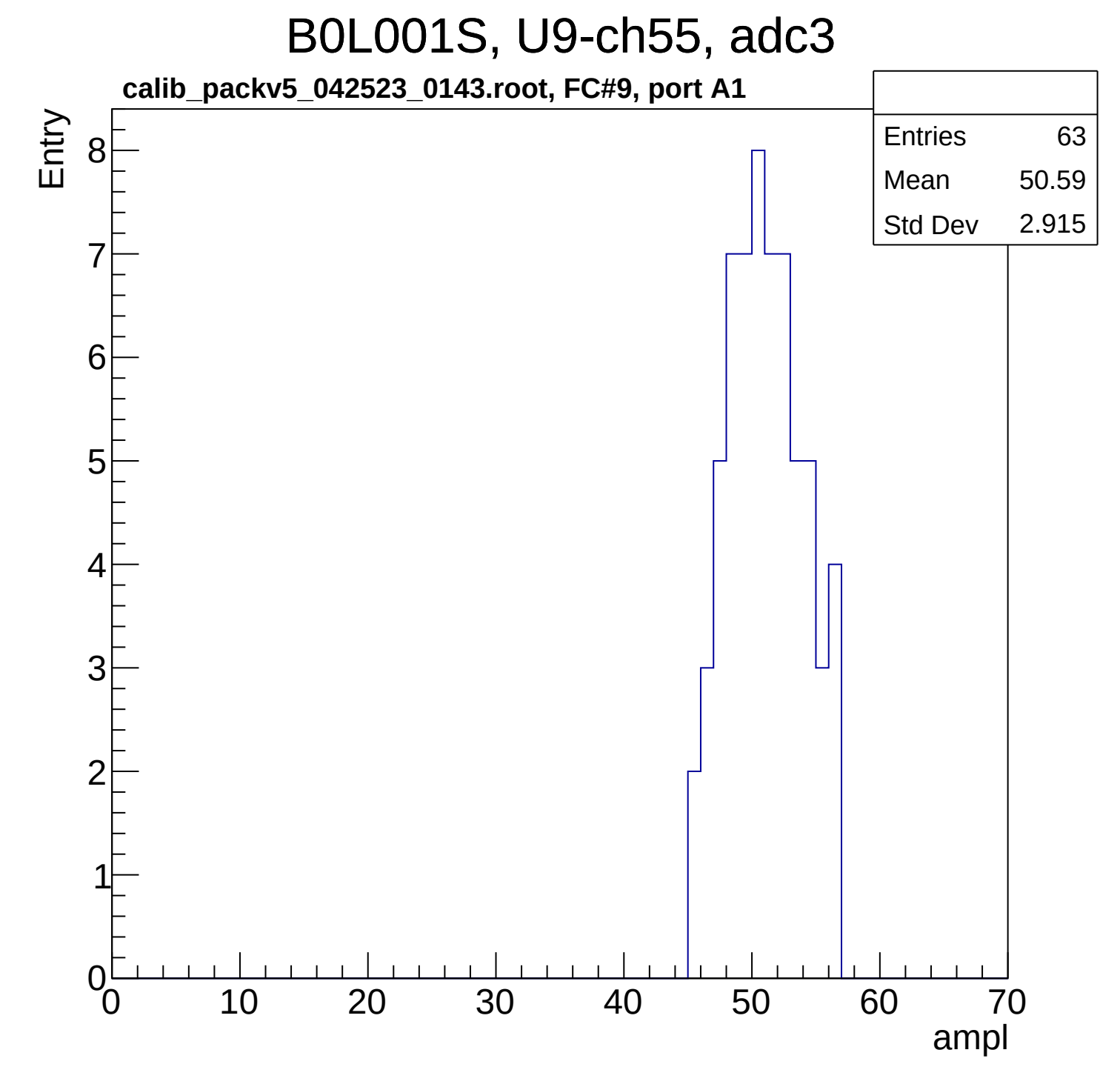

B0L001S, U9-ch55, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 42.75
Std Dev 3.311
Gaus mean : 43.4628
Gaus Width: 3.6023
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch55, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.59
Std Dev 2.915
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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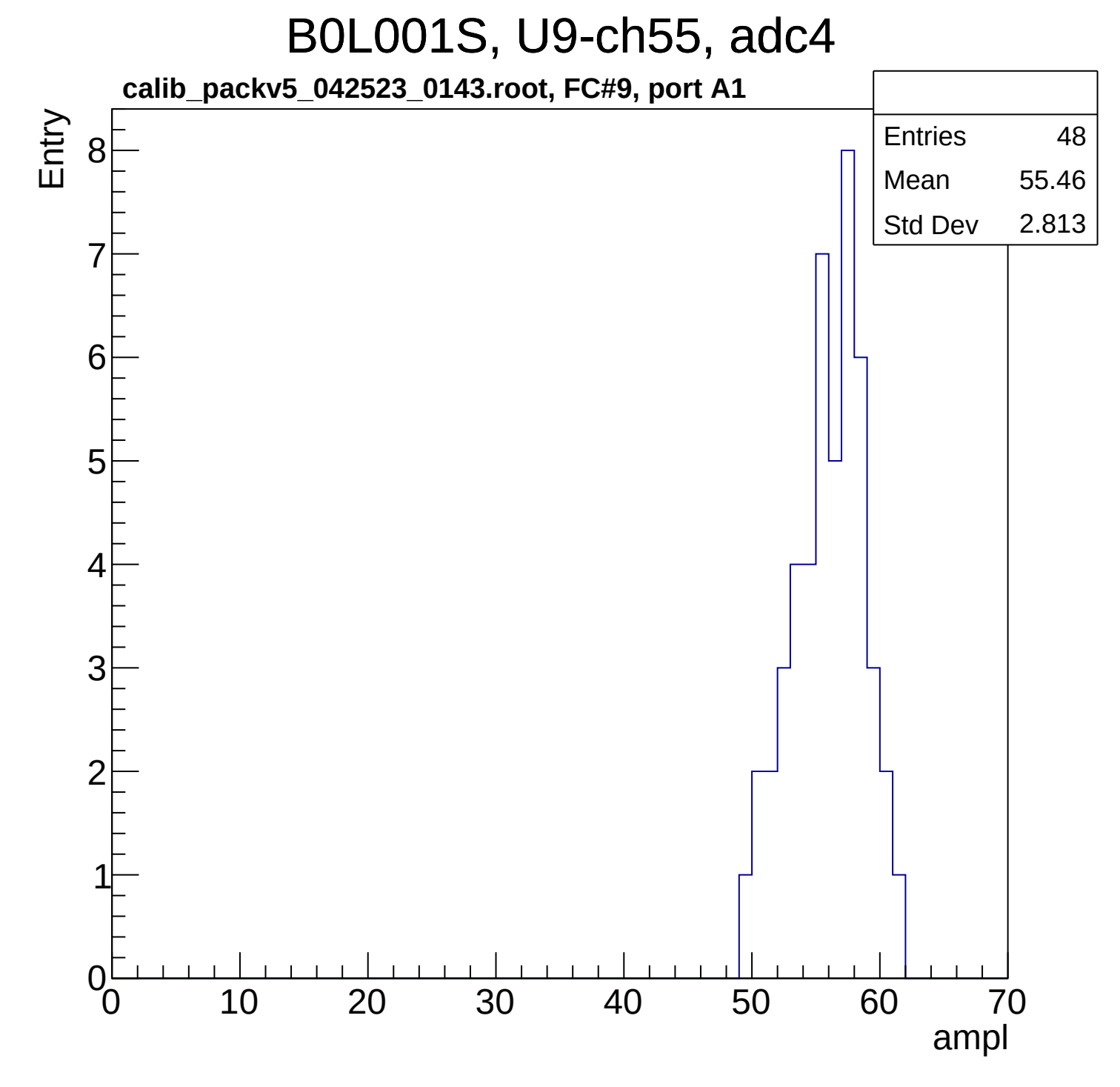
B0L001S, U9-ch55, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 55.46
Std Dev 2.813
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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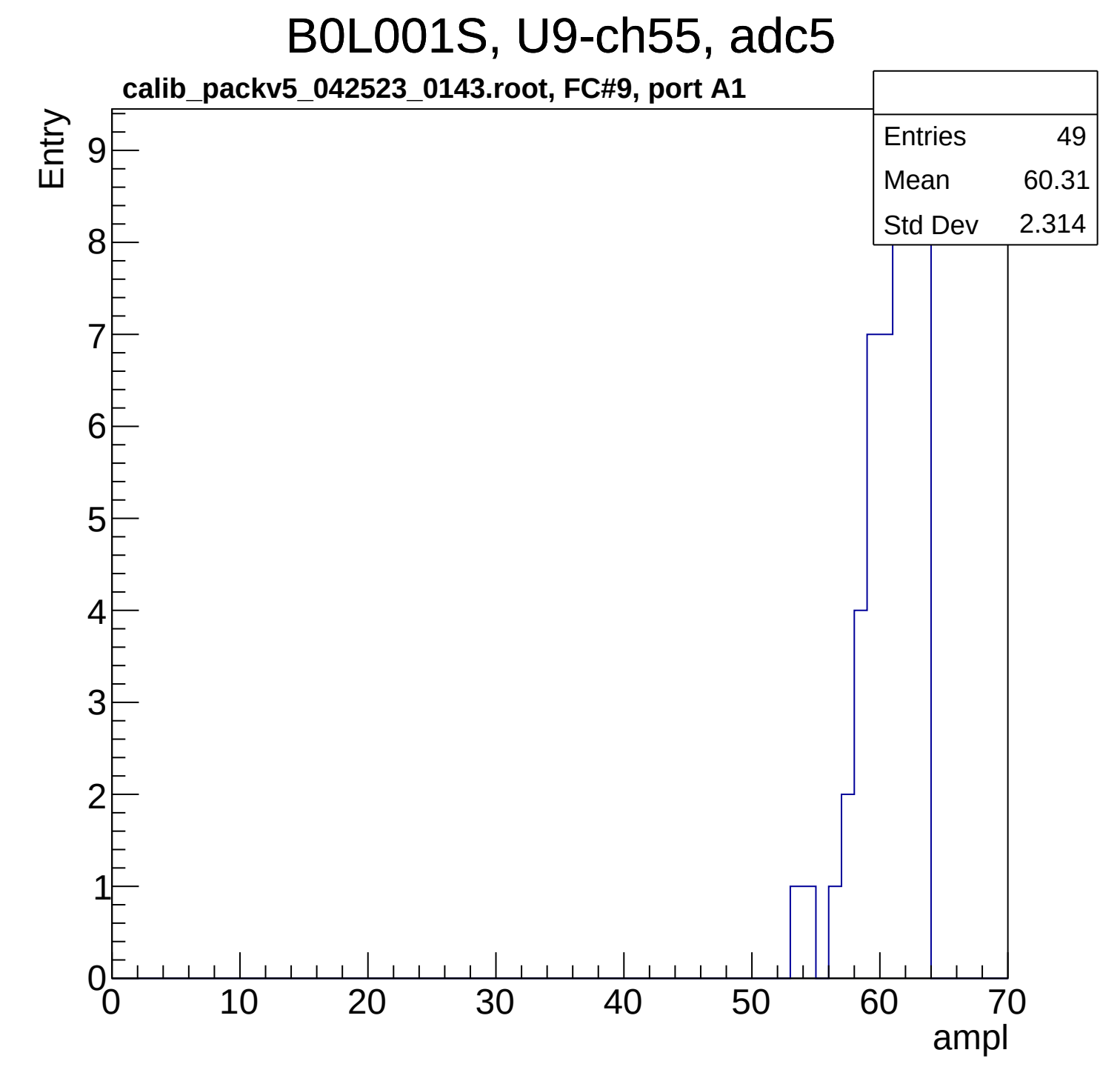
40
50
60
70


B0L001S, U9-ch55, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 60.31
Std Dev 2.314
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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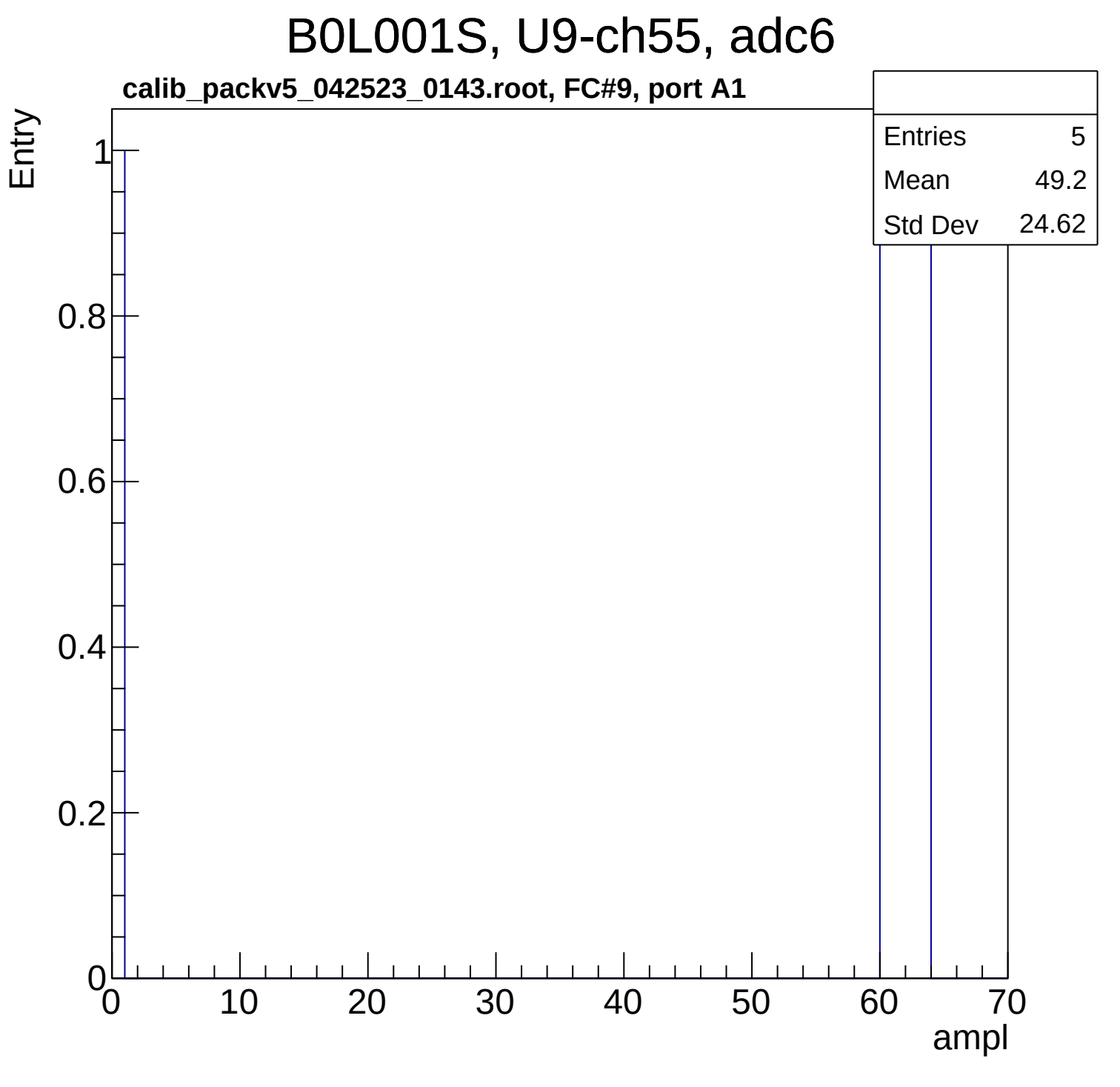
B0L001S, U9-ch55, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.2
Std Dev 24.62
Entry
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0.6
0.4
0.2
0
0
10
20
30
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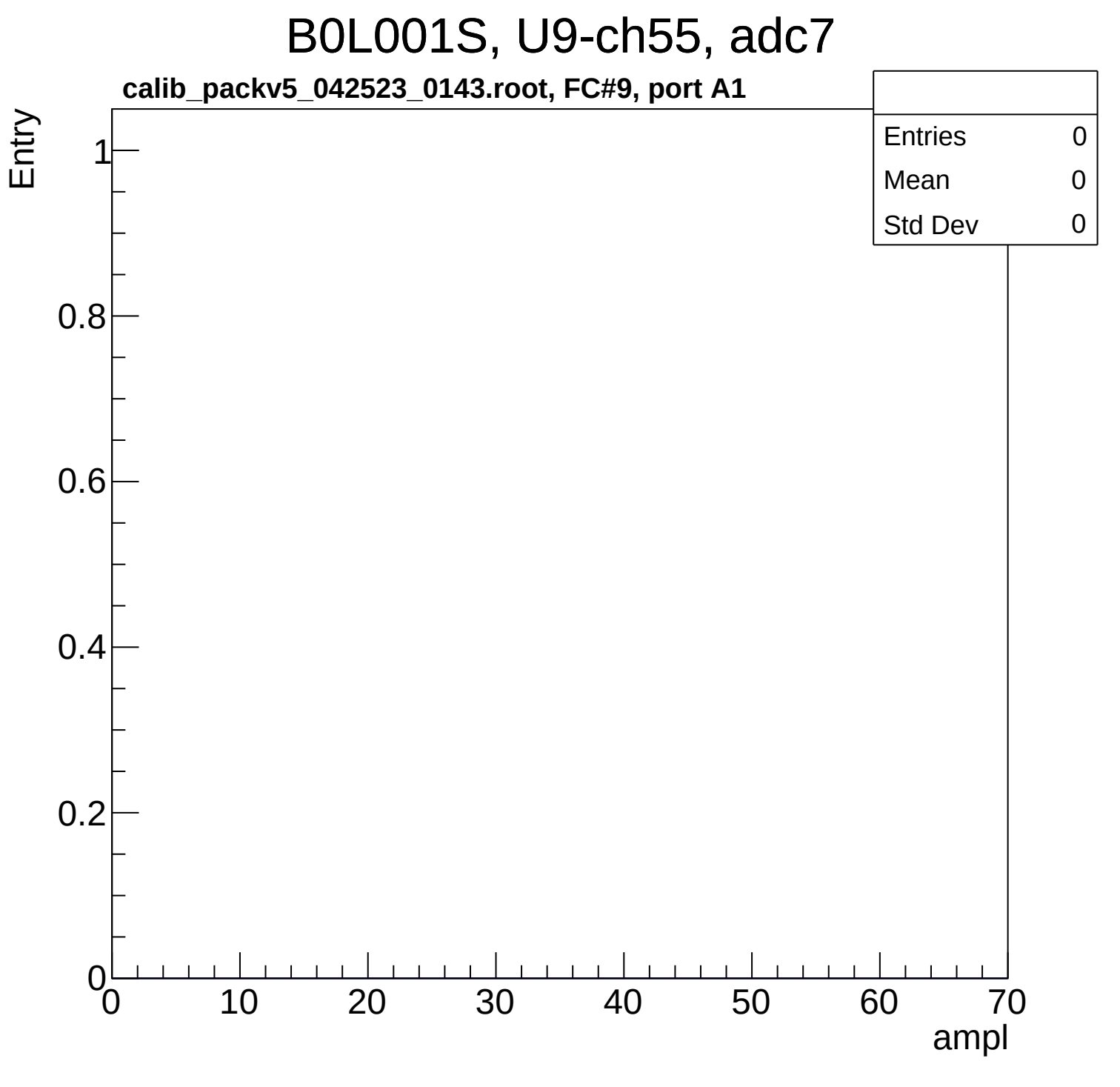
50
60
70
ampl


B0L001S, U9-ch55, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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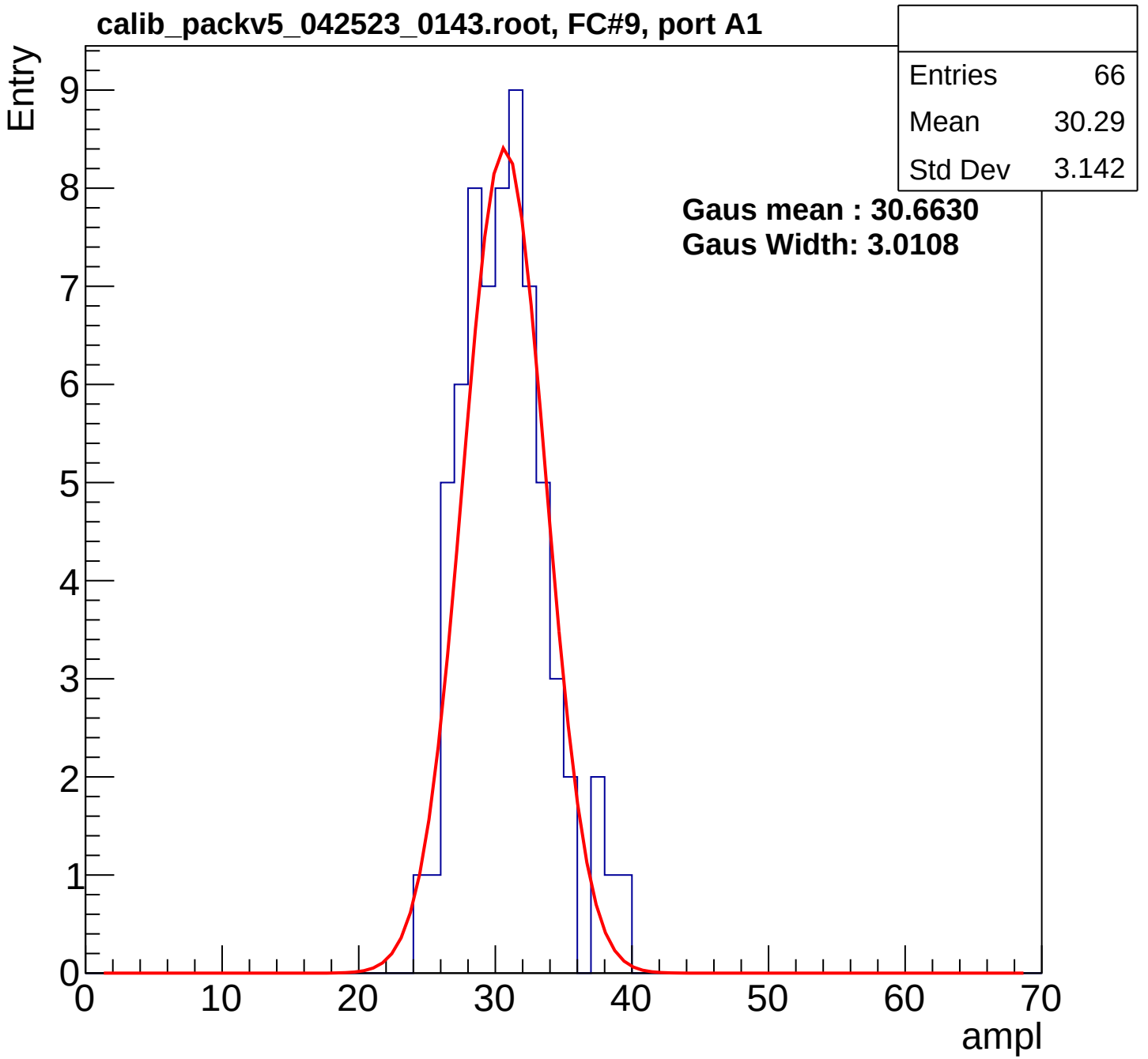
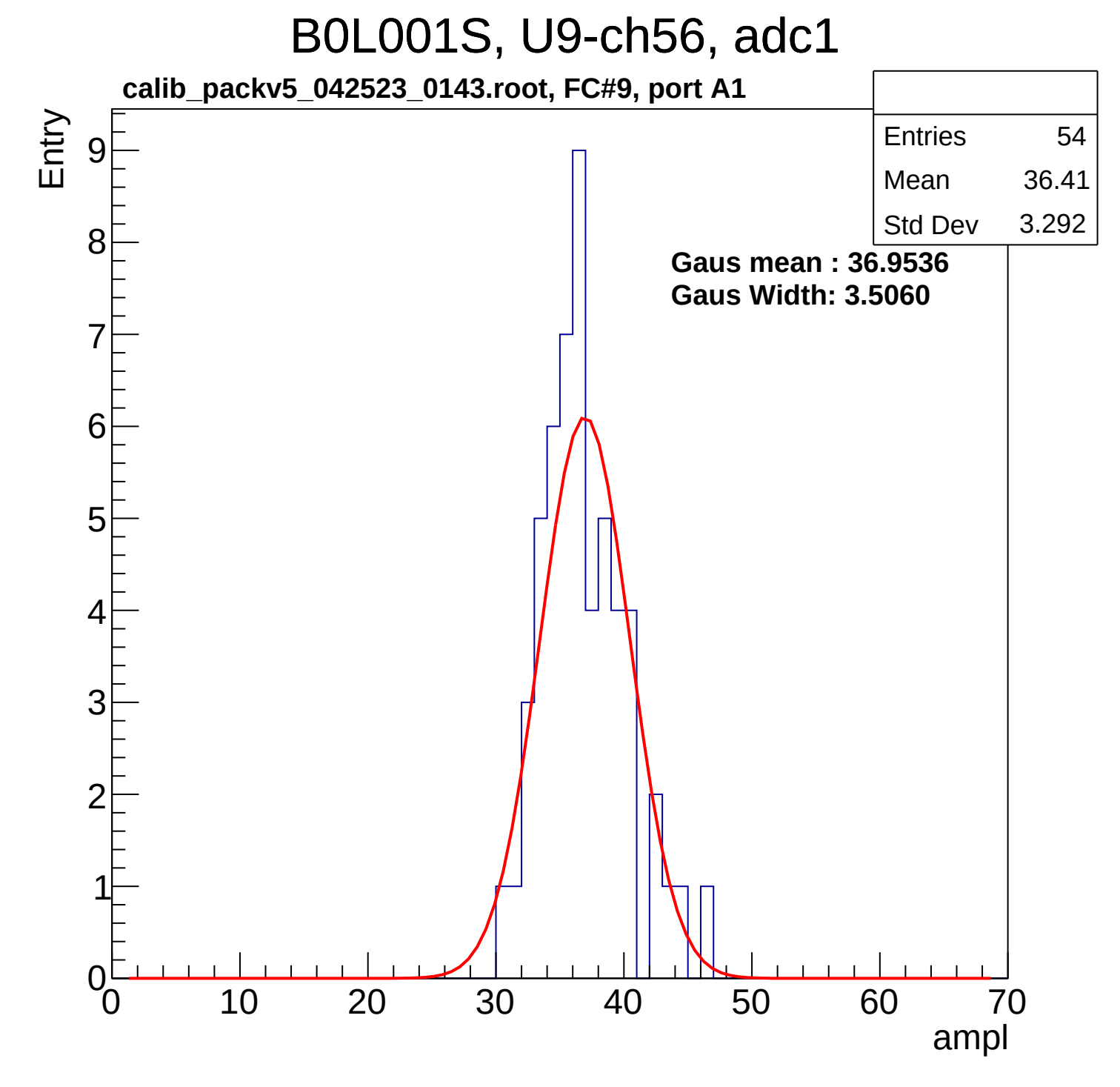
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch56, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 30.29
Std Dev 3.142
Gaus mean : 30.6630
Gaus Width: 3.0108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch56, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 36.41
Std Dev 3.292
Gaus mean : 36.9536
Gaus Width: 3.5060
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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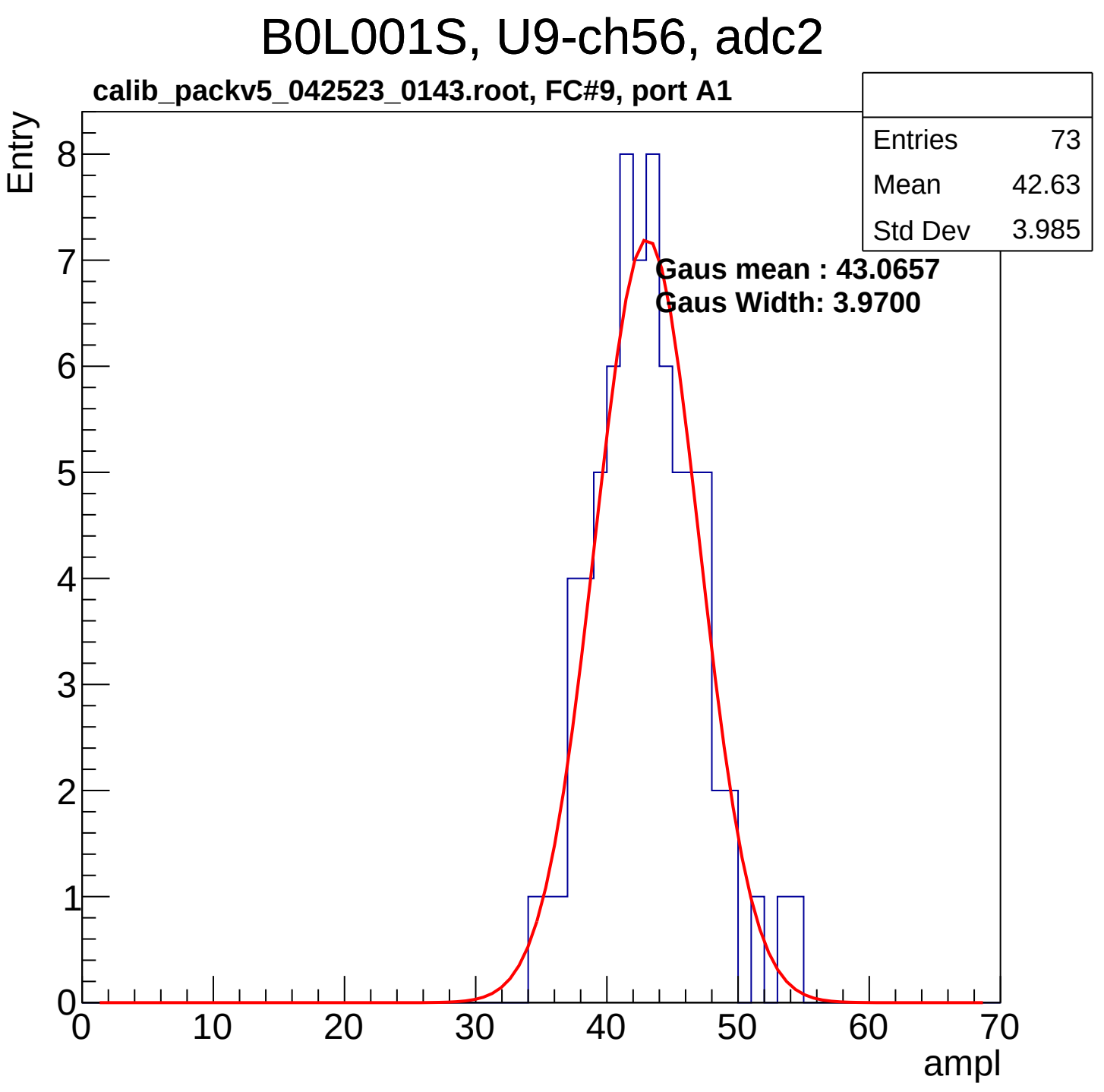
B0L001S, U9-ch56, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 42.63
Std Dev 3.985
Gaus mean : 43.0657
Gaus Width: 3.9700
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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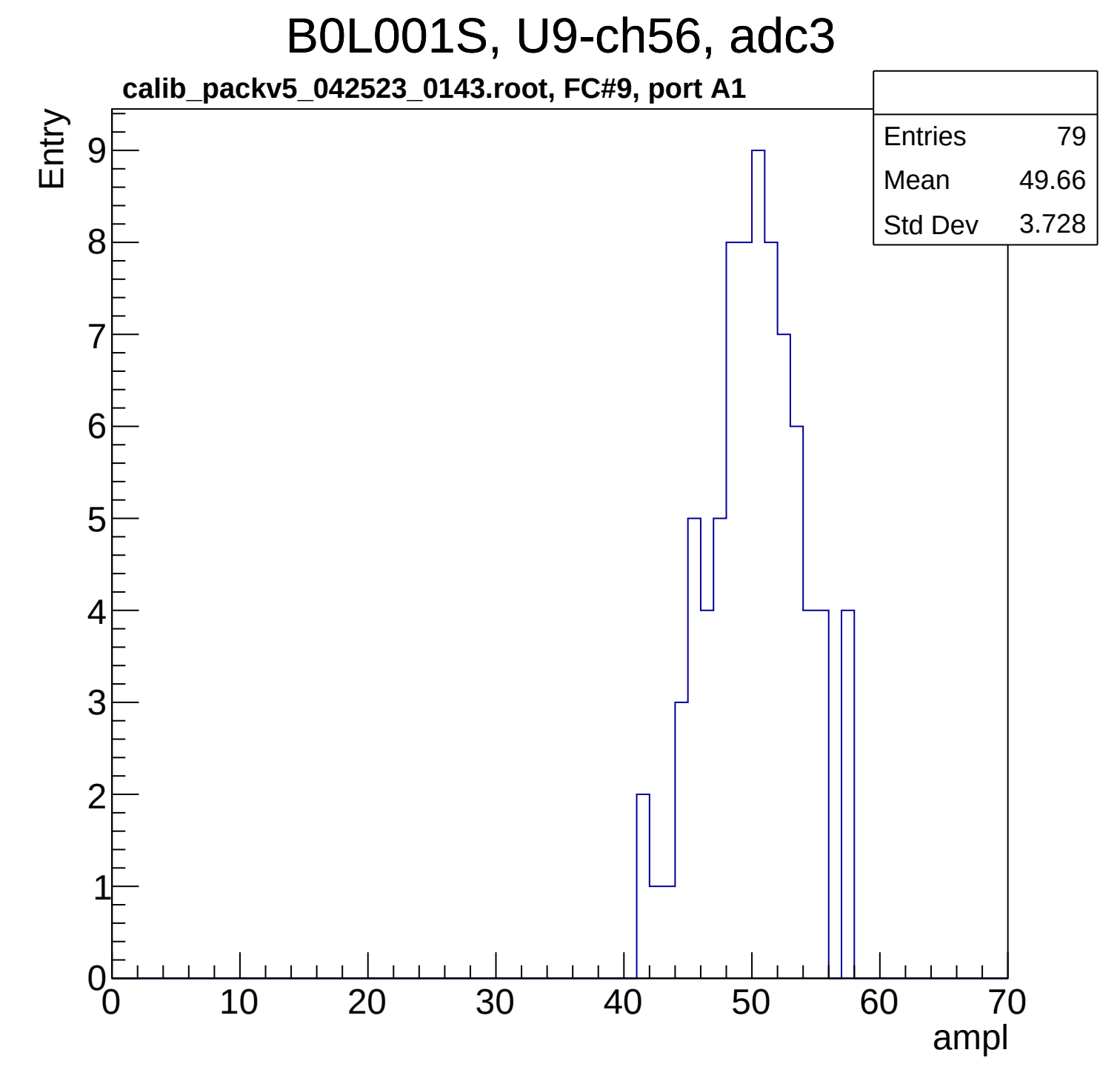
B0L001S, U9-ch56, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 49.66
Std Dev 3.728
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
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60
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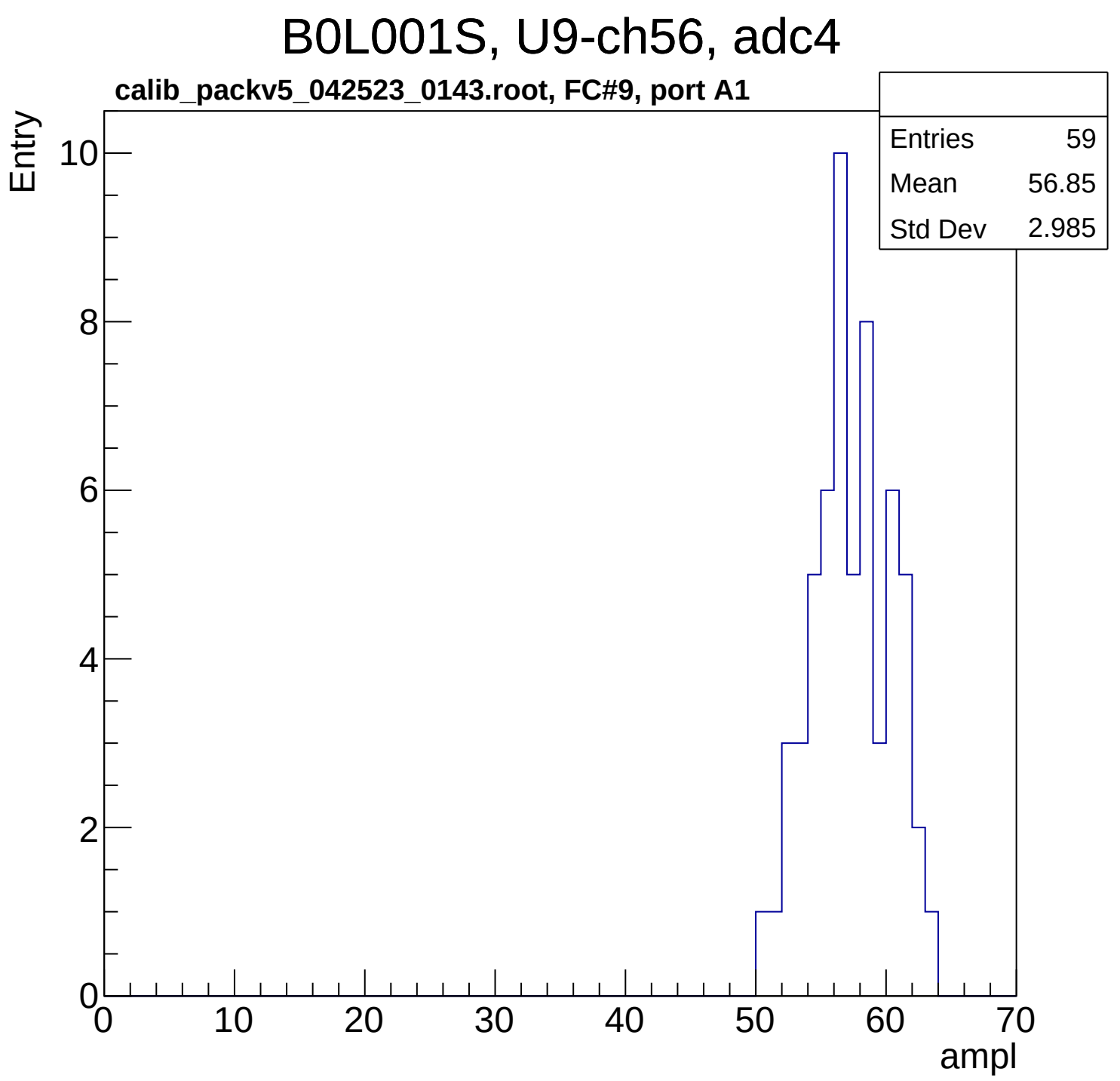
B0L001S, U9-ch56, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.85
Std Dev 2.985
Entry
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2
4
6
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10
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10
20
30
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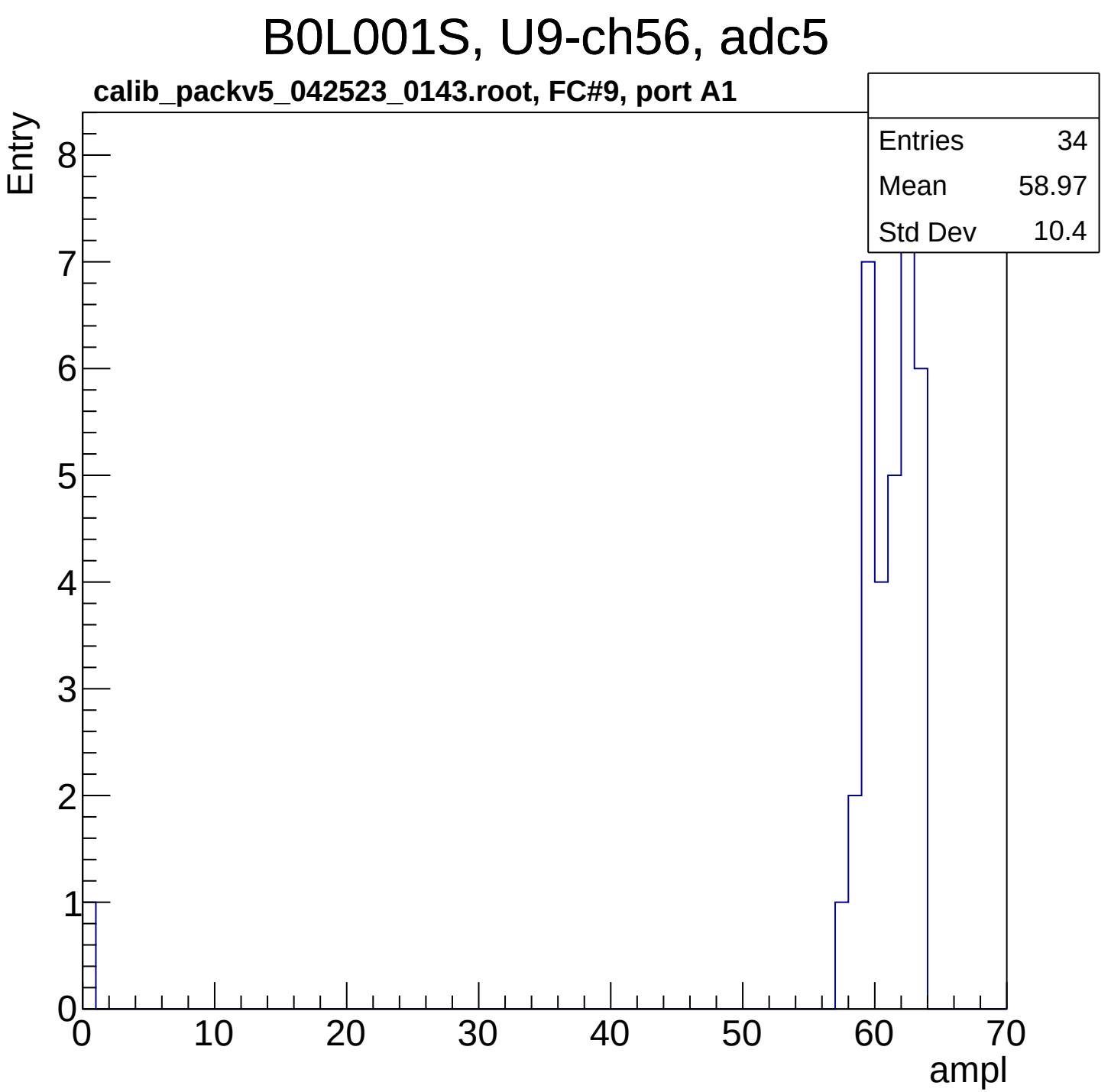
50
60
70
ampl


B0L001S, U9-ch56, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.97
Std Dev 10.4
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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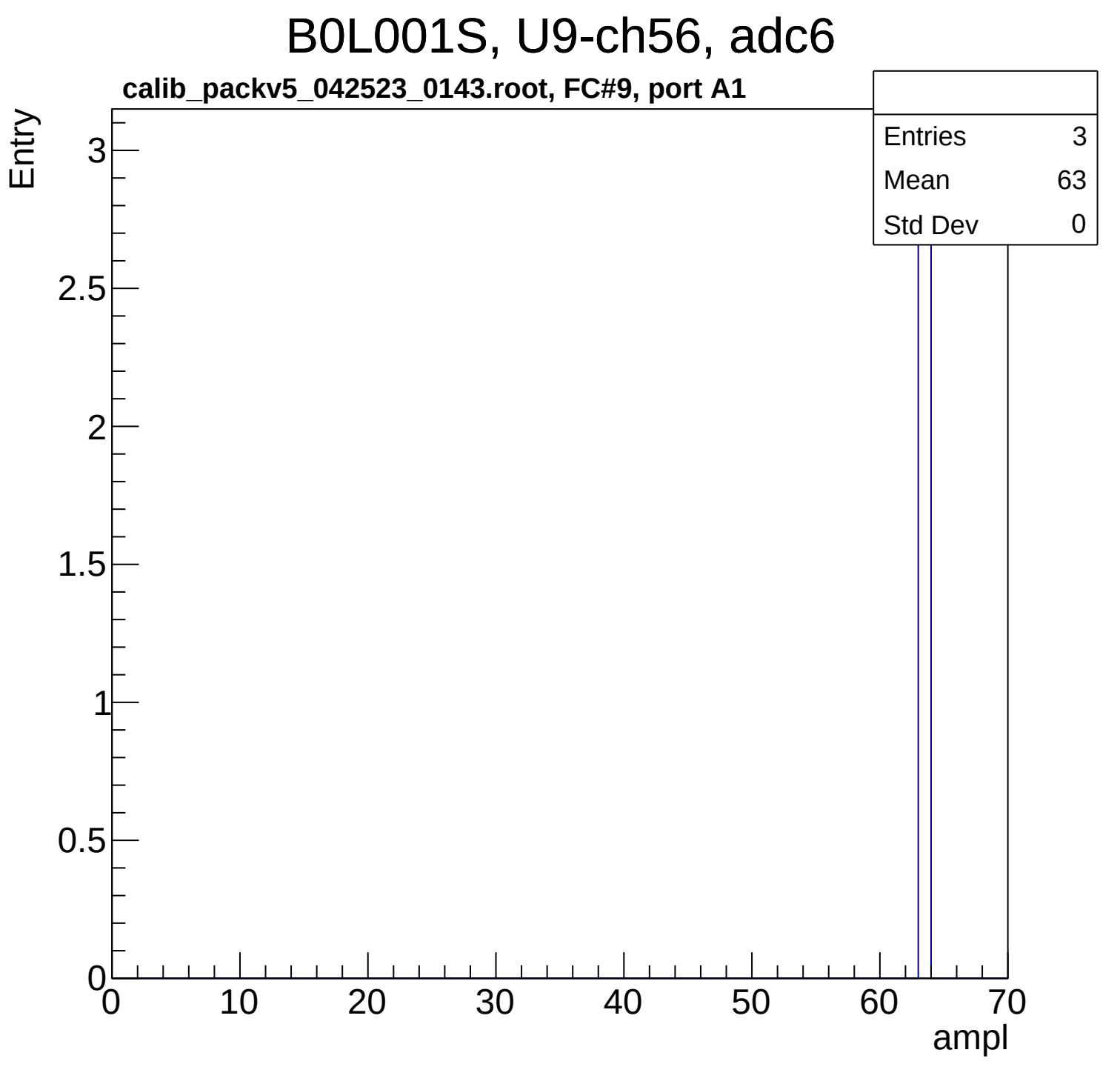
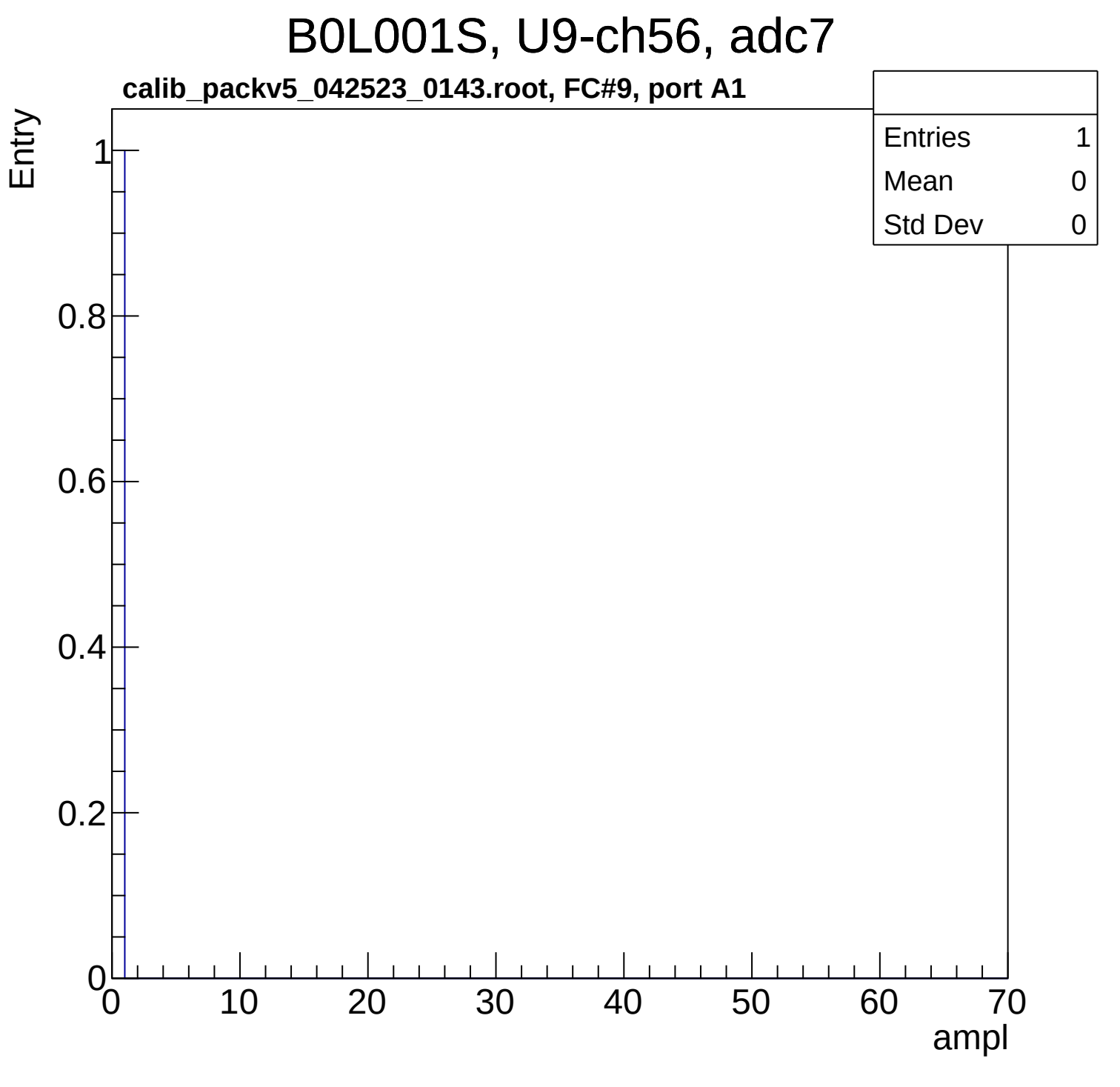

B0L001S, U9-ch56, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch56, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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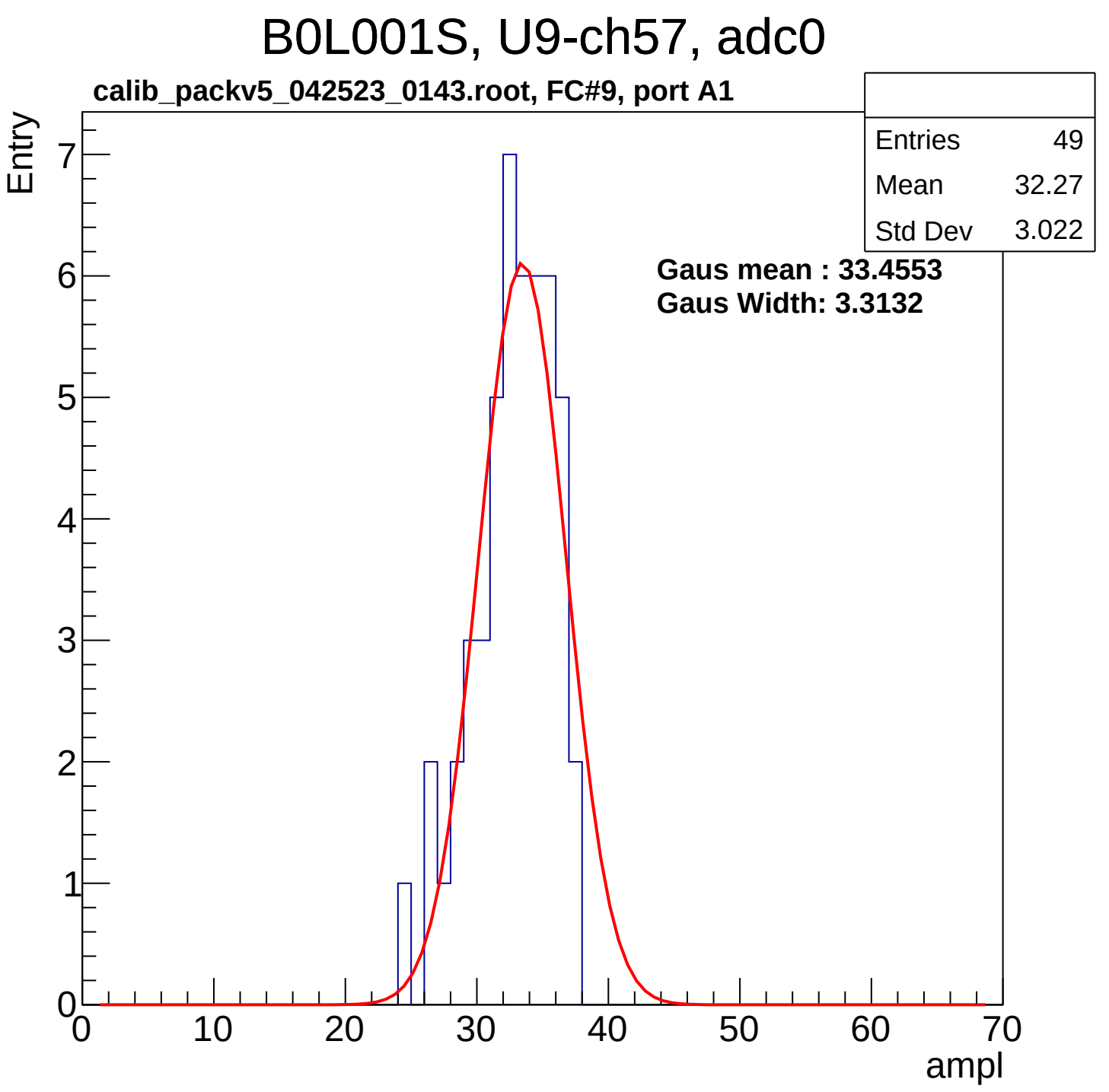
50
60
70
ampl


B0L001S, U9-ch57, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 32.27
Std Dev 3.022
Gaus mean : 33.4553
Gaus Width: 3.3132
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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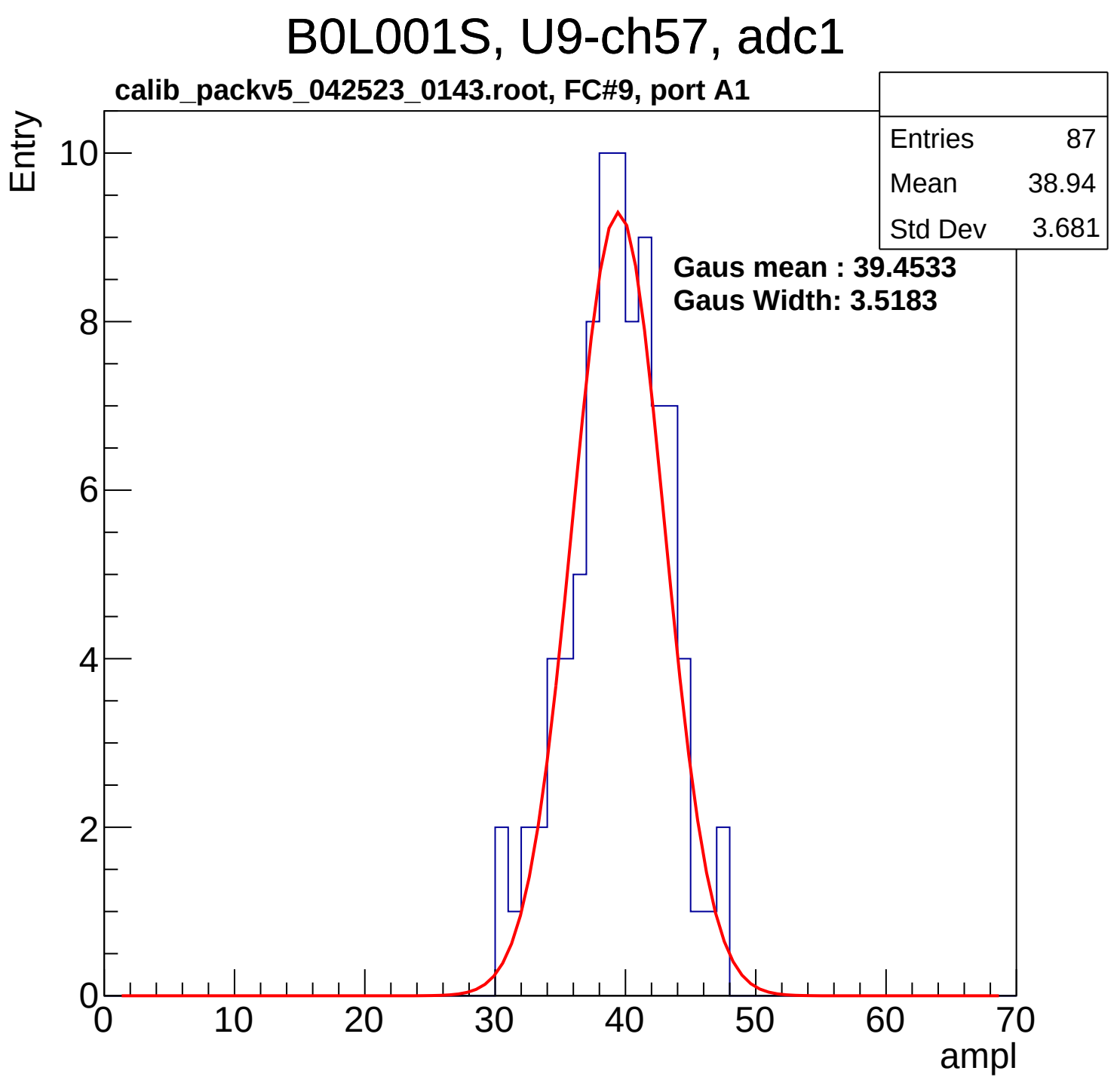
60
70


B0L001S, U9-ch57, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 38.94
Std Dev 3.681
Gaus mean : 39.4533
Gaus Width: 3.5183
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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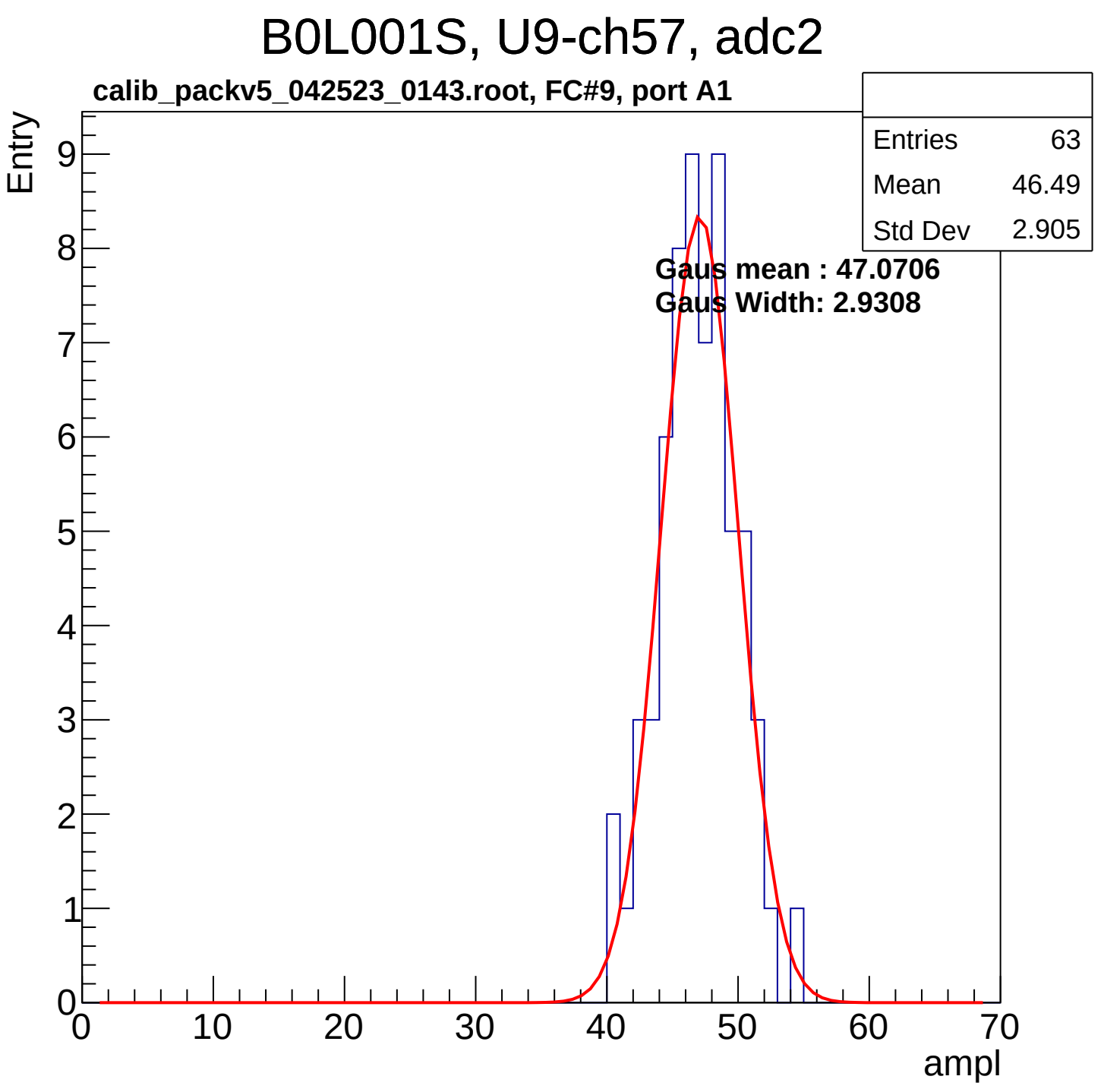
B0L001S, U9-ch57, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 46.49
Std Dev 2.905
Gaus mean : 47.0706
Gaus Width: 2.9308
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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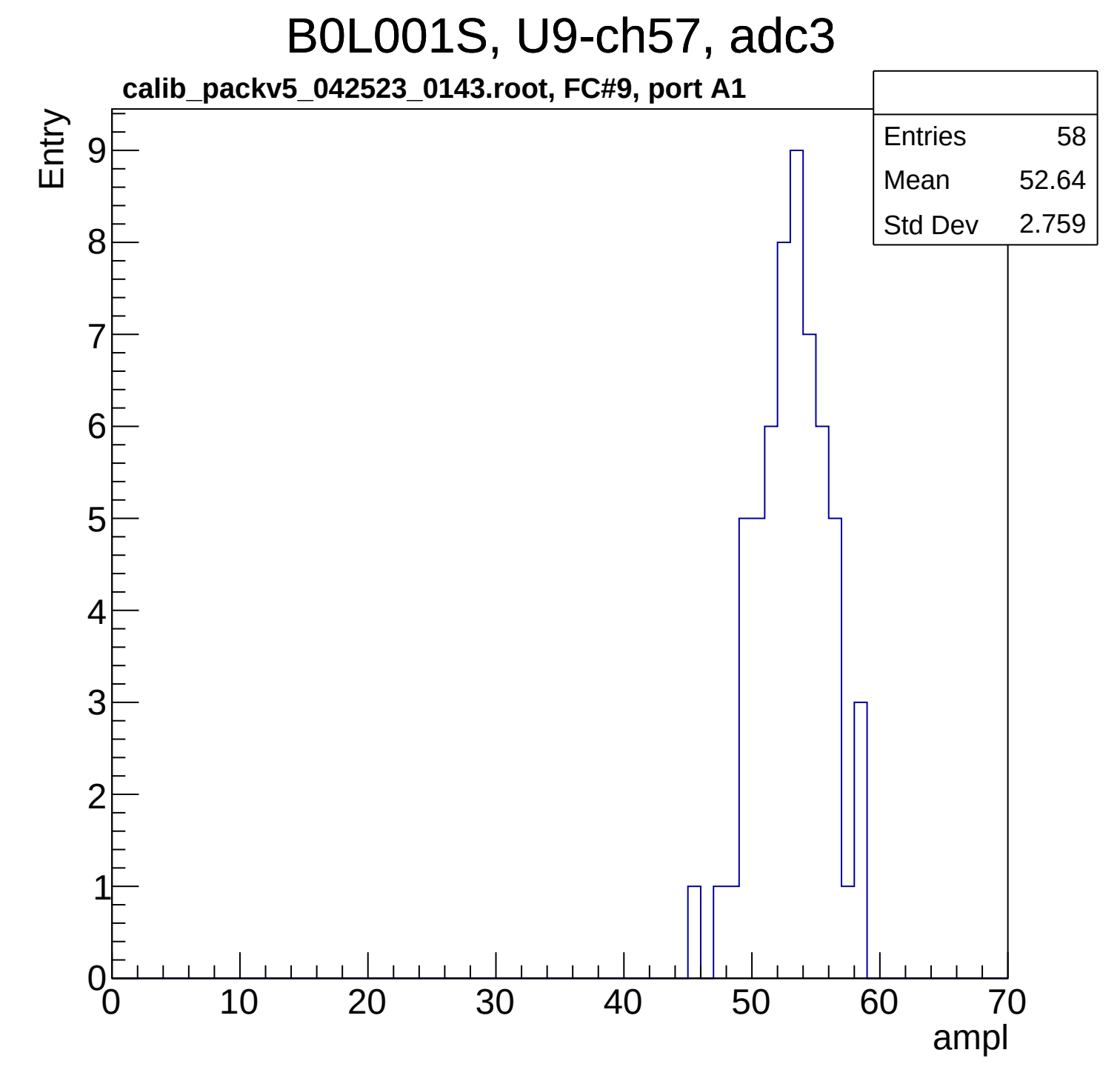
B0L001S, U9-ch57, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 52.64
Std Dev 2.759
Entry
ampl
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2
3
4
5
6
7
8
9
0
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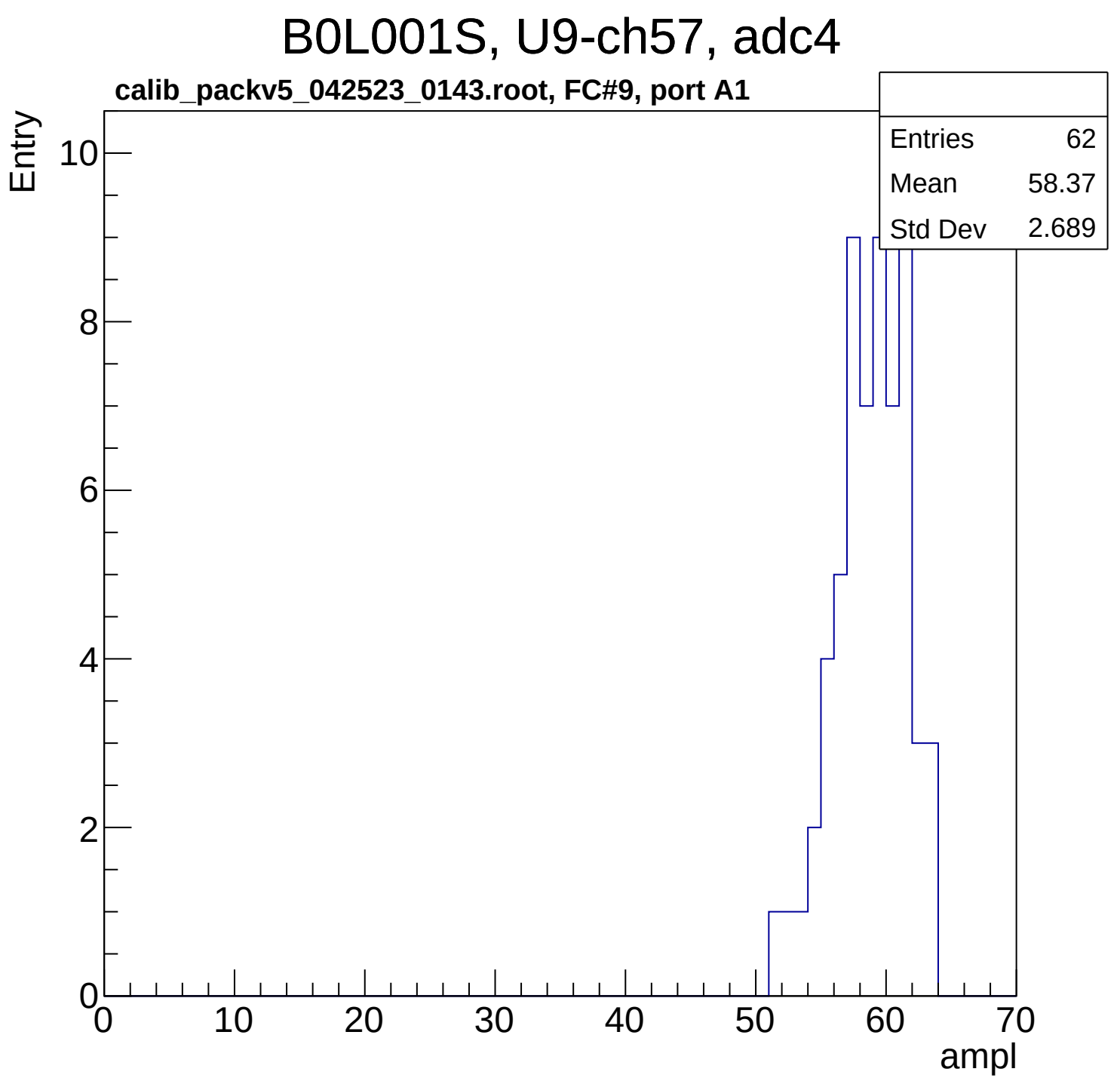
B0L001S, U9-ch57, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 58.37
Std Dev 2.689
Entry
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6
8
10
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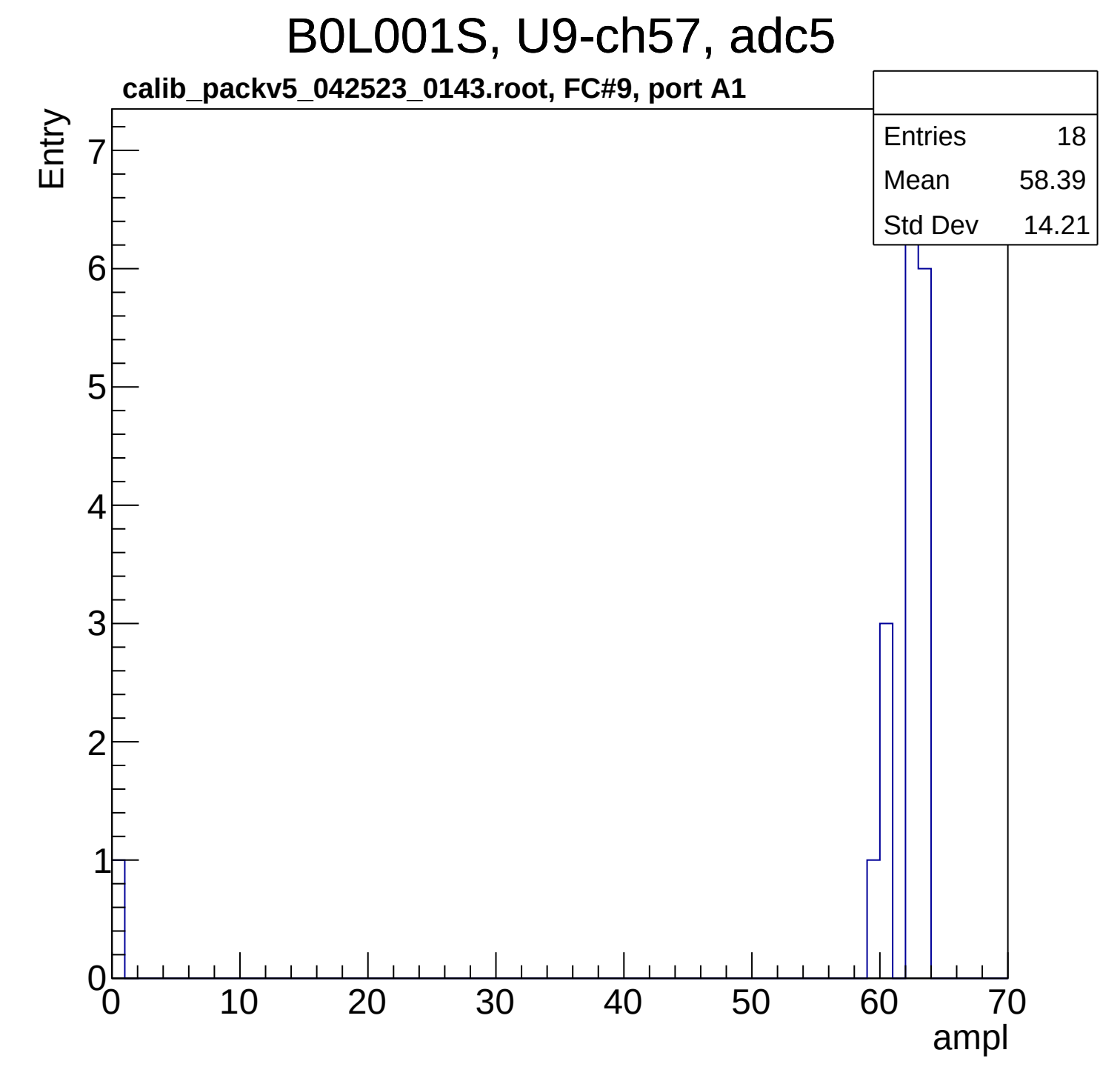
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch57, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 18
Mean 58.39
Std Dev 14.21
Entry
ampl
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1
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3
4
5
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7
0
10
20
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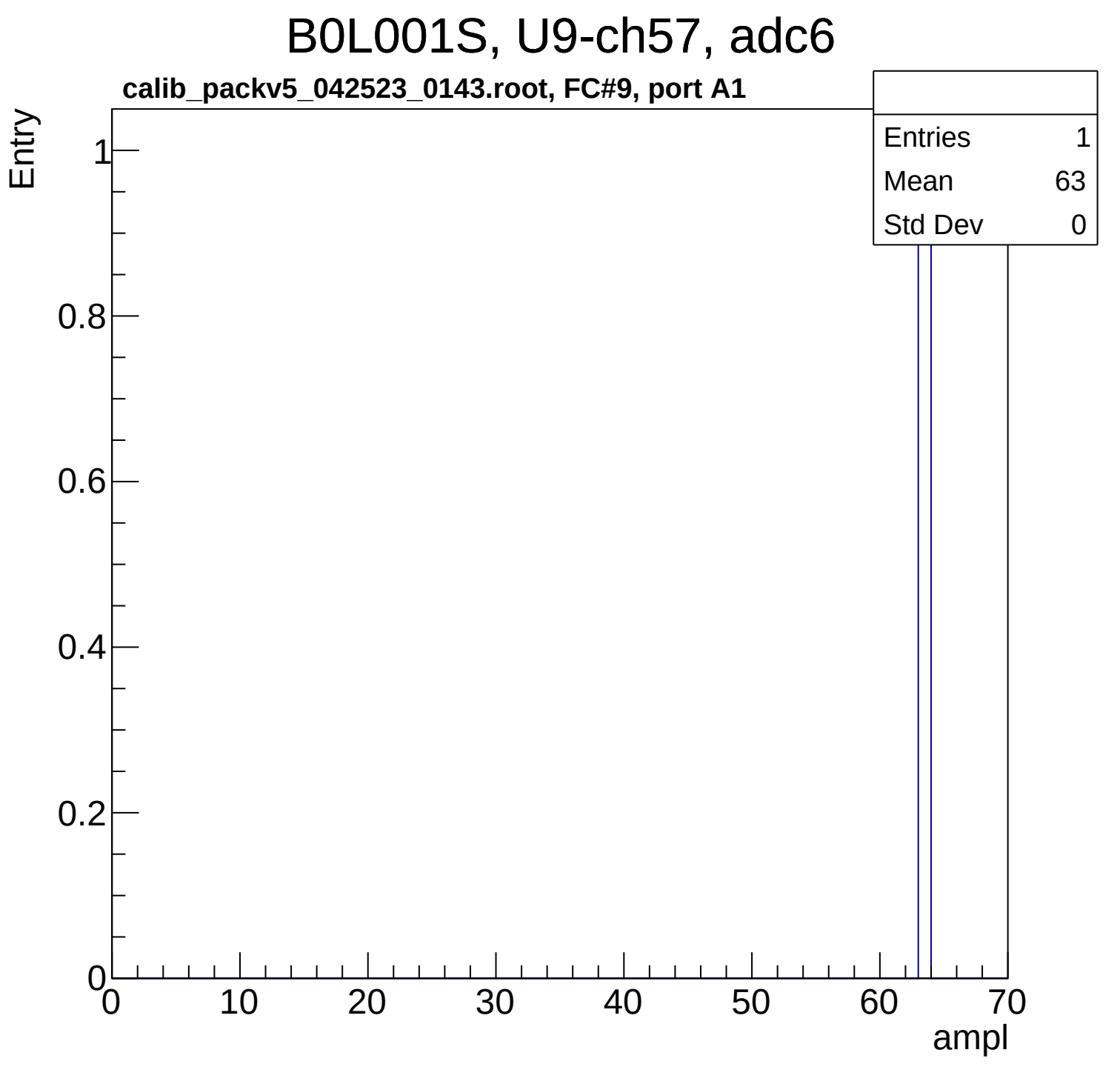
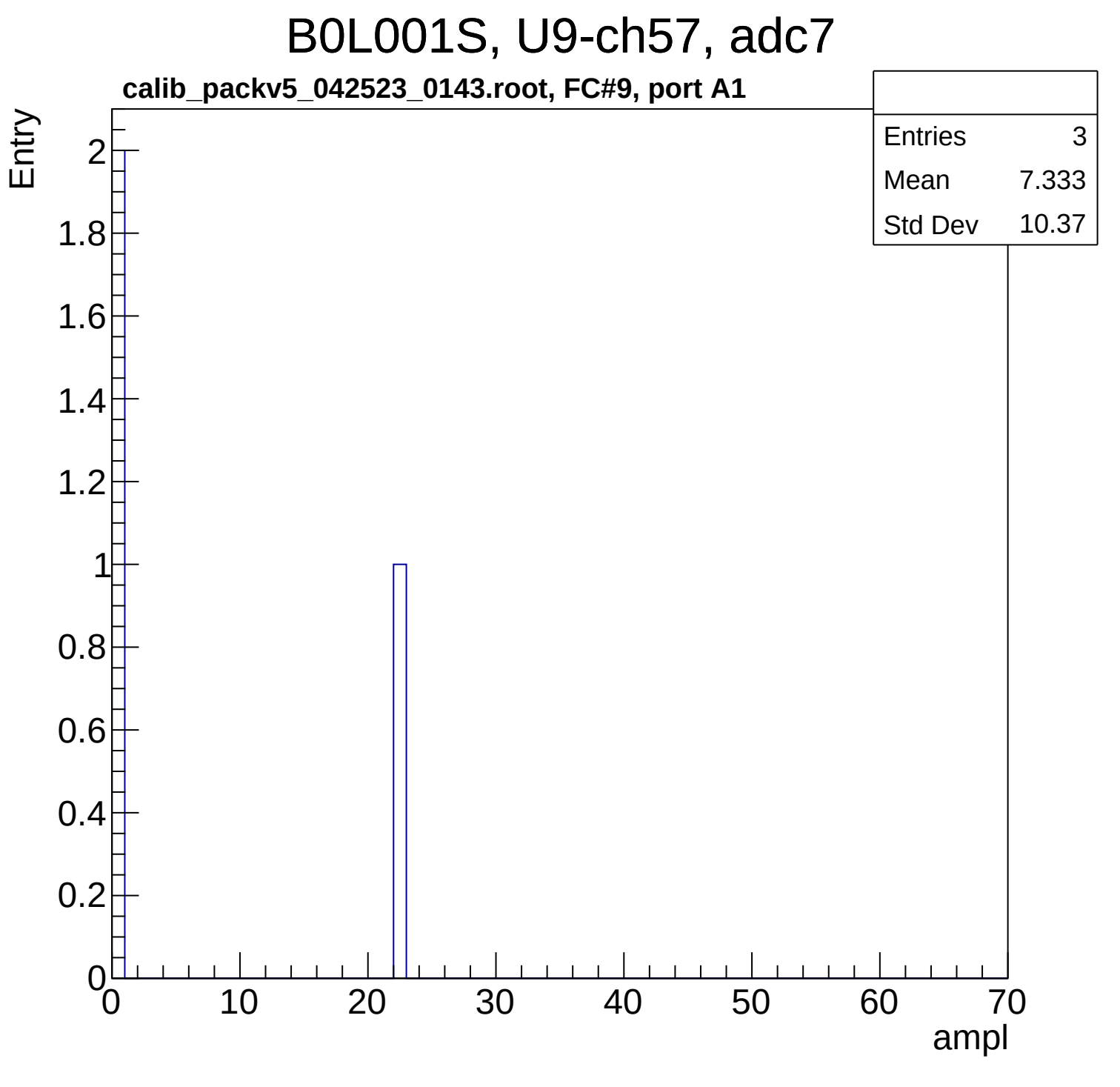
40
50
60
70


B0L001S, U9-ch57, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch57, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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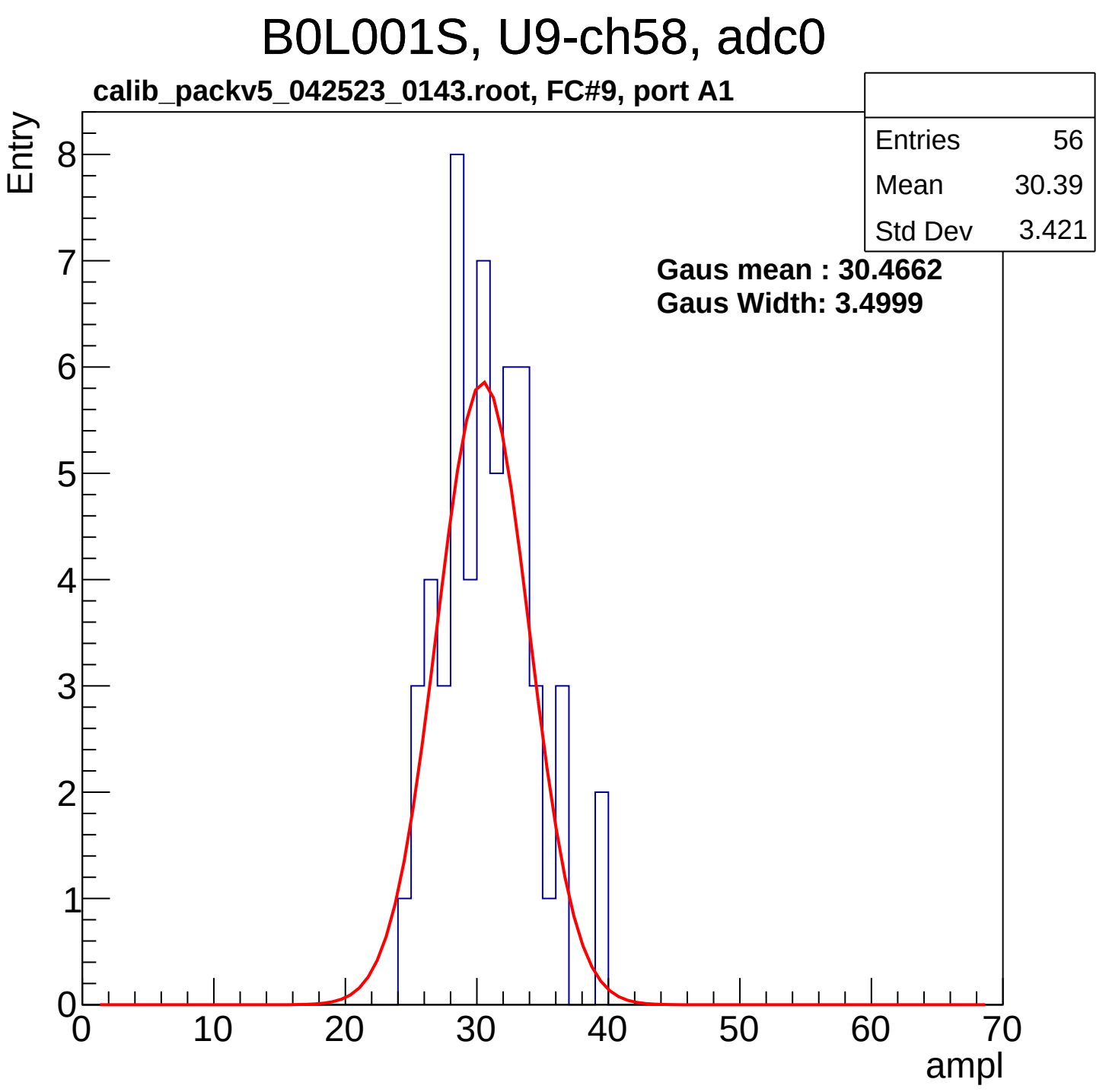
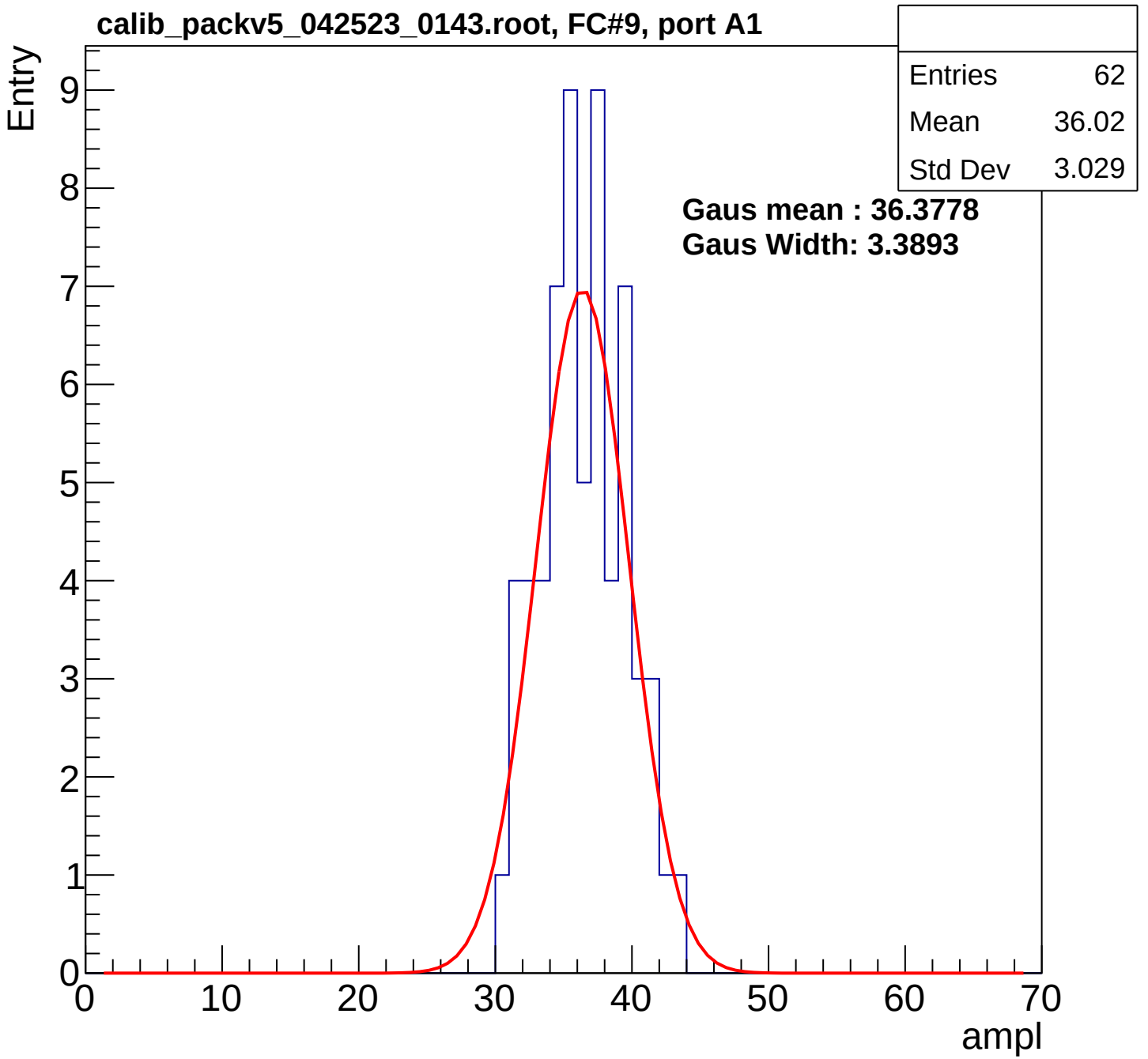
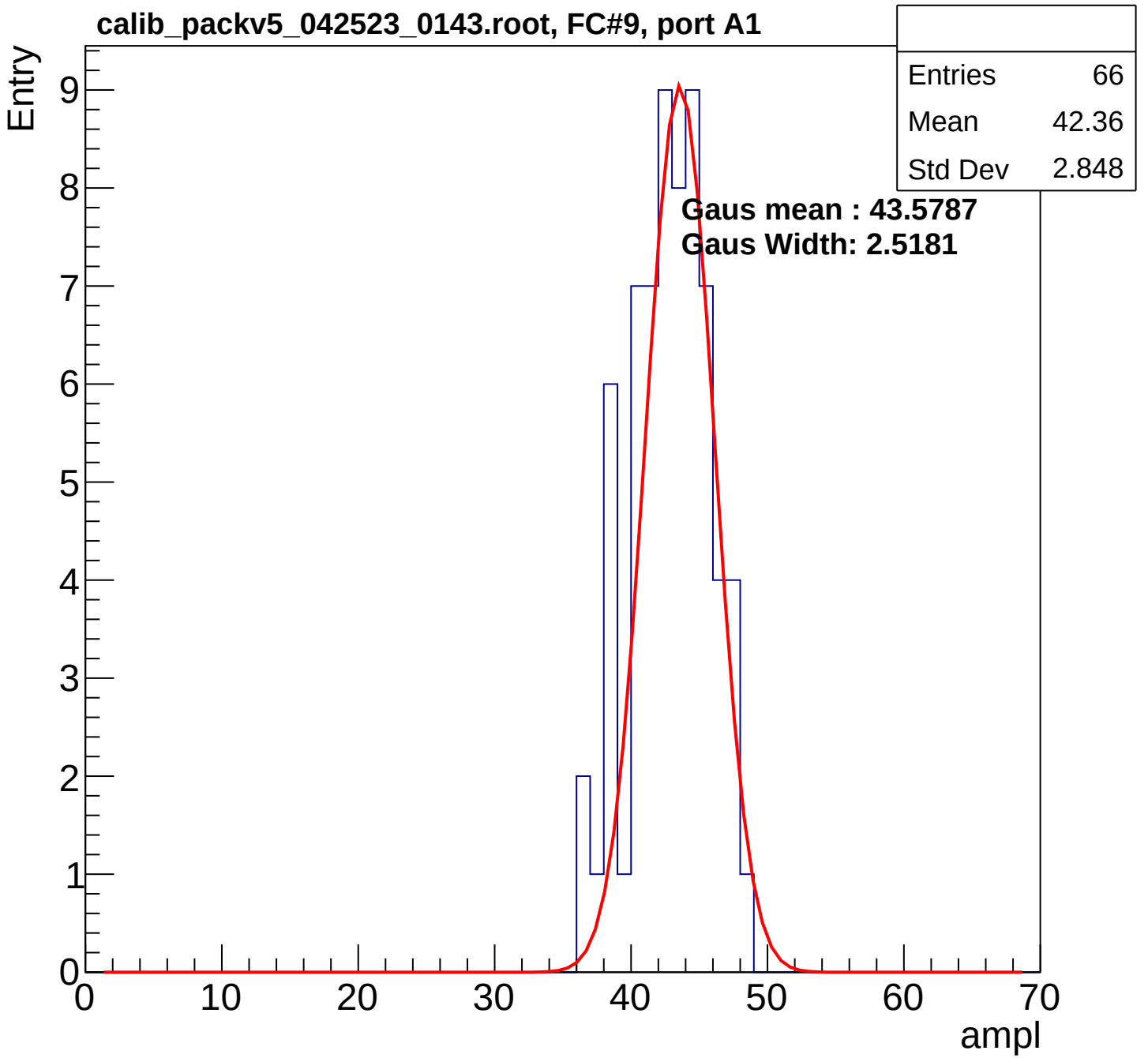

B0L001S, U9-ch58, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.39
Std Dev 3.421
Gaus mean : 30.4662
Gaus Width: 3.4999
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch58, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 36.02
Std Dev 3.029
Gaus mean : 36.3778
Gaus Width: 3.3893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch58, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.36
Std Dev 2.848
Gaus mean : 43.5787
Gaus Width: 2.5181
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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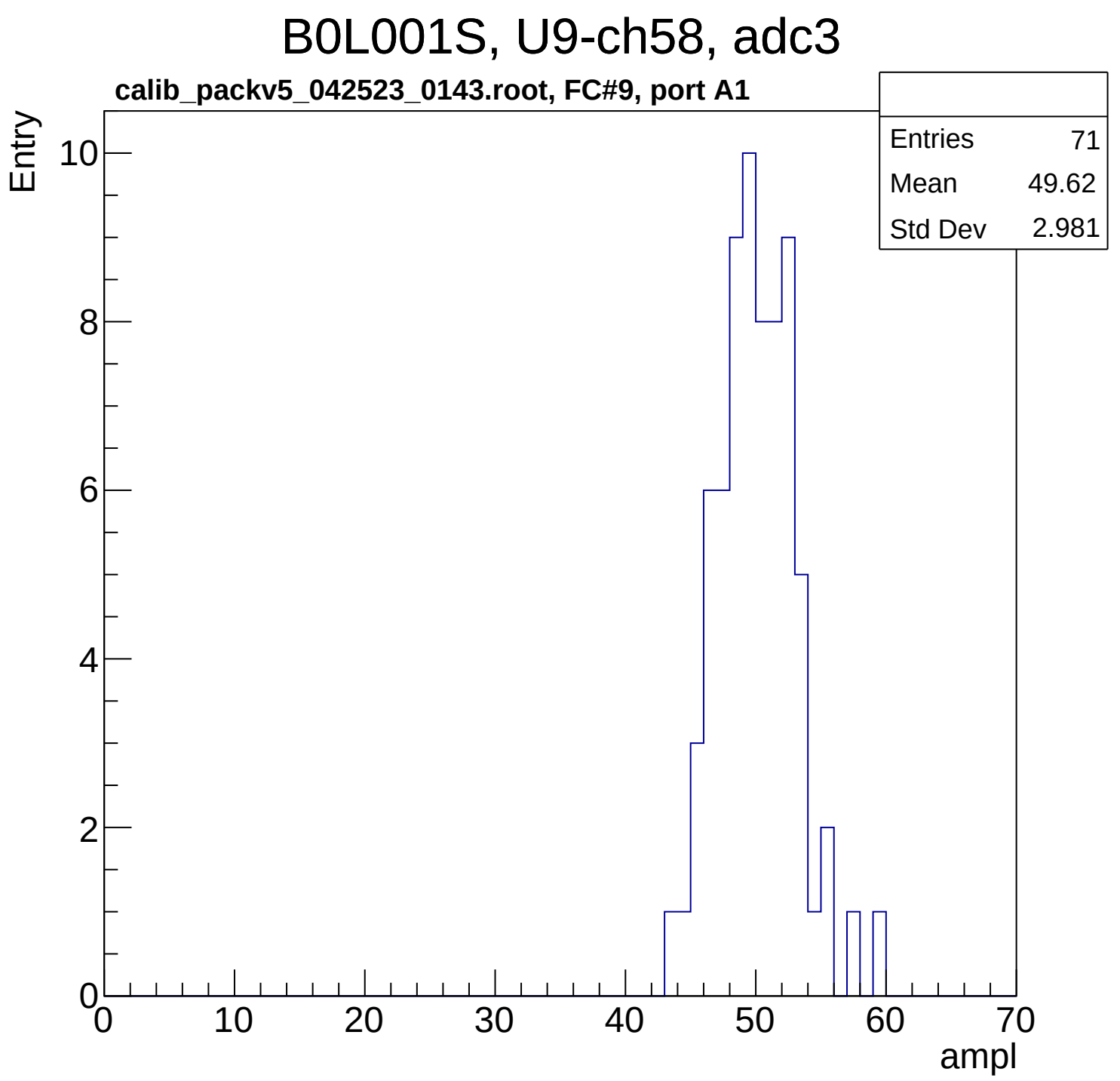
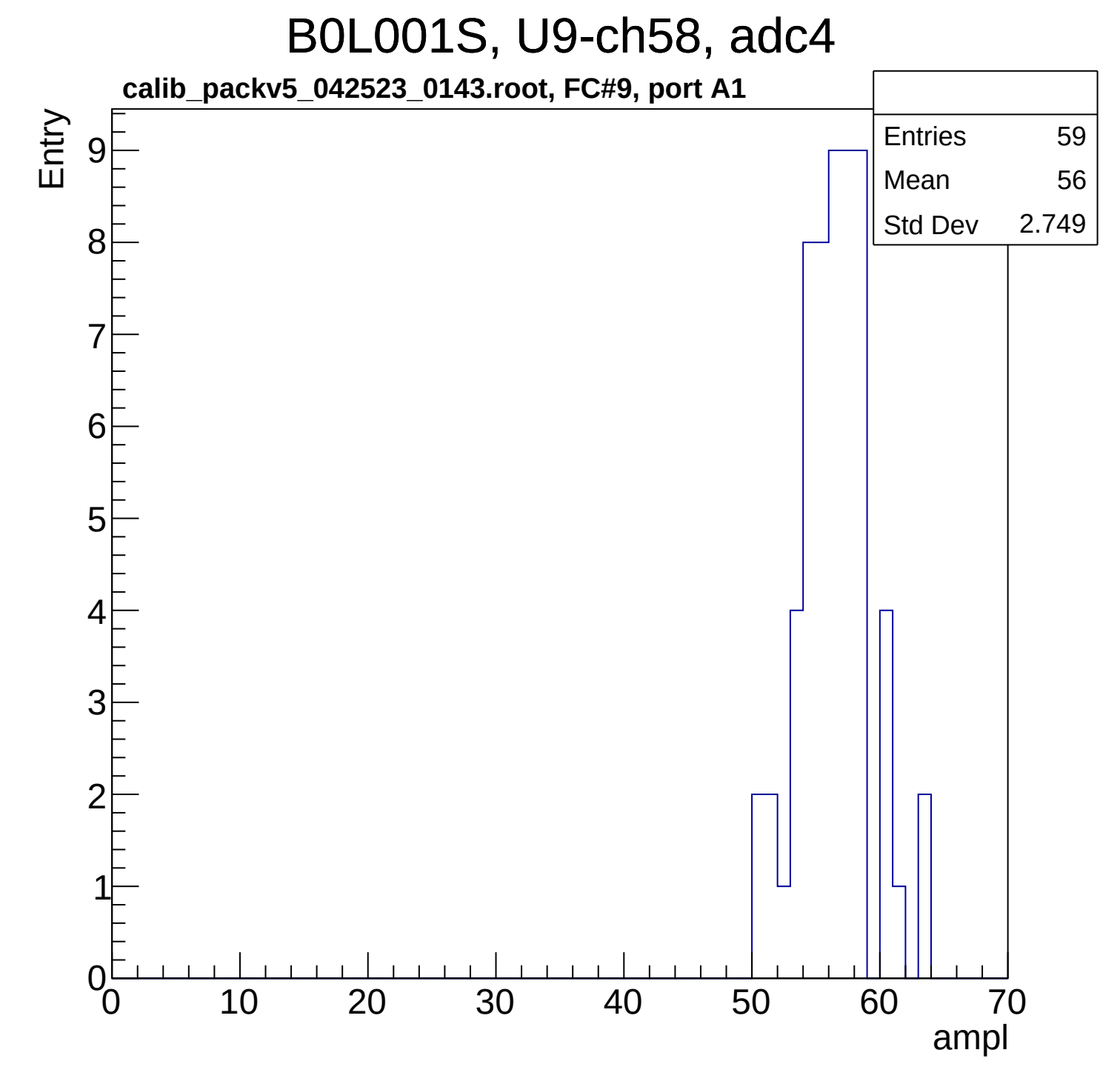

B0L001S, U9-ch58, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 49.62
Std Dev 2.981
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch58, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56
Std Dev 2.749
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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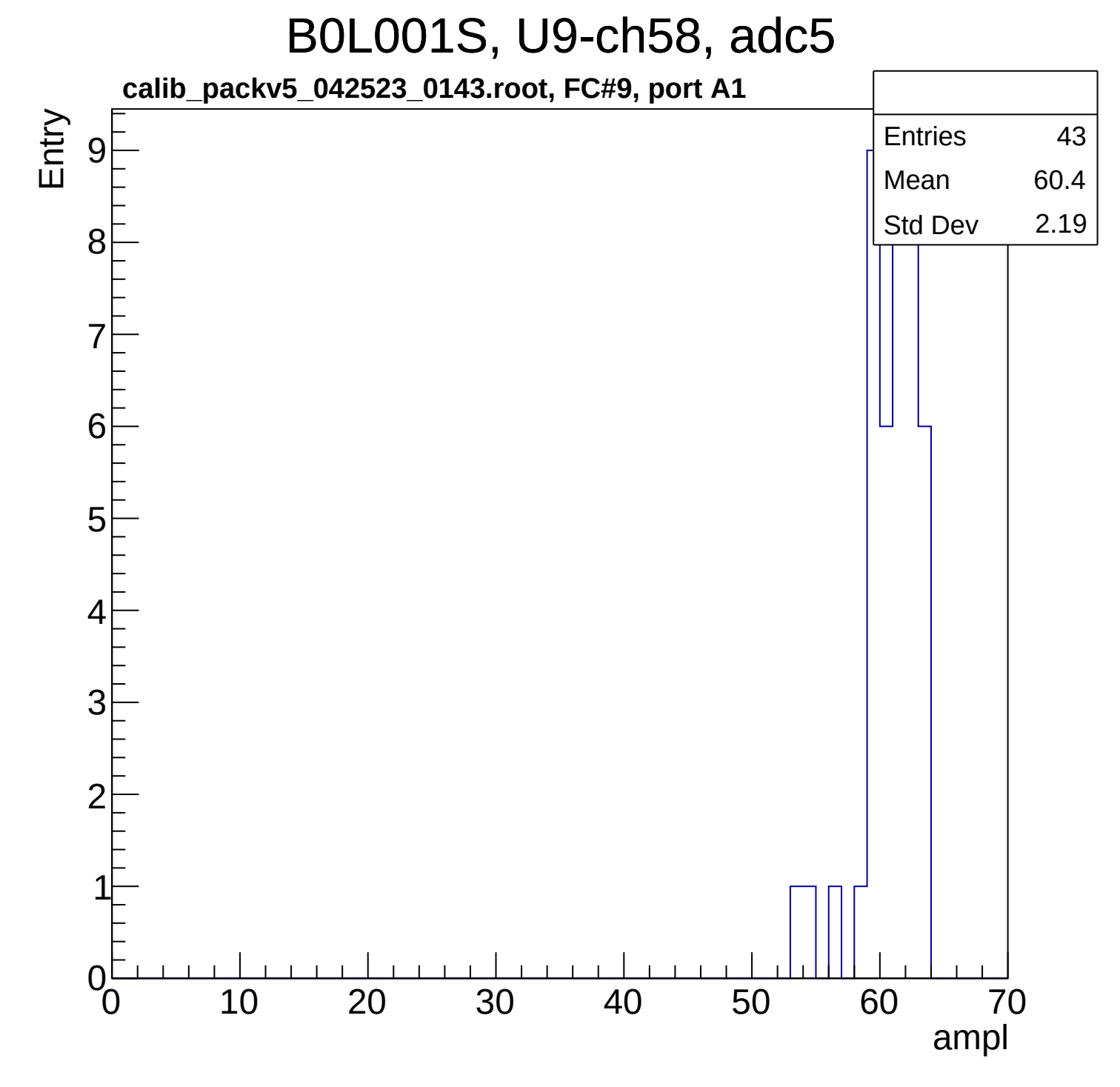
B0L001S, U9-ch58, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 60.4
Std Dev 2.19
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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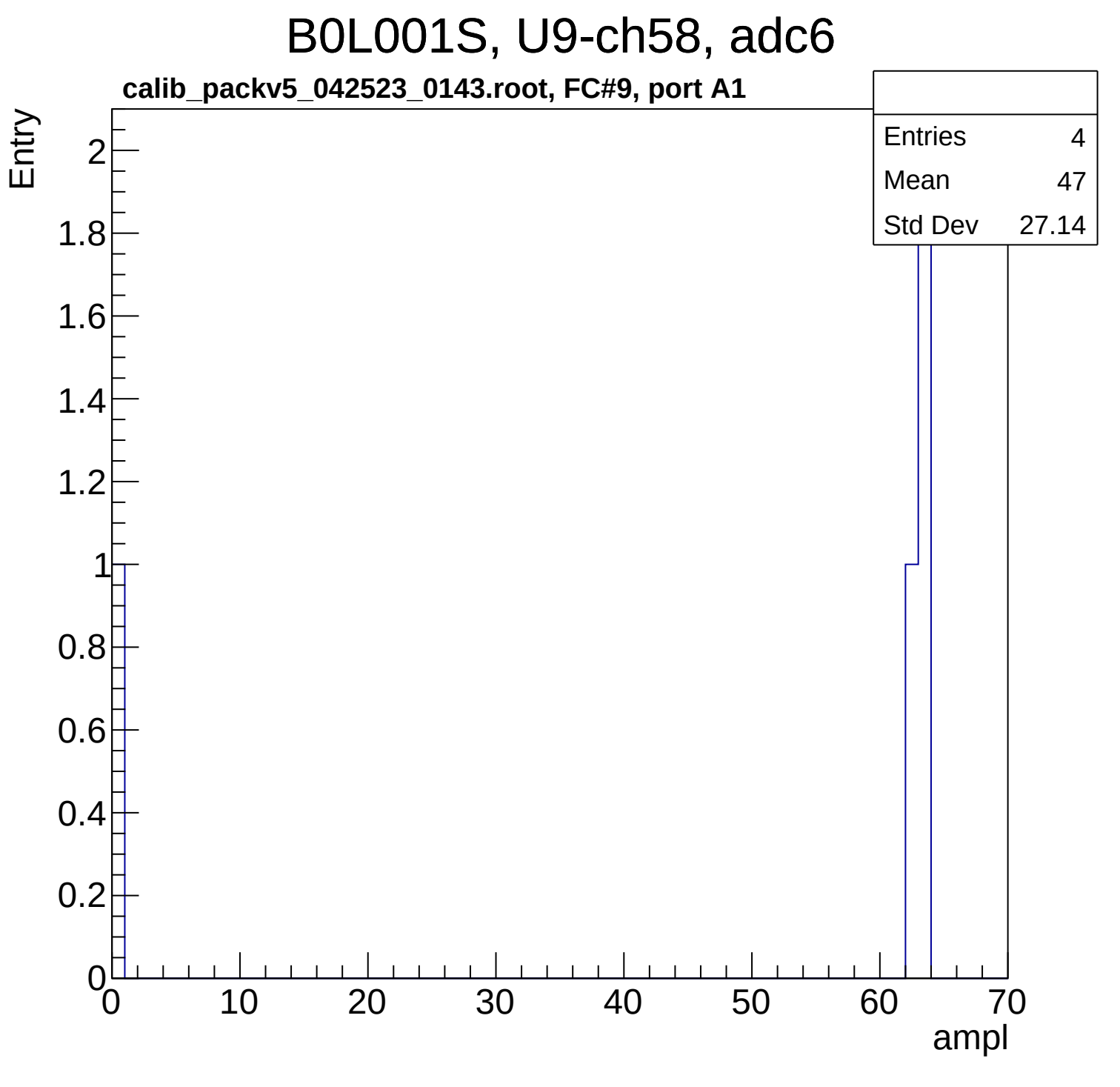
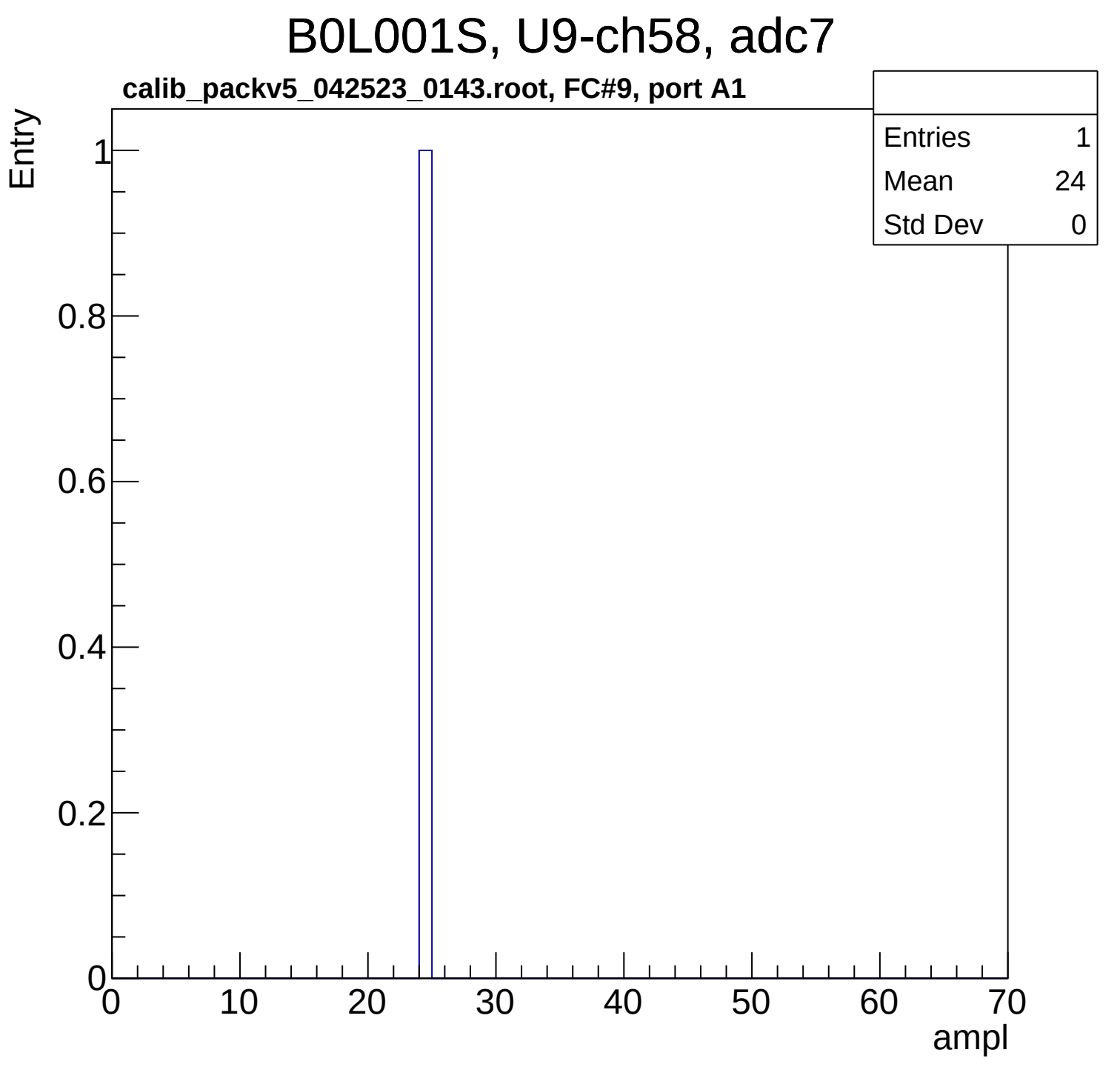
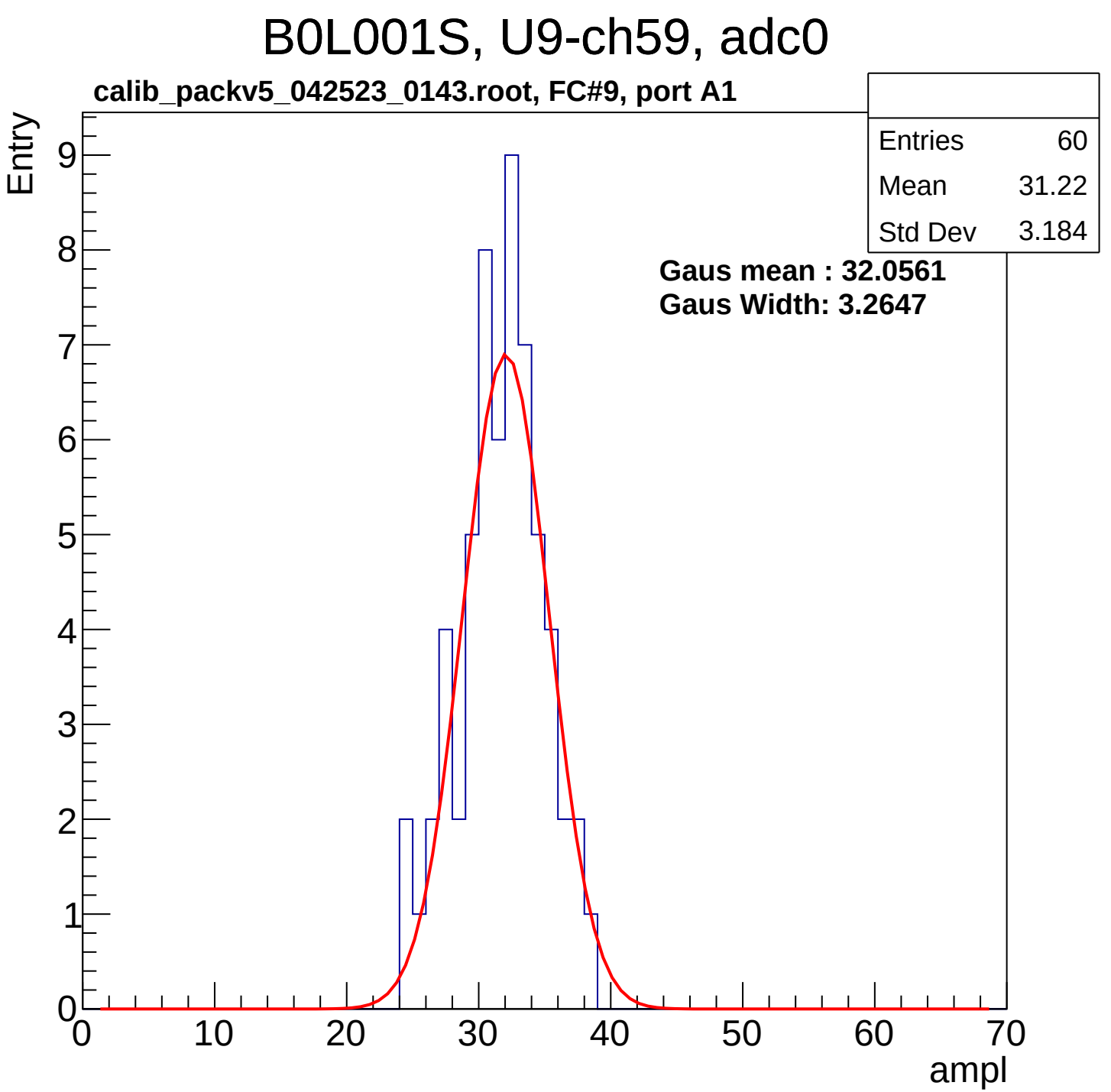
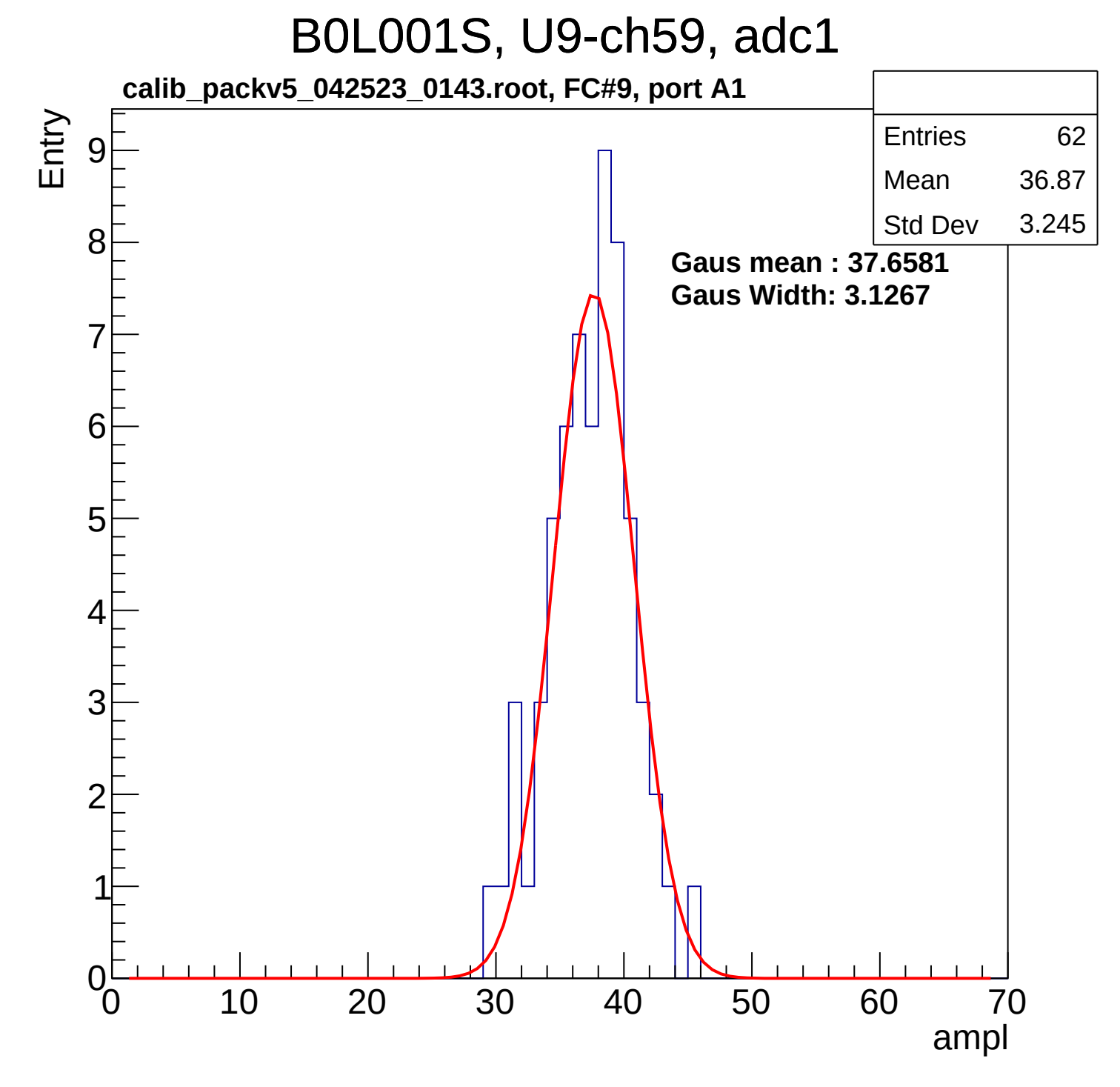
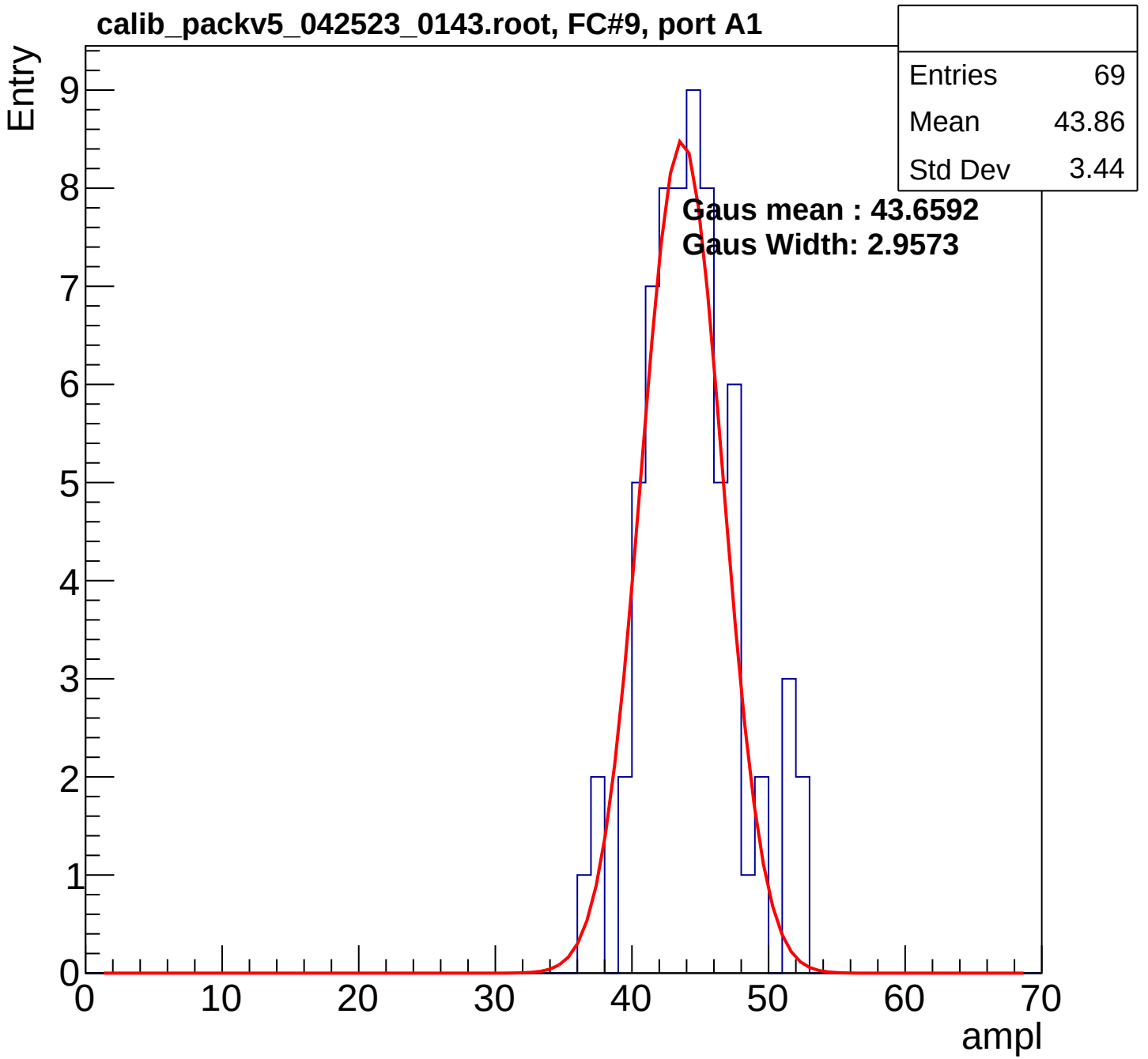

B0L001S, U9-ch58, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch58, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch59, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 31.22
Std Dev 3.184
Gaus mean : 32.0561
Gaus Width: 3.2647
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch59, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 36.87
Std Dev 3.245
Gaus mean : 37.6581
Gaus Width: 3.1267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch59, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 43.86
Std Dev 3.44
Gaus mean : 43.6592
Gaus Width: 2.9573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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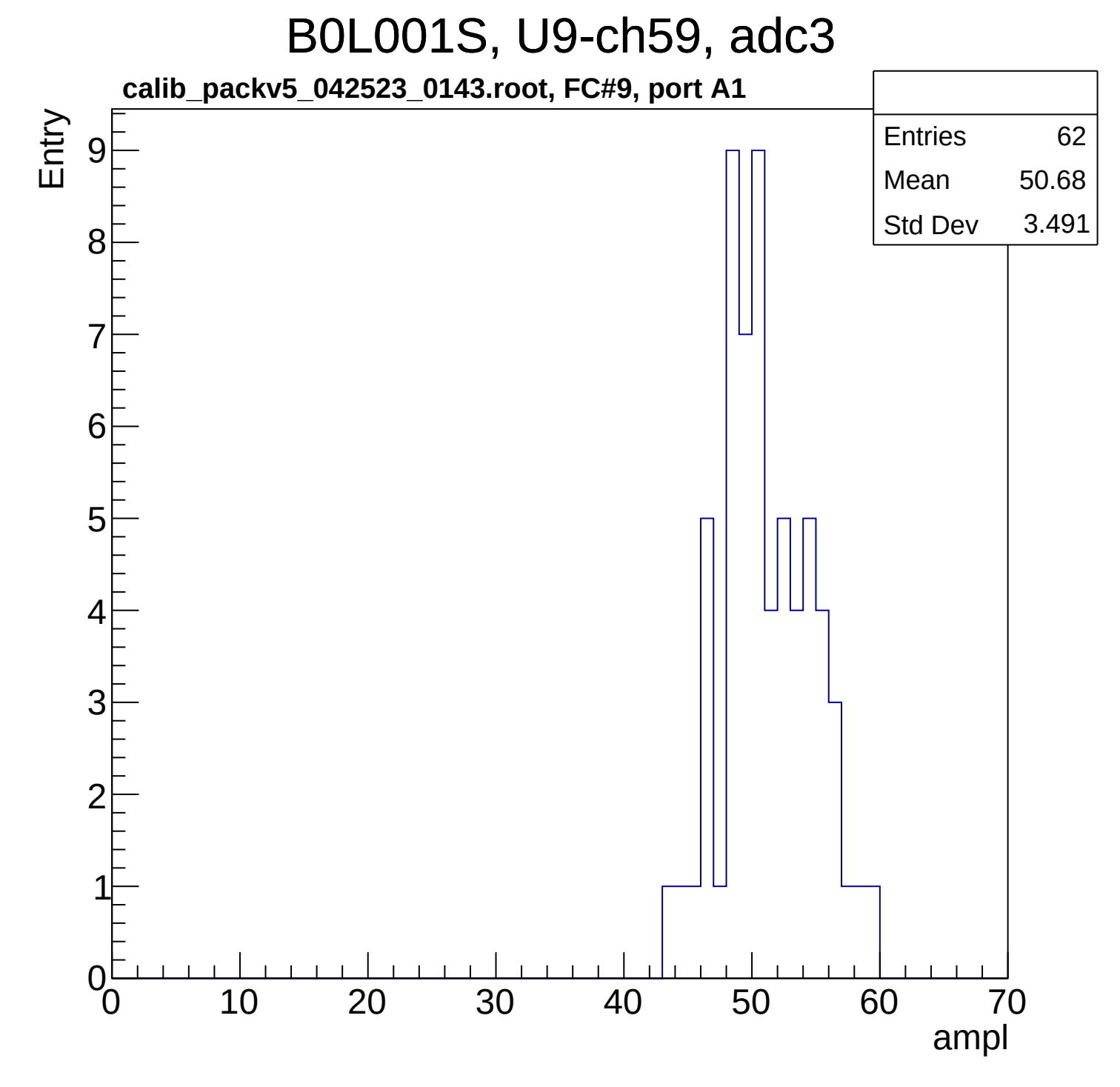
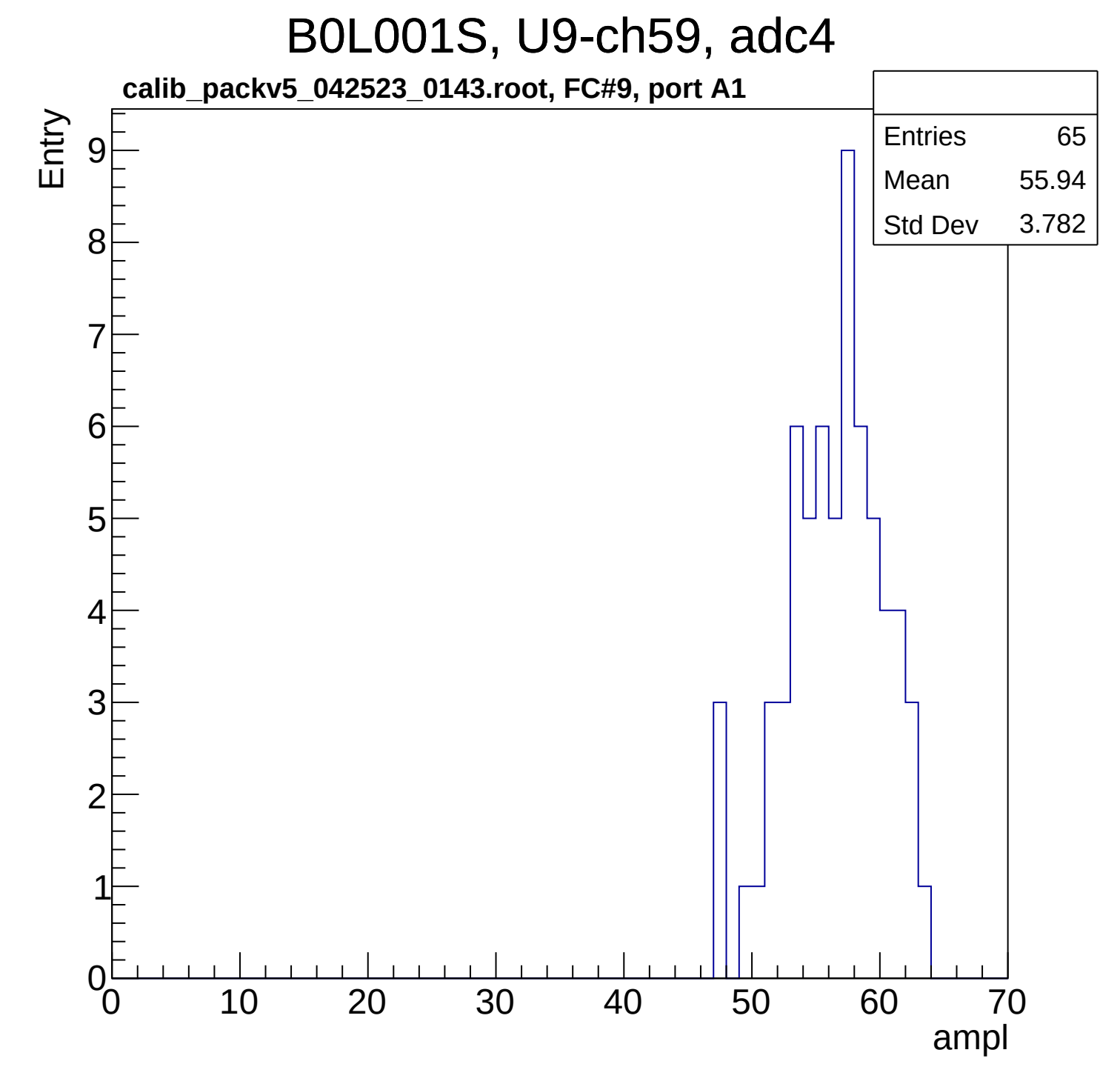
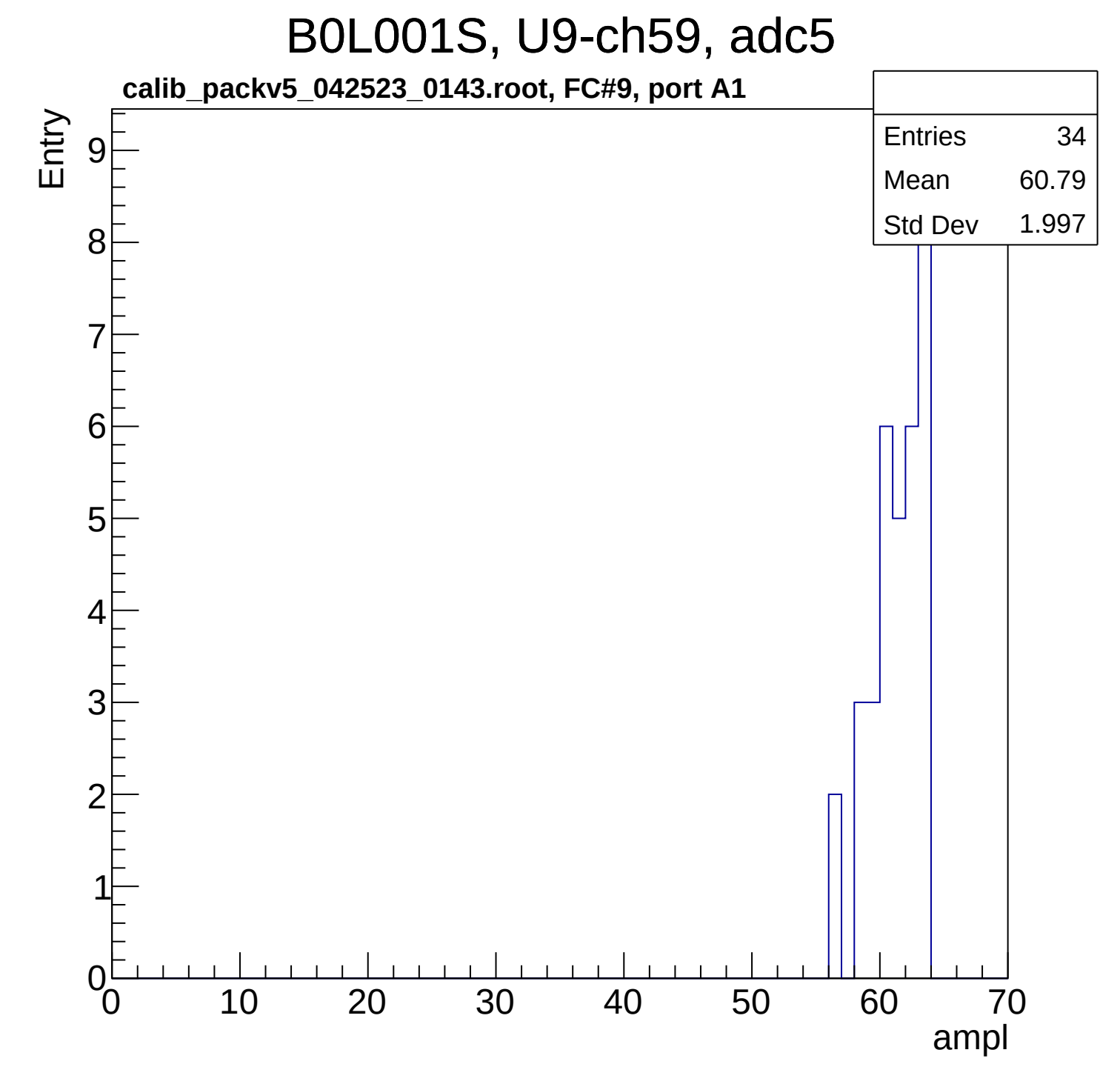
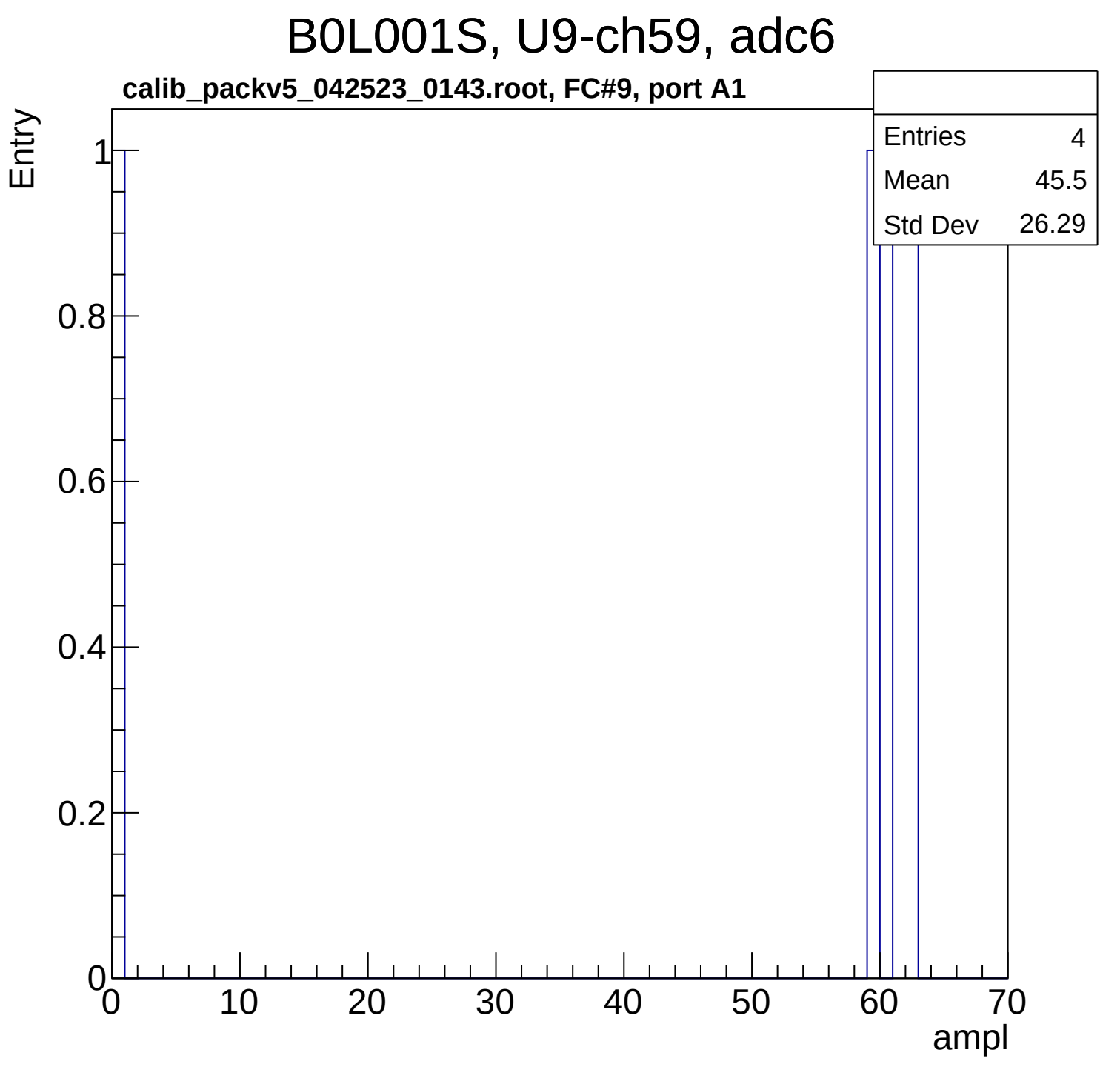
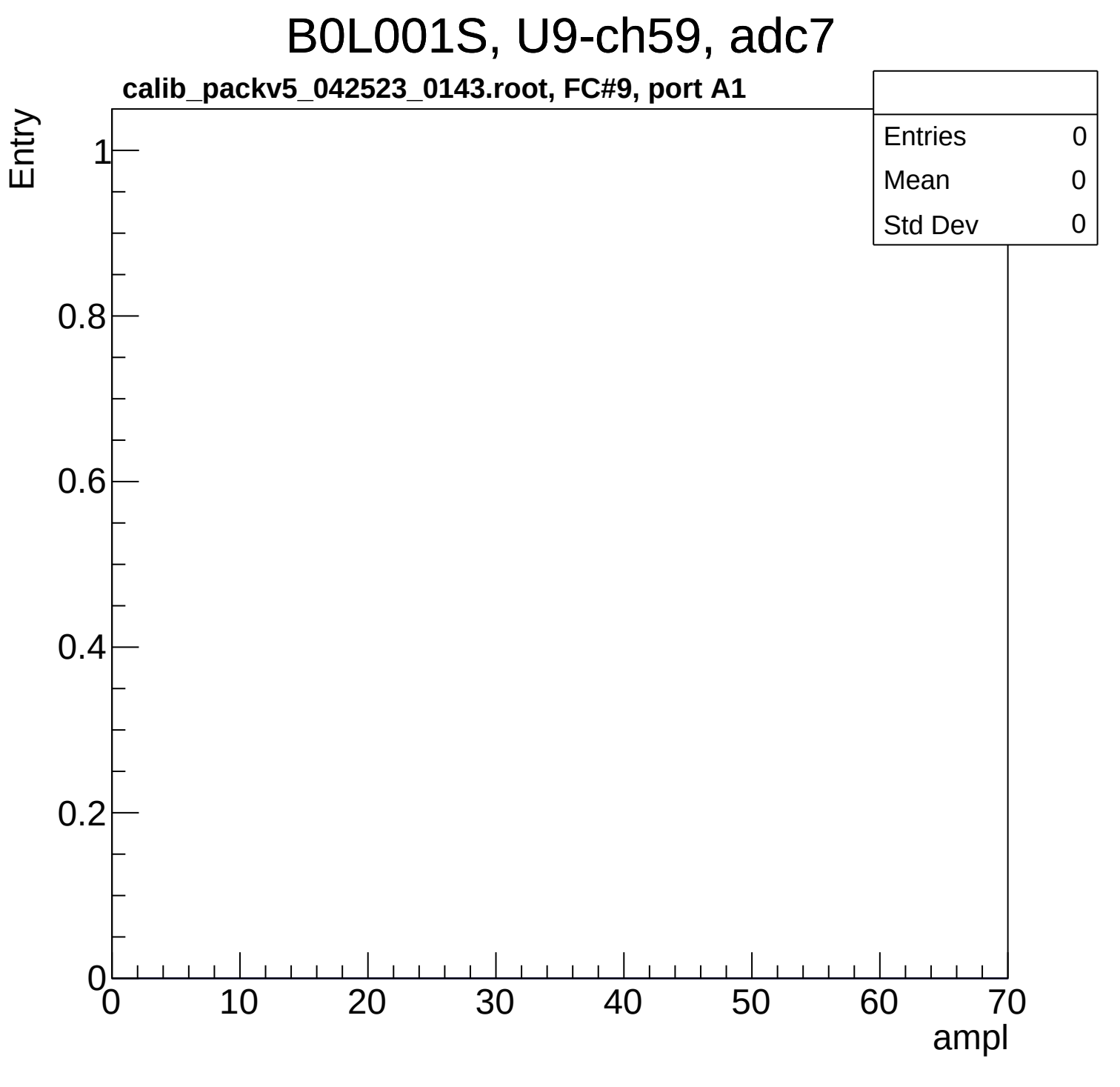
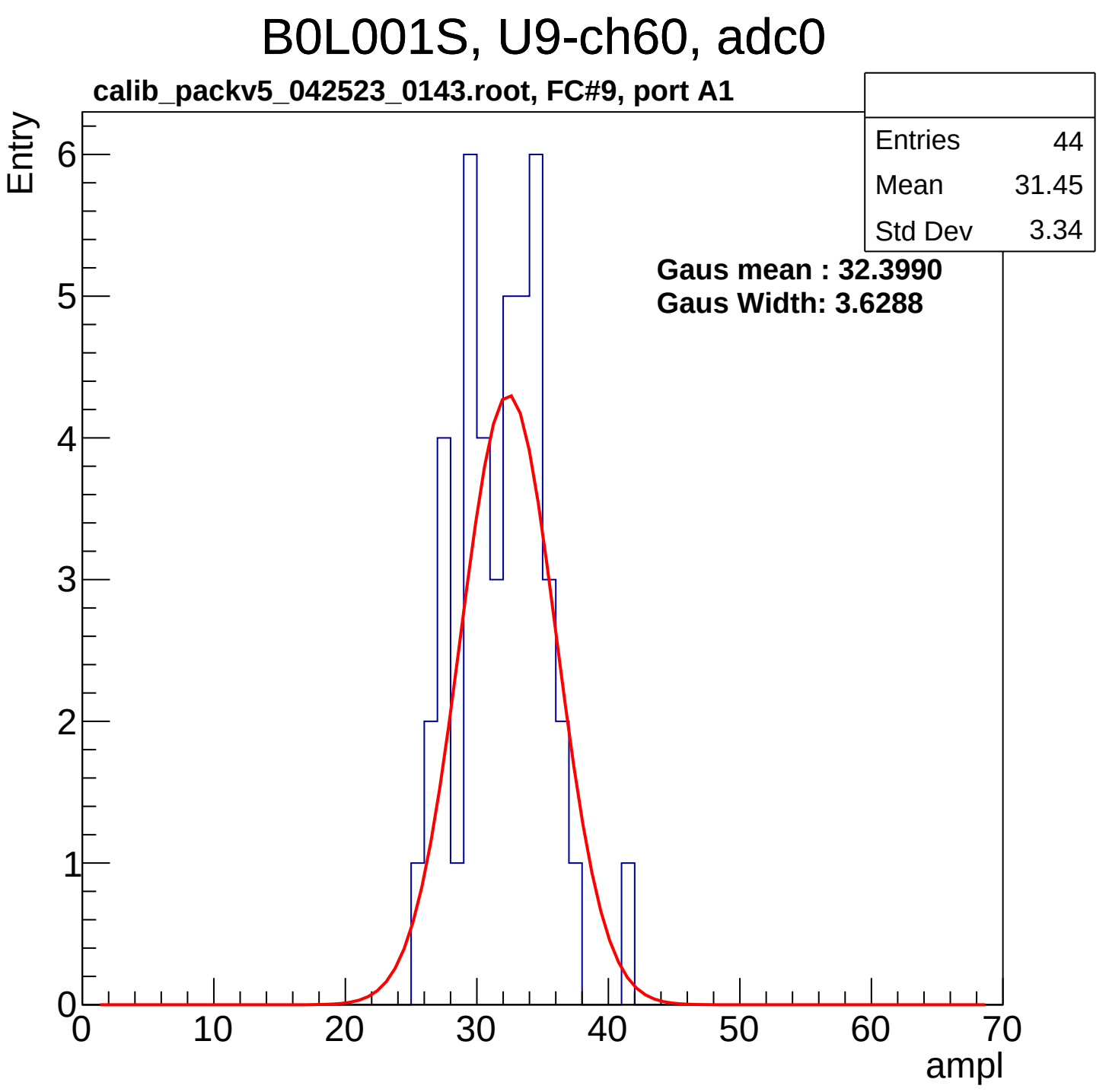

B0L001S, U9-ch59, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.68
Std Dev 3.491
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

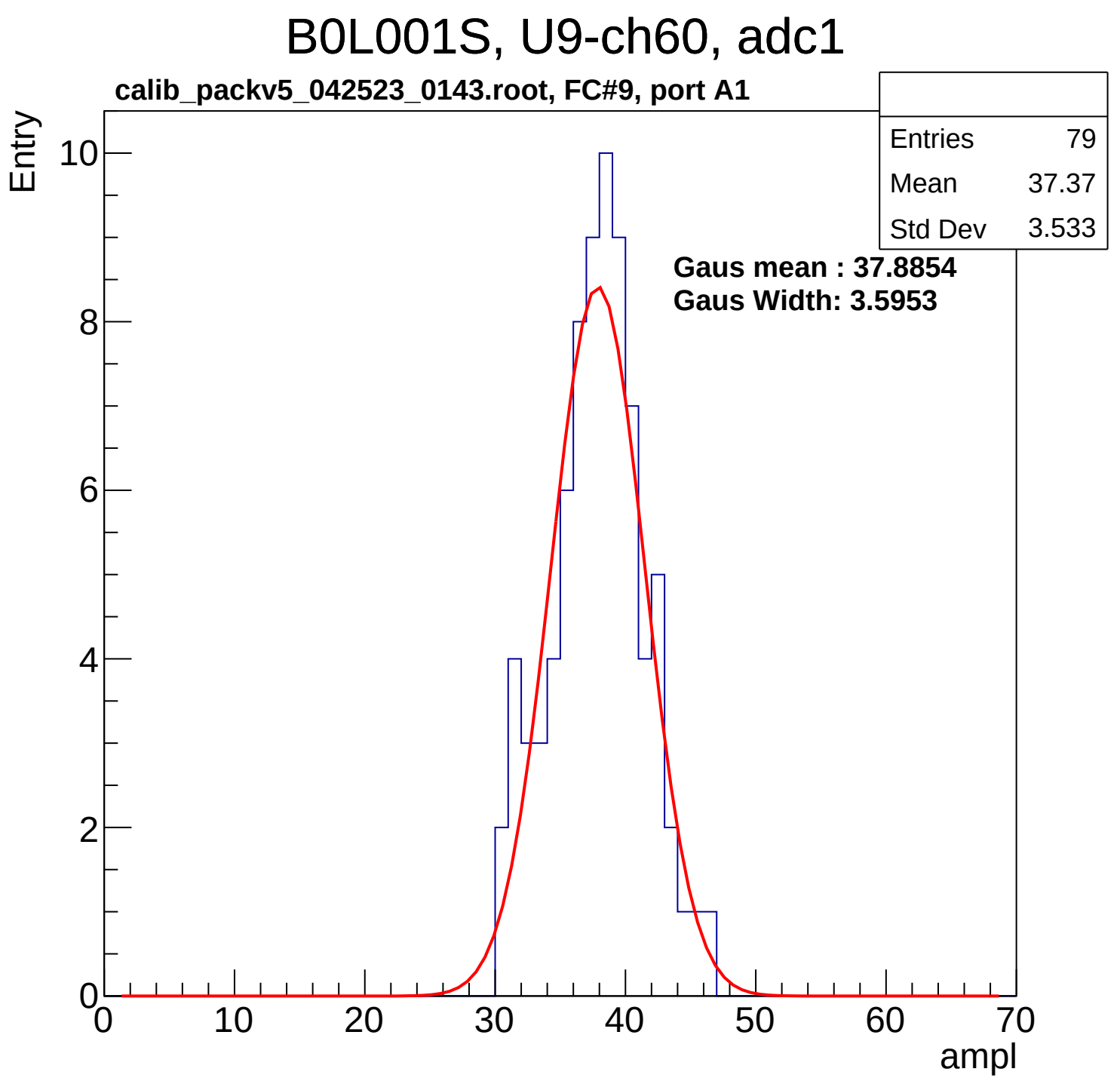

B0L001S, U9-ch59, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 55.94
Std Dev 3.782
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

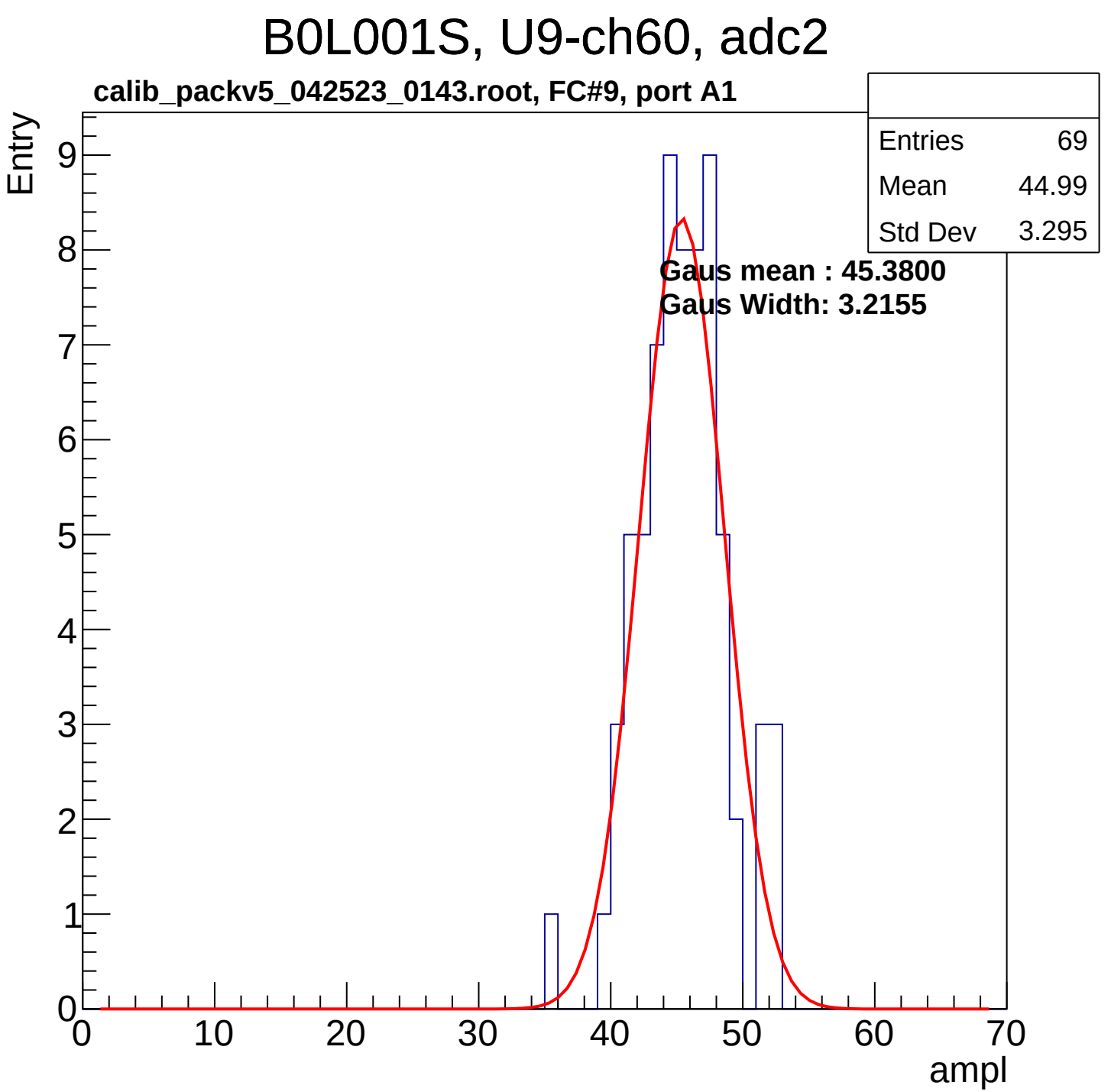

B0L001S, U9-ch59, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 60.79
Std Dev 1.997
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch59, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 45.5
Std Dev 26.29
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch59, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch60, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 31.45
Std Dev 3.34
Gaus mean : 32.3990
Gaus Width: 3.6288
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U9-ch60, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 37.37
Std Dev 3.533
Gaus mean : 37.8854
Gaus Width: 3.5953
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch60, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 44.99
Std Dev 3.295
Gaus mean : 45.3800
Gaus Width: 3.2155
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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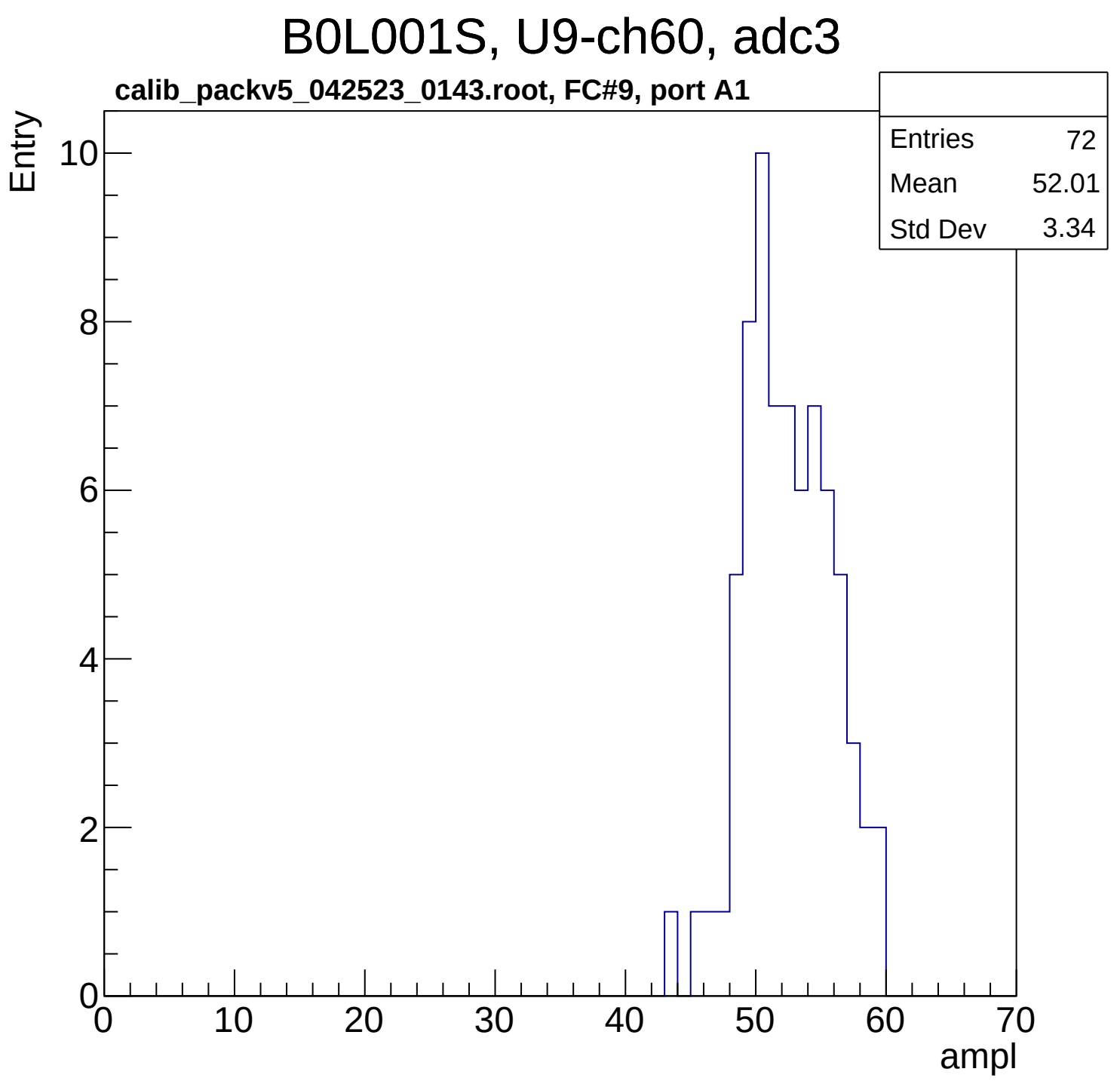
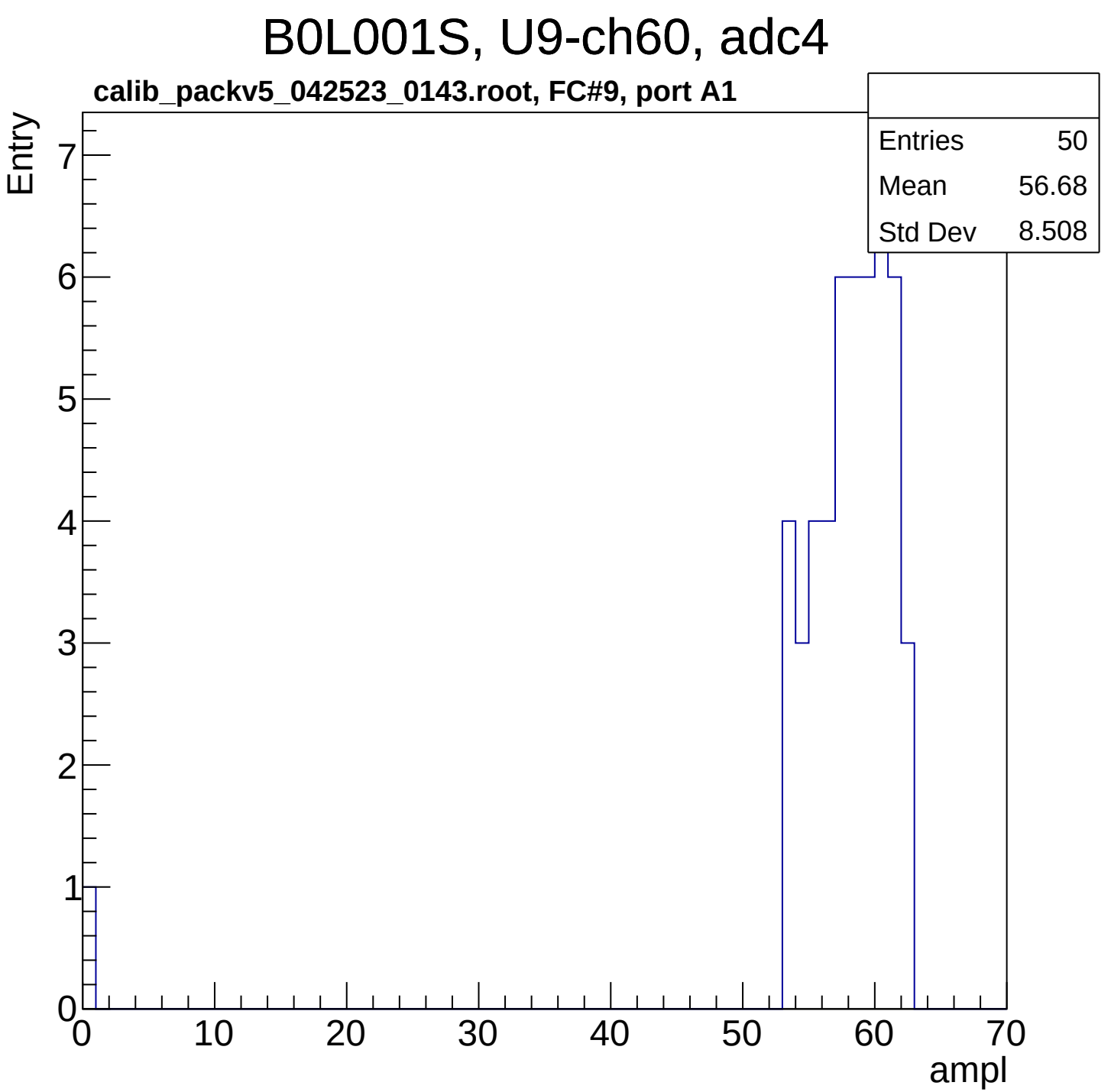
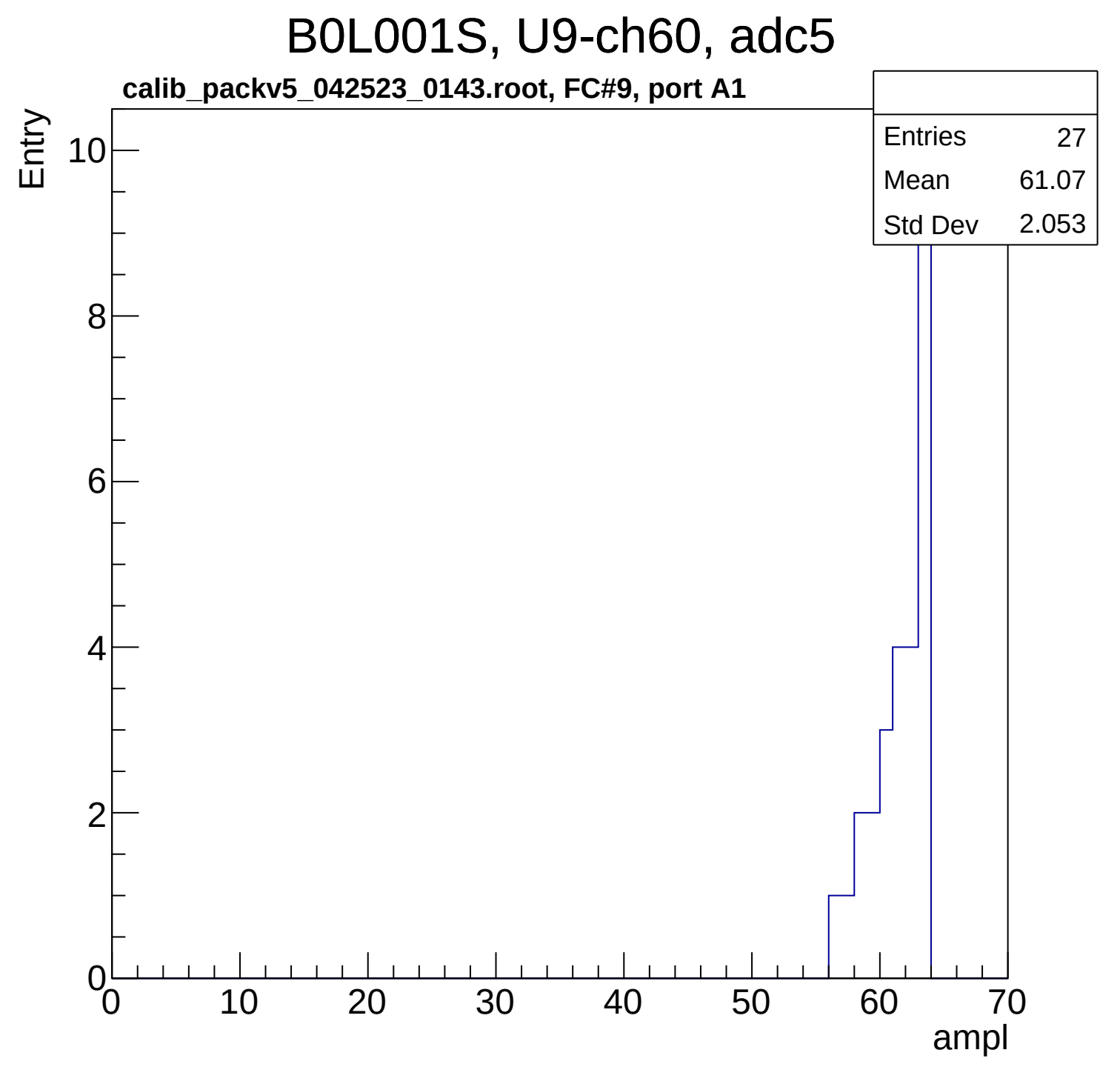
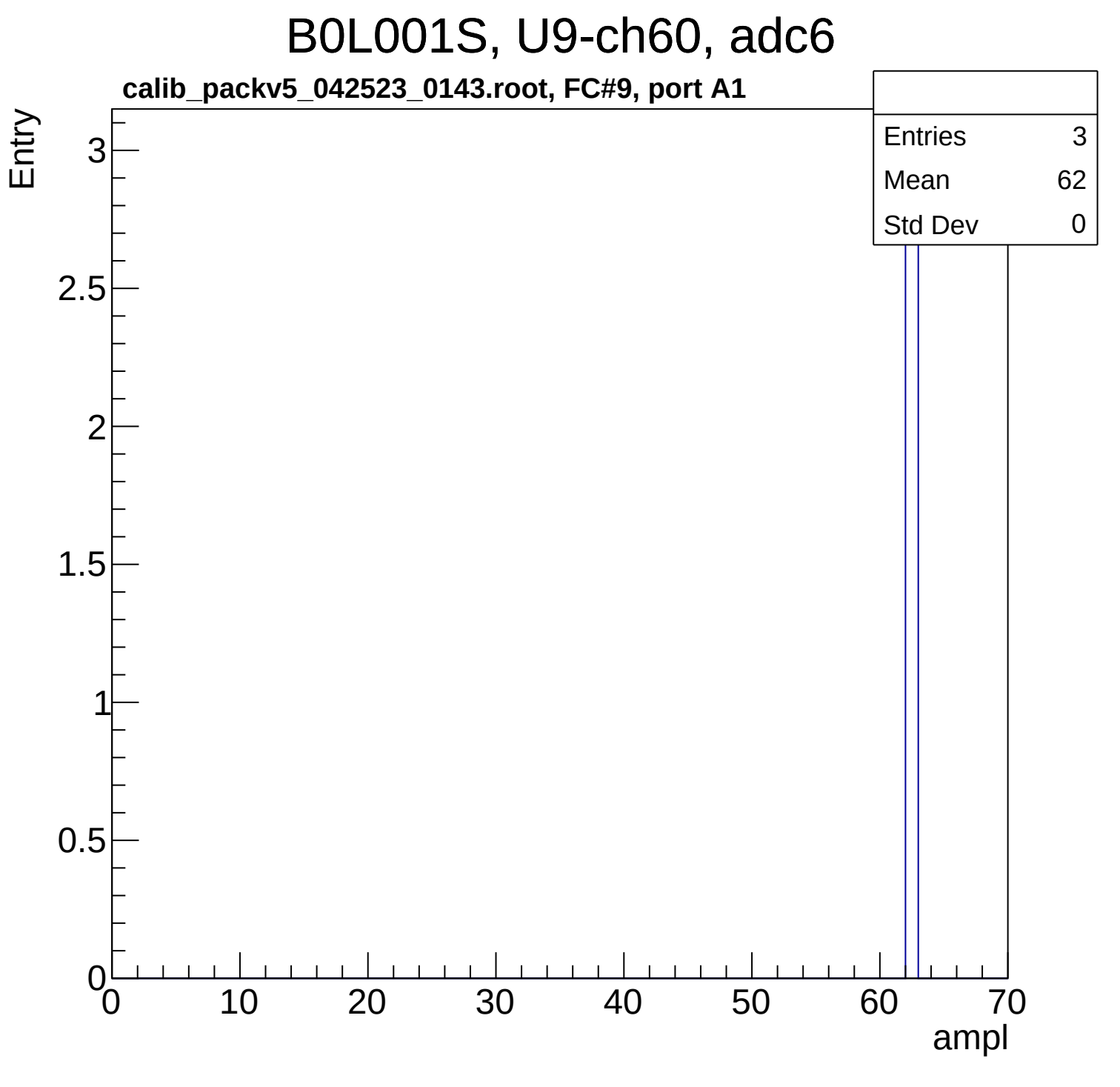
40
50
60
70


B0L001S, U9-ch60, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 52.01
Std Dev 3.34
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch60, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 56.68
Std Dev 8.508
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch60, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 61.07
Std Dev 2.053
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch60, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62
Std Dev 0
Entry
ampl
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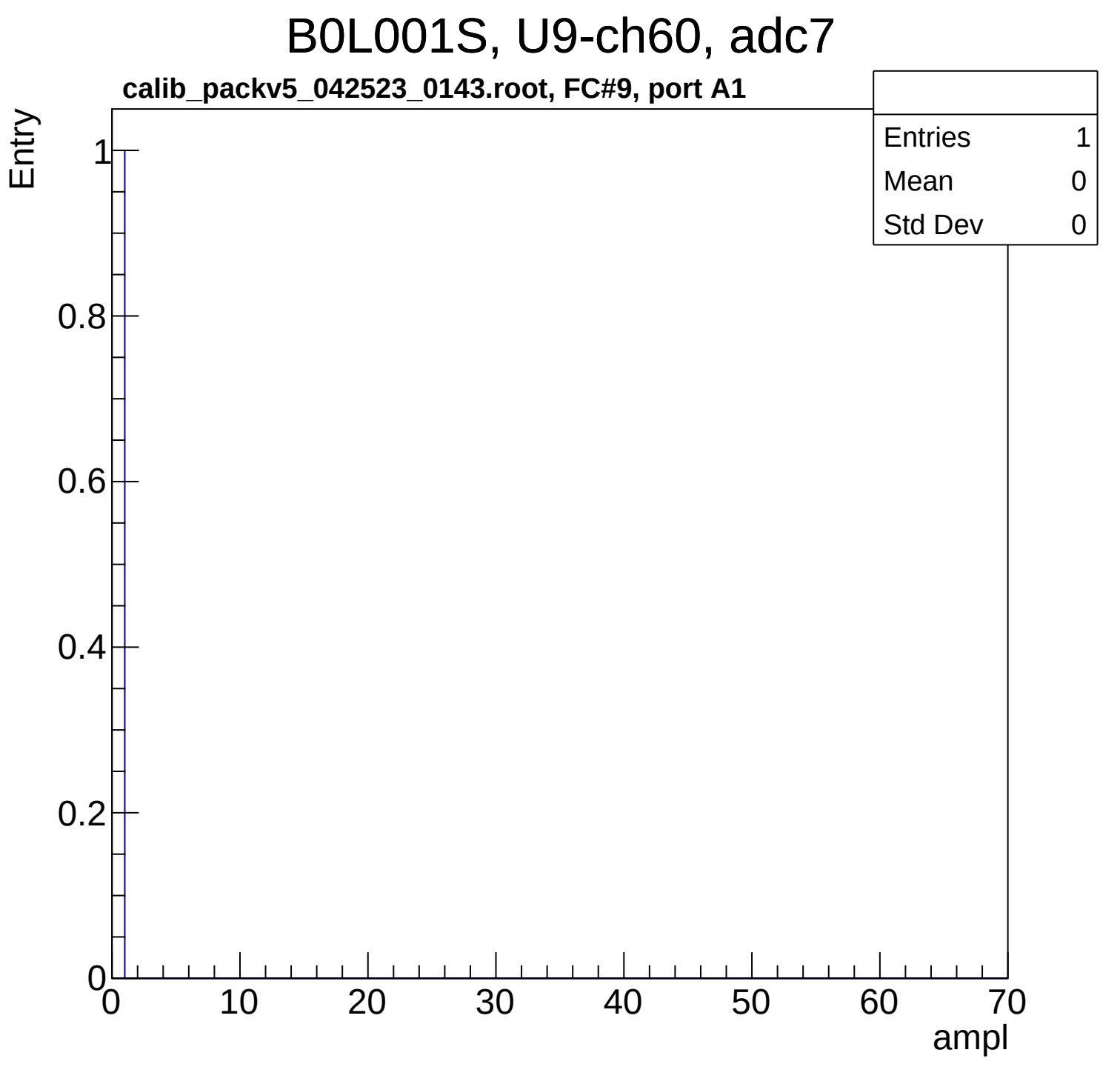
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch60, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
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0
10
20
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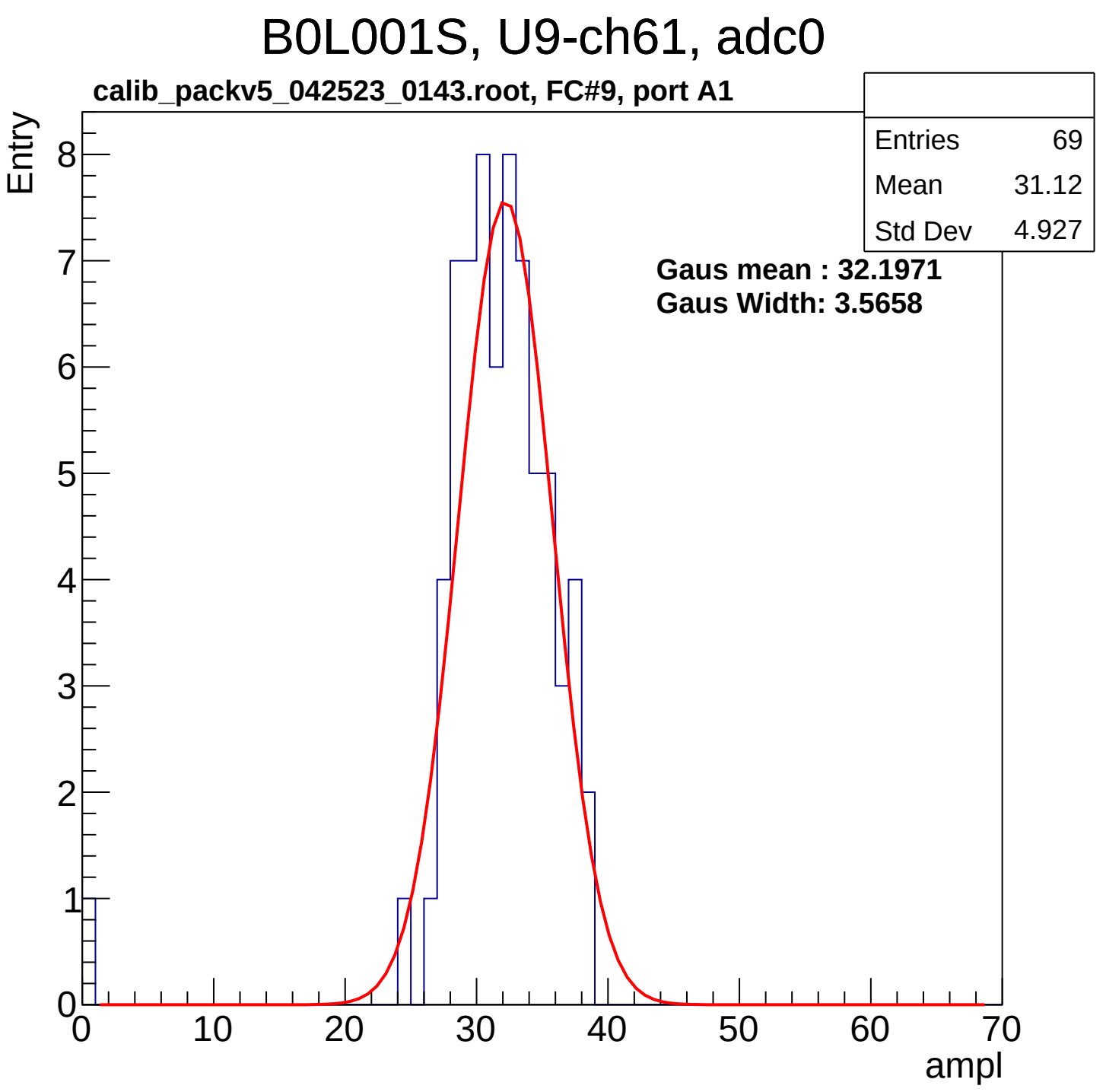
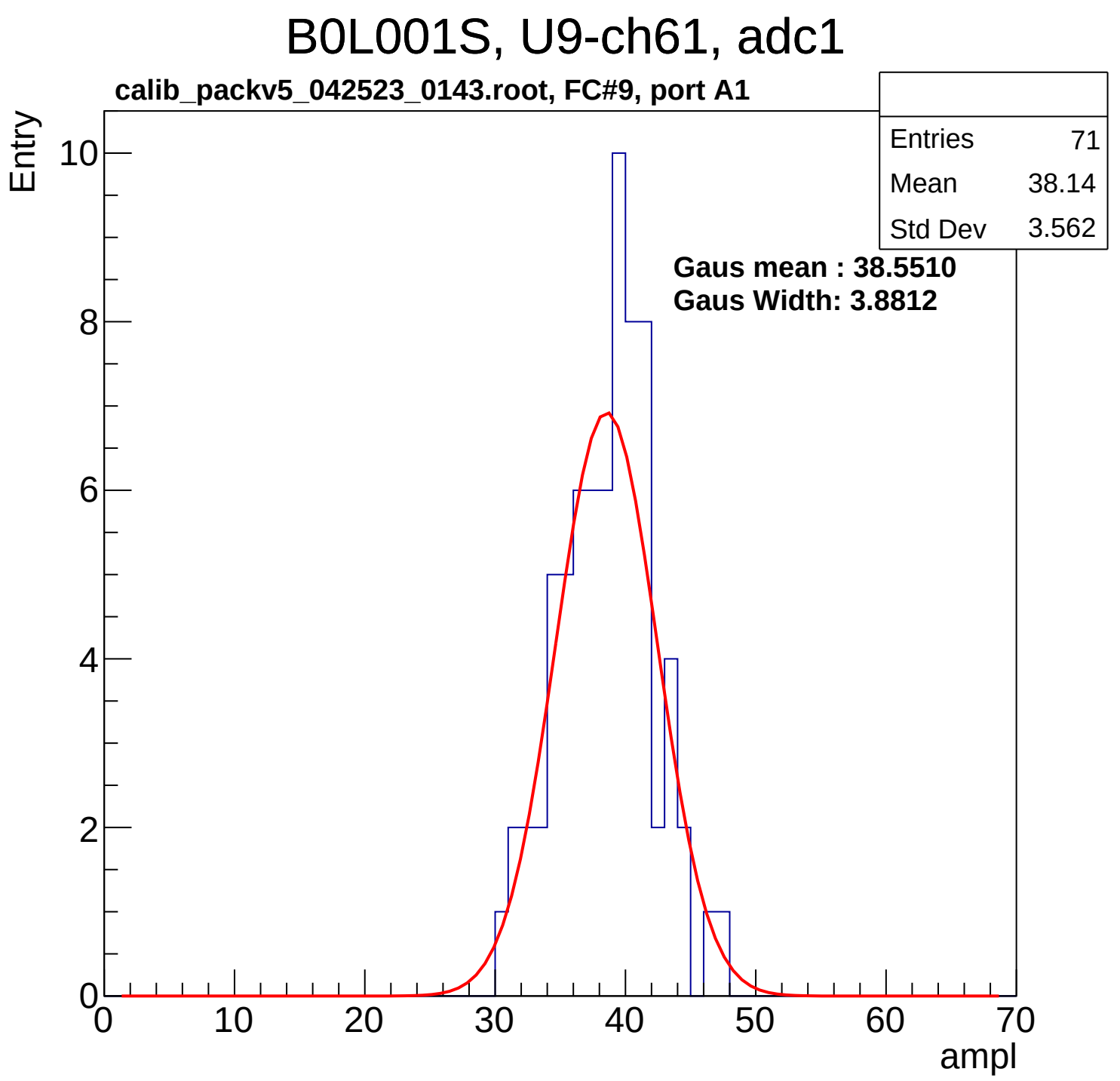
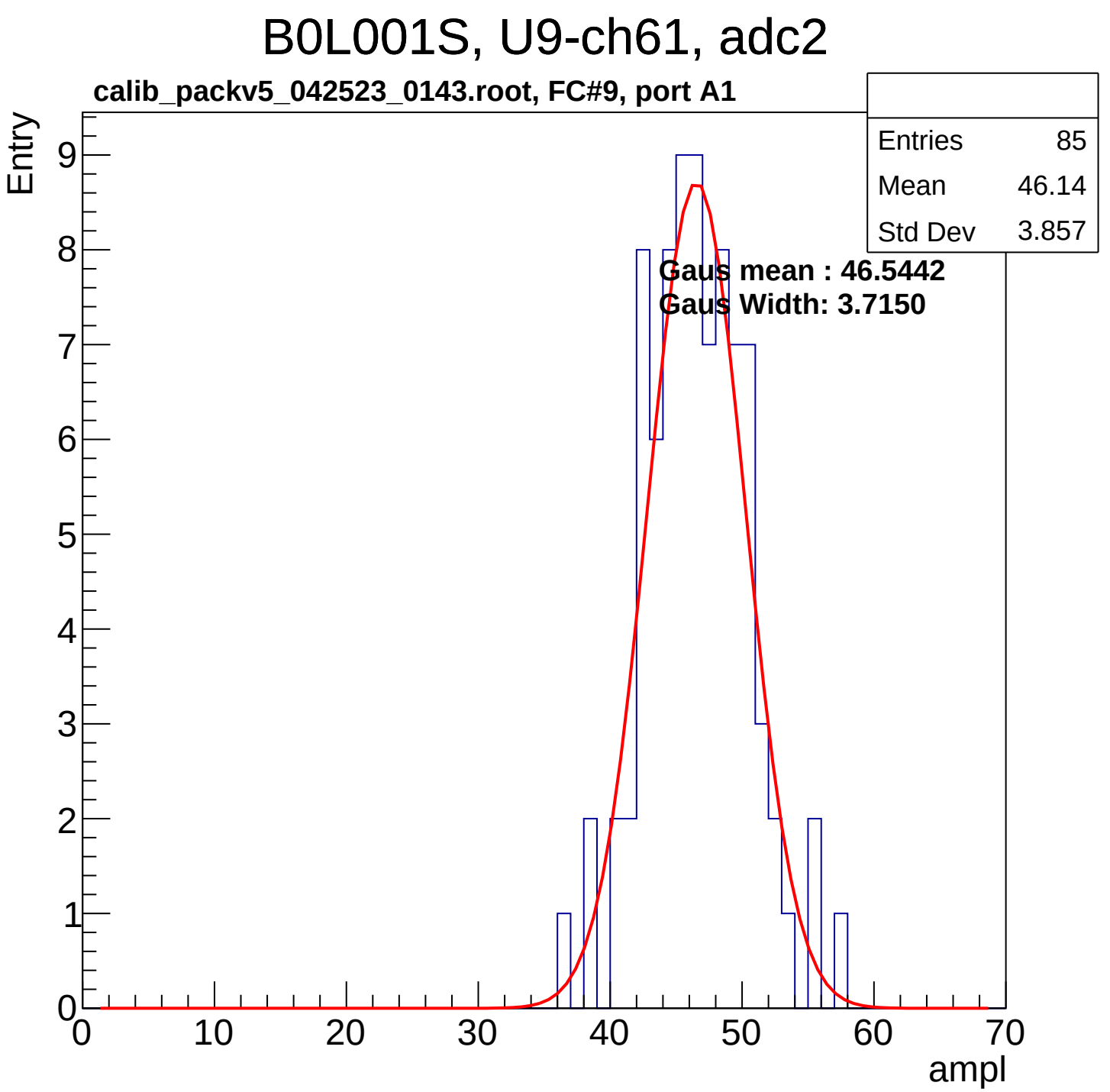
40
50
60
70
ampl


B0L001S, U9-ch61, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 31.12
Std Dev 4.927
Gaus mean : 32.1971
Gaus Width: 3.5658
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch61, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 38.14
Std Dev 3.562
Gaus mean : 38.5510
Gaus Width: 3.8812
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch61, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 46.14
Std Dev 3.857
Gaus mean : 46.5442
Gaus Width: 3.7150
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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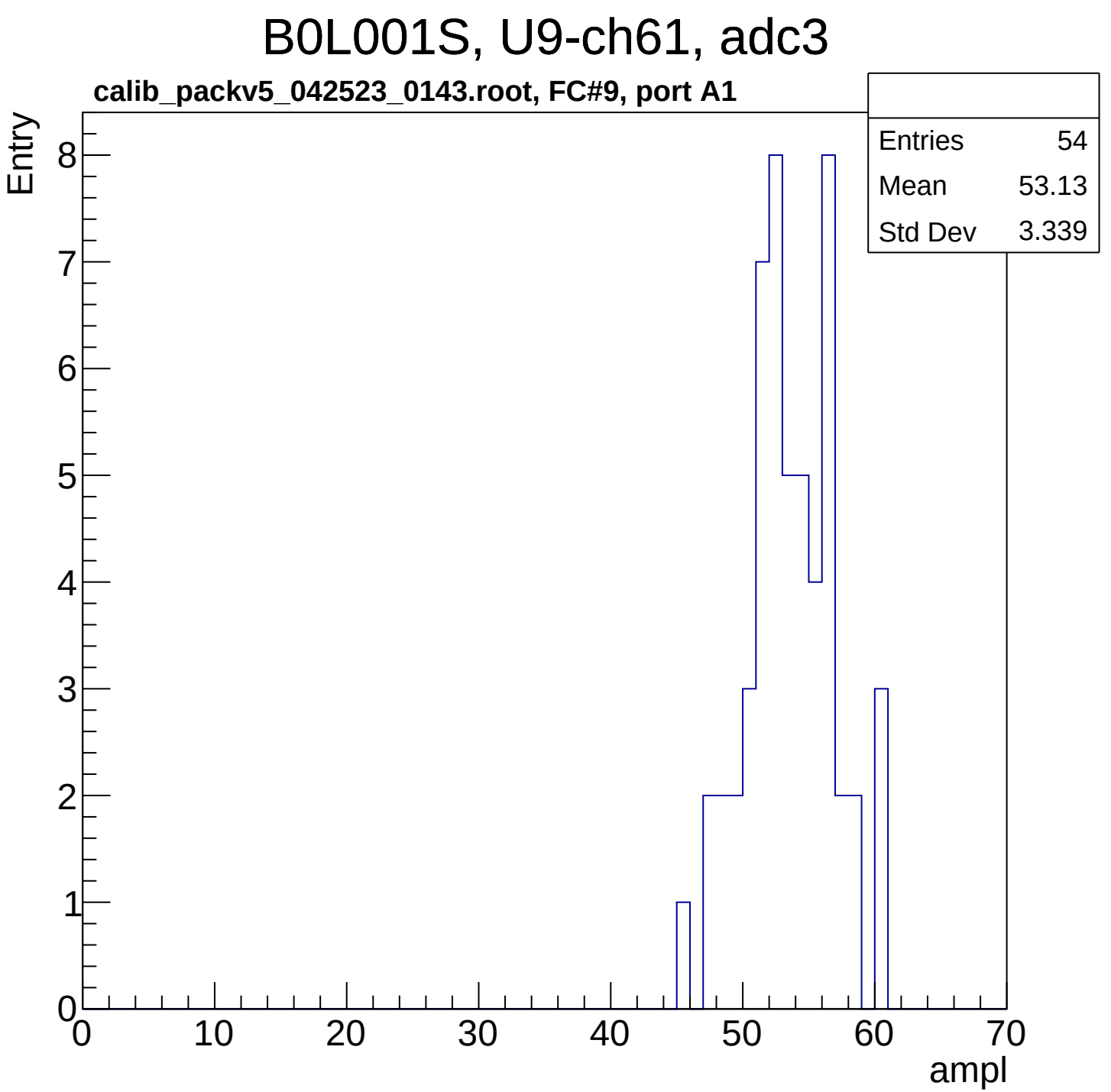
B0L001S, U9-ch61, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 53.13
Std Dev 3.339
Entry
ampl
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1
2
3
4
5
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0
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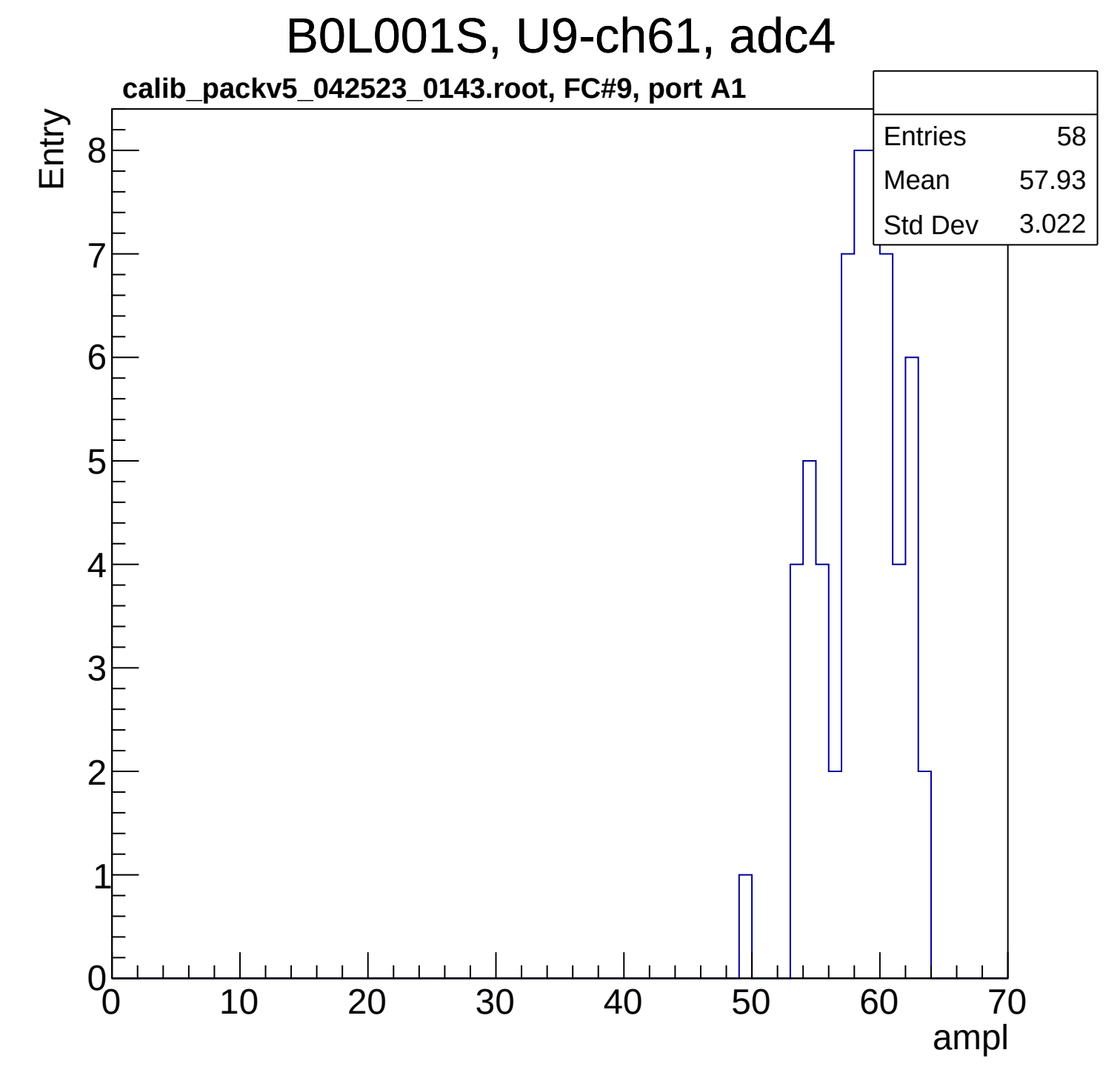
B0L001S, U9-ch61, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.93
Std Dev 3.022
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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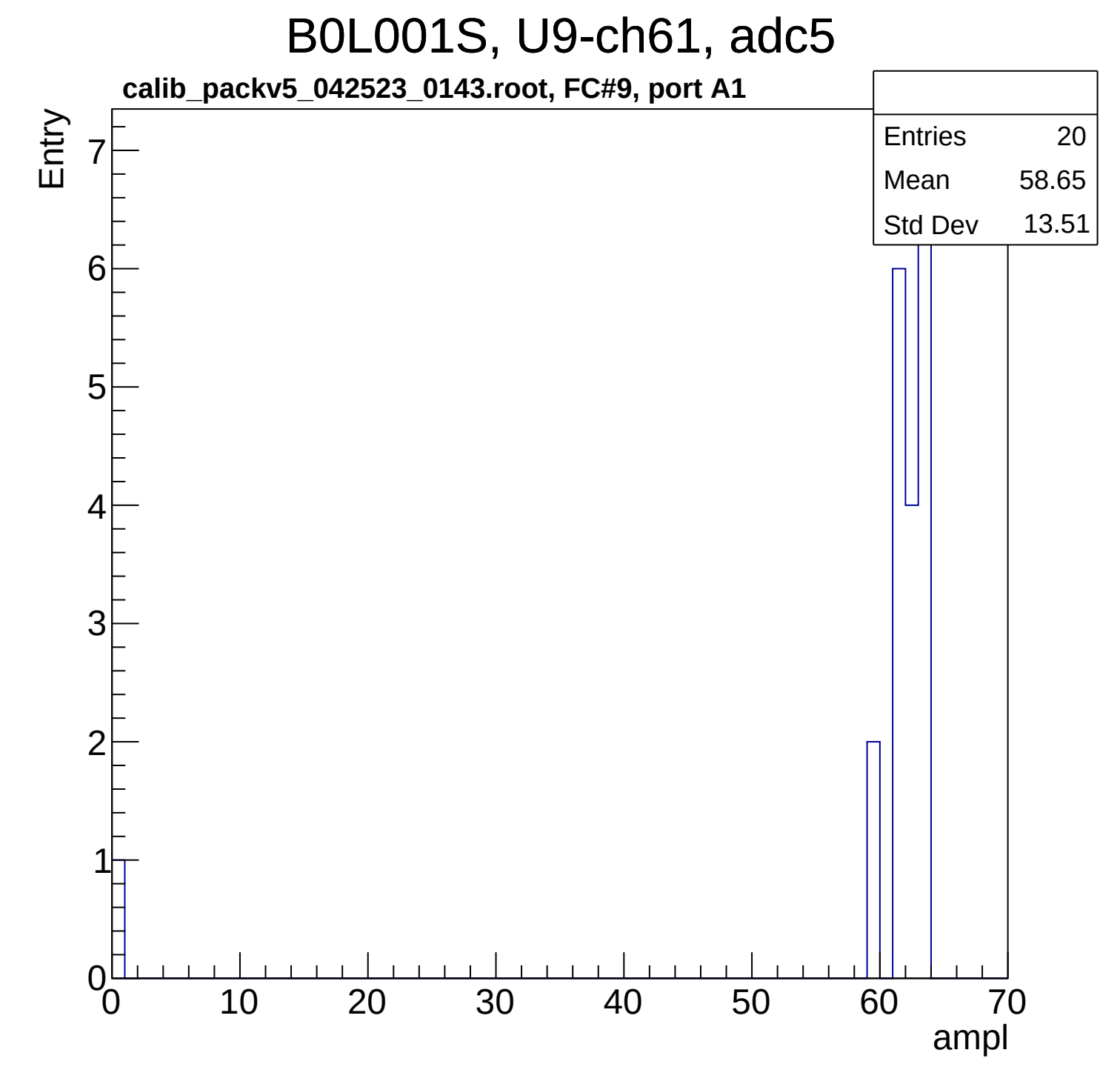
30
40
50
60
70


B0L001S, U9-ch61, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 58.65
Std Dev 13.51
Entry
0
1
2
3
4
5
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7
0
10
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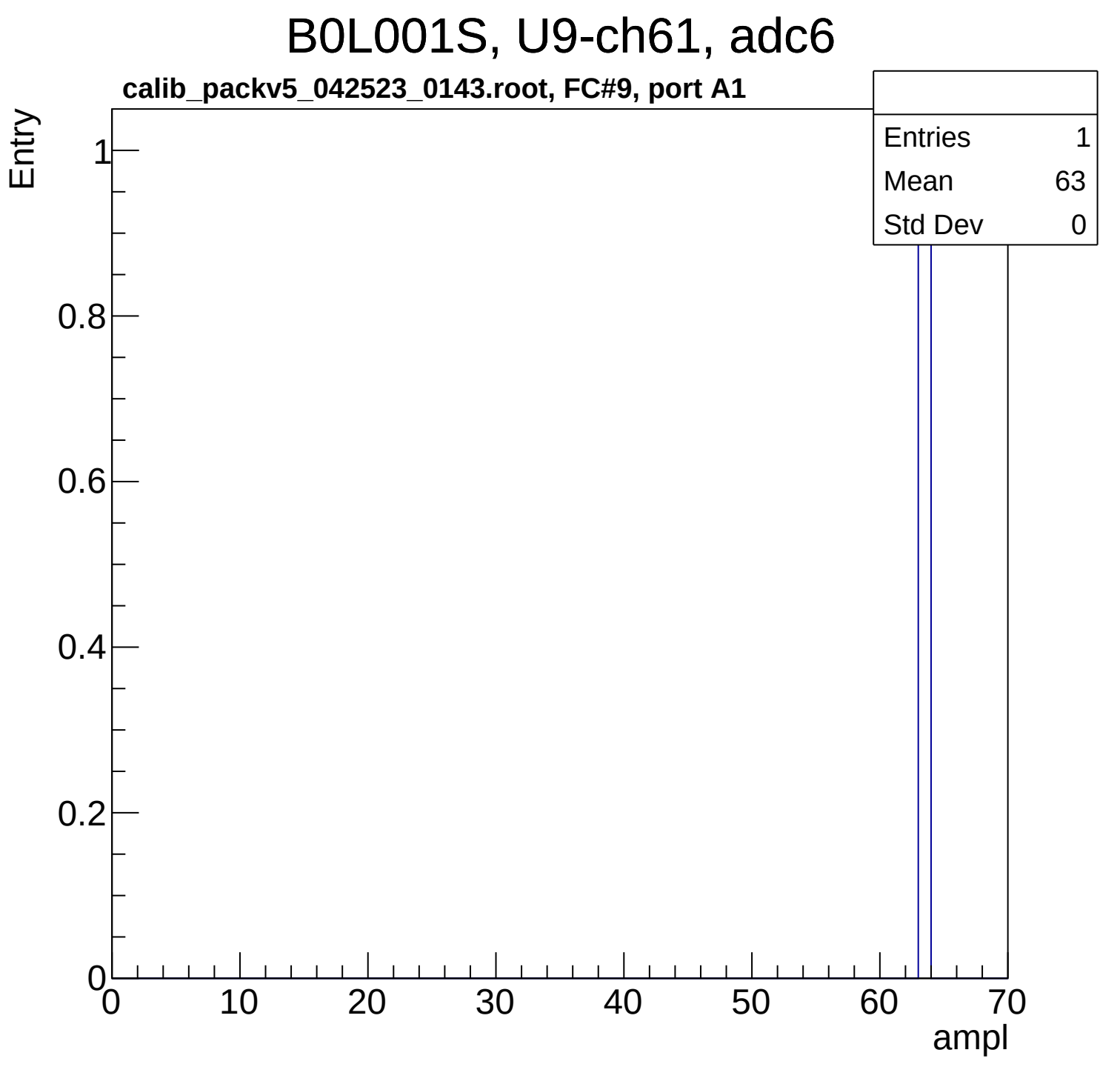
50
60
70
ampl


B0L001S, U9-ch61, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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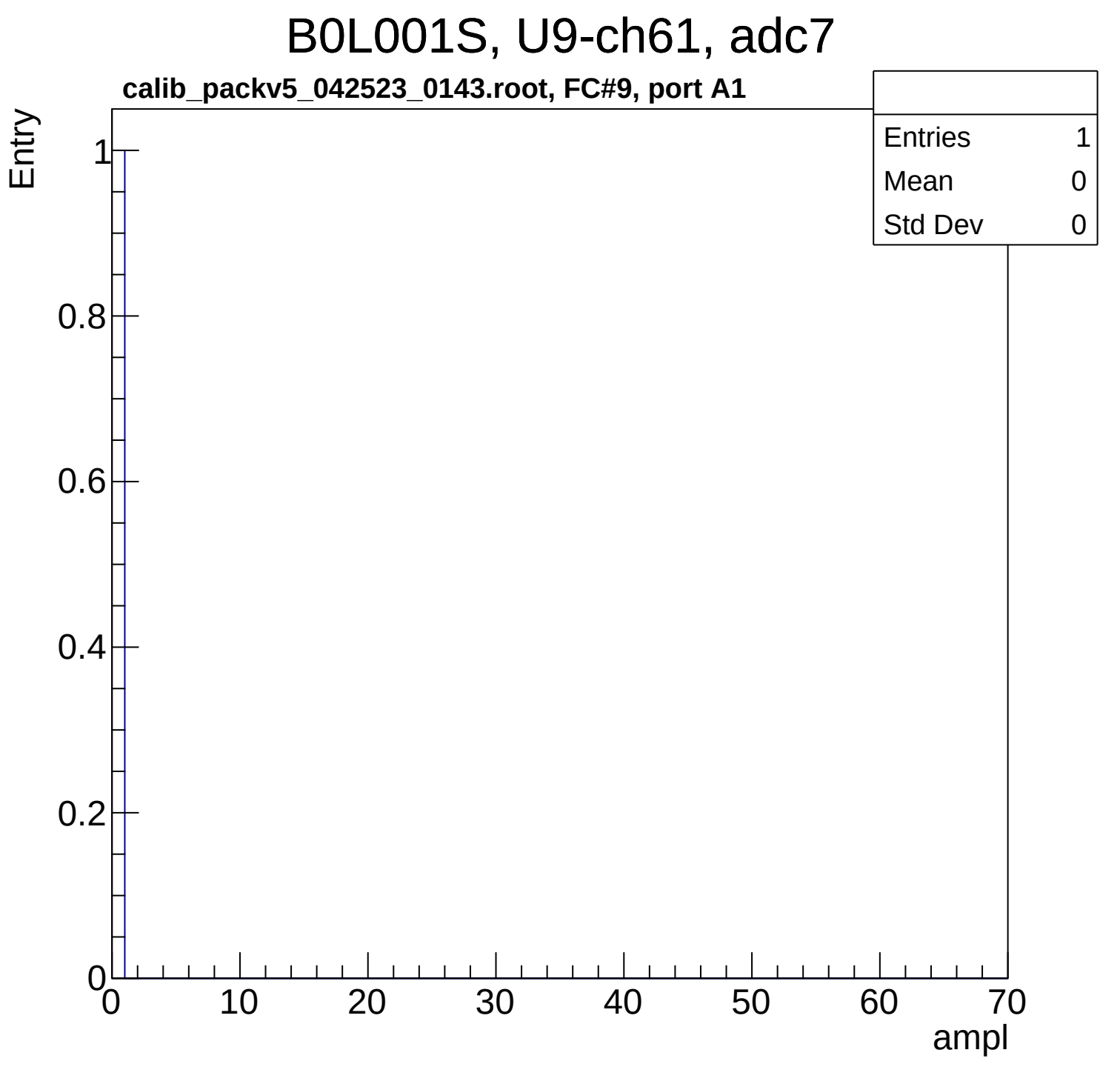
50
60
70
ampl


B0L001S, U9-ch61, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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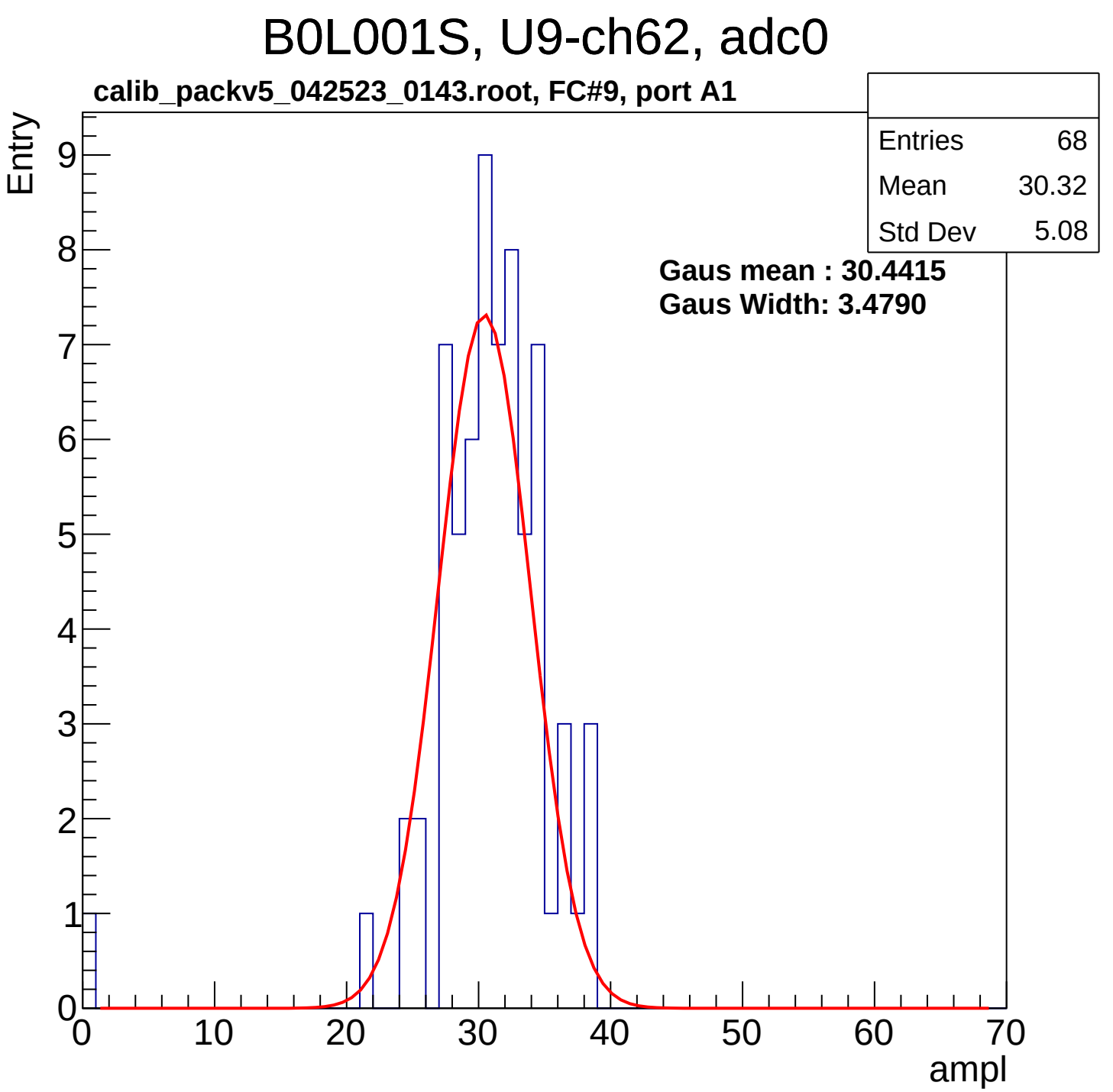
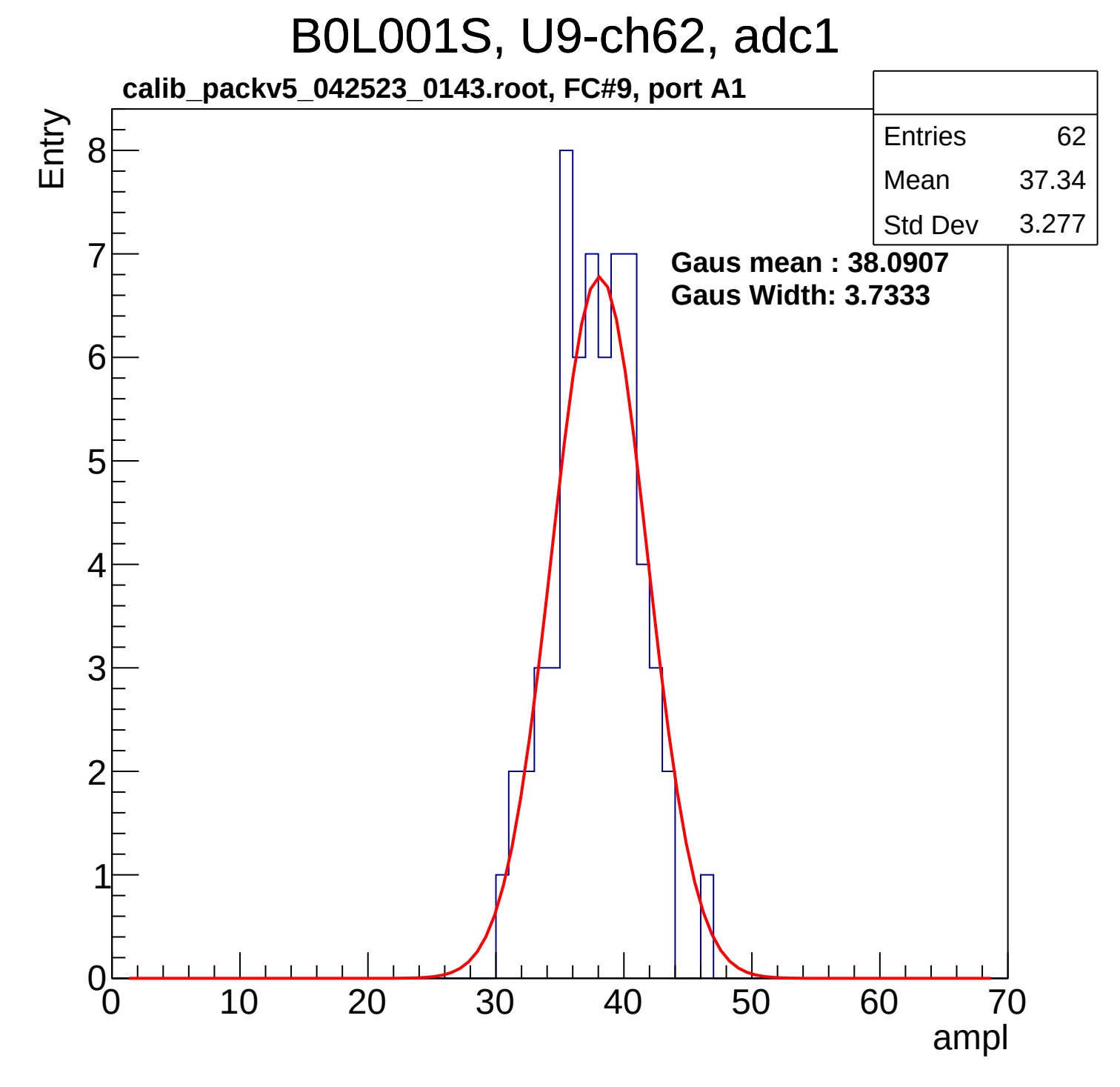
50
60
70
ampl


B0L001S, U9-ch62, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.32
Std Dev 5.08
Gaus mean : 30.4415
Gaus Width: 3.4790
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch62, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 37.34
Std Dev 3.277
Gaus mean : 38.0907
Gaus Width: 3.7333
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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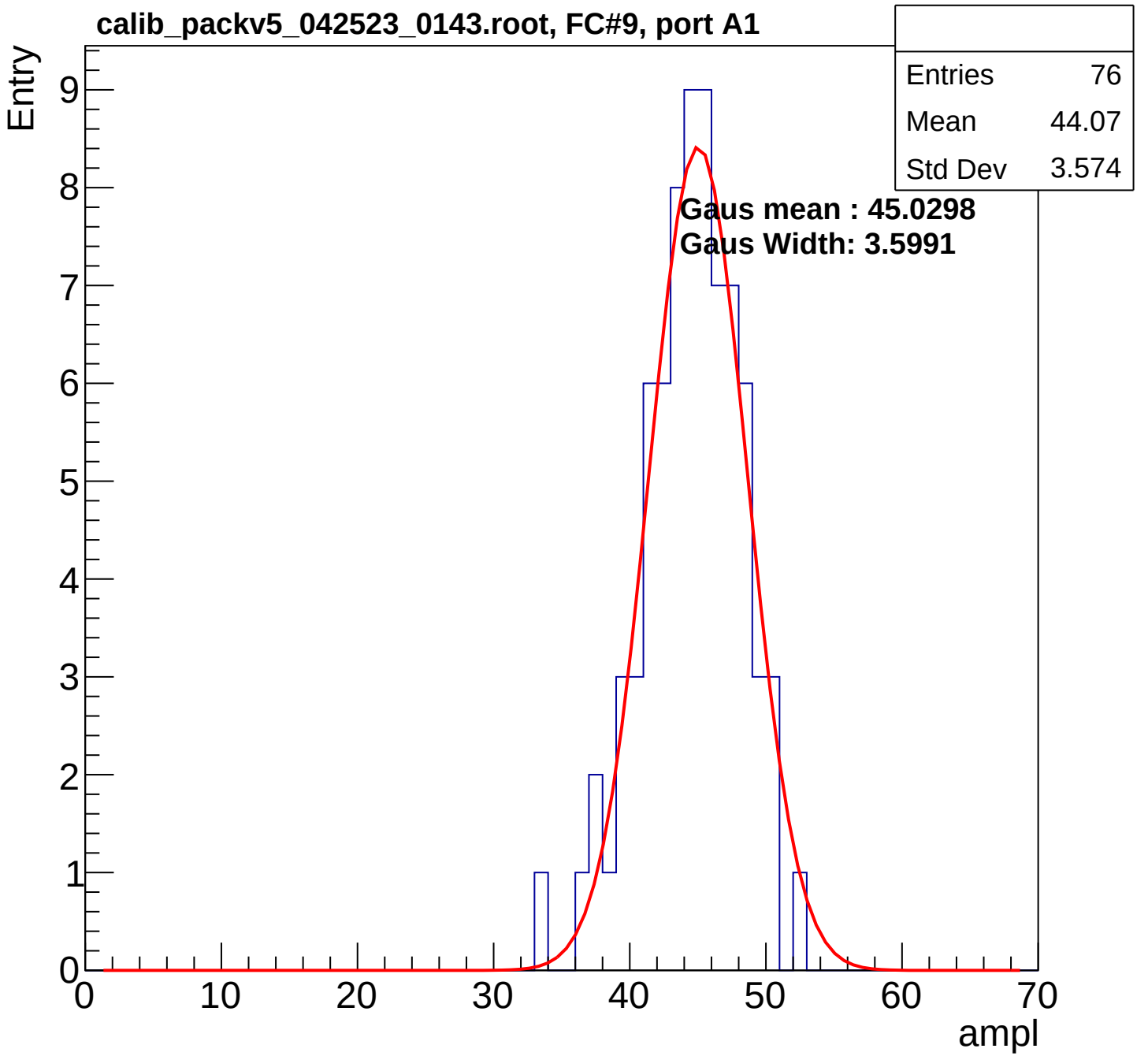
60
70


B0L001S, U9-ch62, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 44.07
Std Dev 3.574
Gaus mean : 45.0298
Gaus Width: 3.5991
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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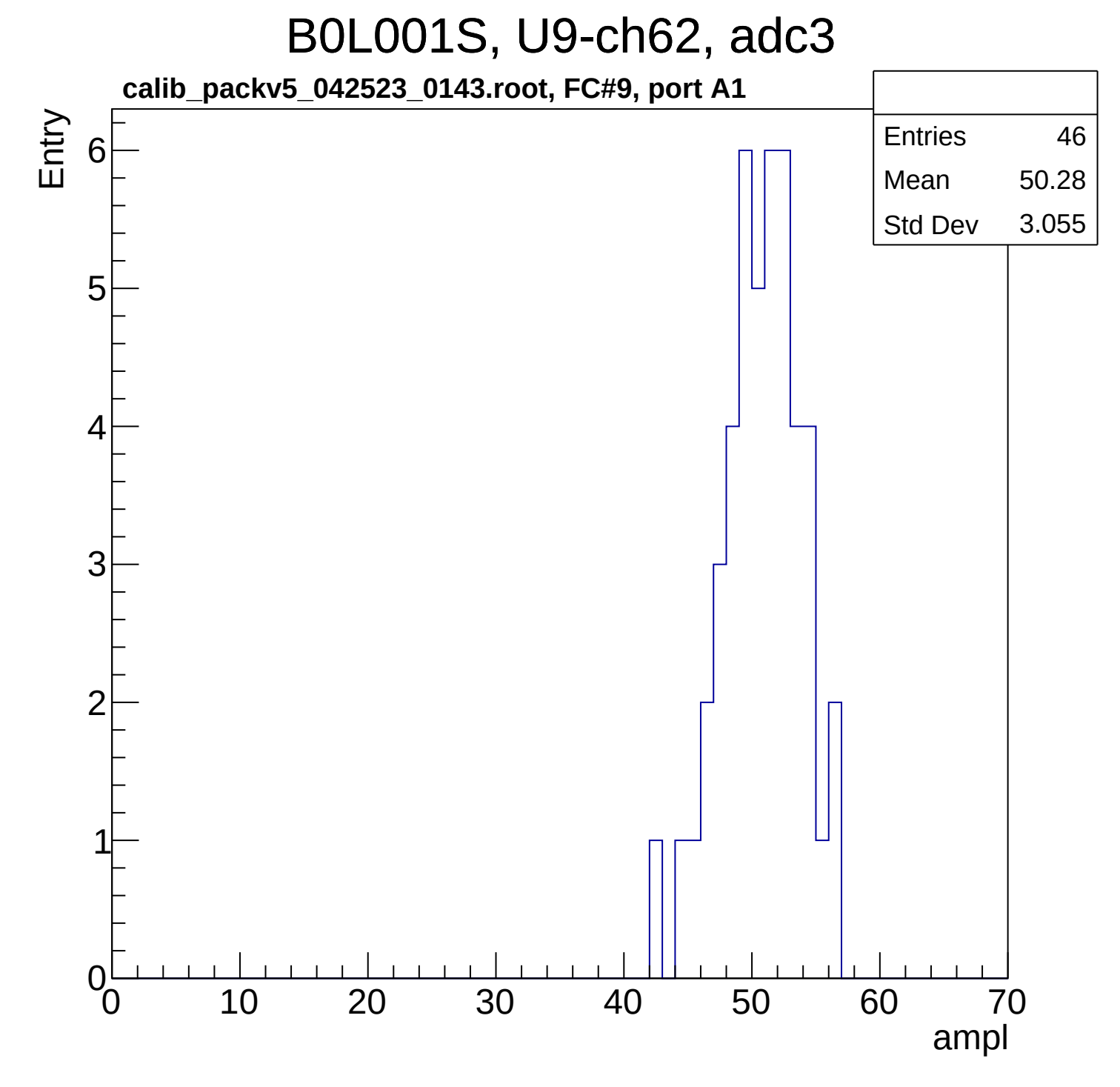
B0L001S, U9-ch62, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 50.28
Std Dev 3.055
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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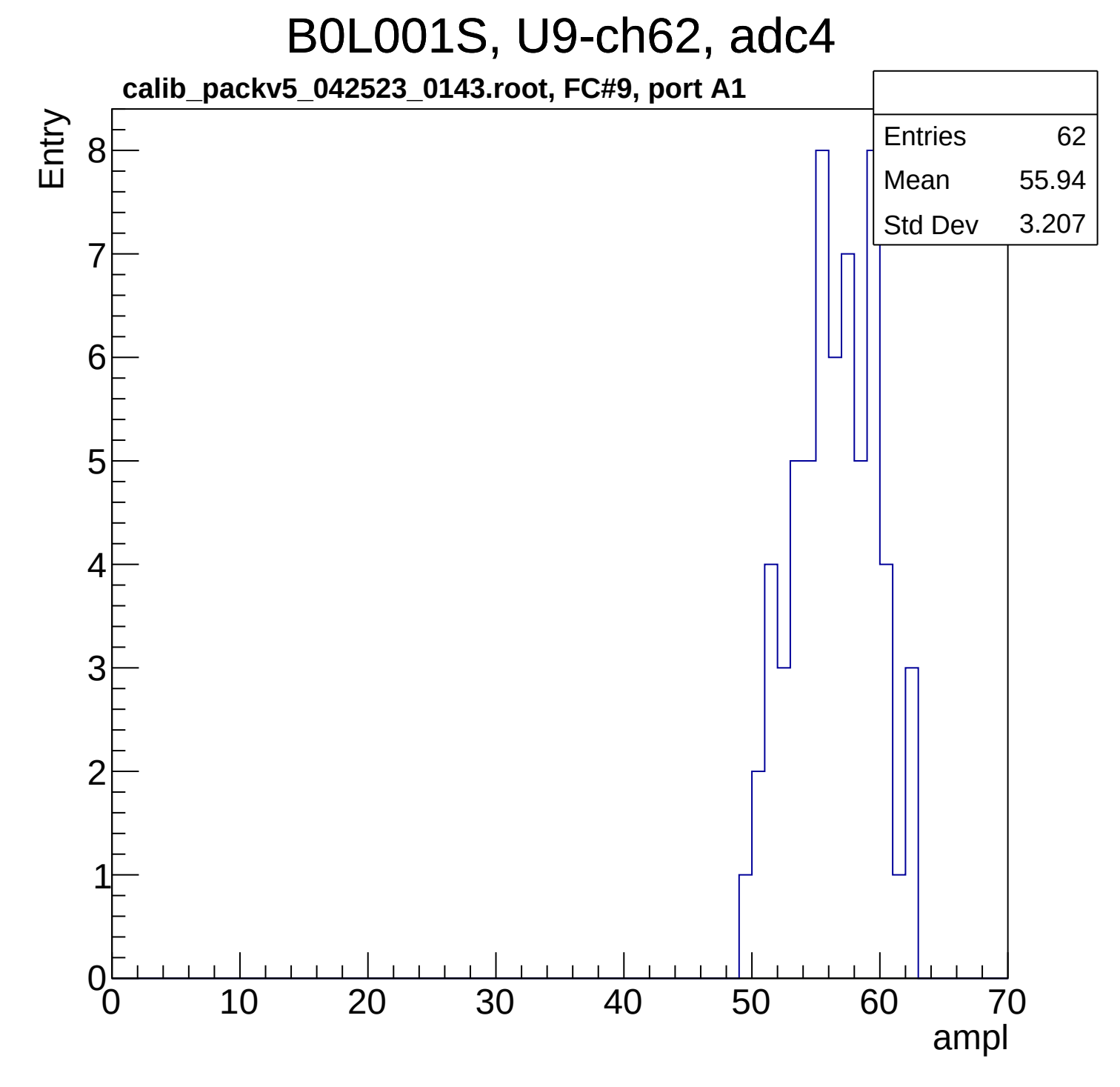
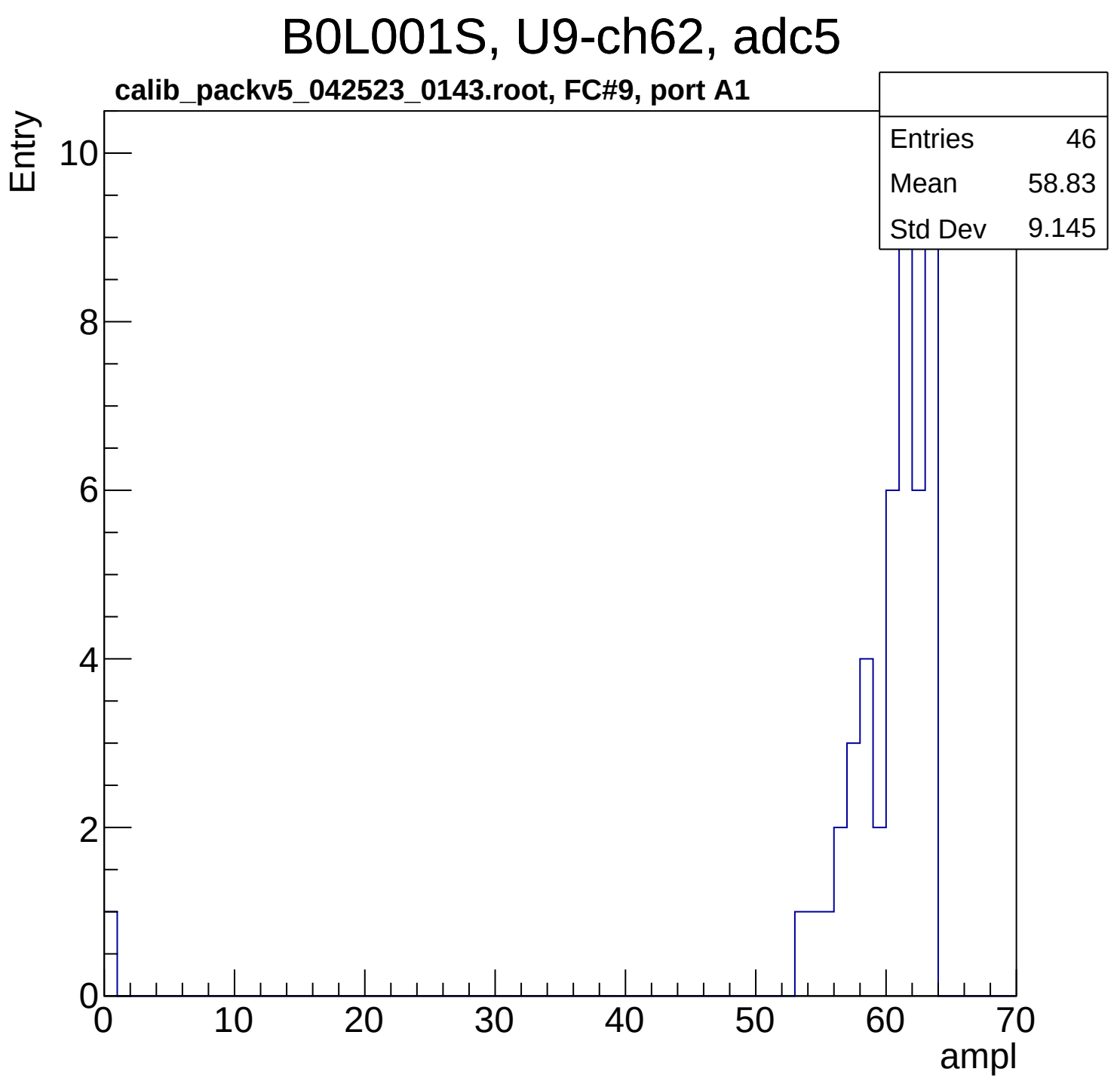
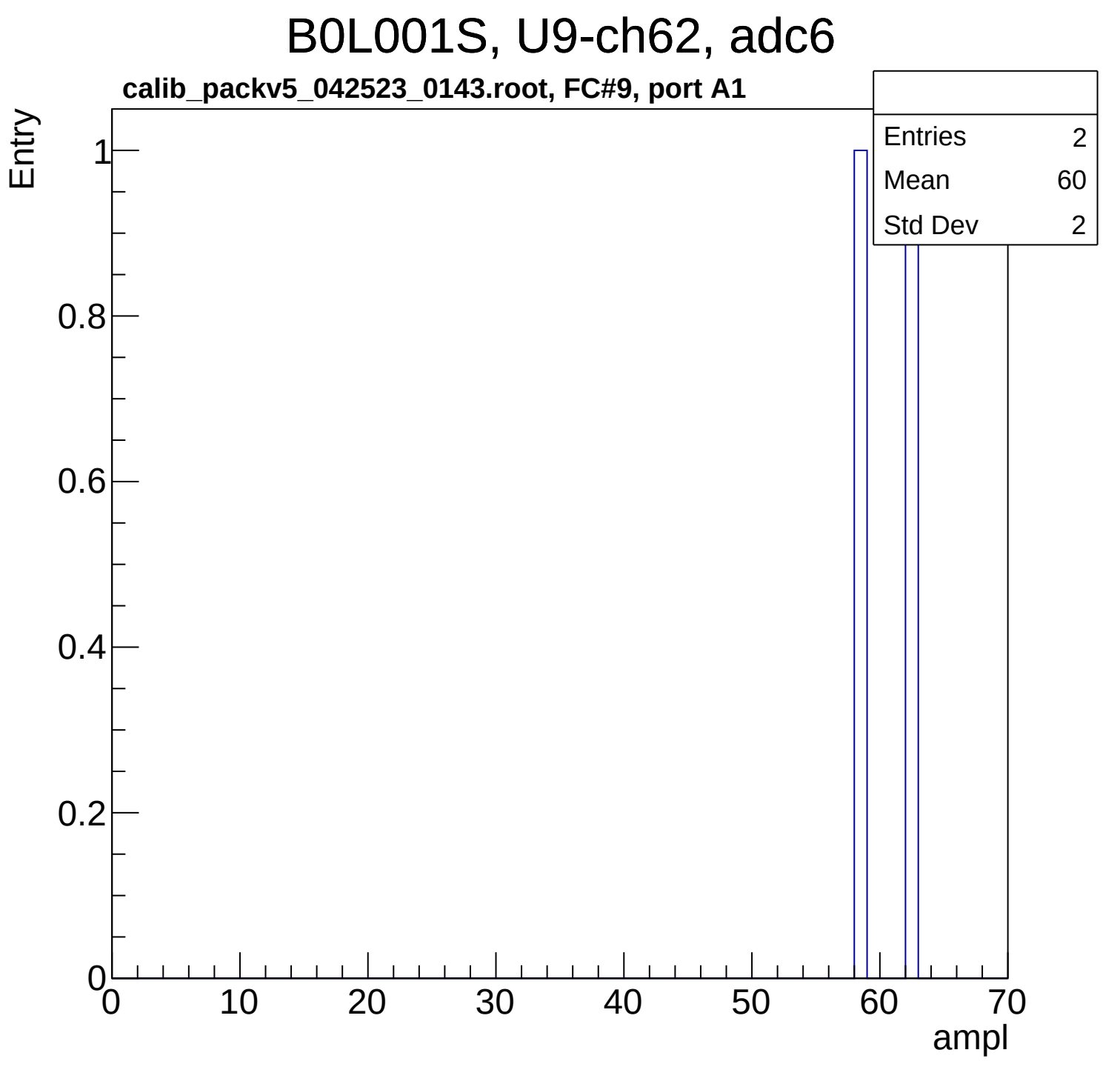
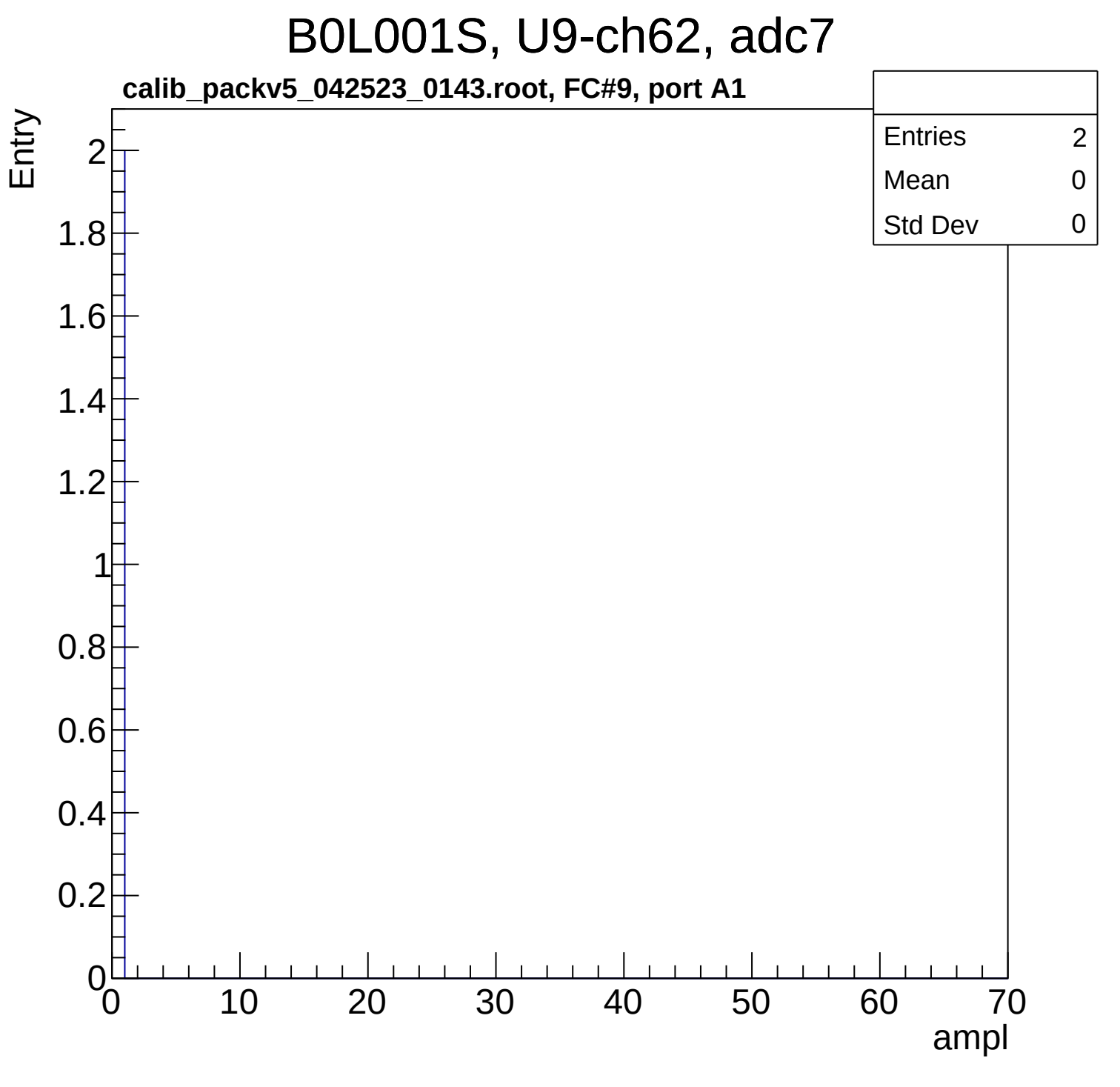
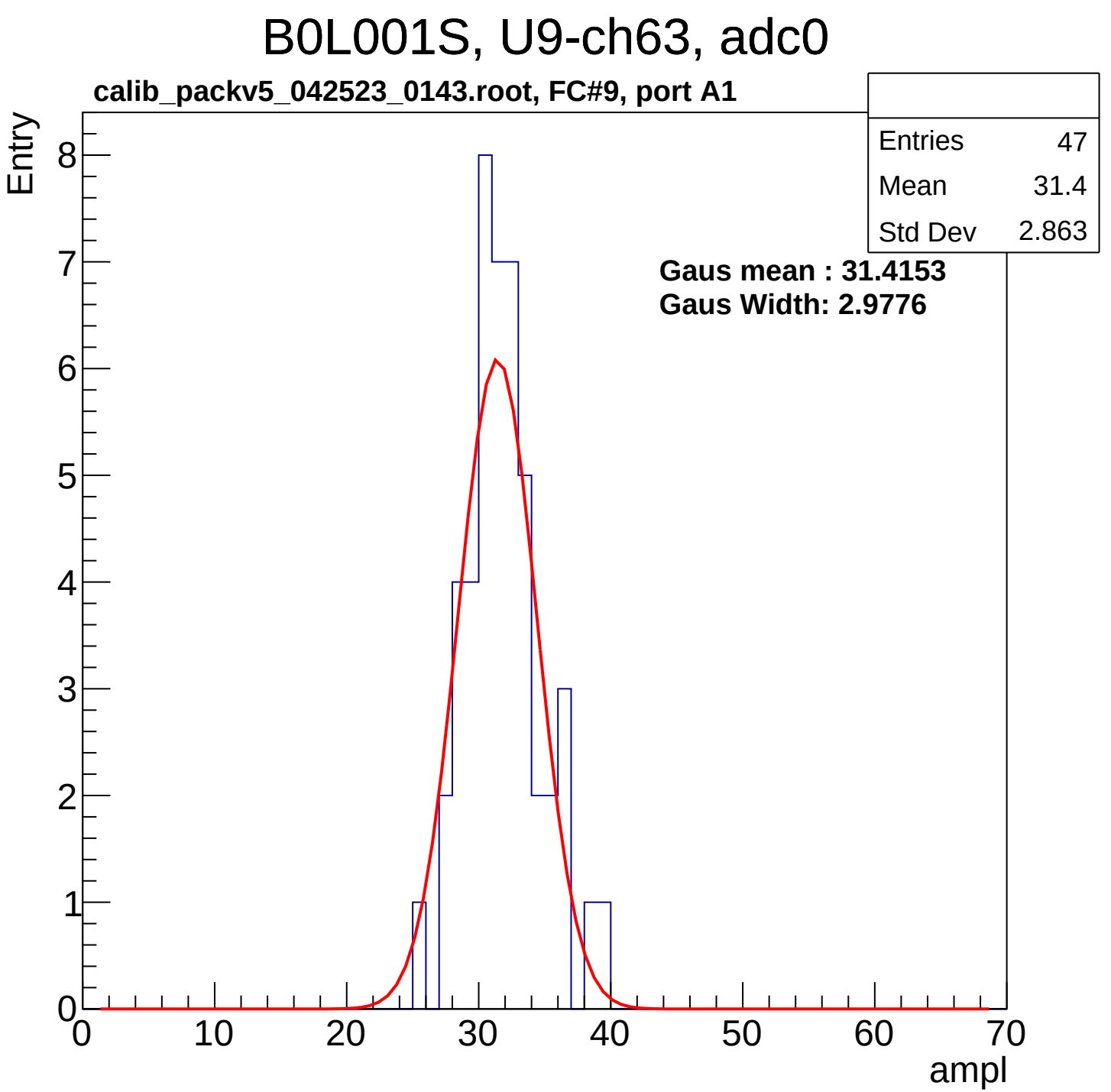
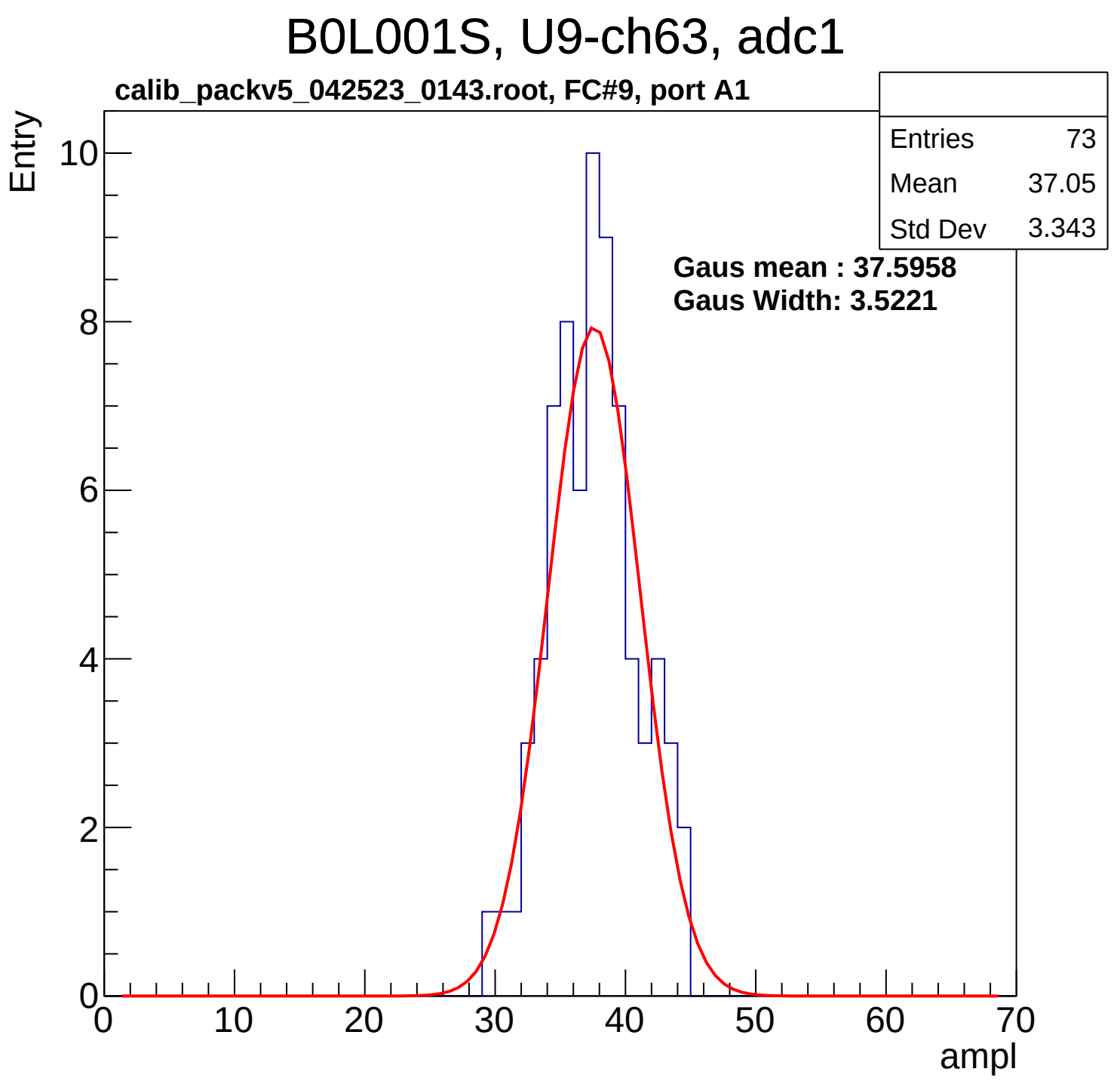

B0L001S, U9-ch62, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.94
Std Dev 3.207
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

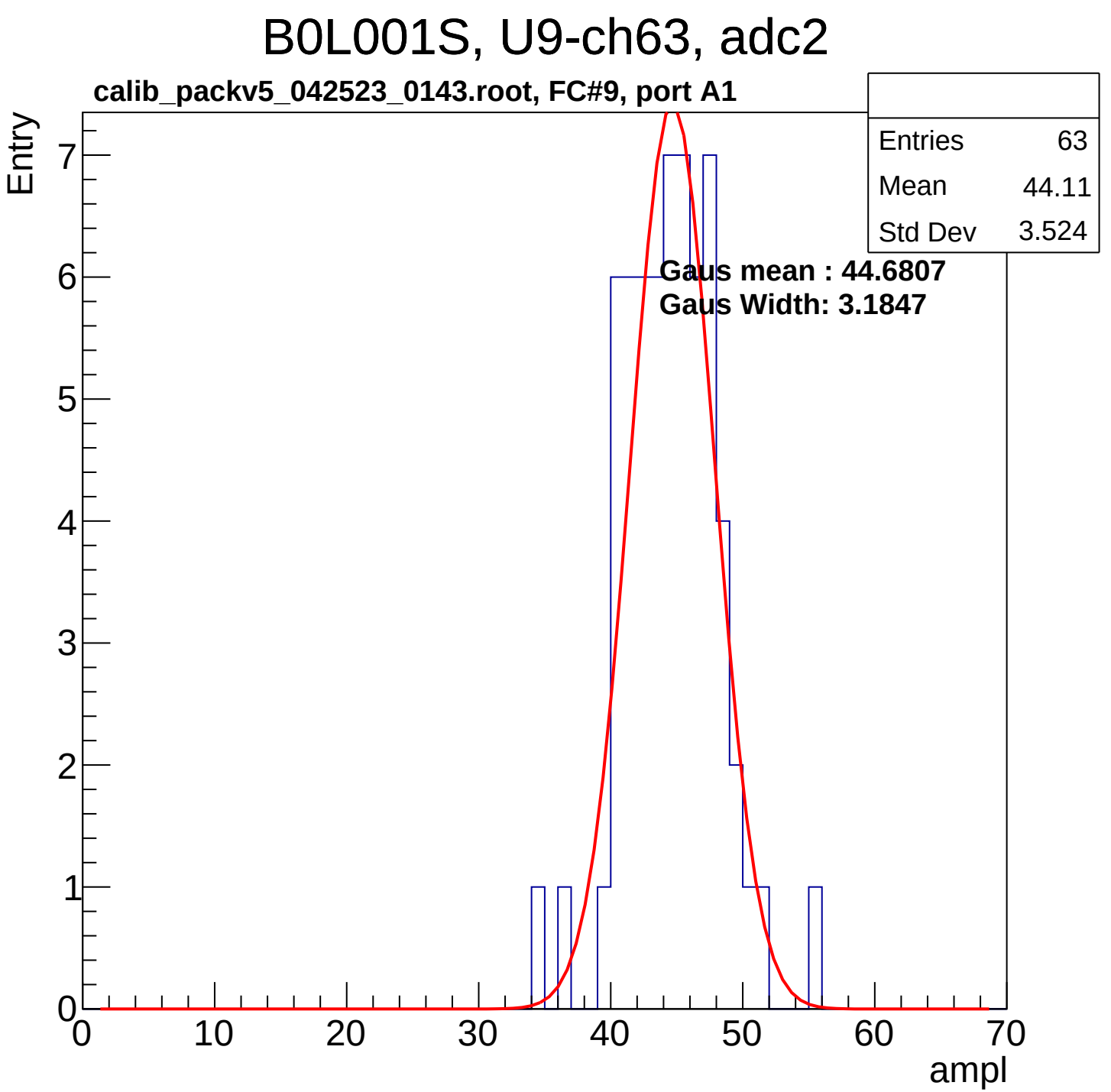

B0L001S, U9-ch62, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.83
Std Dev 9.145
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

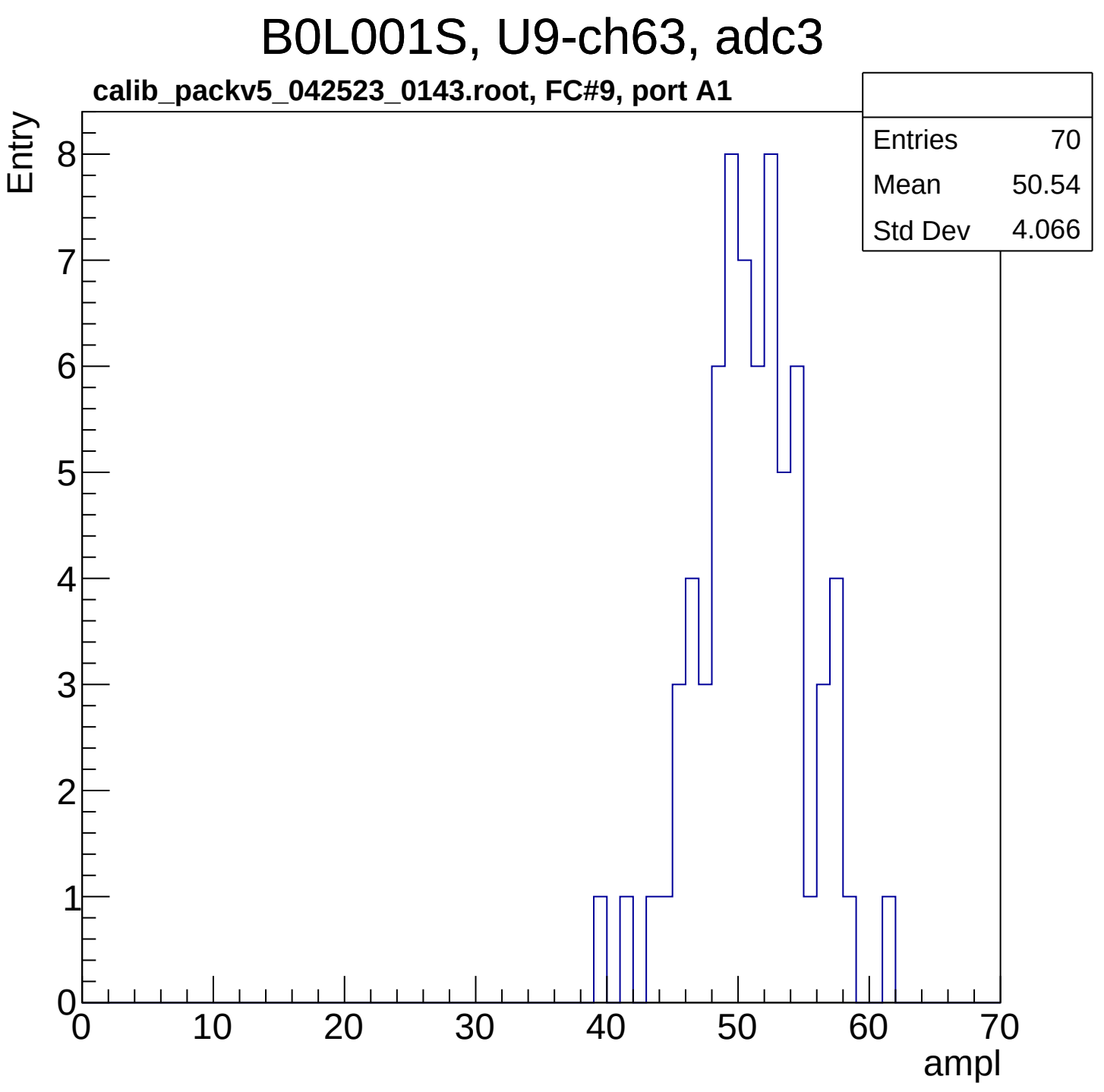

B0L001S, U9-ch62, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 60
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

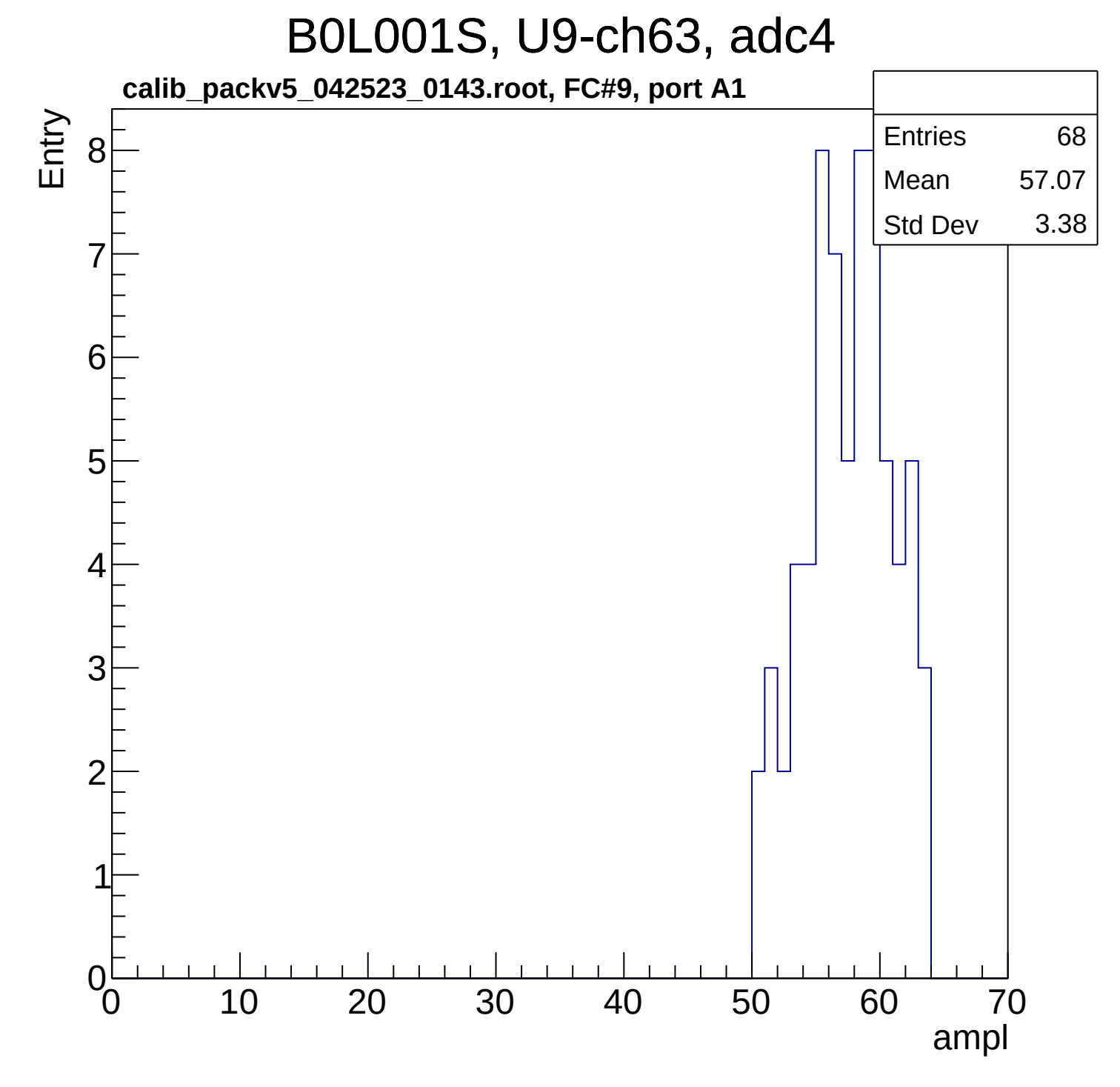

B0L001S, U9-ch62, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

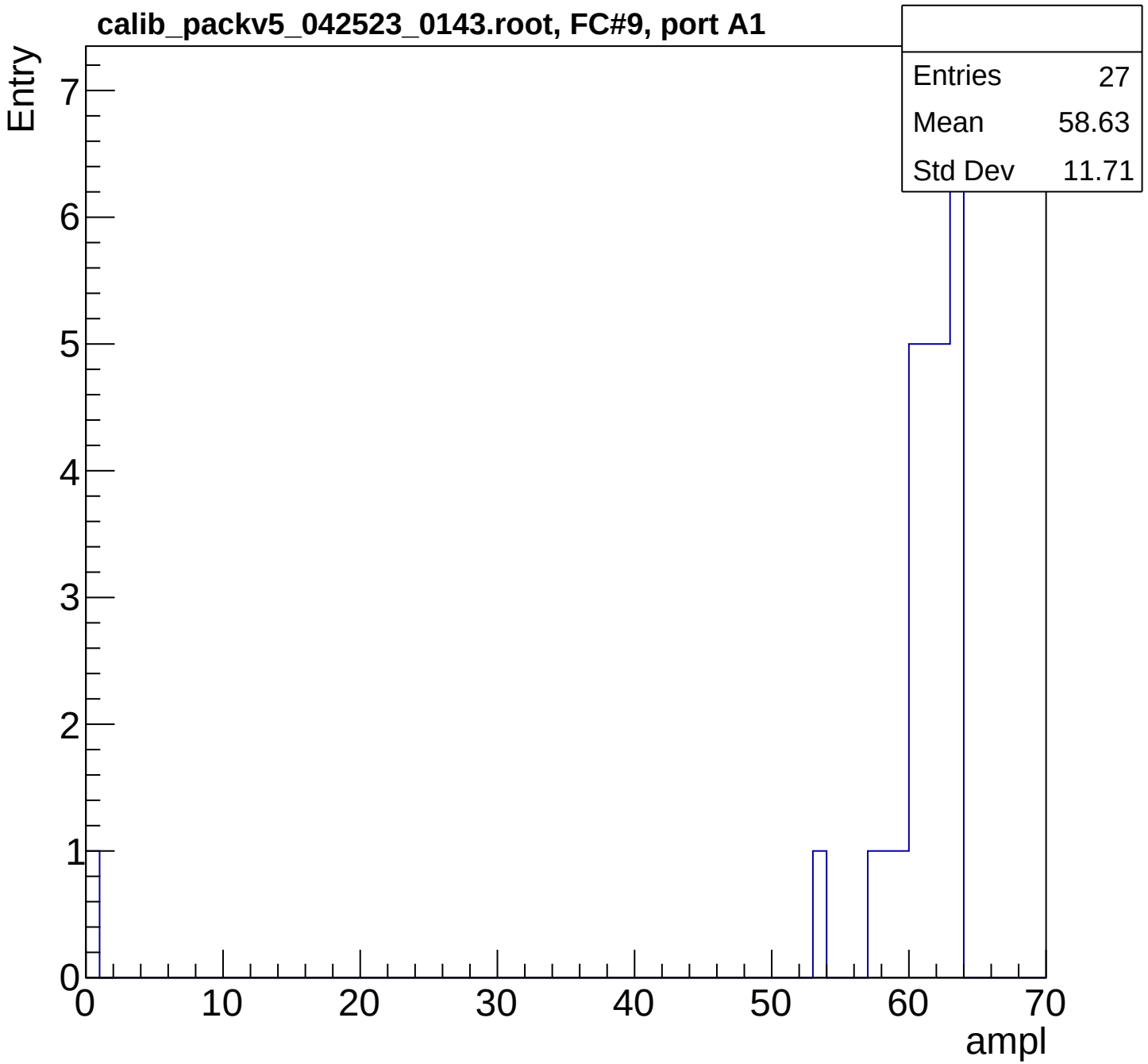

B0L001S, U9-ch63, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 31.4
Std Dev 2.863
Gaus mean : 31.4153
Gaus Width: 2.9776
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

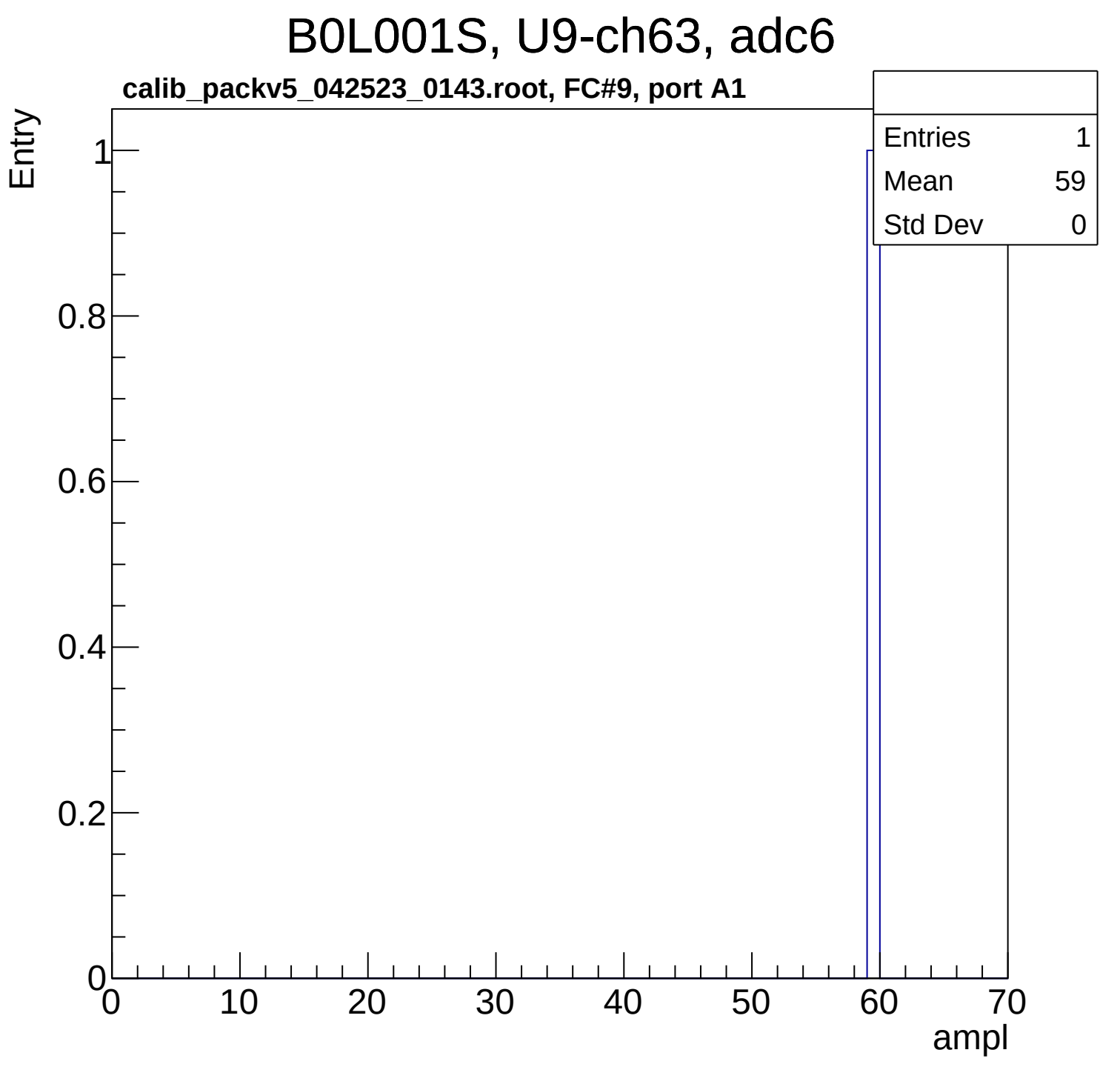

B0L001S, U9-ch63, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.05
Std Dev 3.343
Gaus mean : 37.5958
Gaus Width: 3.5221
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

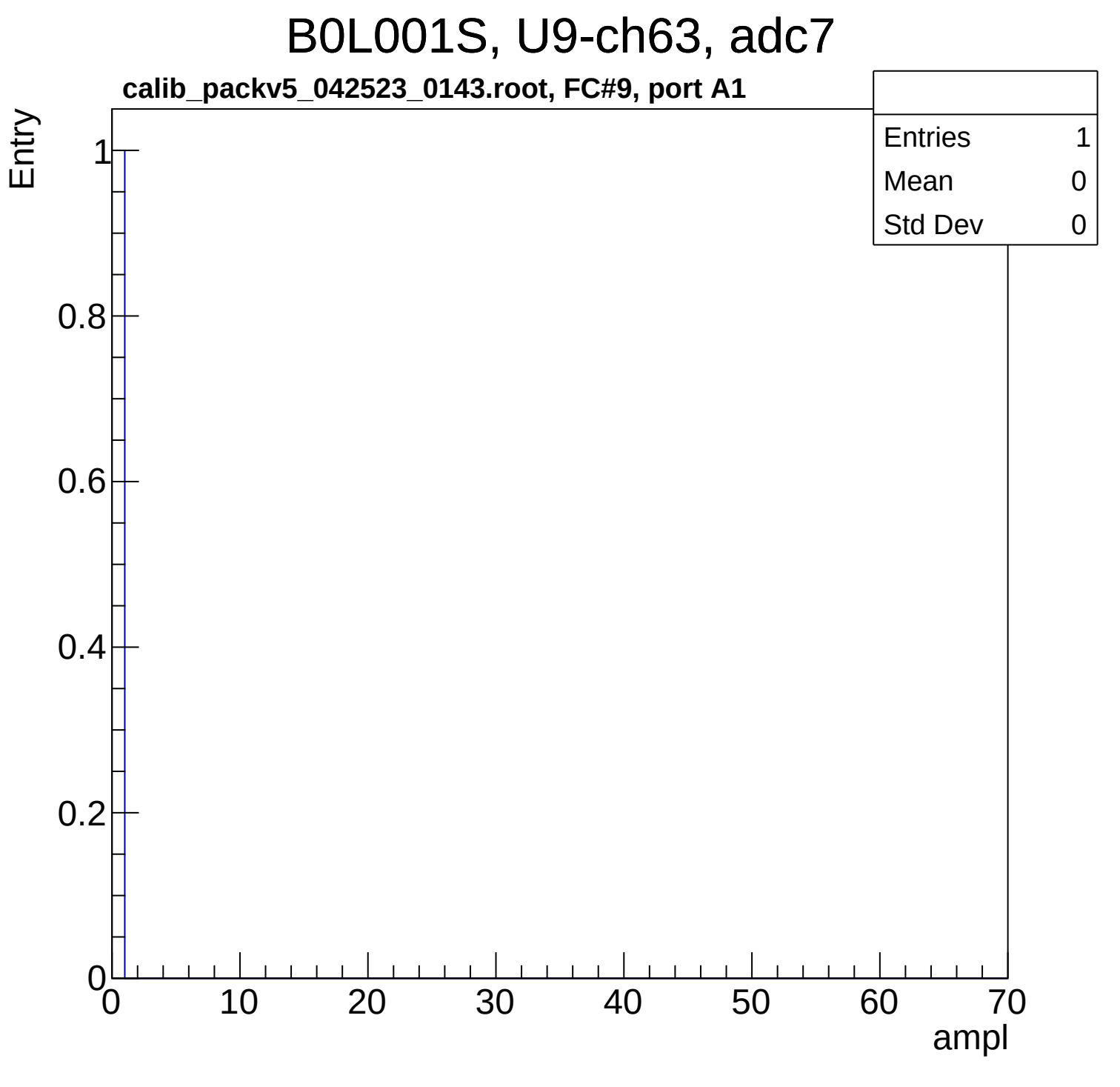

B0L001S, U9-ch63, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.11
Std Dev 3.524
Gaus mean : 44.6807
Gaus Width: 3.1847
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

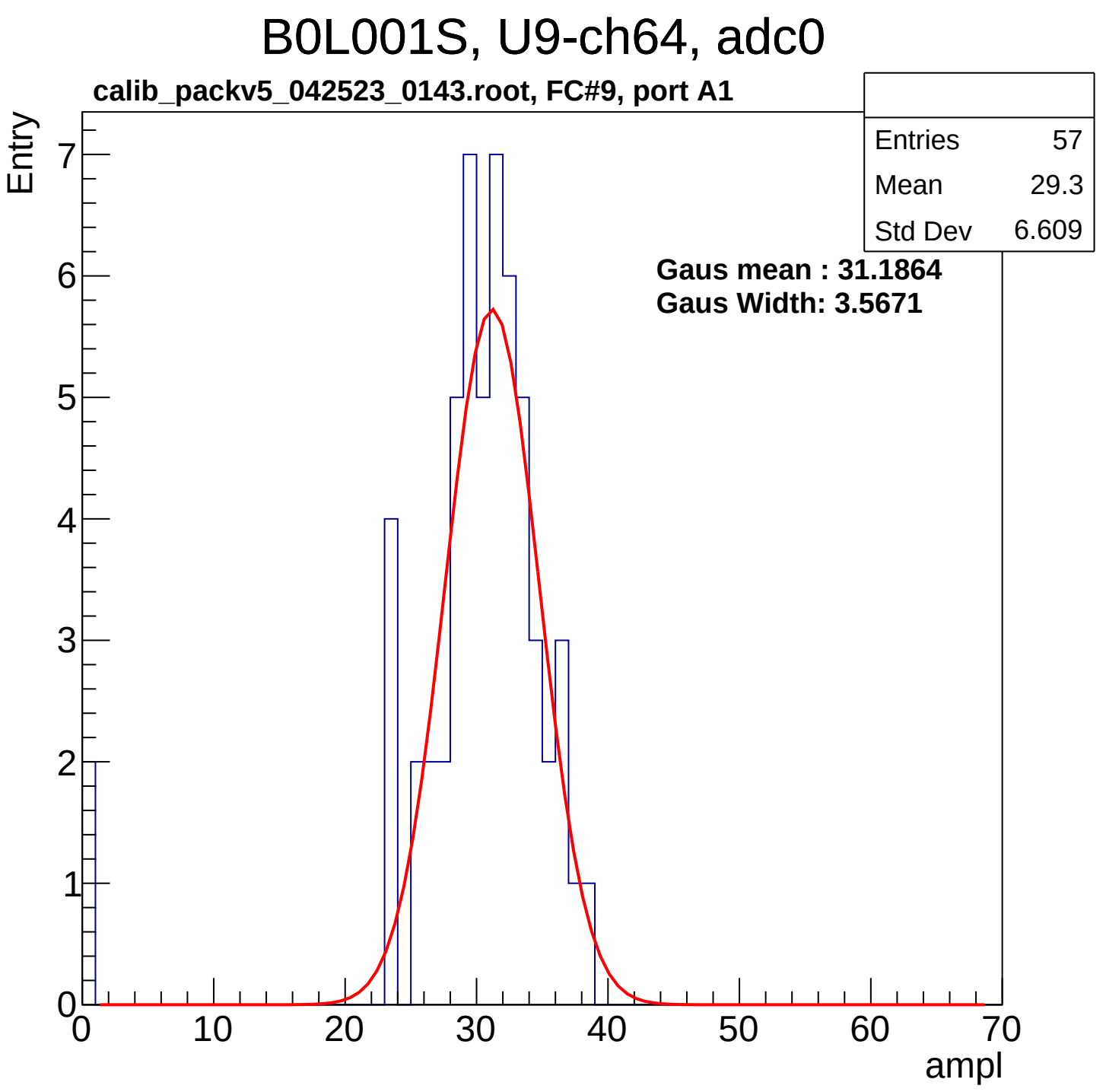

B0L001S, U9-ch63, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.54
Std Dev 4.066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

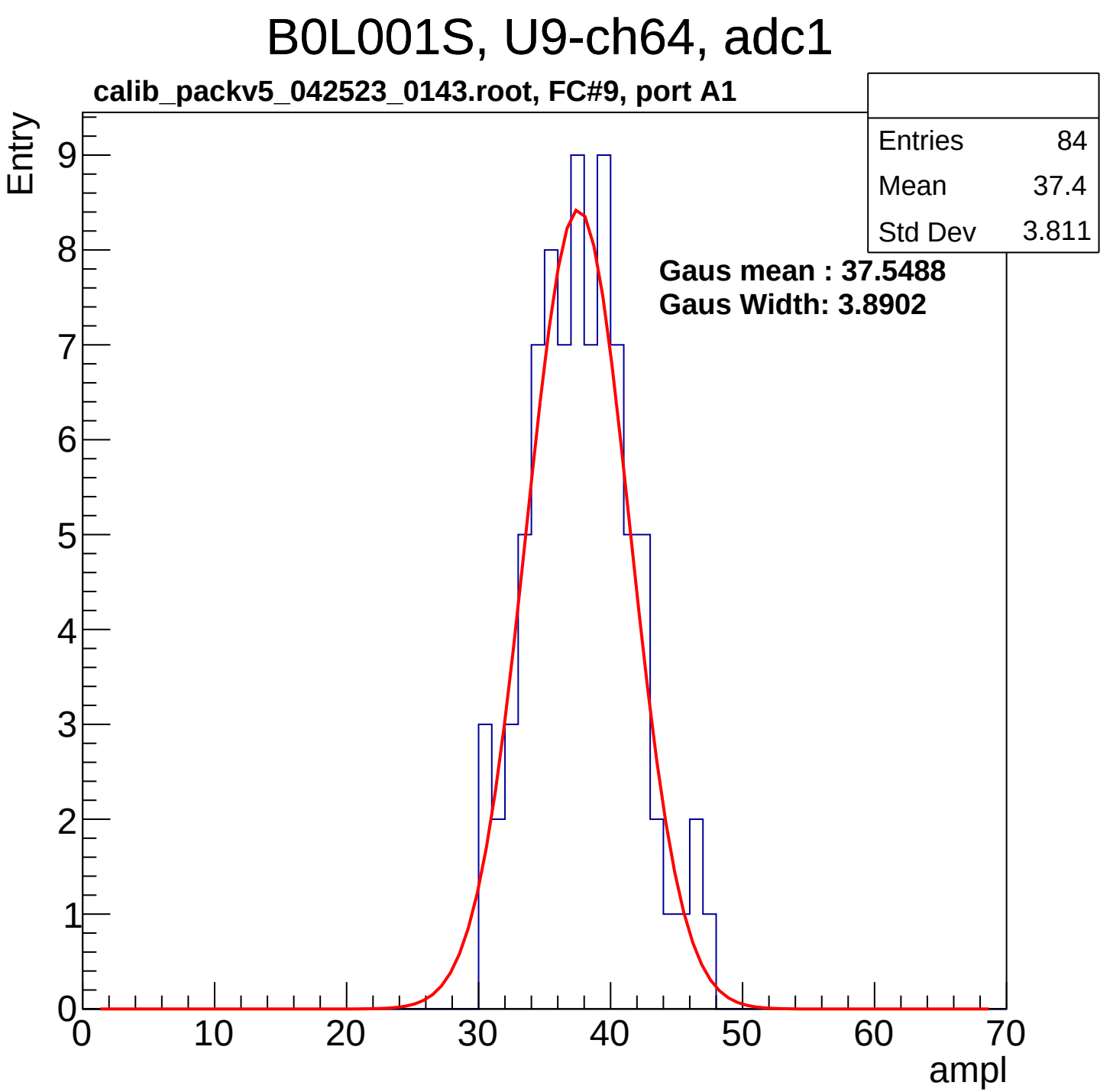

B0L001S, U9-ch63, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 57.07
Std Dev 3.38
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch63, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.63
Std Dev 11.71
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch63, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 59
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch63, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch64, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 29.3
Std Dev 6.609
Gaus mean : 31.1864
Gaus Width: 3.5671
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch64, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 37.4
Std Dev 3.811
Gaus mean : 37.5488
Gaus Width: 3.8902
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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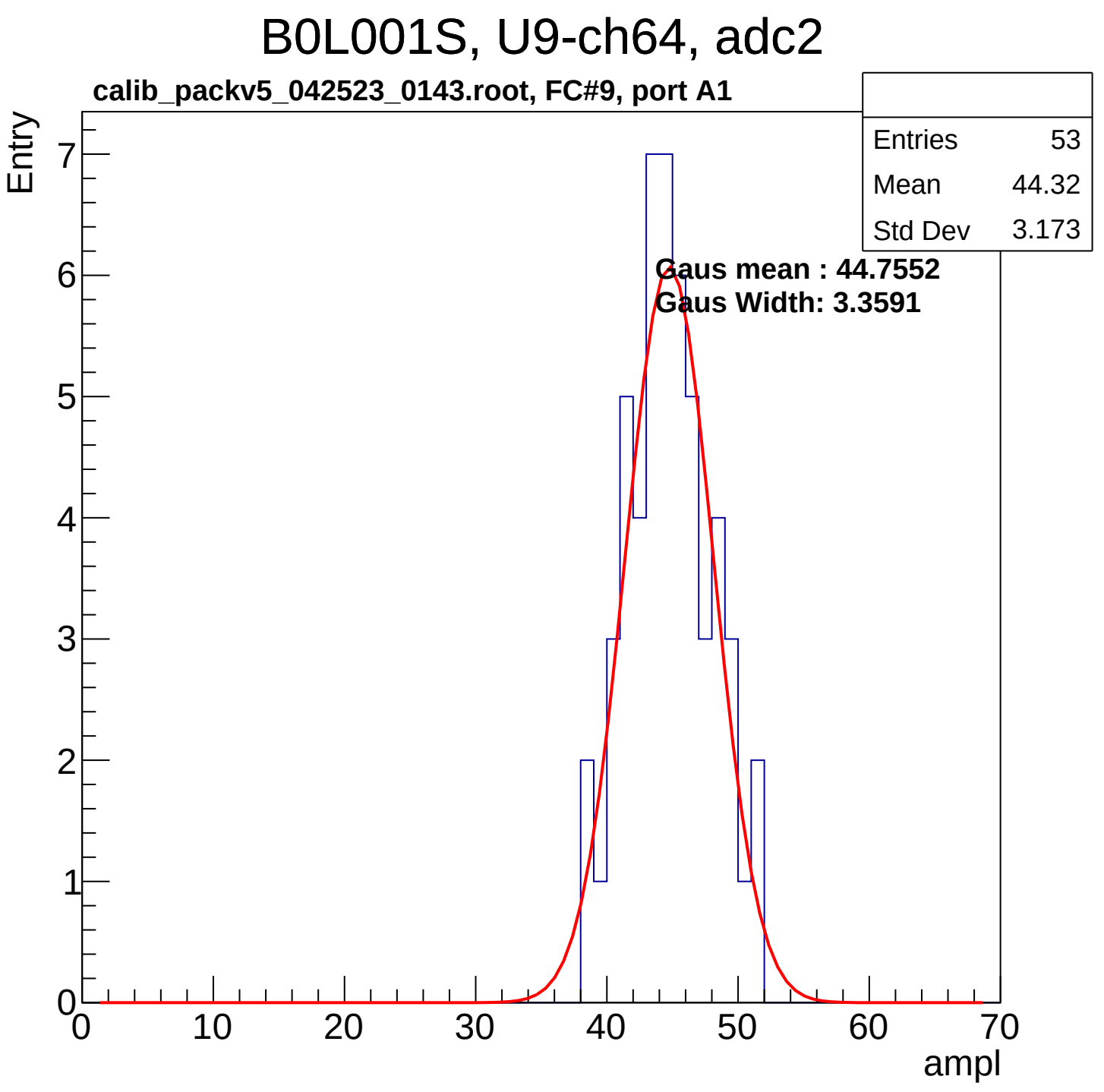
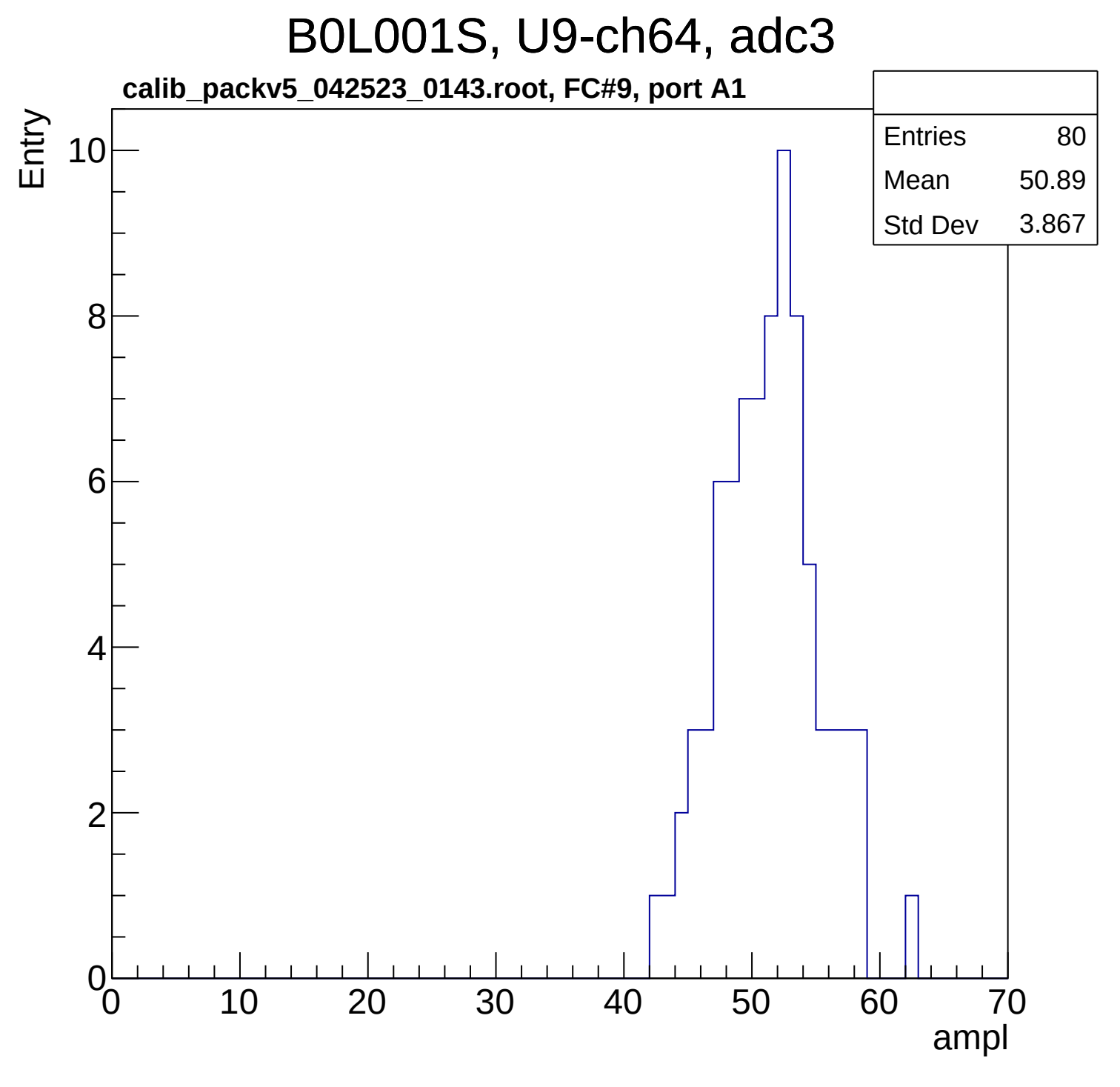

B0L001S, U9-ch64, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 44.32
Std Dev 3.173
Gaus mean : 44.7552
Gaus Width: 3.3591
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch64, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 50.89
Std Dev 3.867
Entry
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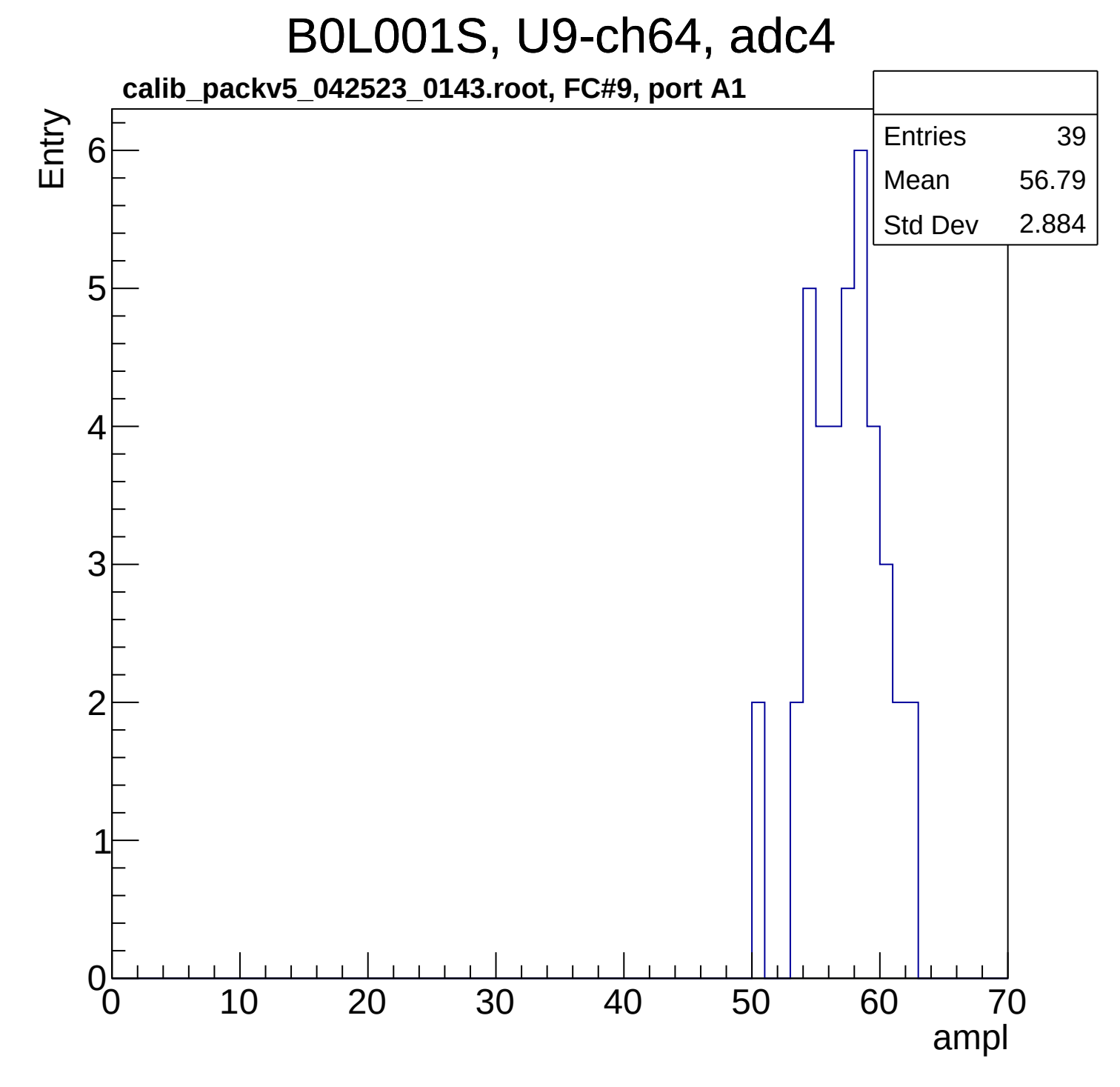
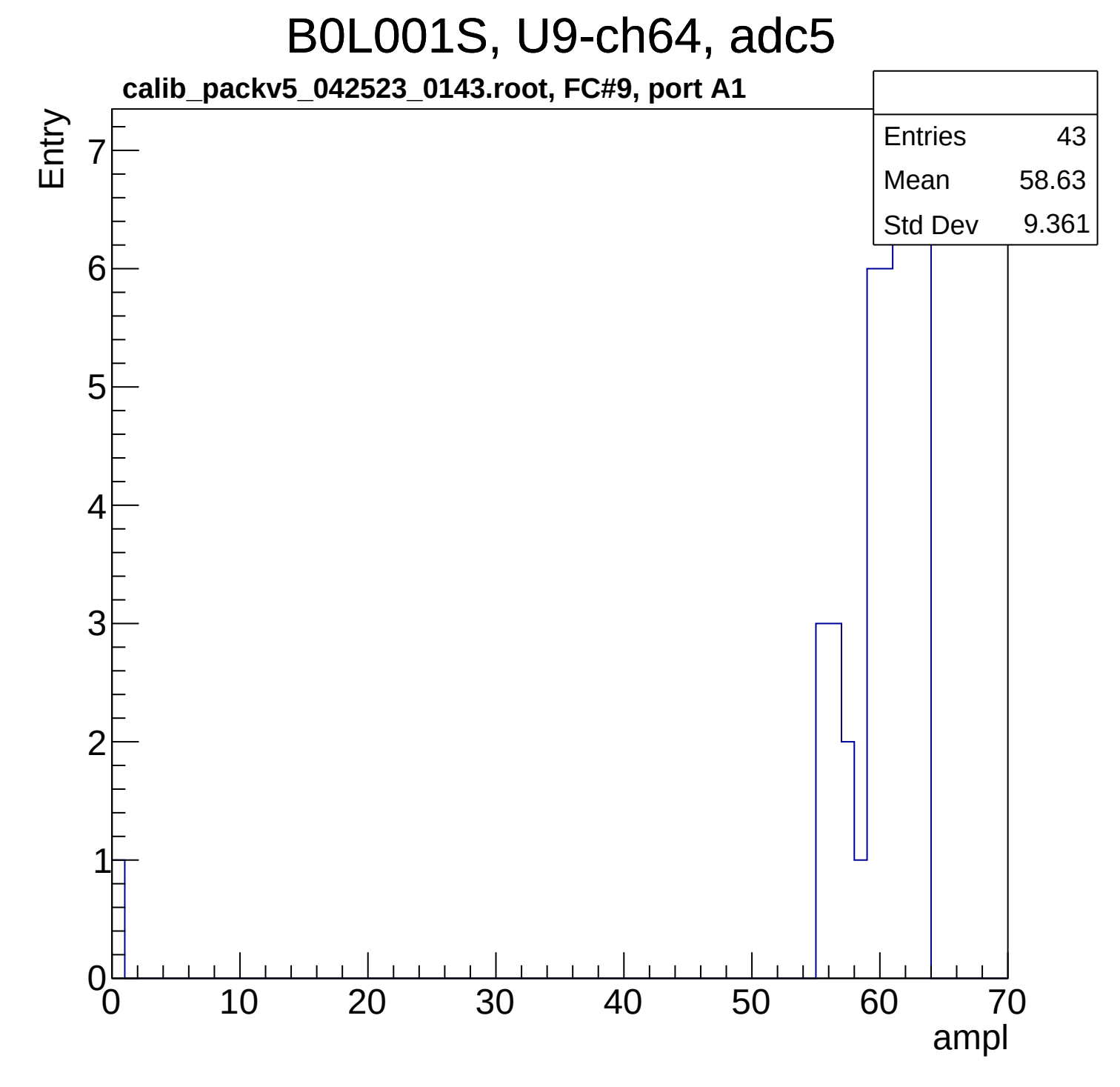
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch64, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 56.79
Std Dev 2.884
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U9-ch64, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.63
Std Dev 9.361
Entry
ampl
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3
4
5
6
7
0
10
20
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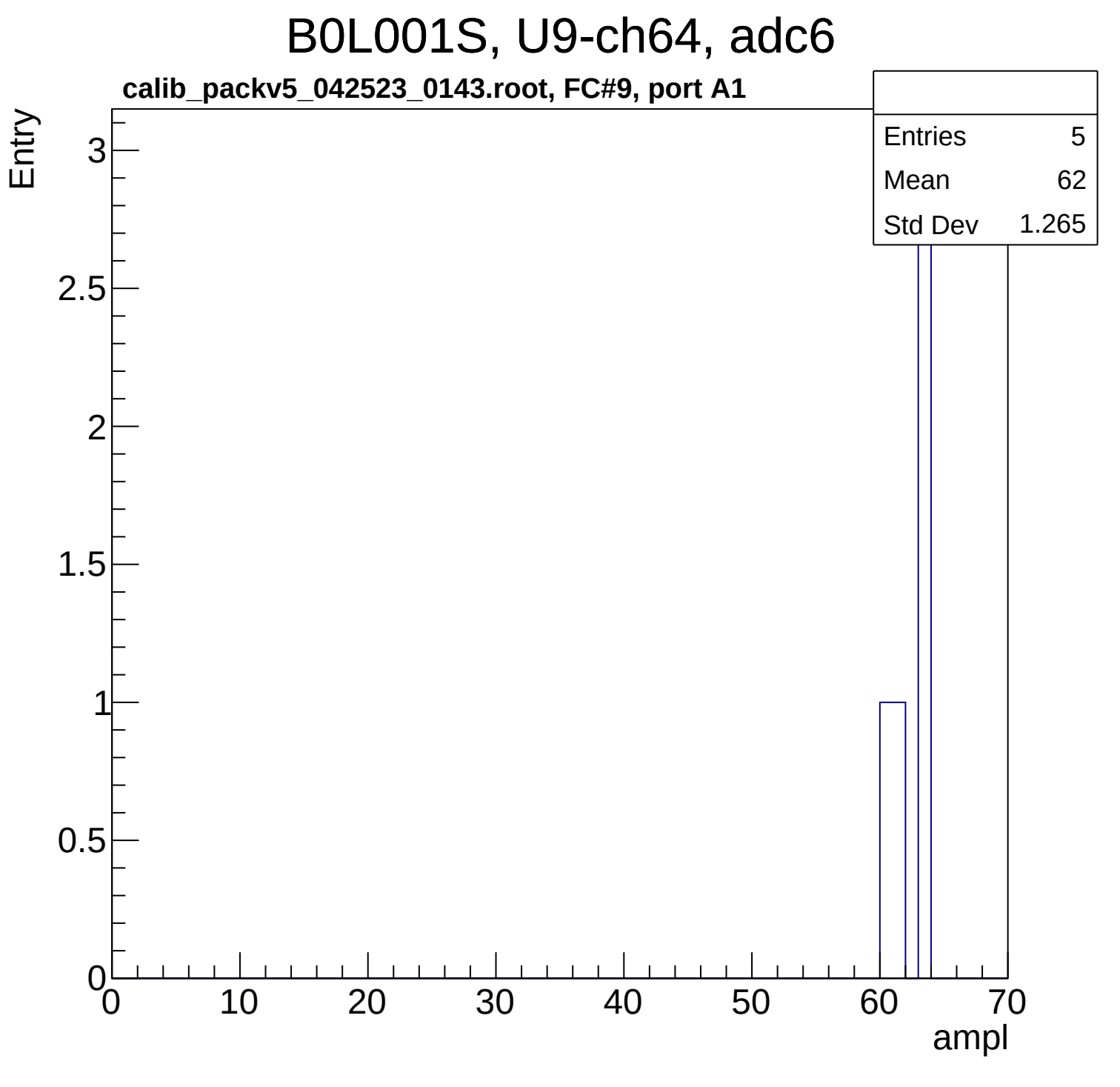
40
50
60
70


B0L001S, U9-ch64, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62
Std Dev 1.265
Entry
ampl
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1
1.5
2
2.5
3
0
10
20
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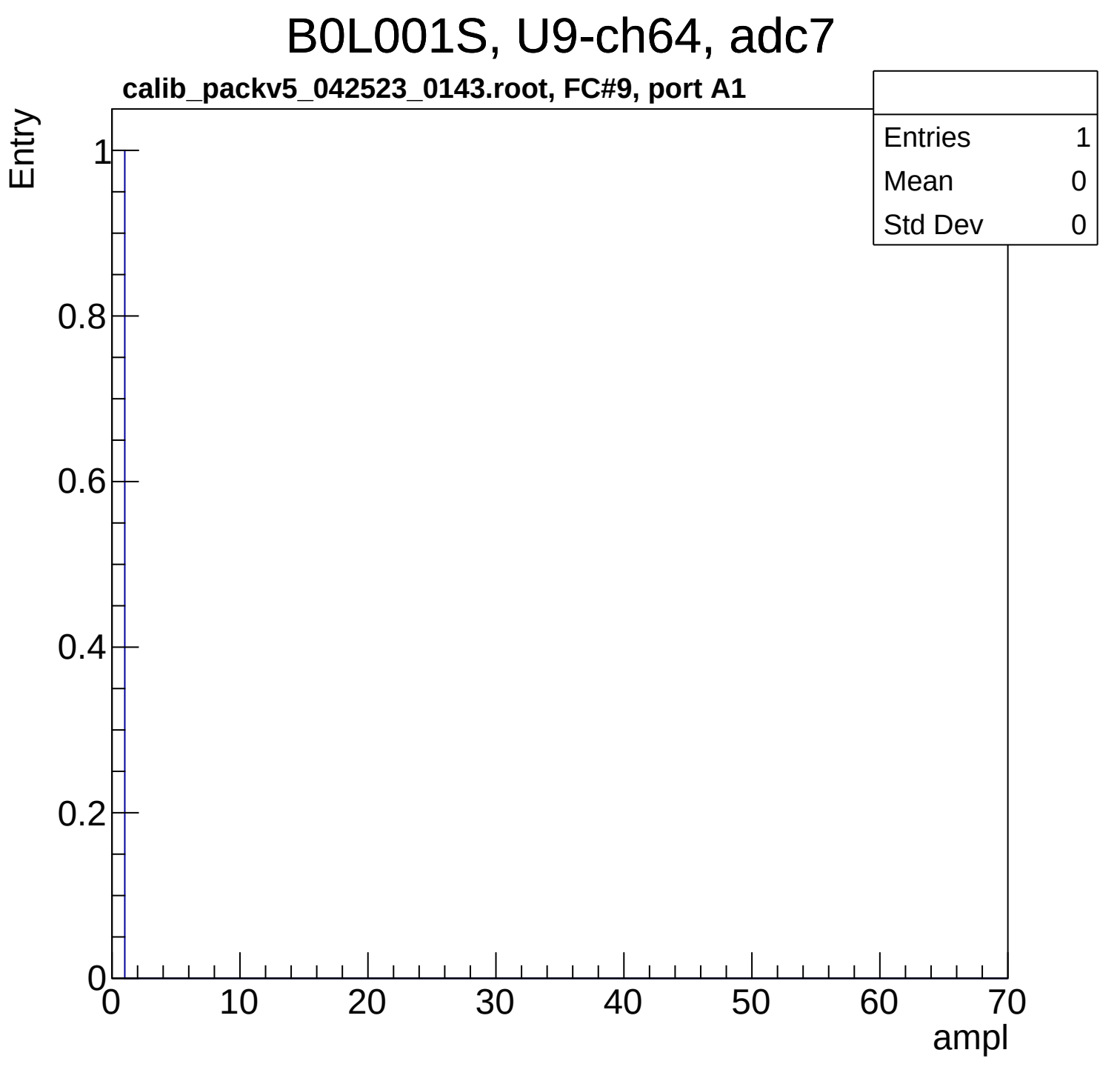
40
50
60
70


B0L001S, U9-ch64, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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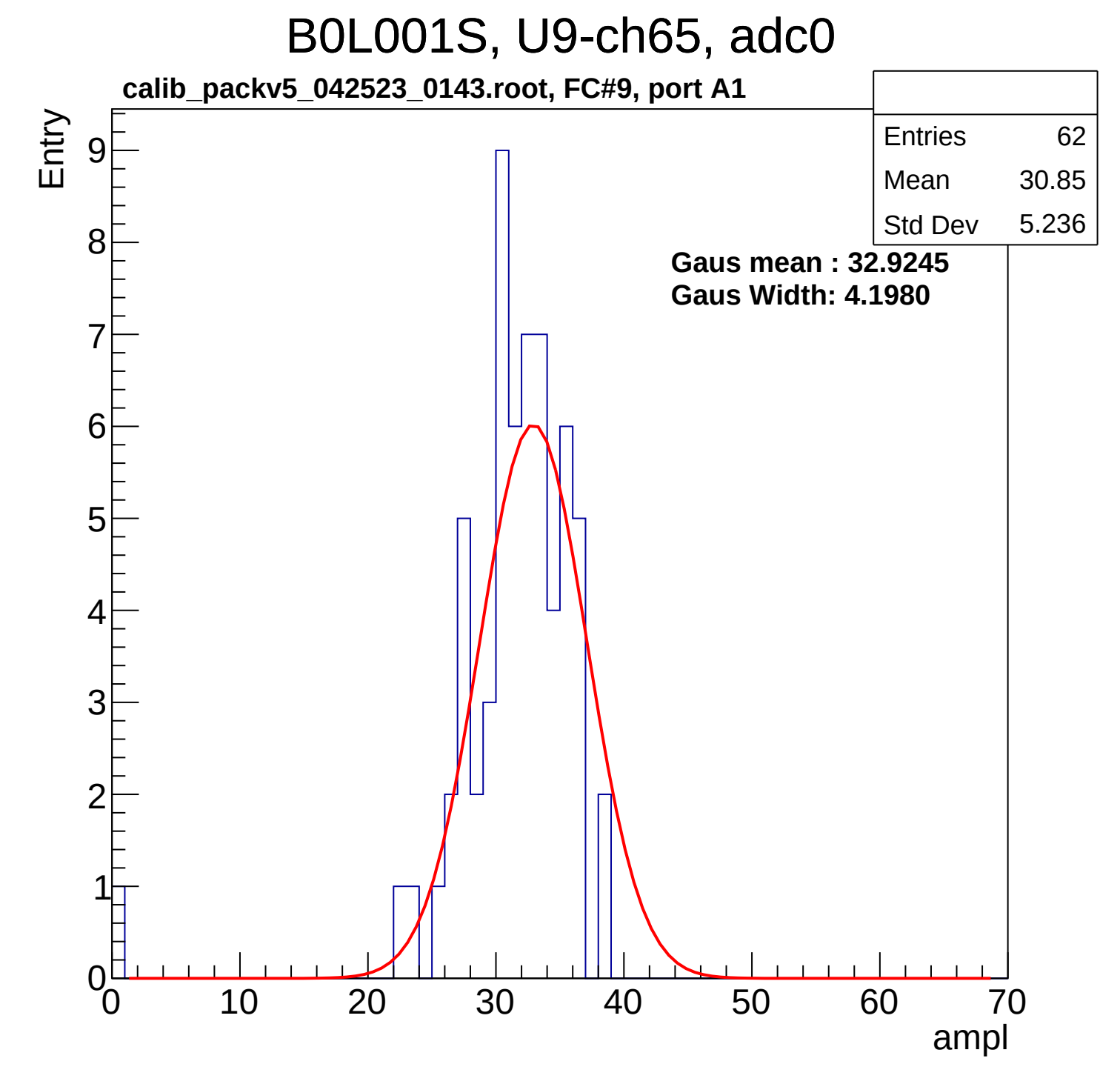
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch65, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 30.85
Std Dev 5.236
Gaus mean : 32.9245
Gaus Width: 4.1980
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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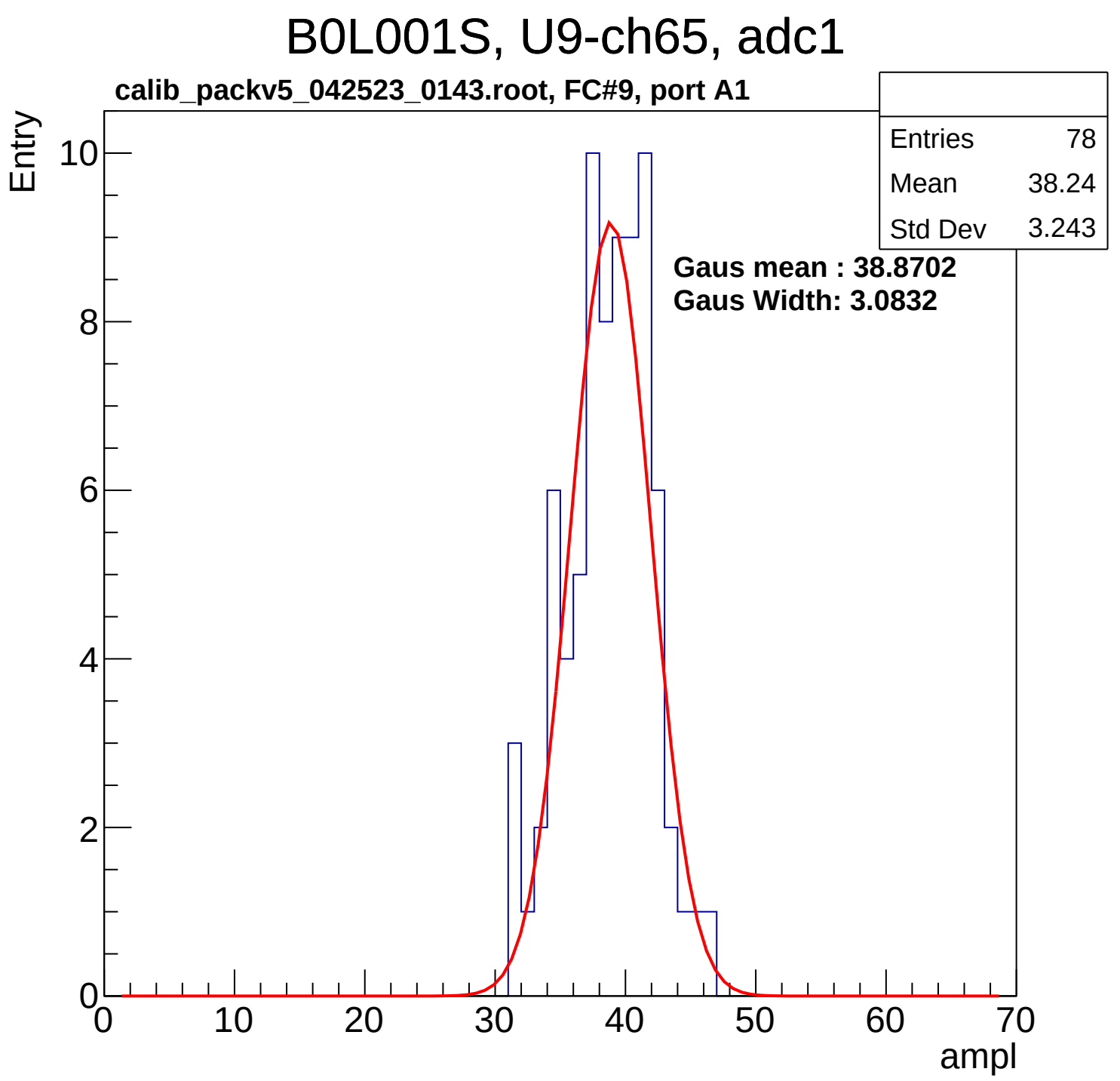
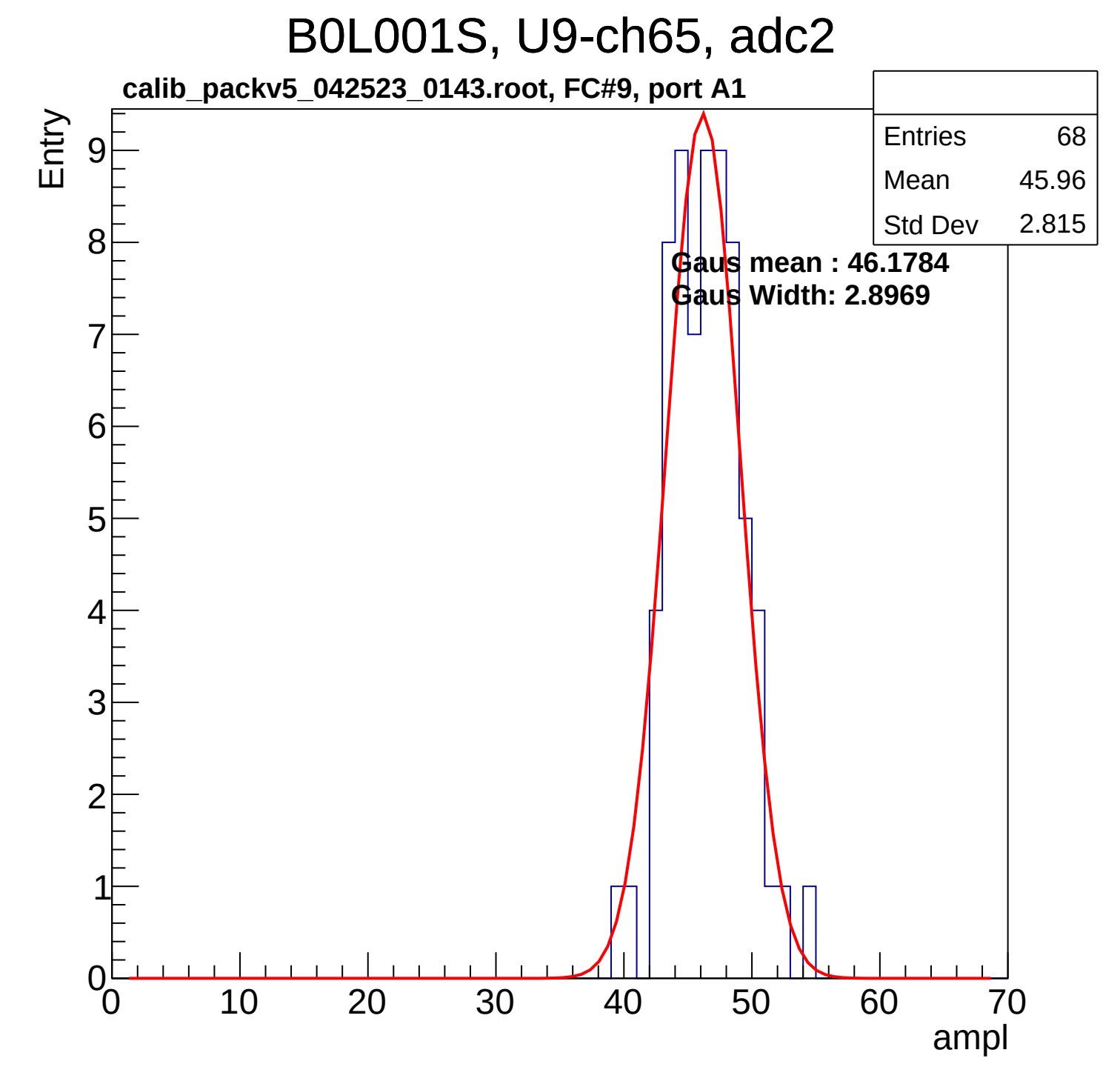

B0L001S, U9-ch65, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 38.24
Std Dev 3.243
Gaus mean : 38.8702
Gaus Width: 3.0832
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch65, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 45.96
Std Dev 2.815
Gaus mean : 46.1784
Gaus Width: 2.8969
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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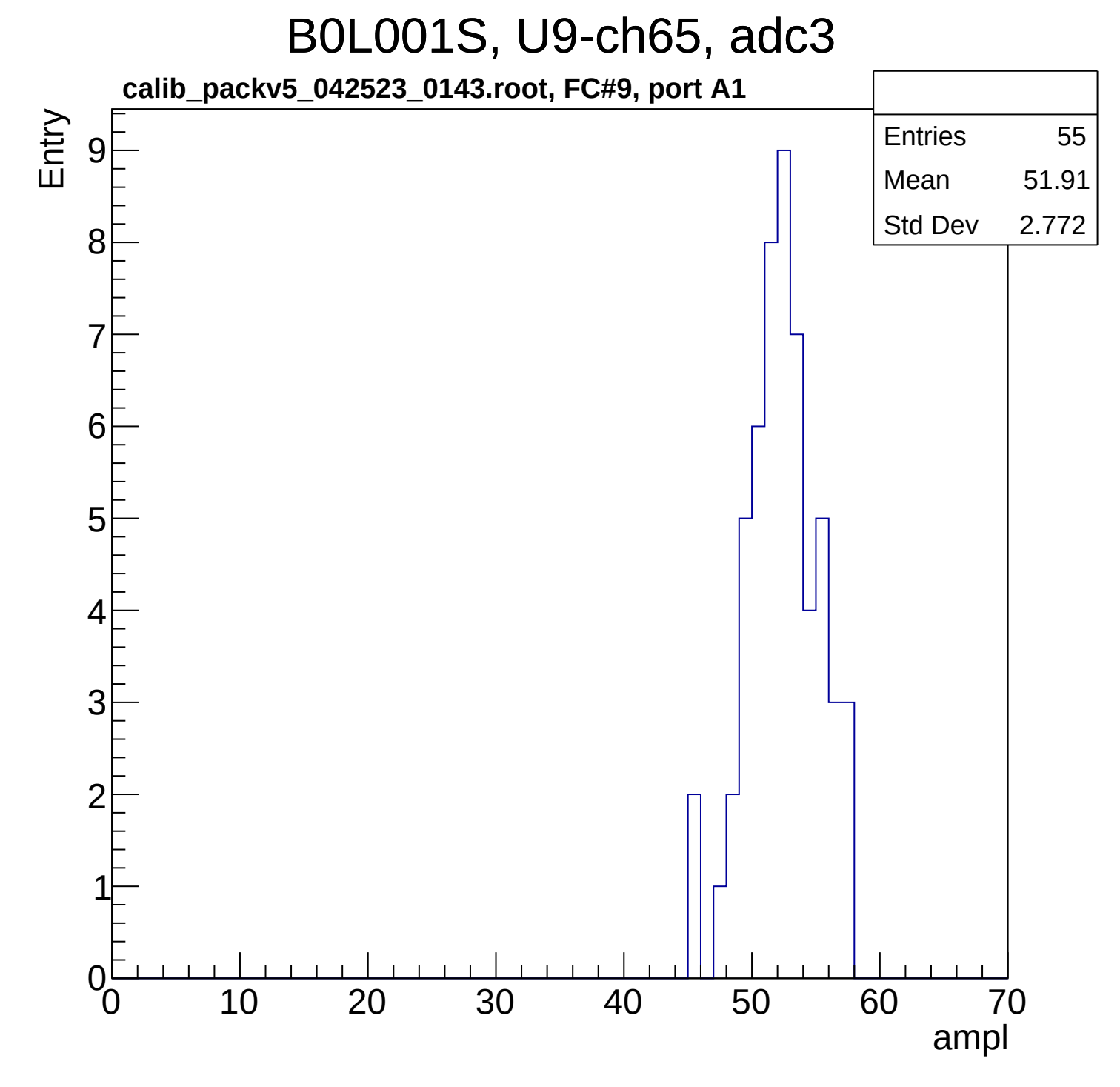
B0L001S, U9-ch65, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 51.91
Std Dev 2.772
Entry
ampl
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3
4
5
6
7
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0
10
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40
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60
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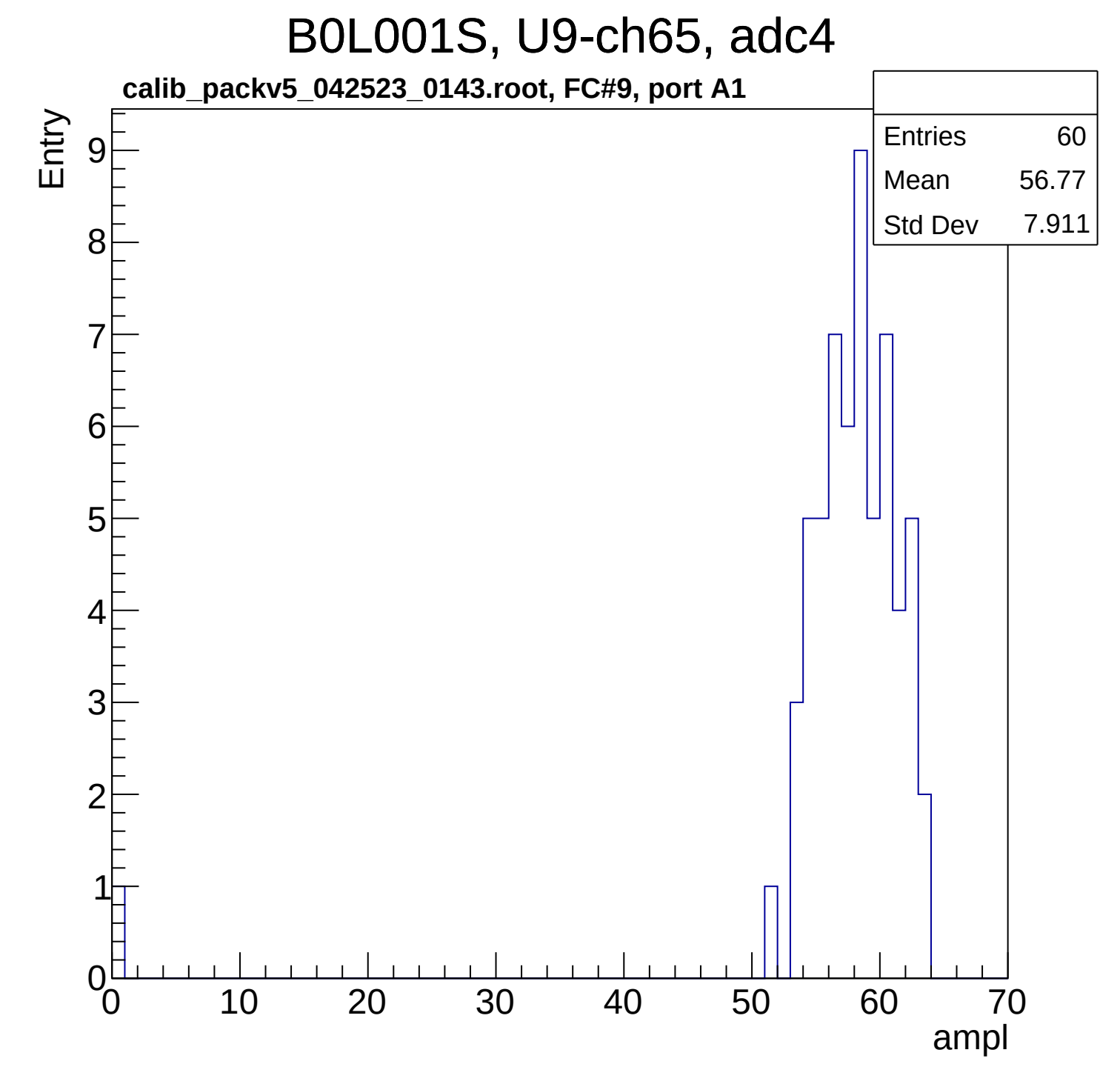
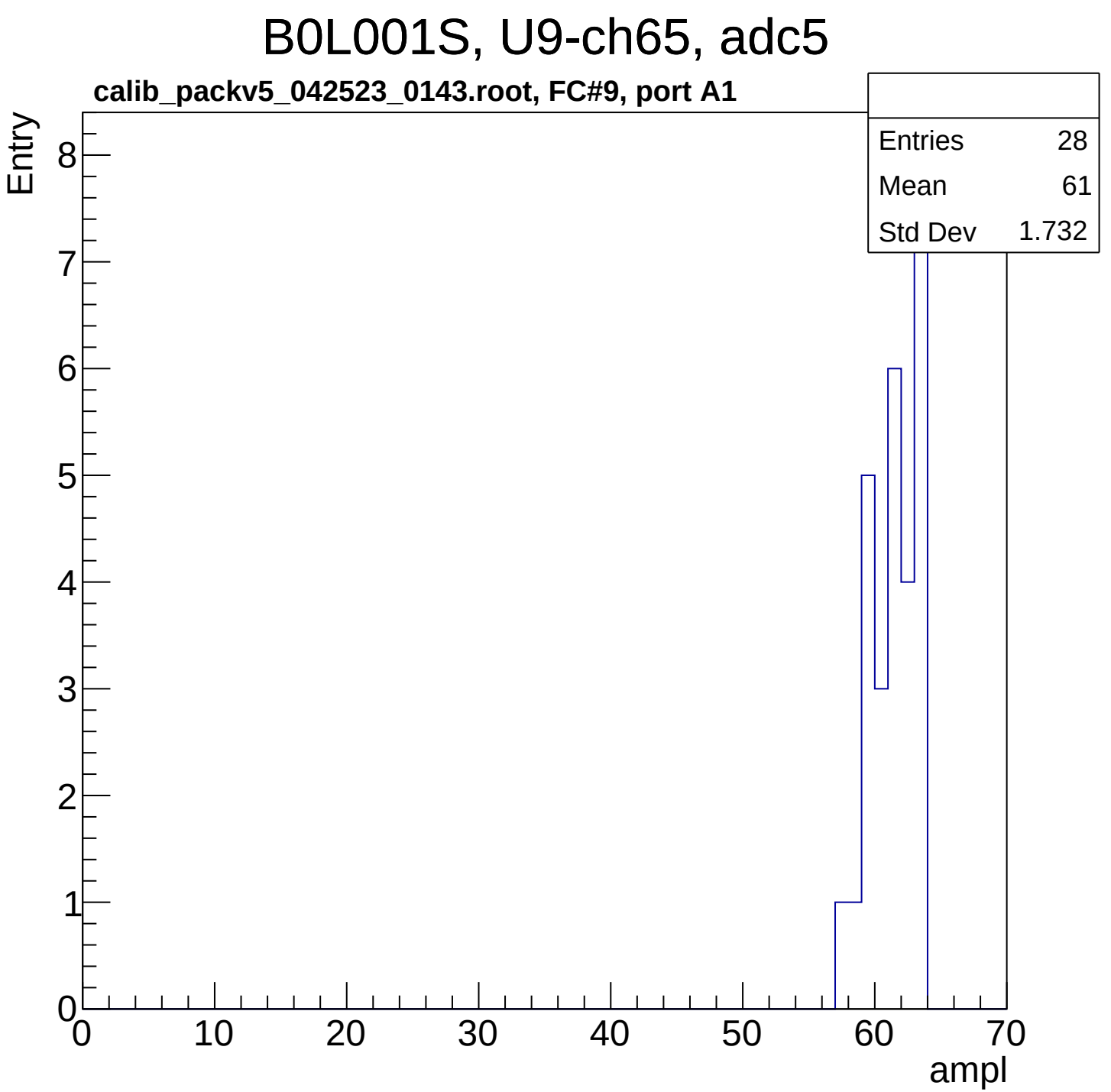

B0L001S, U9-ch65, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.77
Std Dev 7.911
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch65, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 61
Std Dev 1.732
Entry
0
1
2
3
4
5
6
7
8
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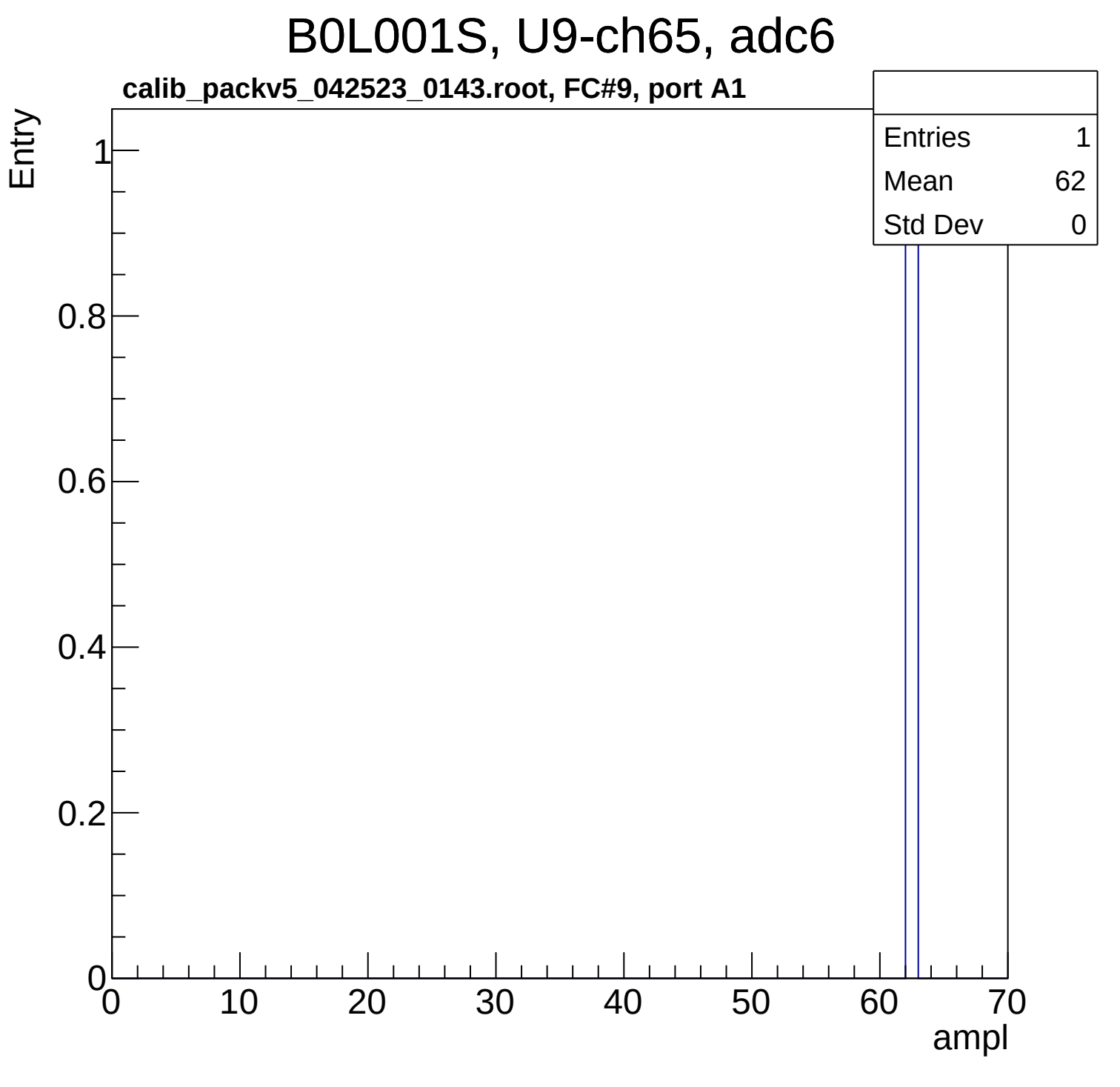
ampl


B0L001S, U9-ch65, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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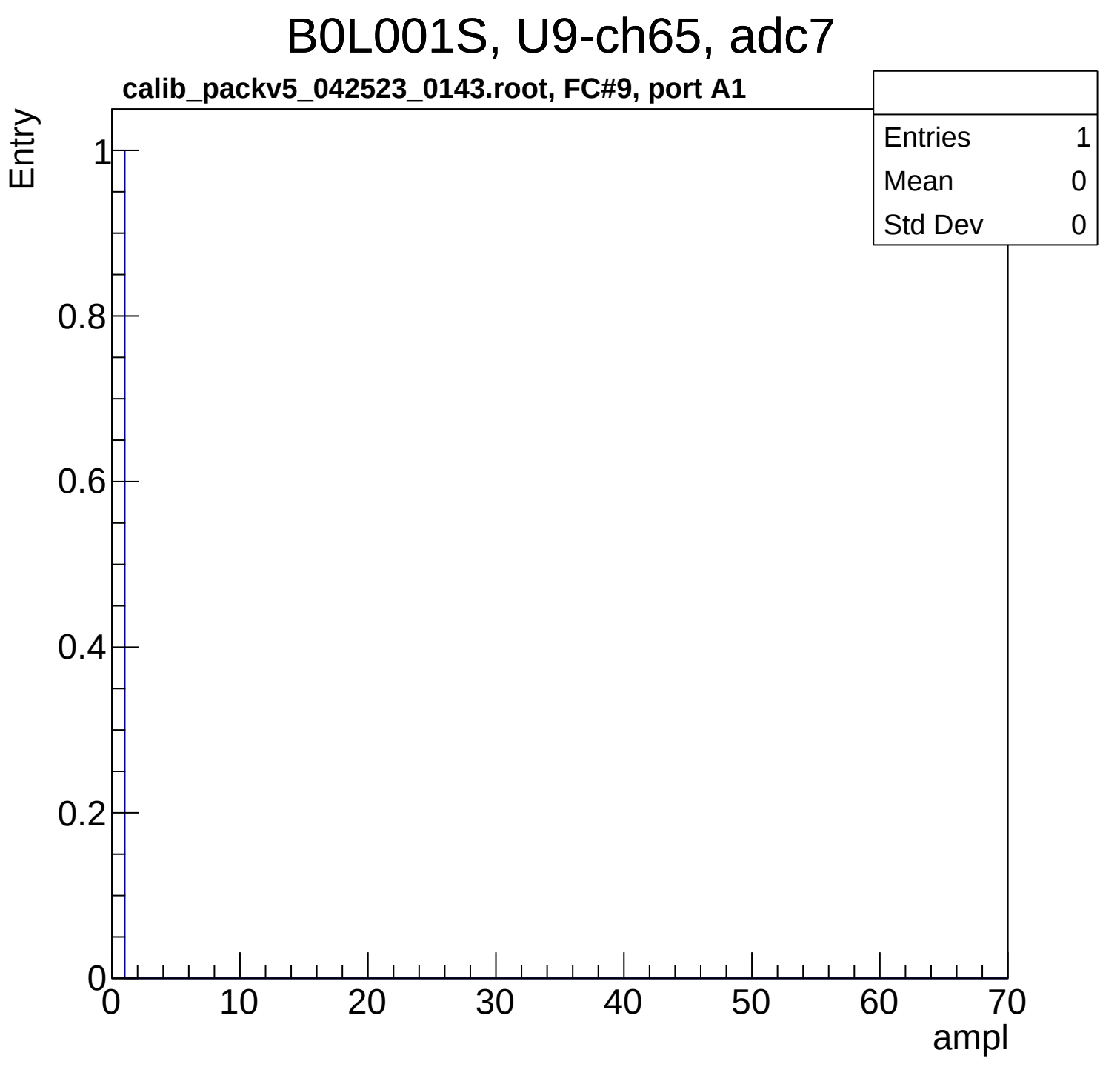
50
60
70
ampl


B0L001S, U9-ch65, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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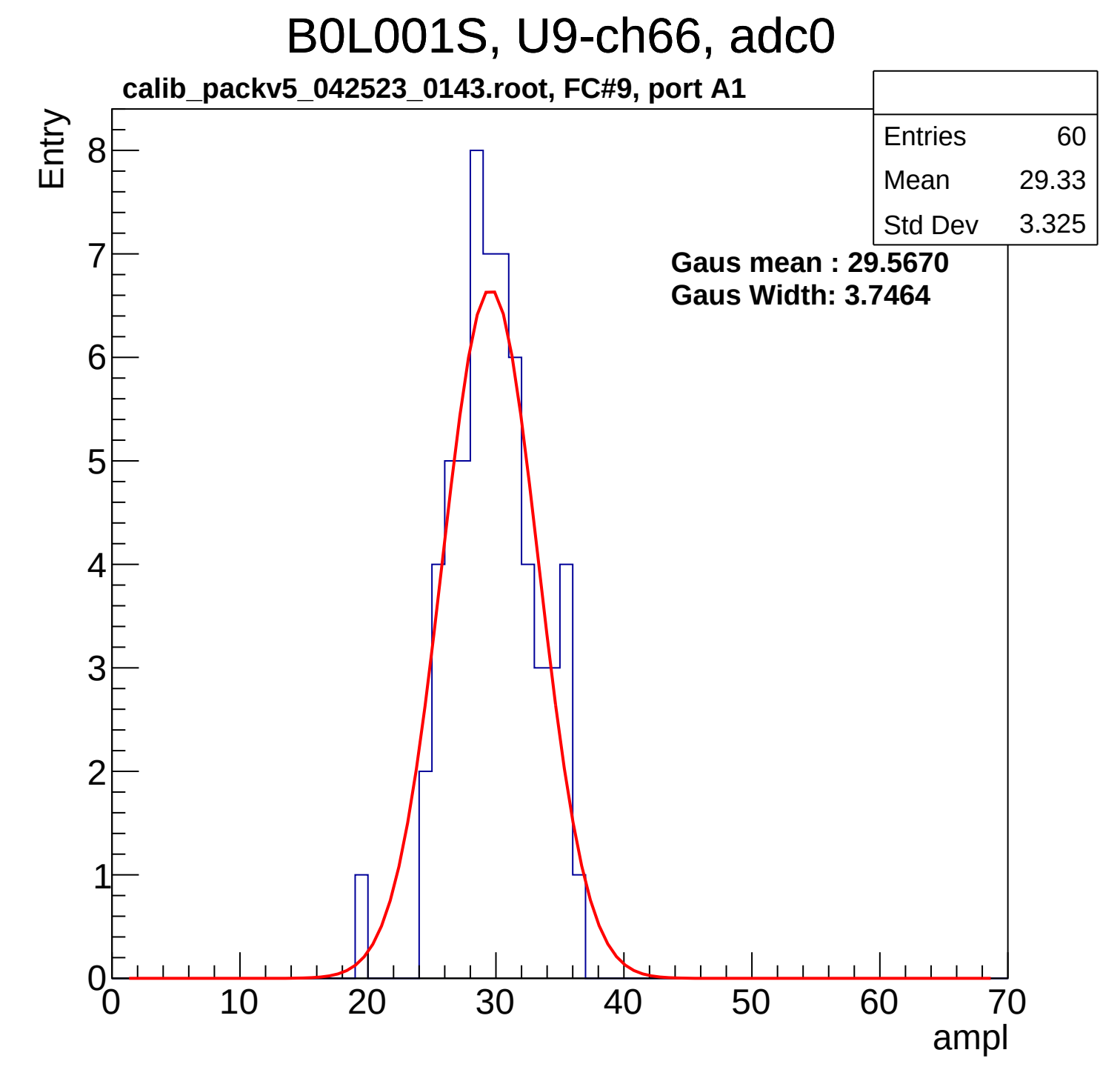
50
60
70
ampl


B0L001S, U9-ch66, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 29.33
Std Dev 3.325
Gaus mean : 29.5670
Gaus Width: 3.7464
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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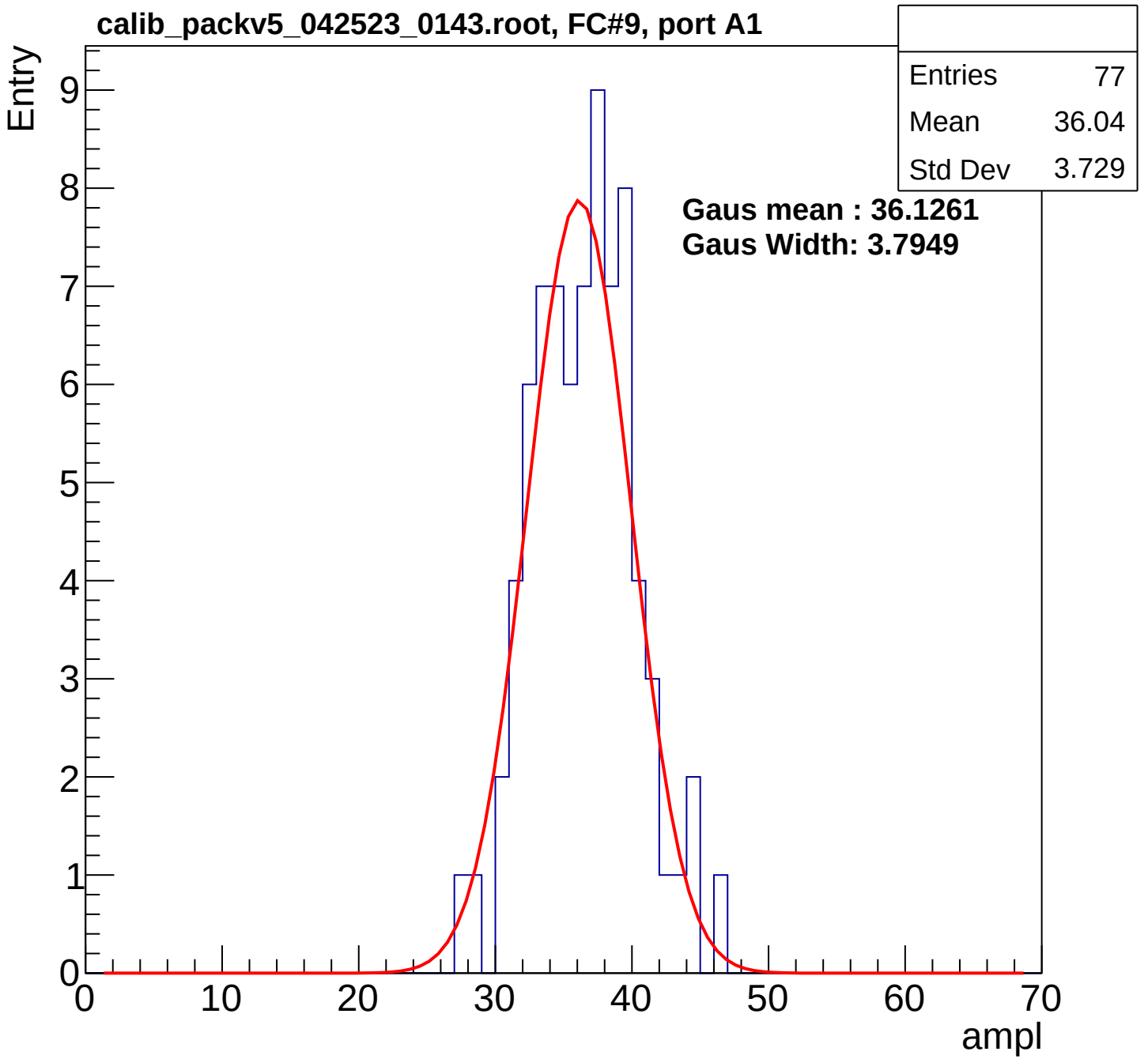
B0L001S, U9-ch66, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 36.04
Std Dev 3.729
Gaus mean : 36.1261
Gaus Width: 3.7949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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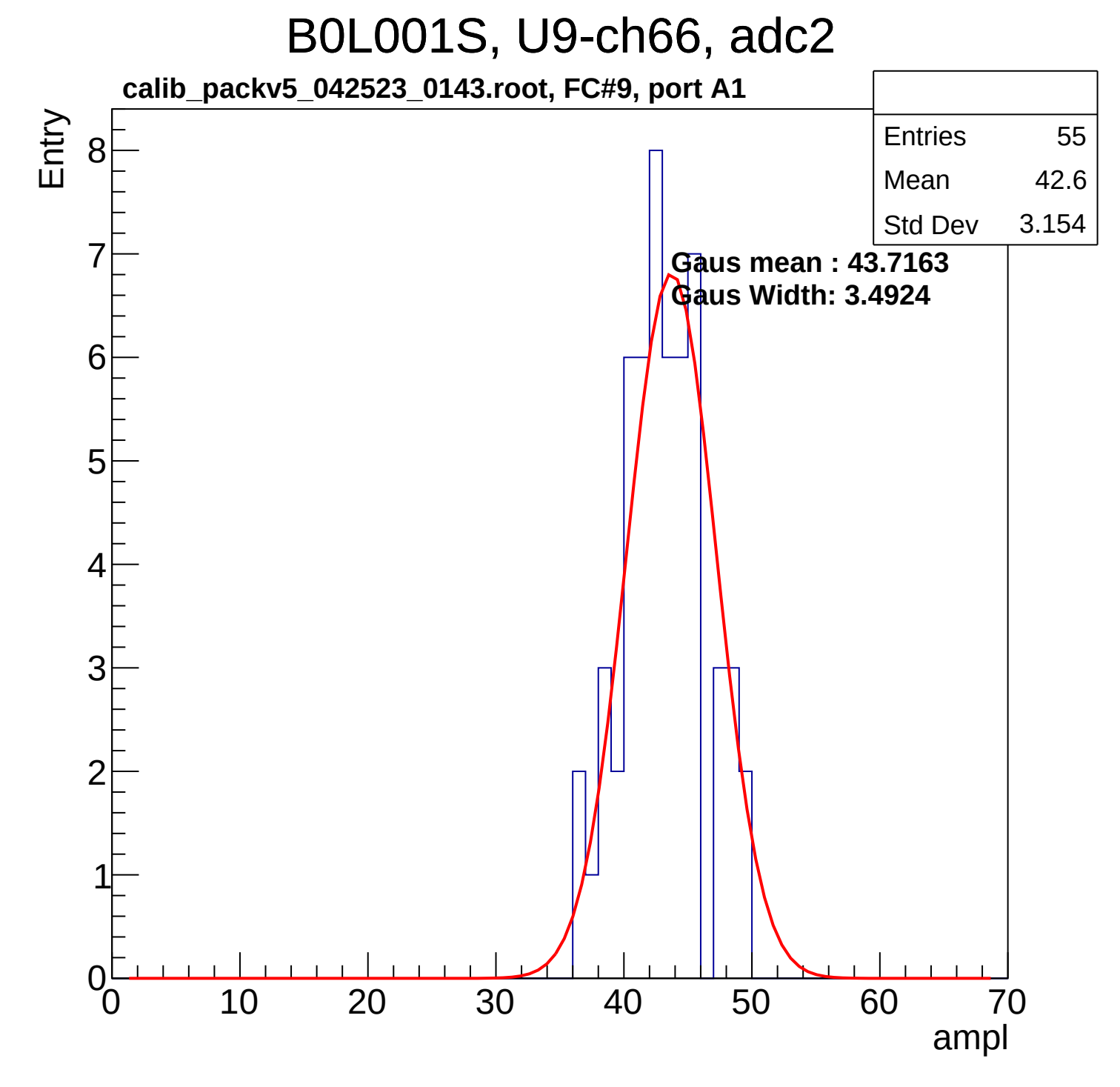
B0L001S, U9-ch66, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.6
Std Dev 3.154
Gaus mean : 43.7163
Gaus Width: 3.4924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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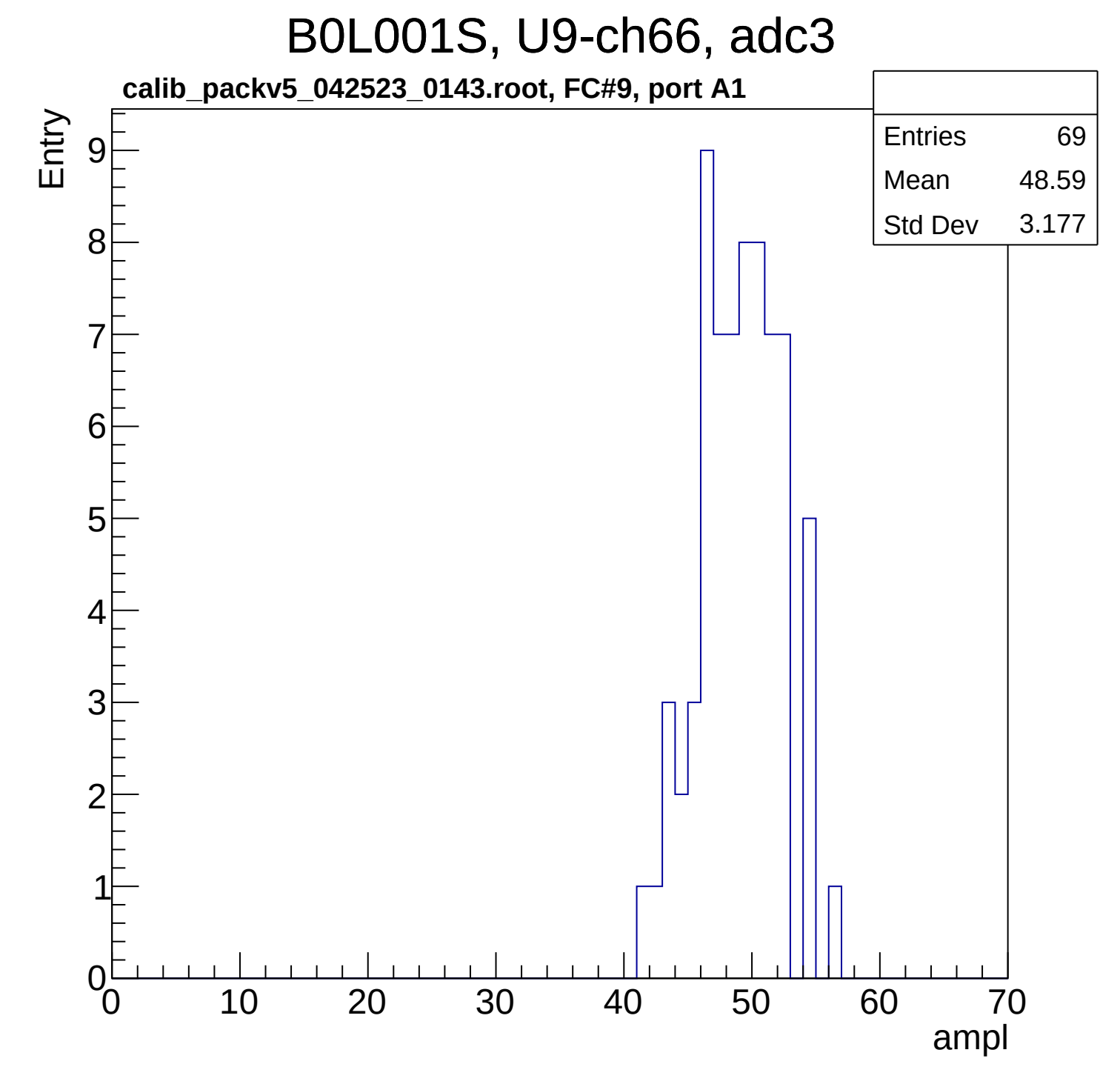
B0L001S, U9-ch66, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 48.59
Std Dev 3.177
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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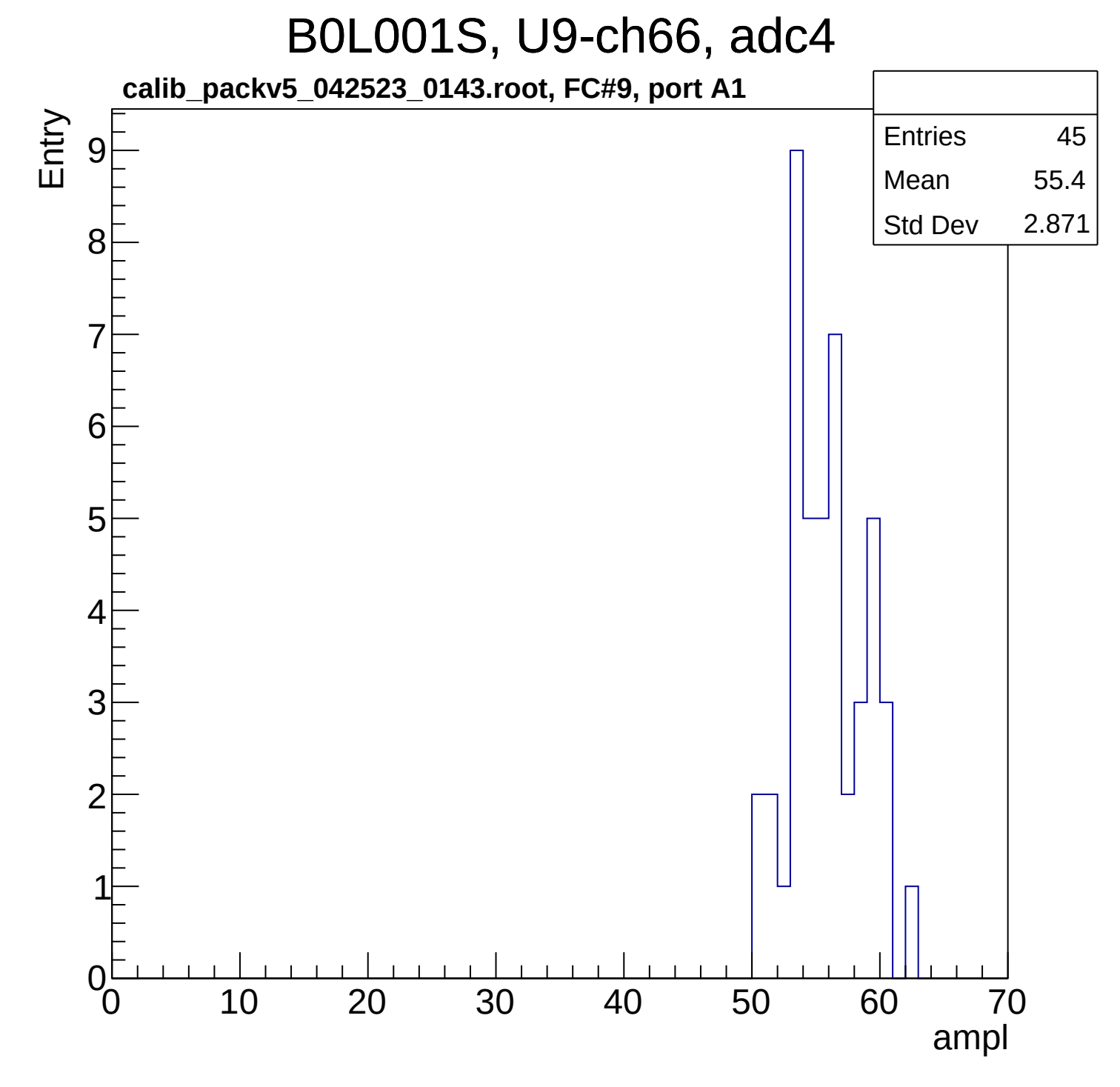
B0L001S, U9-ch66, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 55.4
Std Dev 2.871
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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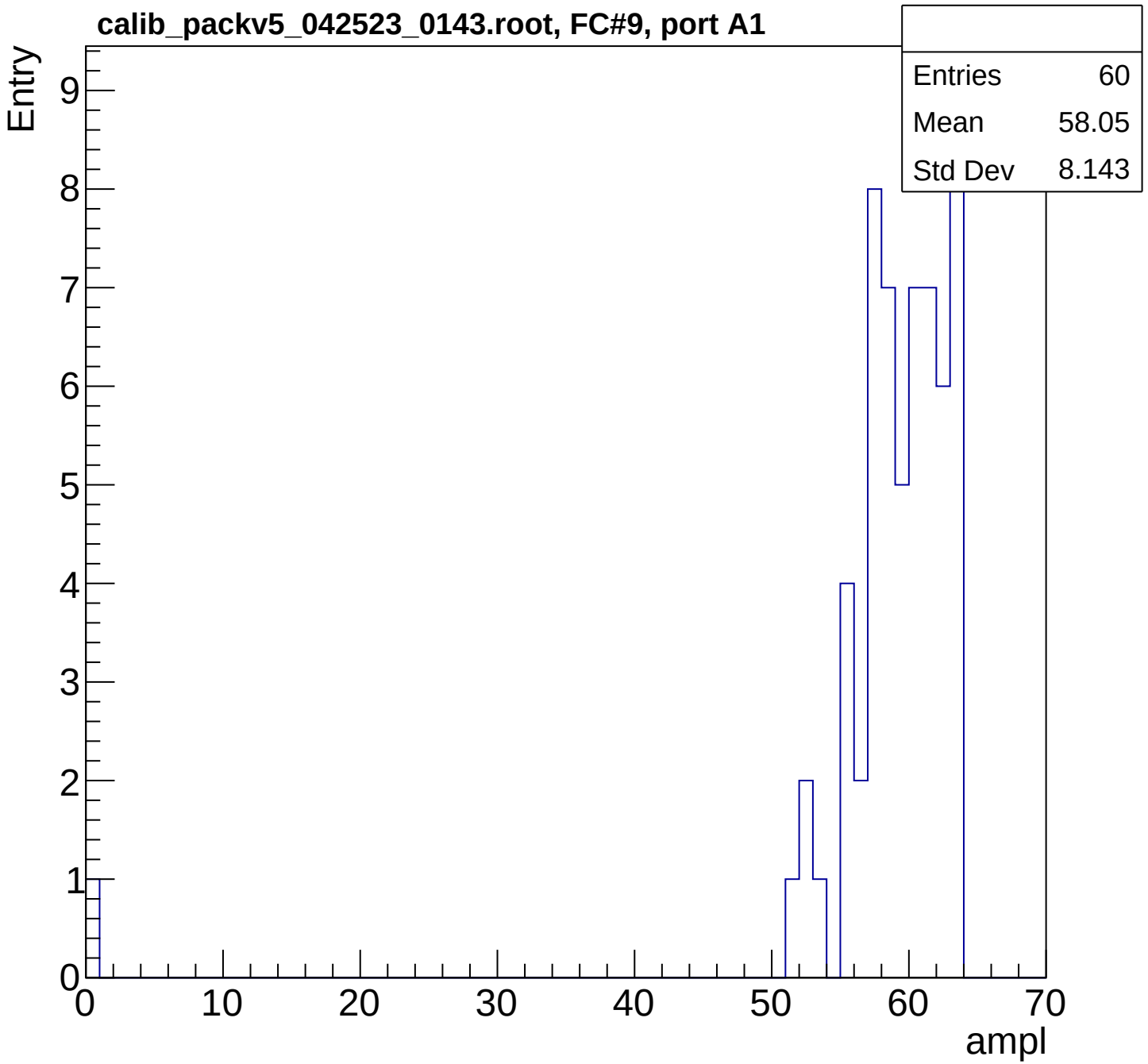
B0L001S, U9-ch66, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 58.05
Std Dev 8.143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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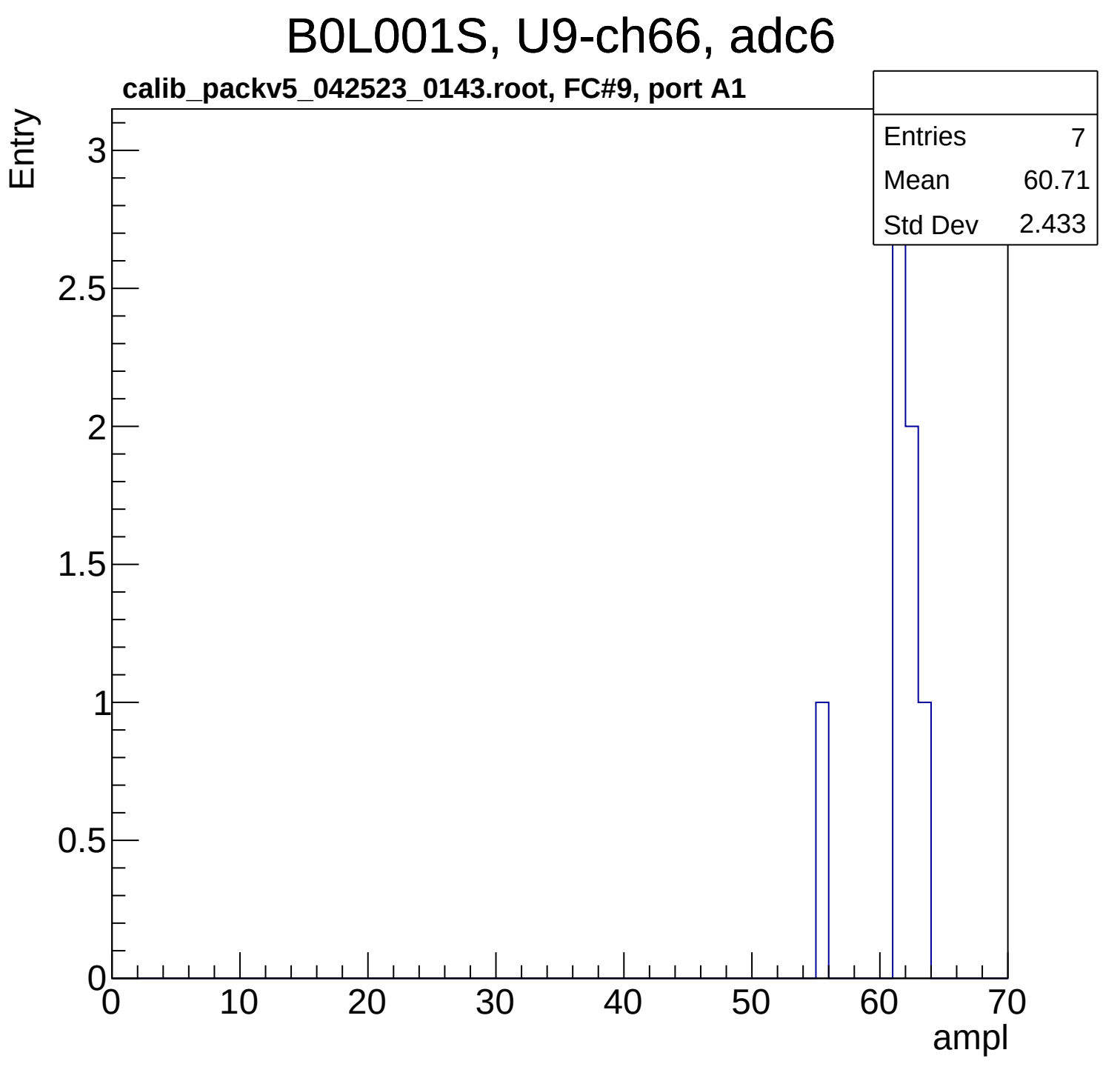
B0L001S, U9-ch66, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 60.71
Std Dev 2.433
Entry
ampl
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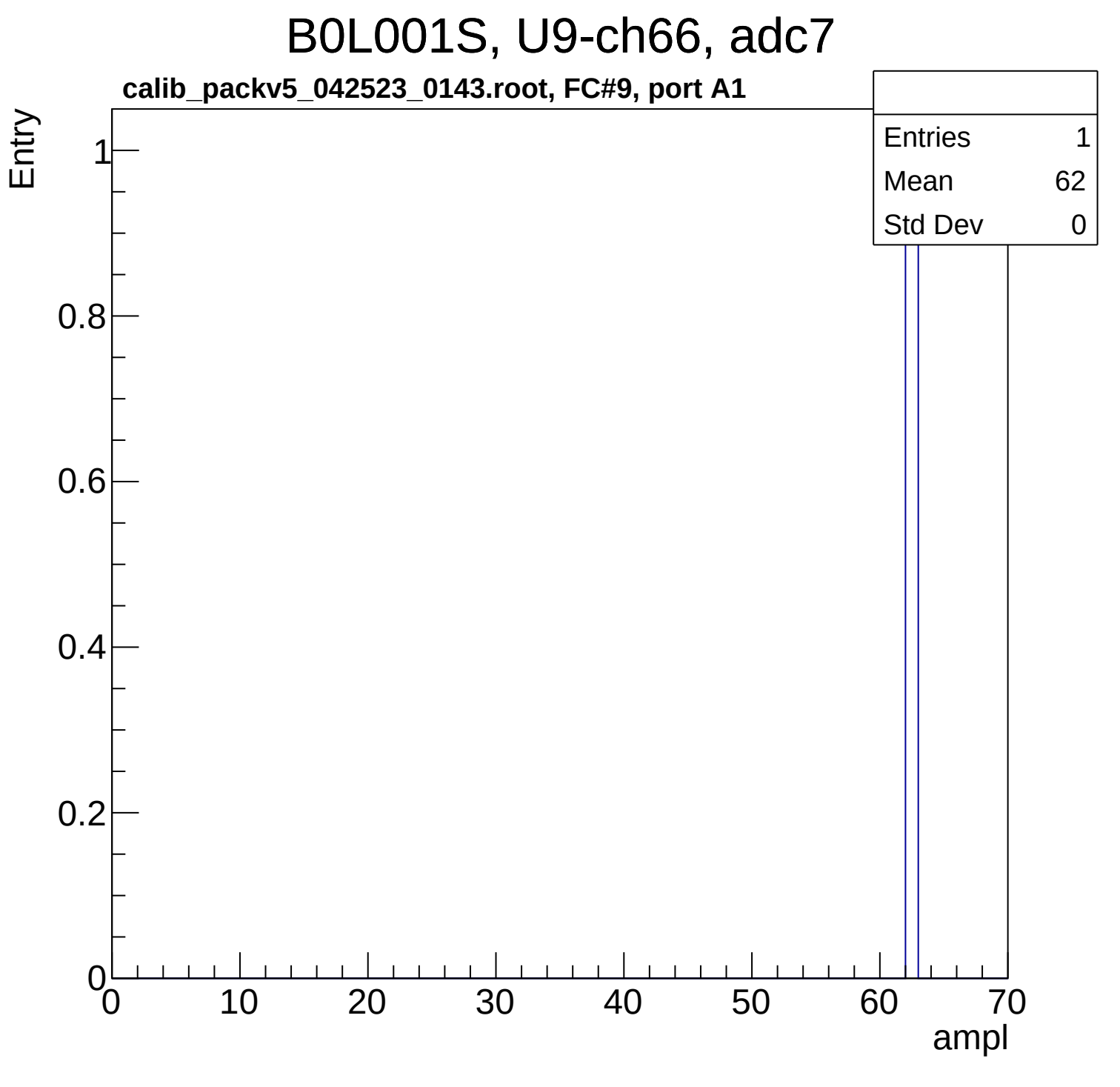
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch66, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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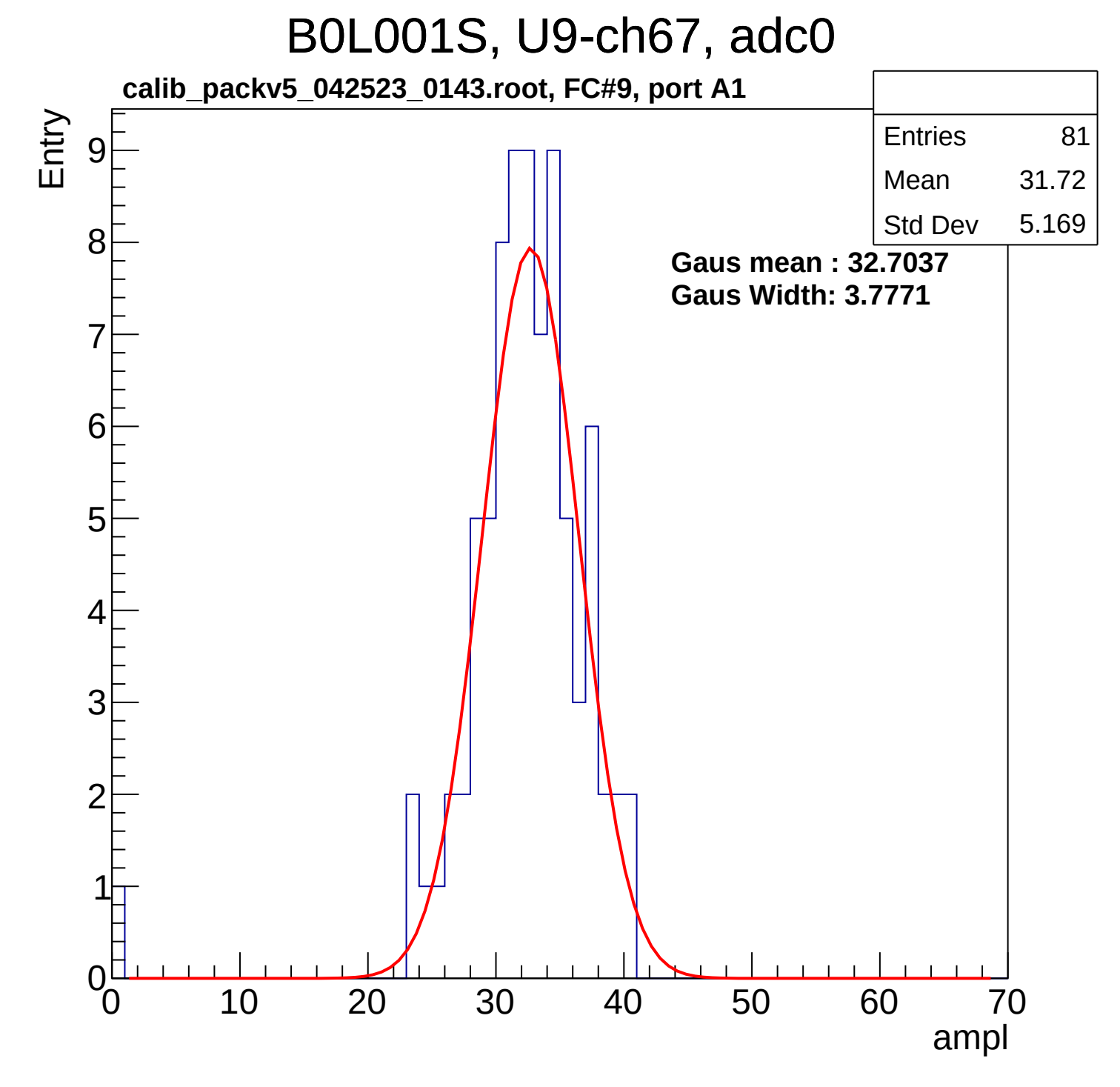
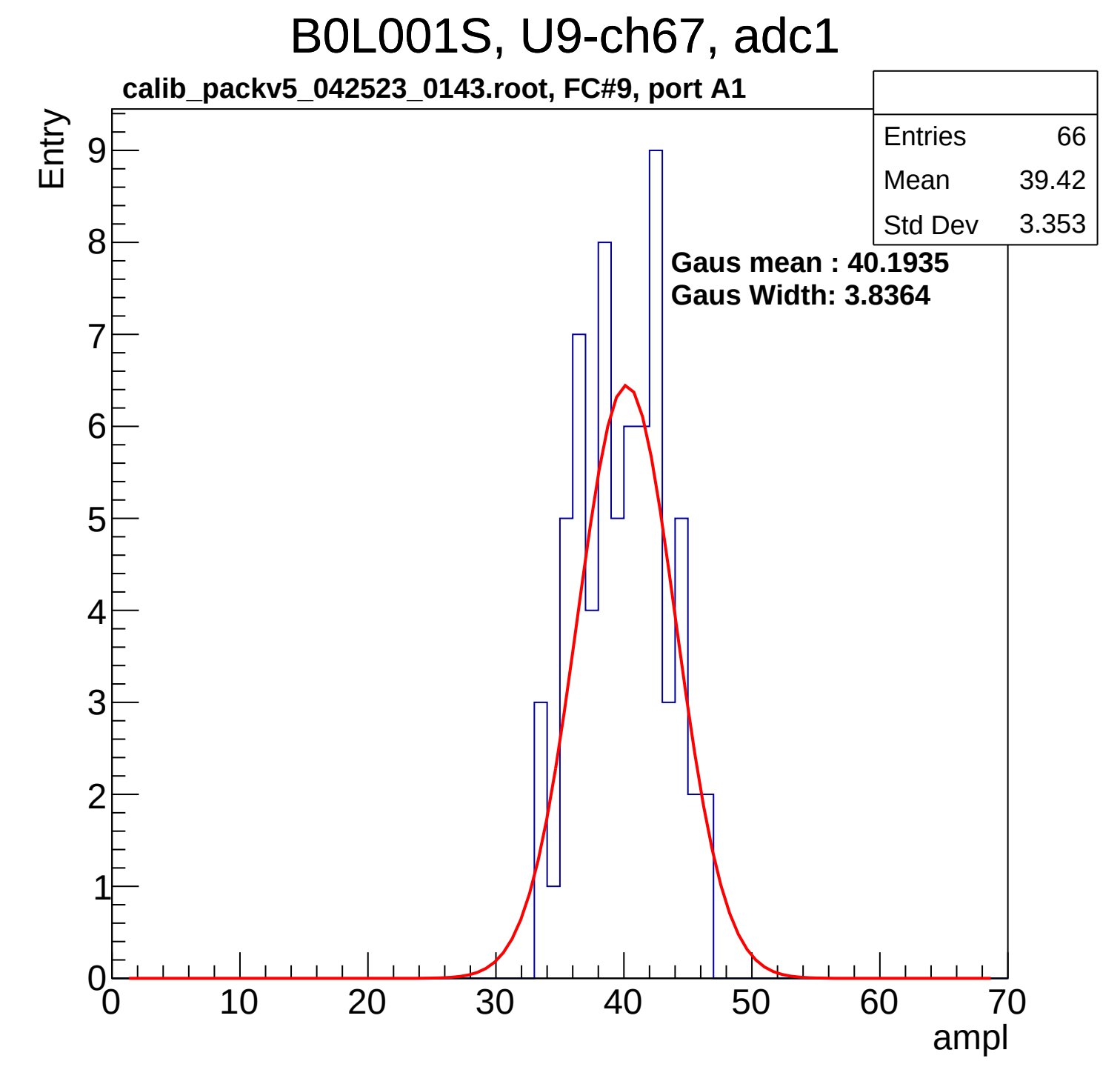
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch67, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 31.72
Std Dev 5.169
Gaus mean : 32.7037
Gaus Width: 3.7771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch67, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 39.42
Std Dev 3.353
Gaus mean : 40.1935
Gaus Width: 3.8364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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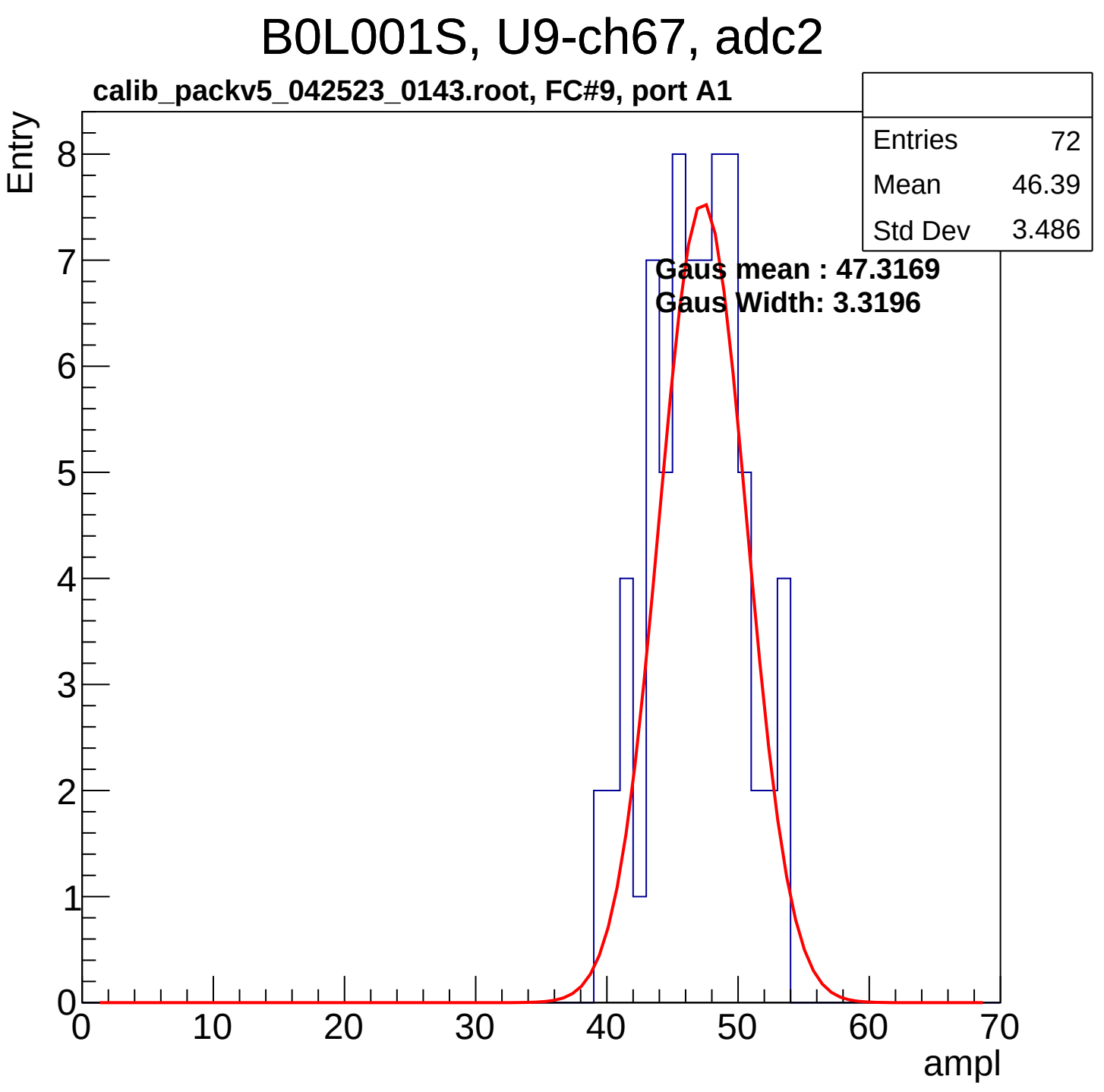
B0L001S, U9-ch67, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 46.39
Std Dev 3.486
Gaus mean : 47.3169
Gaus Width: 3.3196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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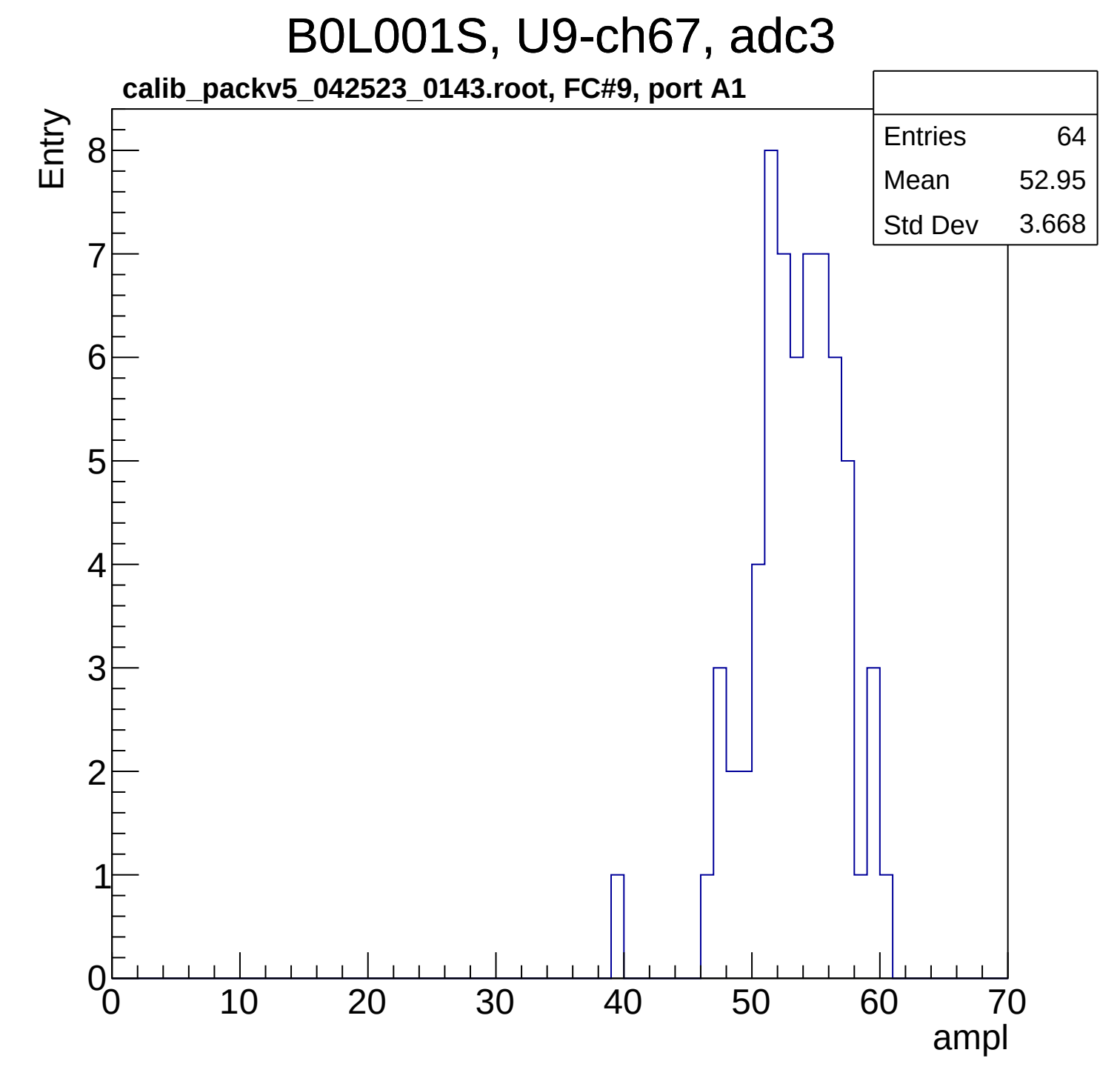
B0L001S, U9-ch67, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 52.95
Std Dev 3.668
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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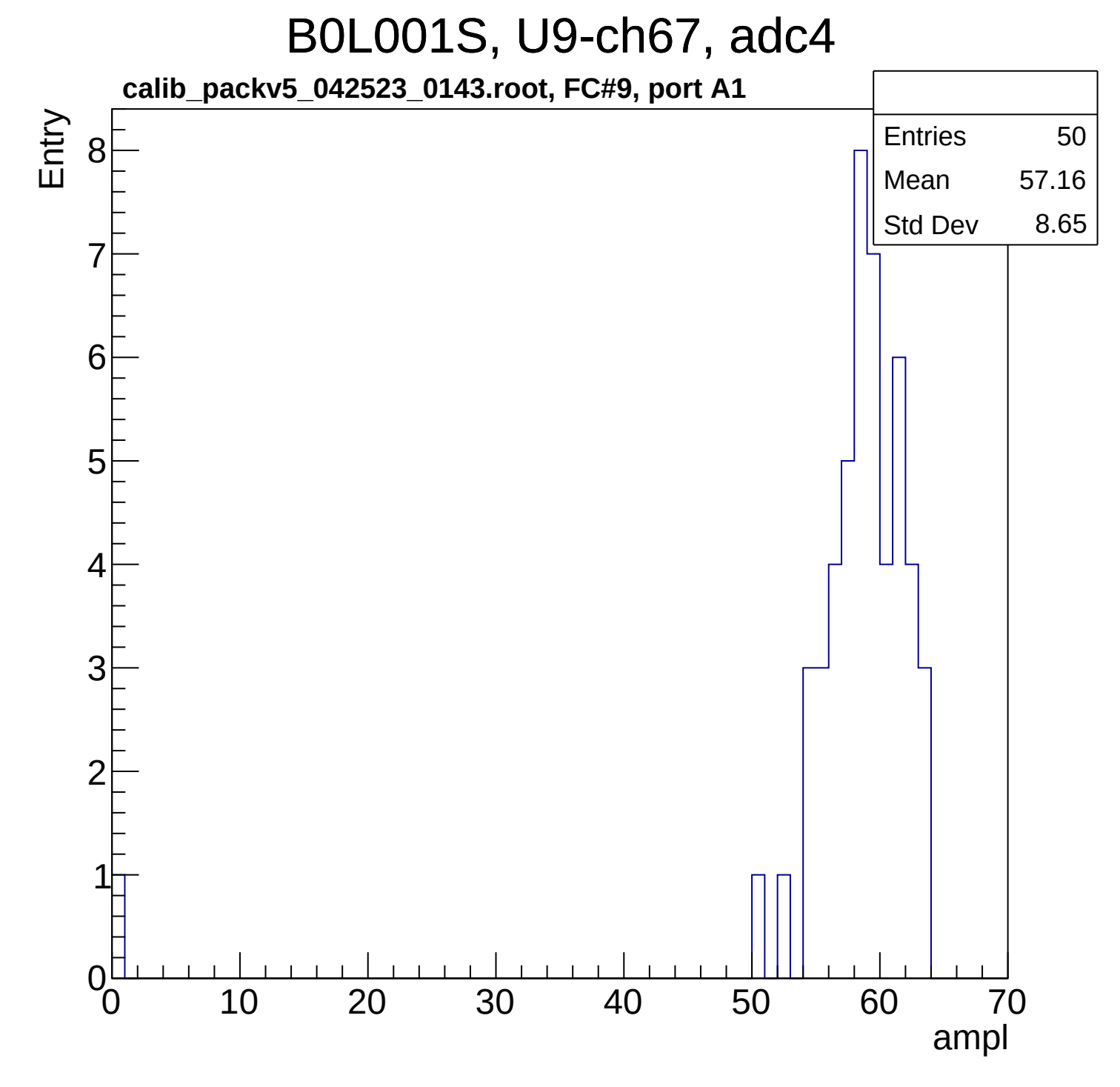
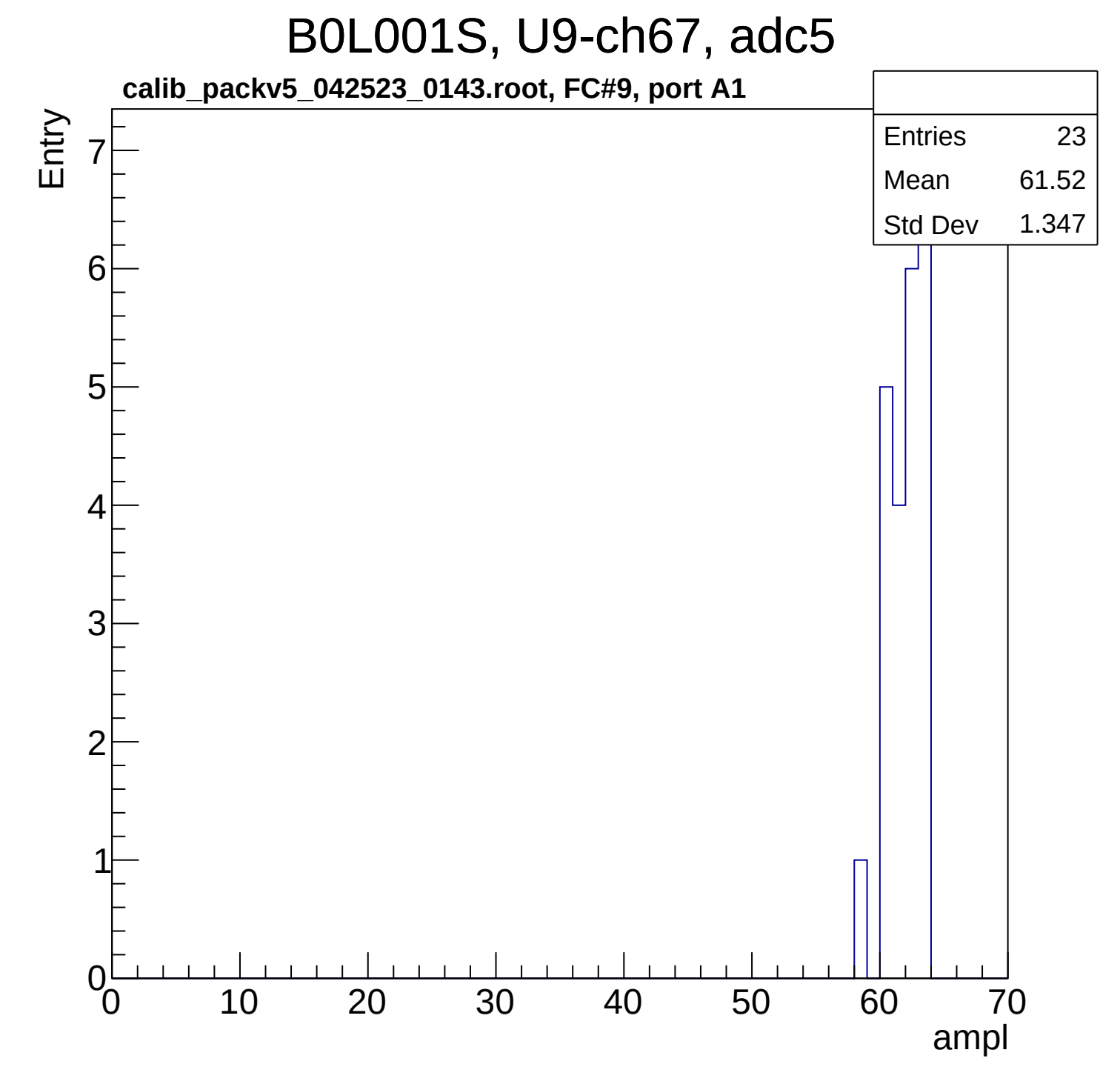
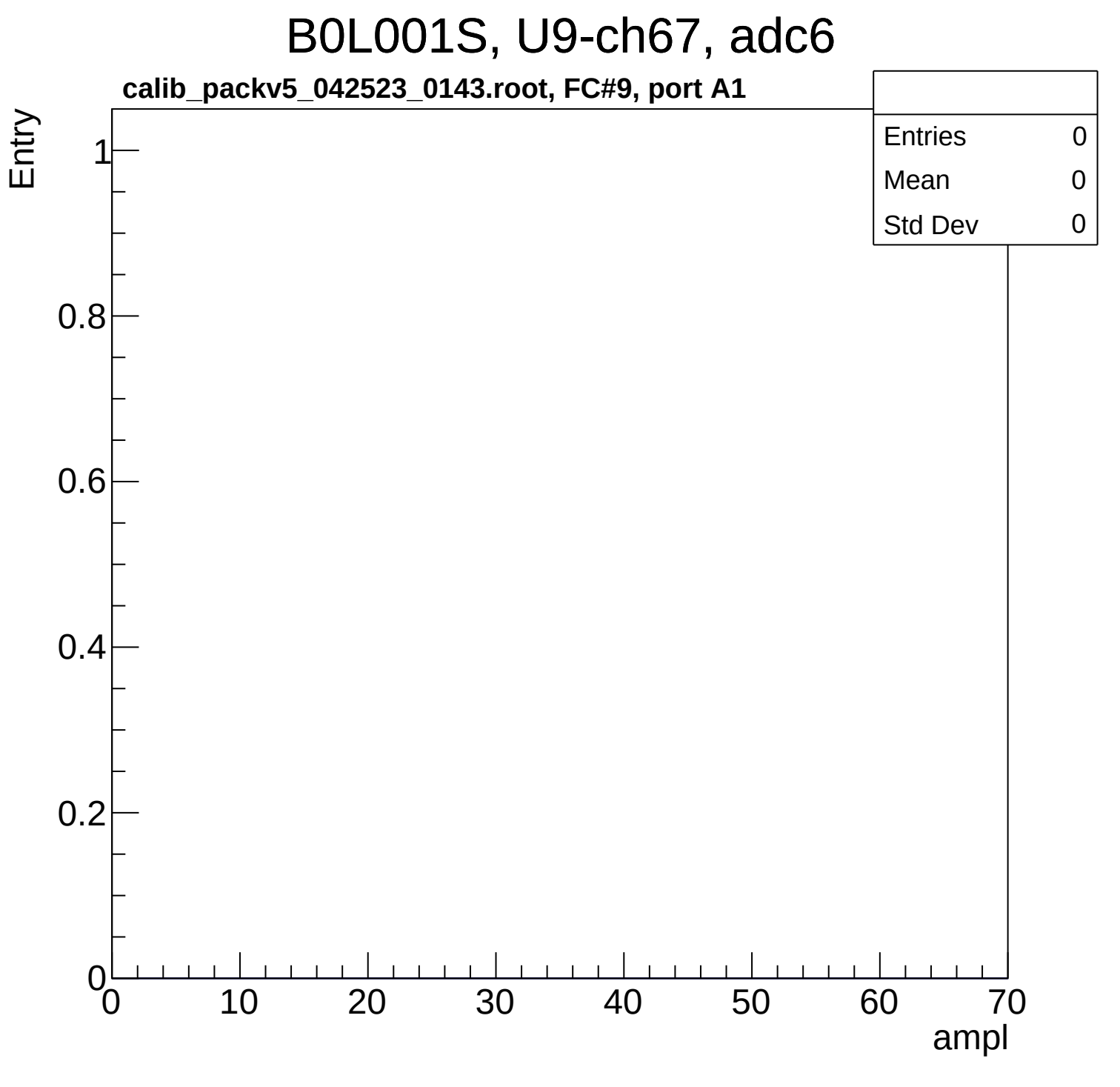
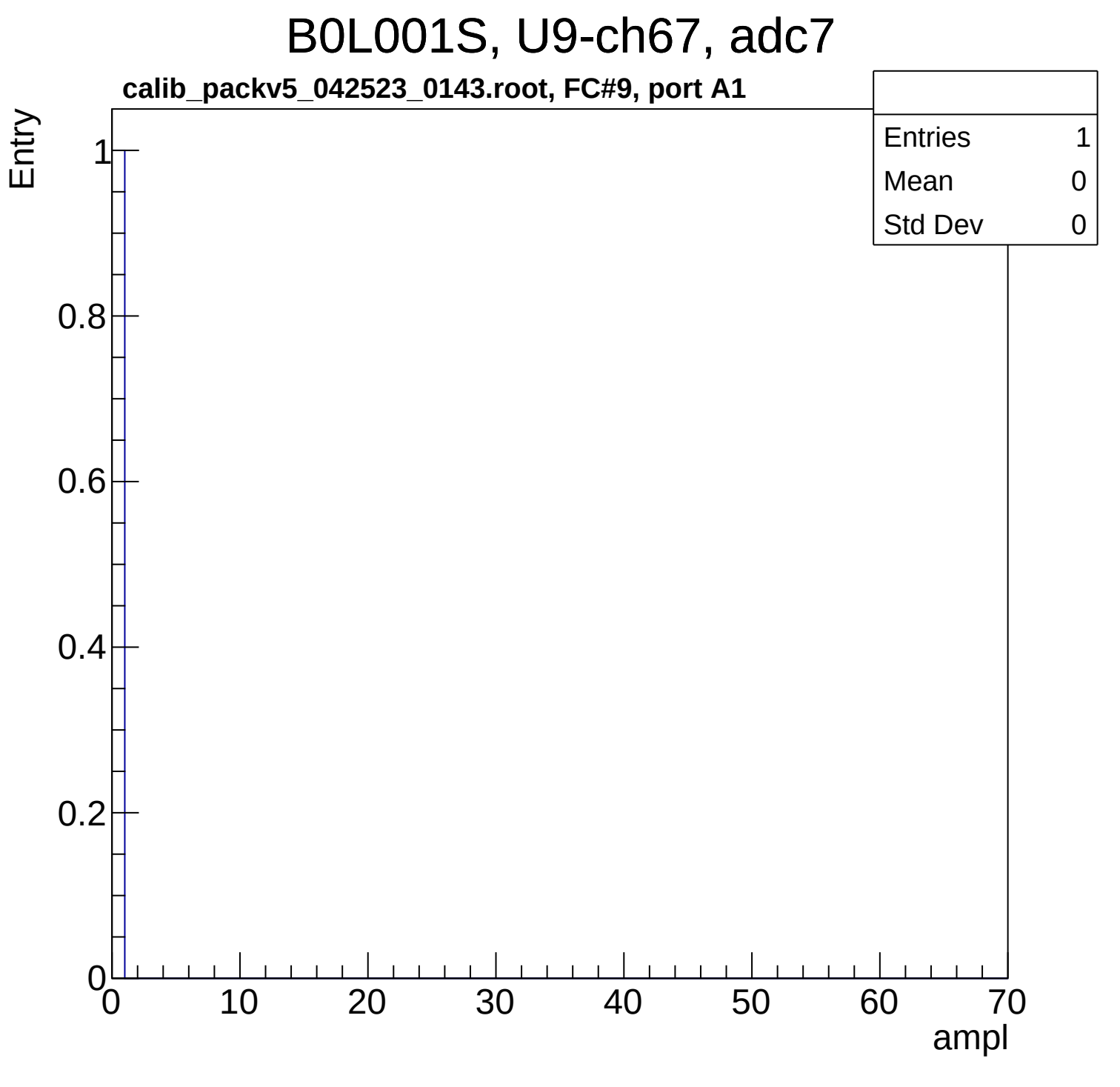

B0L001S, U9-ch67, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.16
Std Dev 8.65
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch67, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 23
Mean 61.52
Std Dev 1.347
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch67, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch67, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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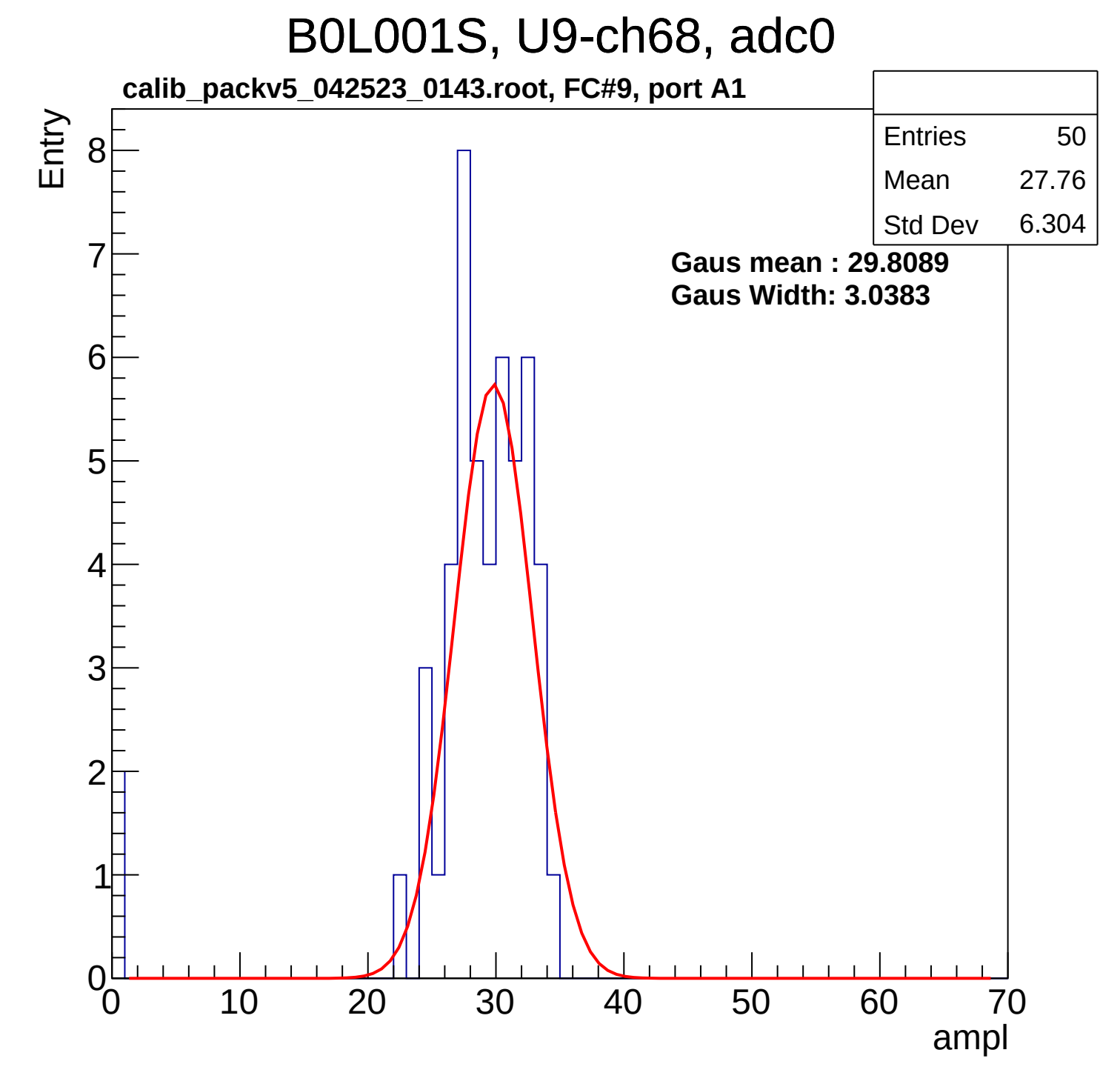
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch68, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 27.76
Std Dev 6.304
Gaus mean : 29.8089
Gaus Width: 3.0383
Entry
ampl
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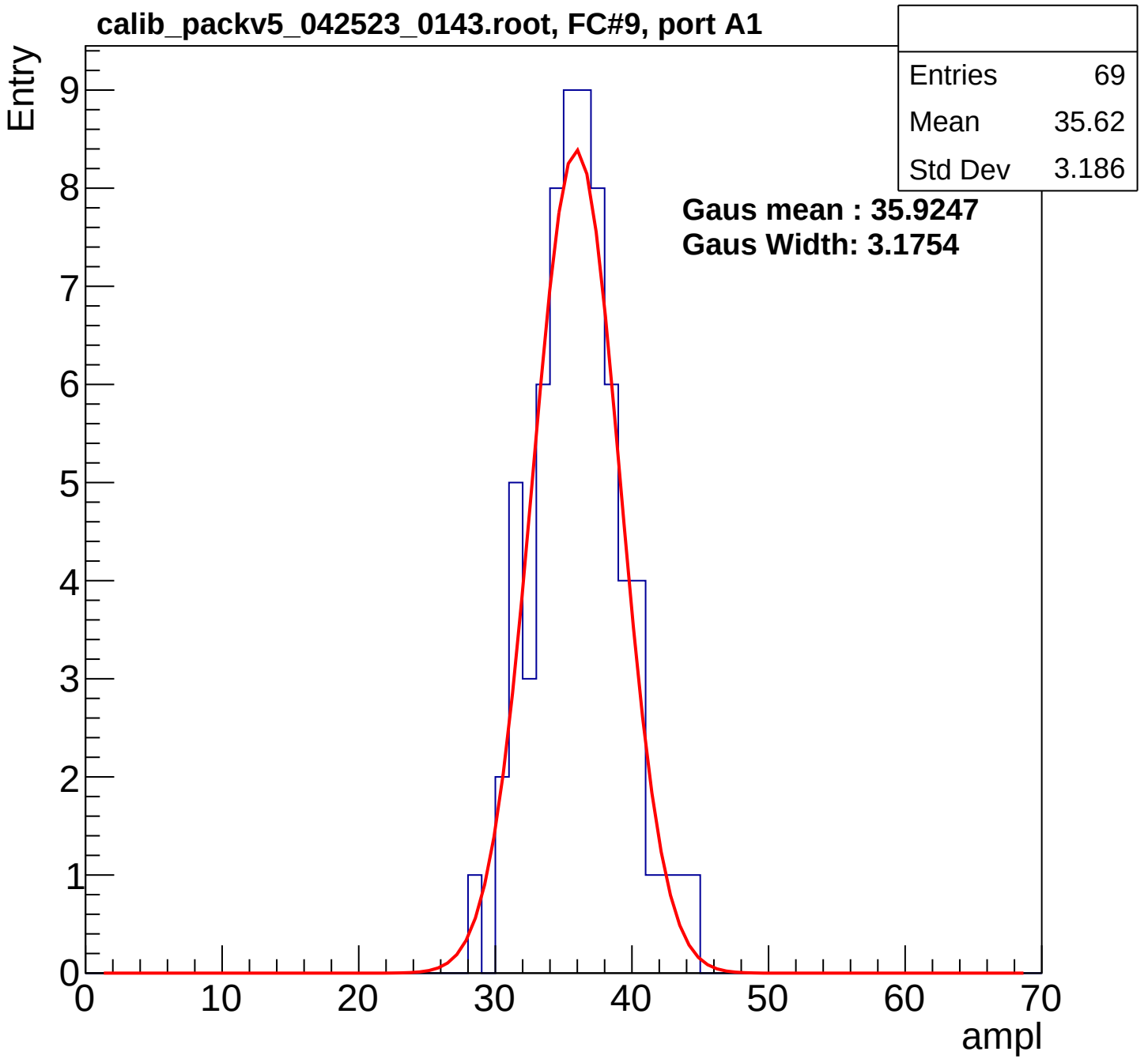
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch68, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 35.62
Std Dev 3.186
Gaus mean : 35.9247
Gaus Width: 3.1754
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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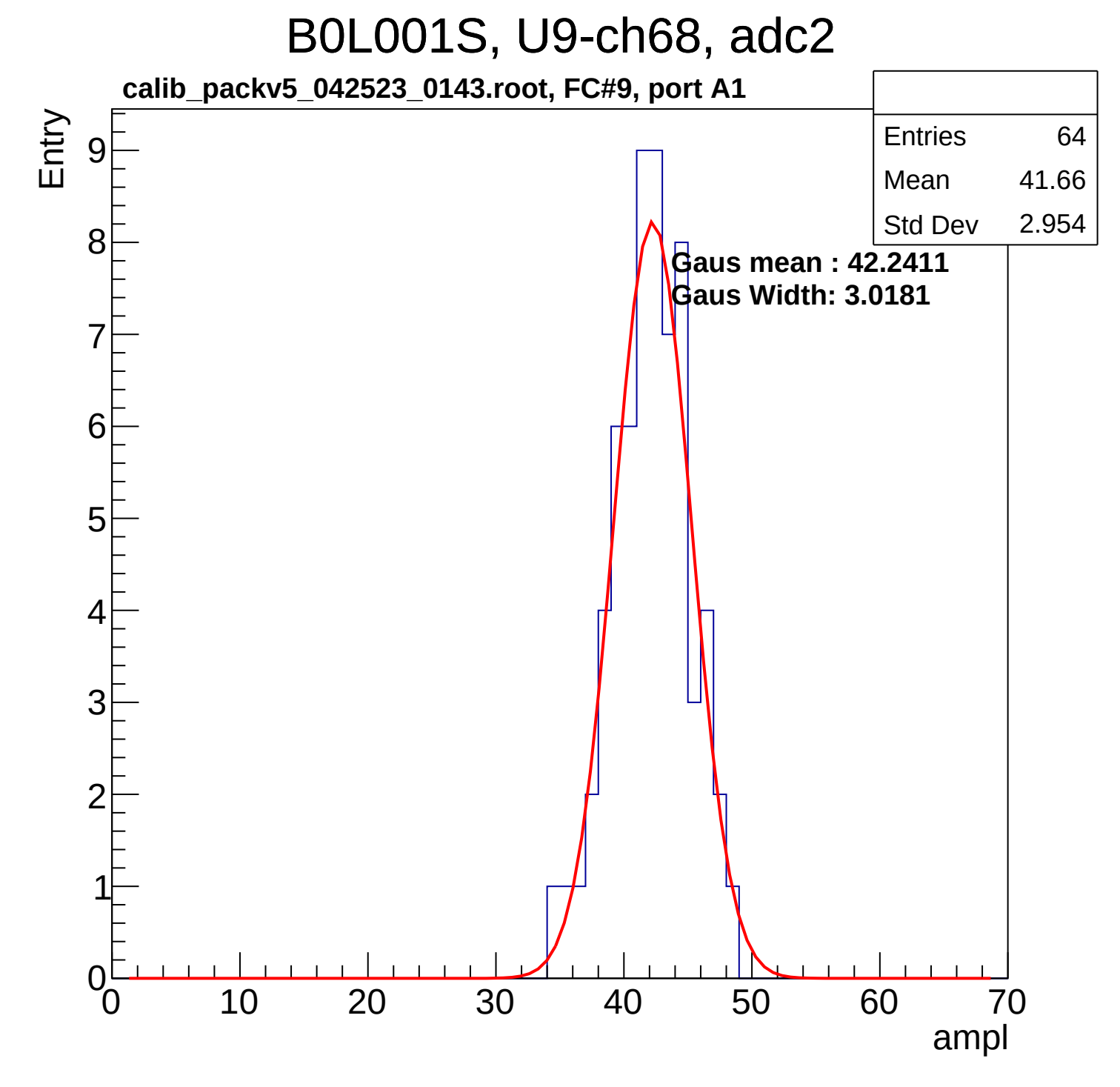
60
70


B0L001S, U9-ch68, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 41.66
Std Dev 2.954
Gaus mean : 42.2411
Gaus Width: 3.0181
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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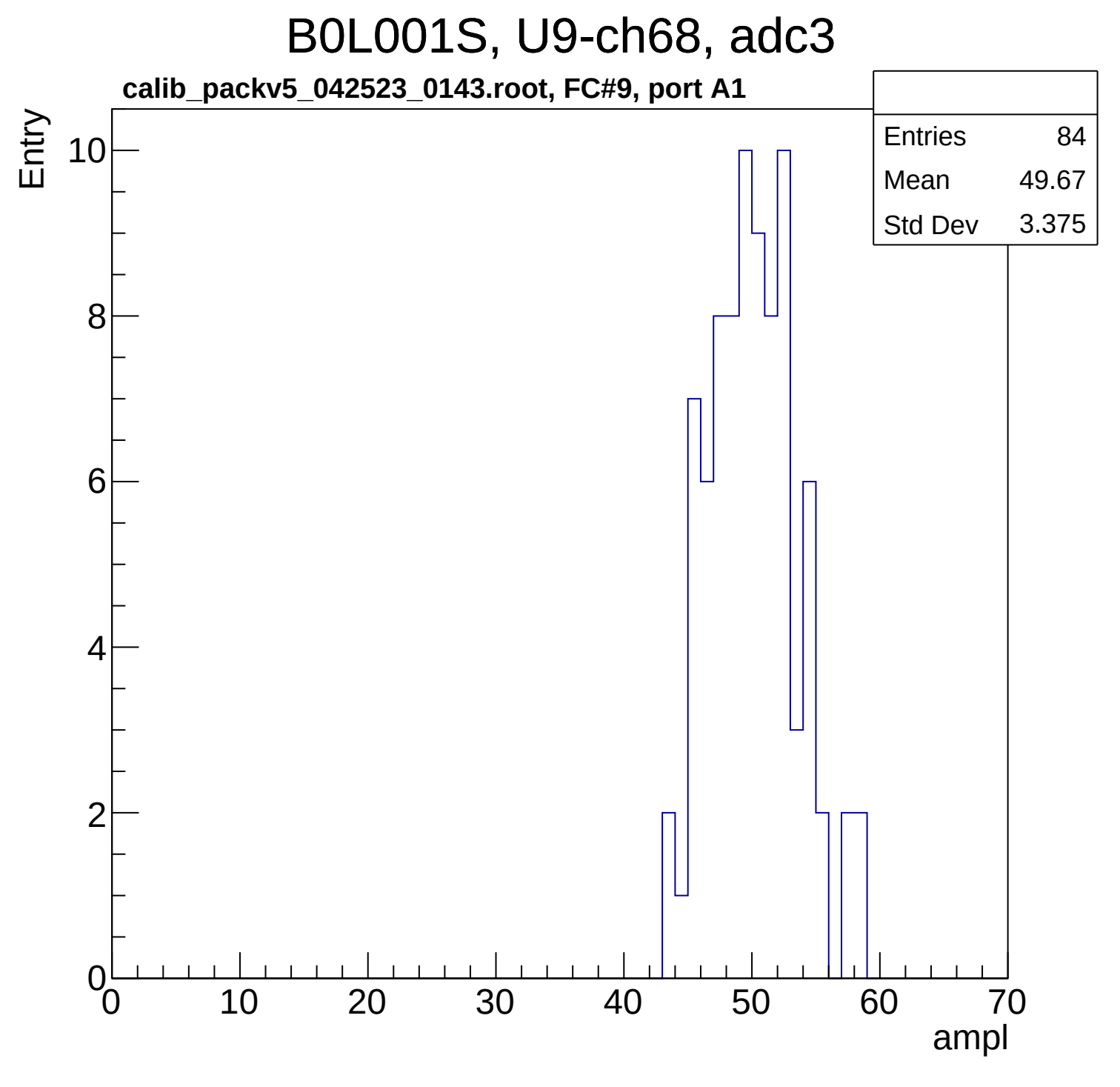
B0L001S, U9-ch68, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 49.67
Std Dev 3.375
Entry
ampl
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4
6
8
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0
10
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40
50
60
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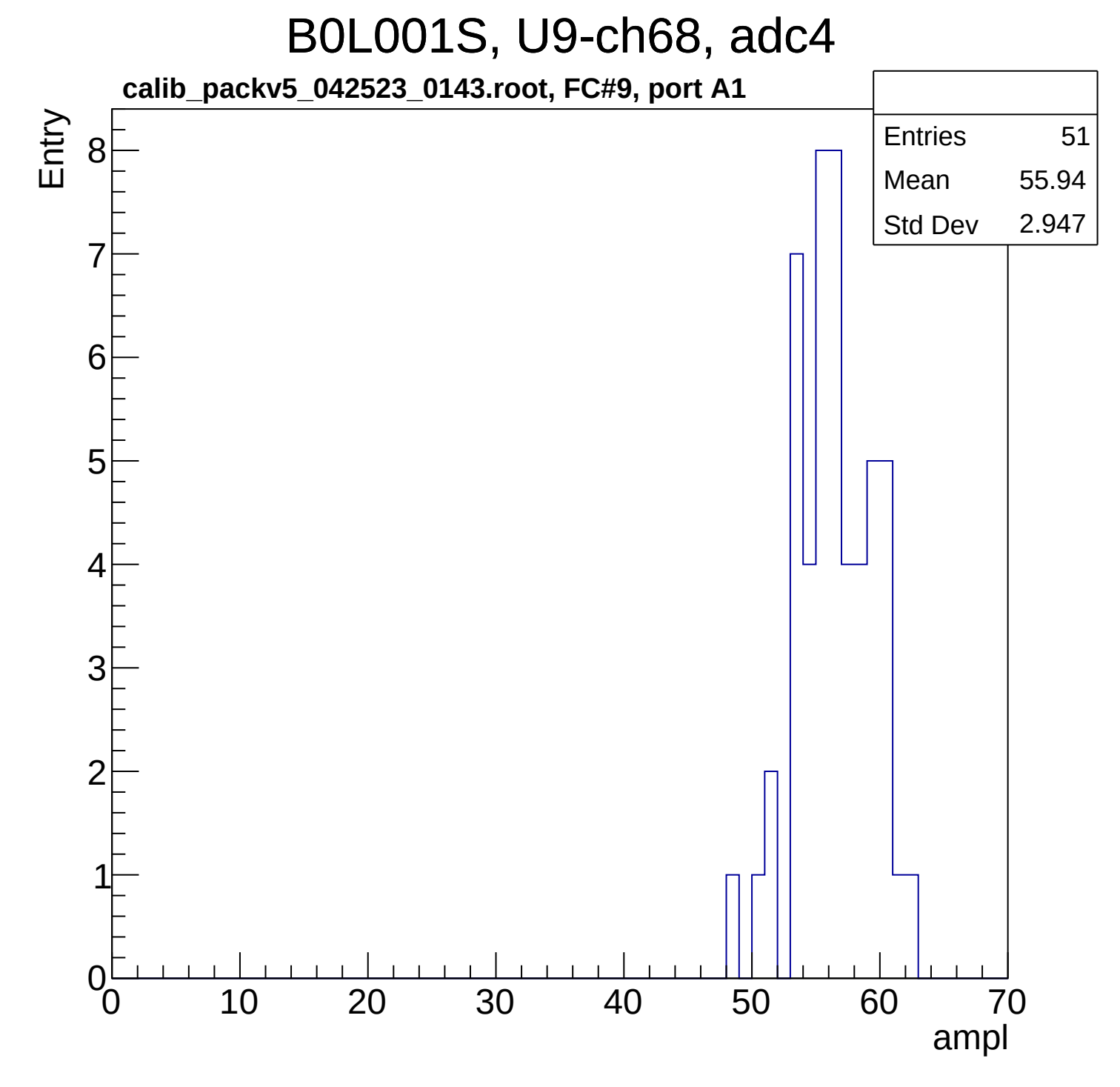
B0L001S, U9-ch68, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 55.94
Std Dev 2.947
Entry
ampl
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1
2
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5
6
7
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0
10
20
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40
50
60
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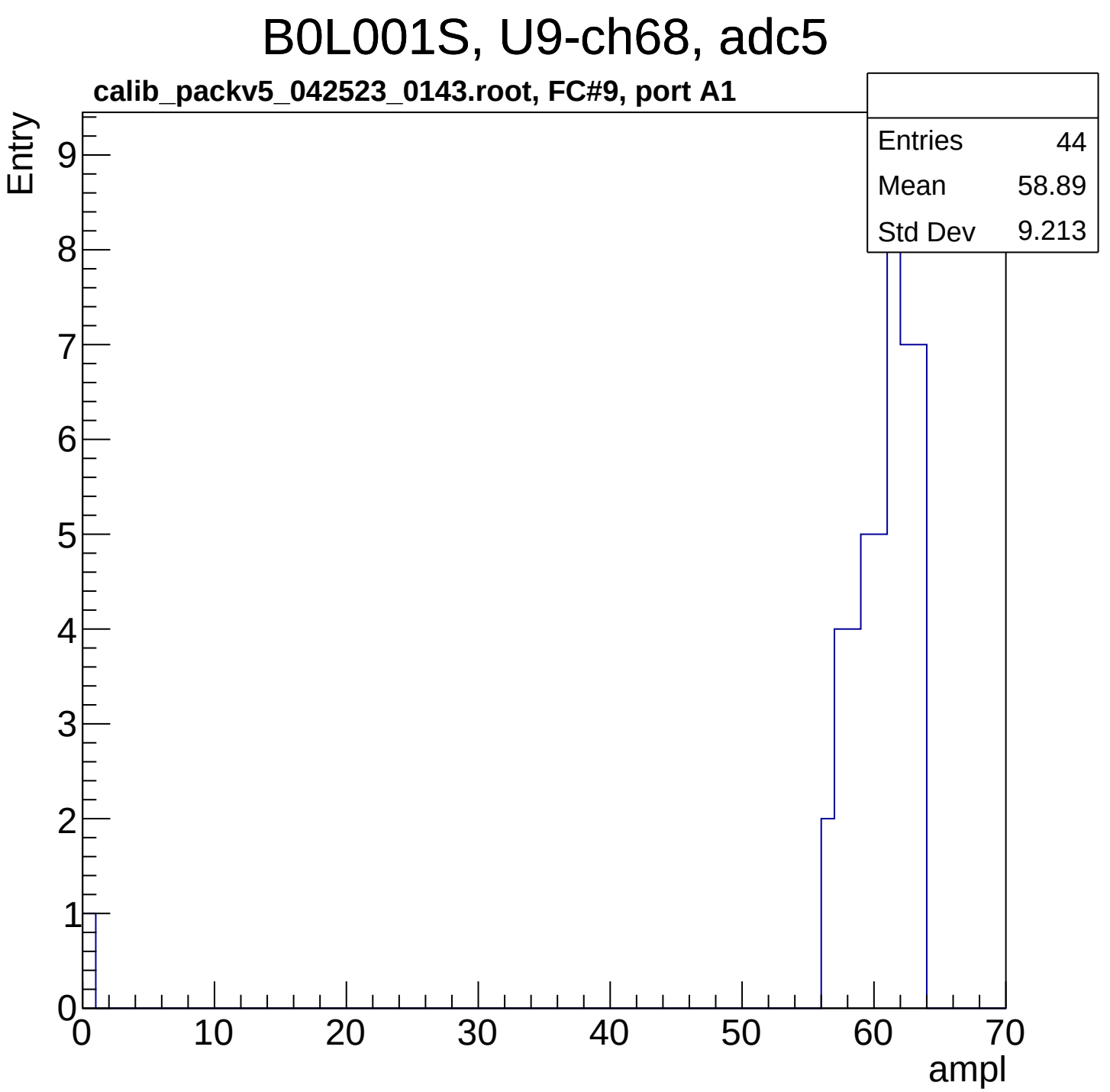
B0L001S, U9-ch68, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.89
Std Dev 9.213
Entry
ampl
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3
4
5
6
7
8
9
0
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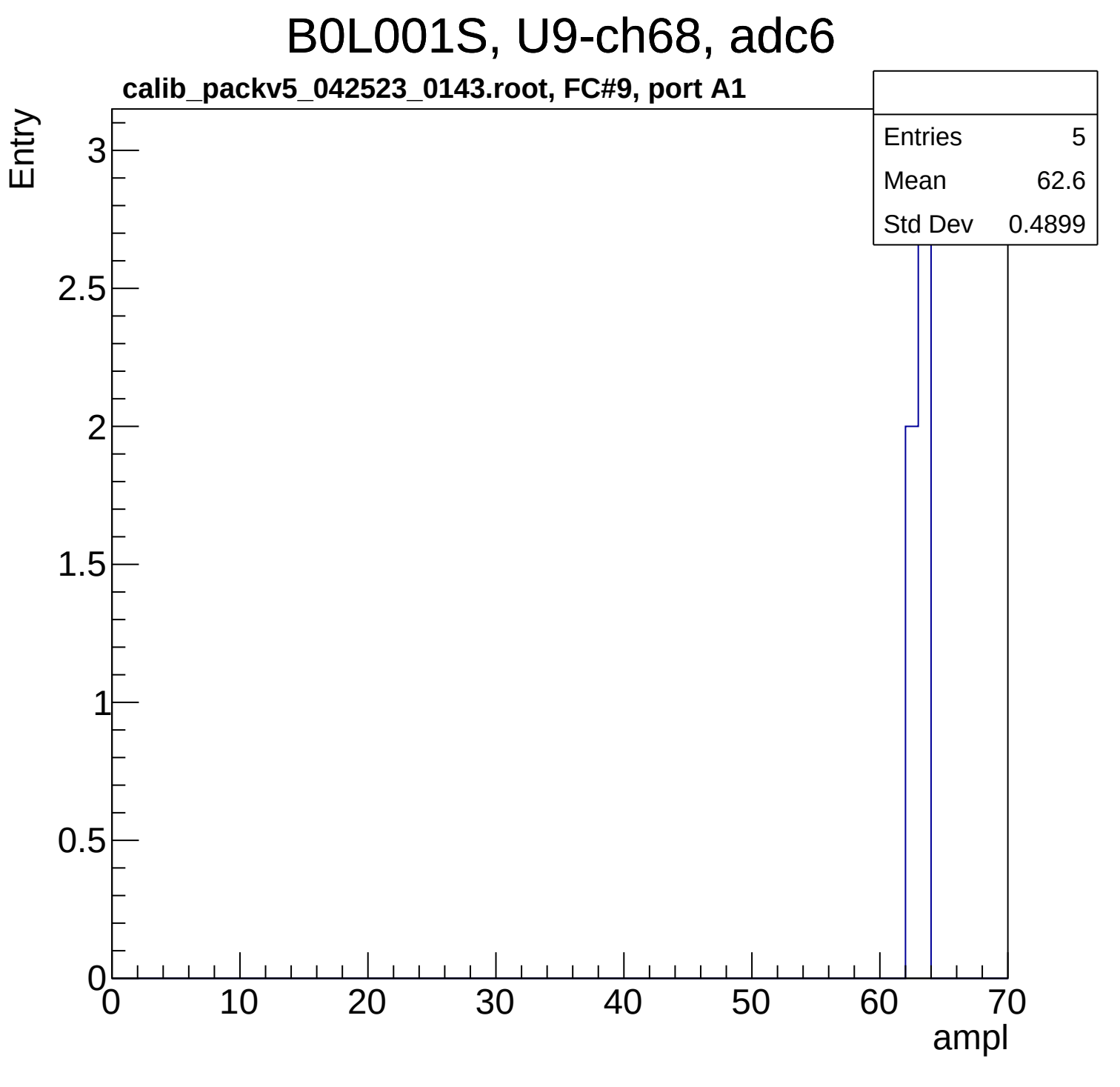
B0L001S, U9-ch68, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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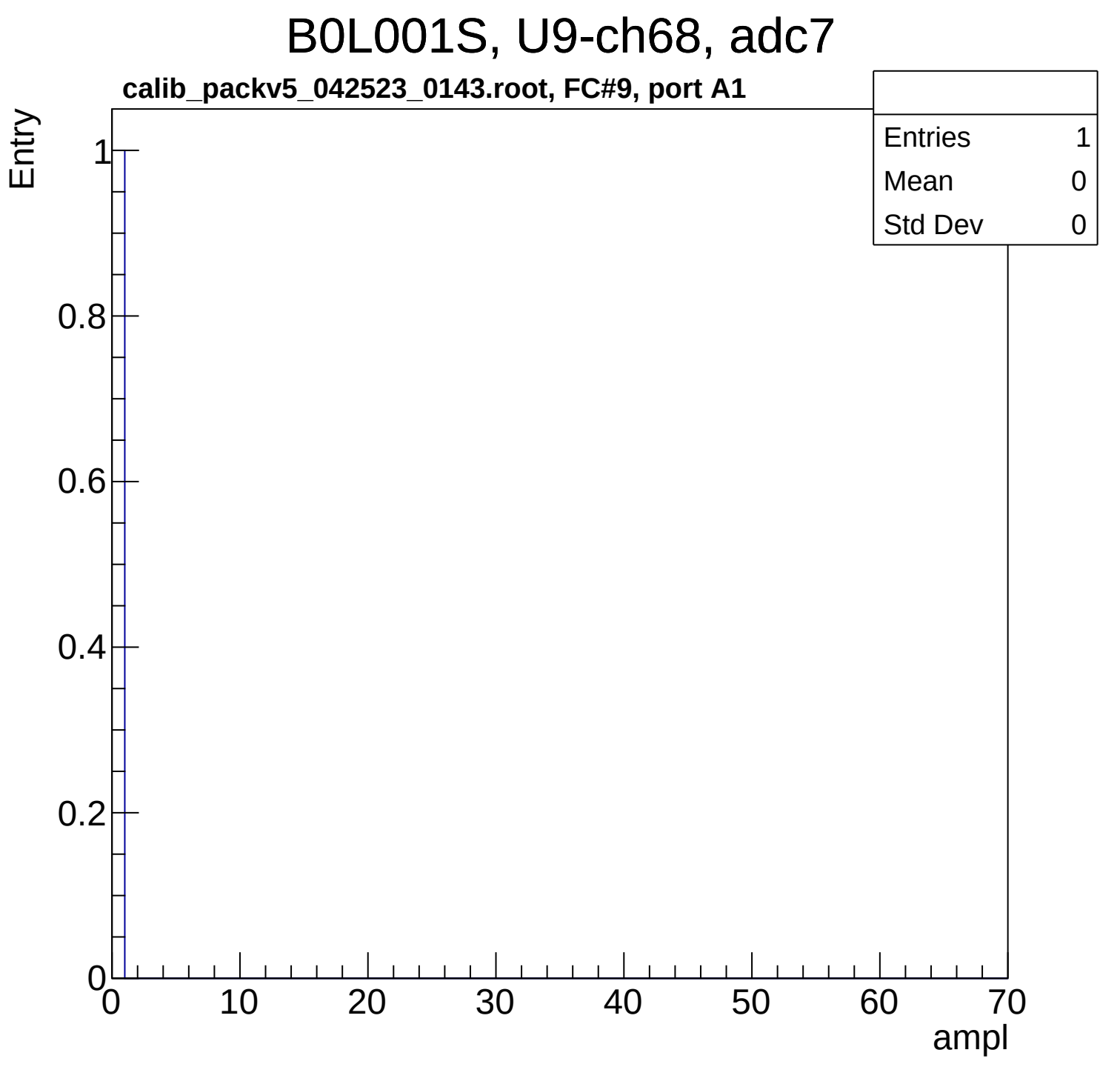
40
50
60
70


B0L001S, U9-ch68, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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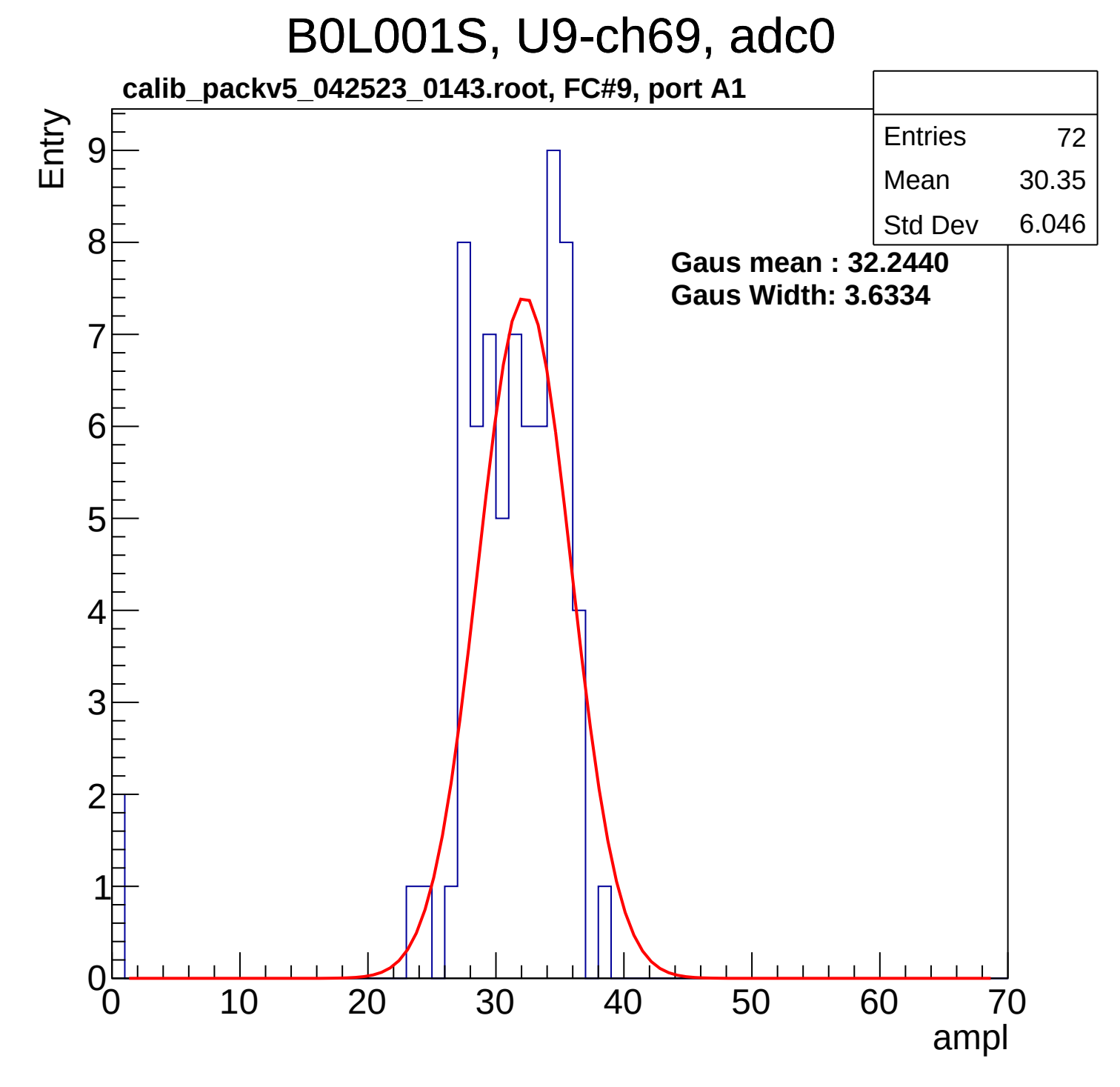
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch69, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.35
Std Dev 6.046
Gaus mean : 32.2440
Gaus Width: 3.6334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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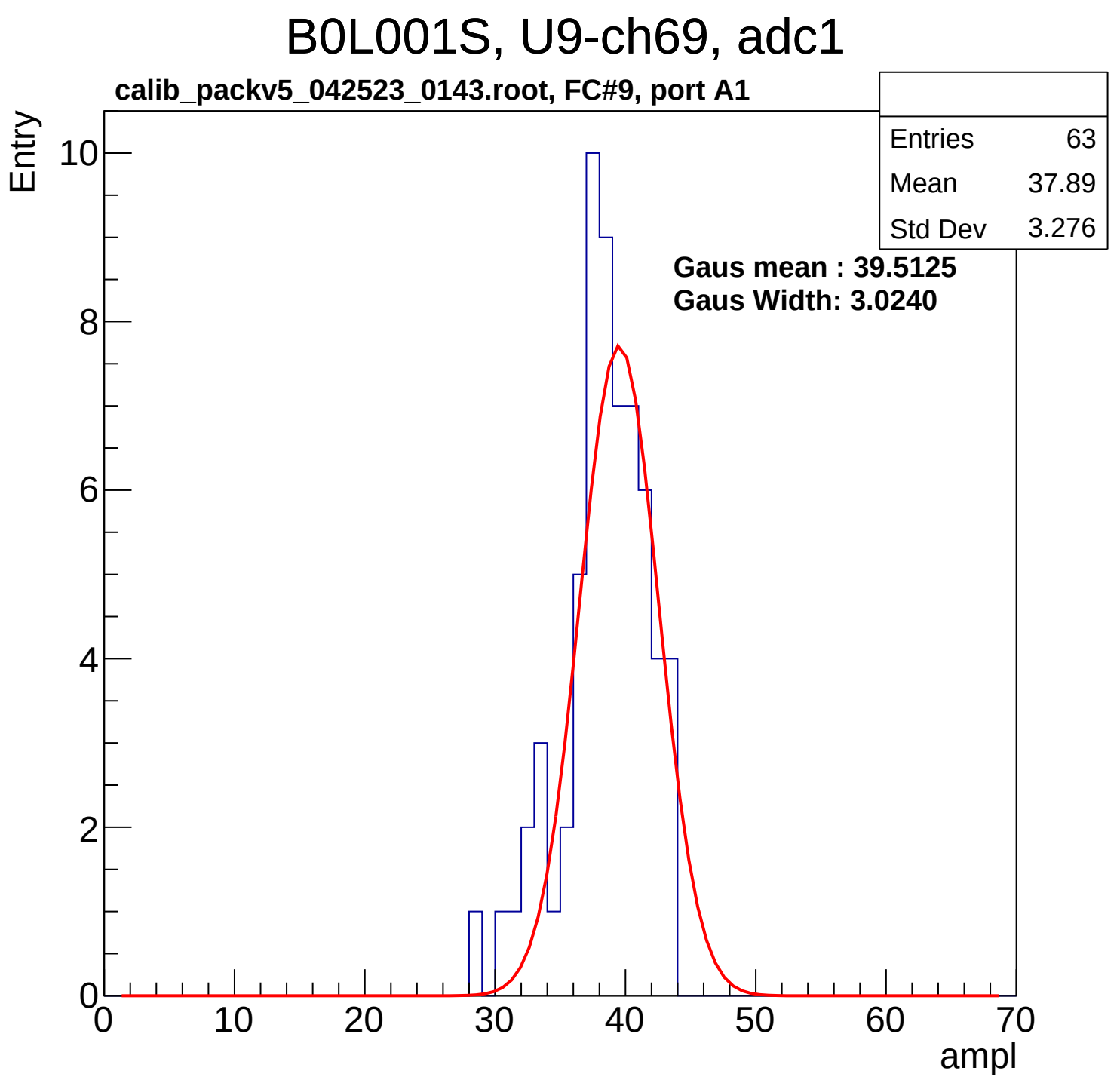
B0L001S, U9-ch69, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.89
Std Dev 3.276
Gaus mean : 39.5125
Gaus Width: 3.0240
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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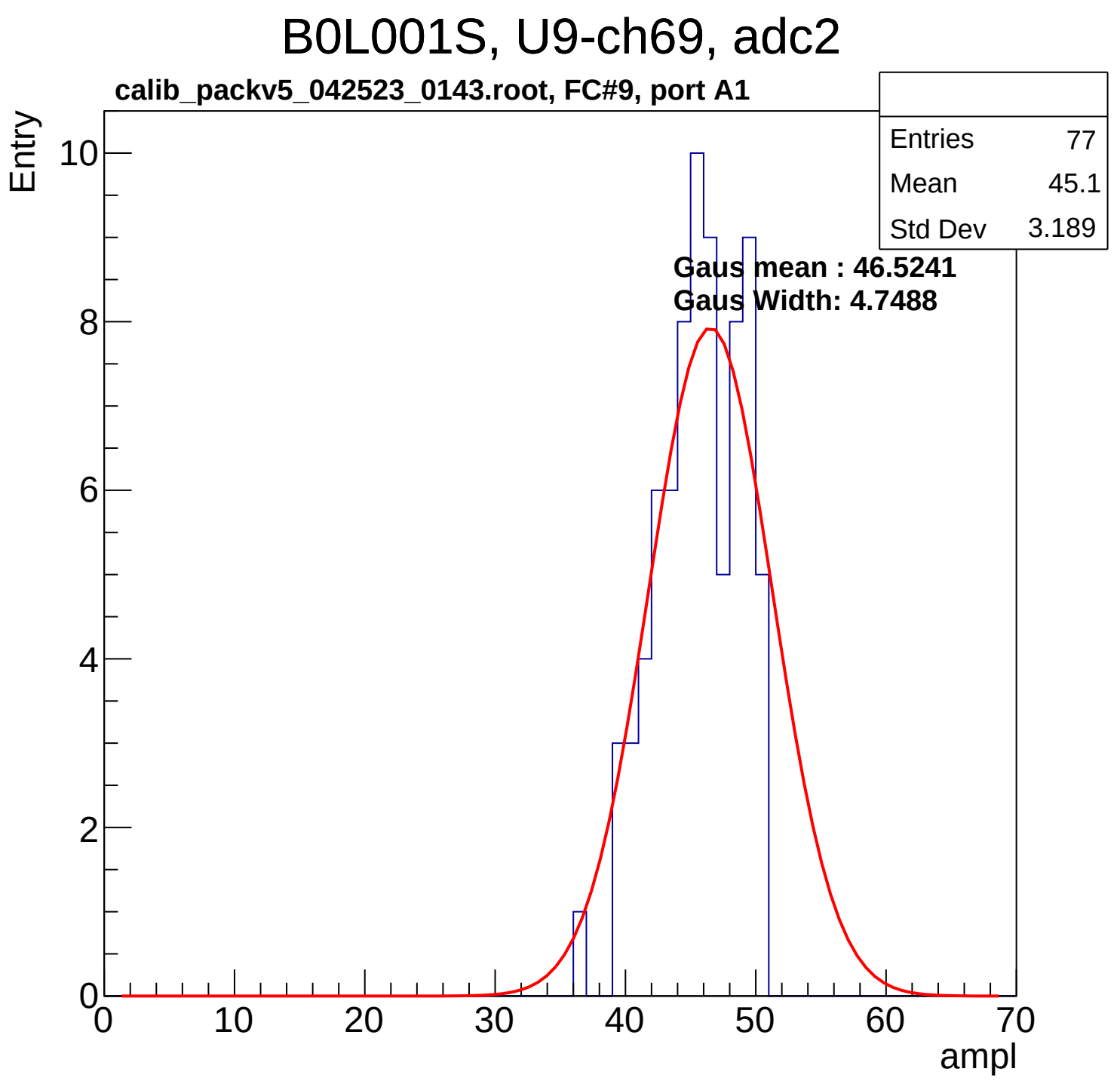
B0L001S, U9-ch69, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 45.1
Std Dev 3.189
Gaus mean : 46.5241
Gaus Width: 4.7488
Entry
ampl
0
2
4
6
8
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0
10
20
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40
50
60
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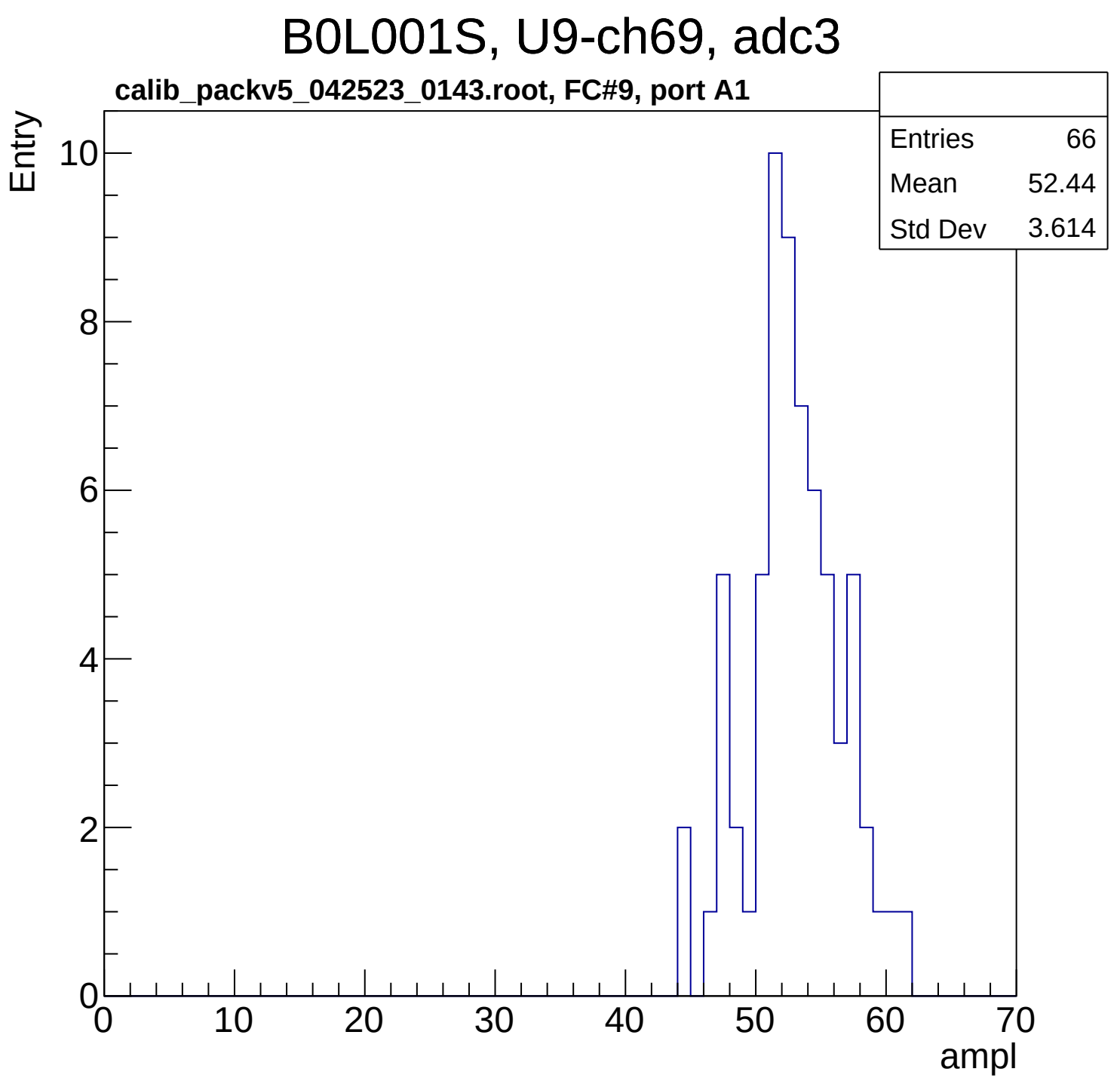
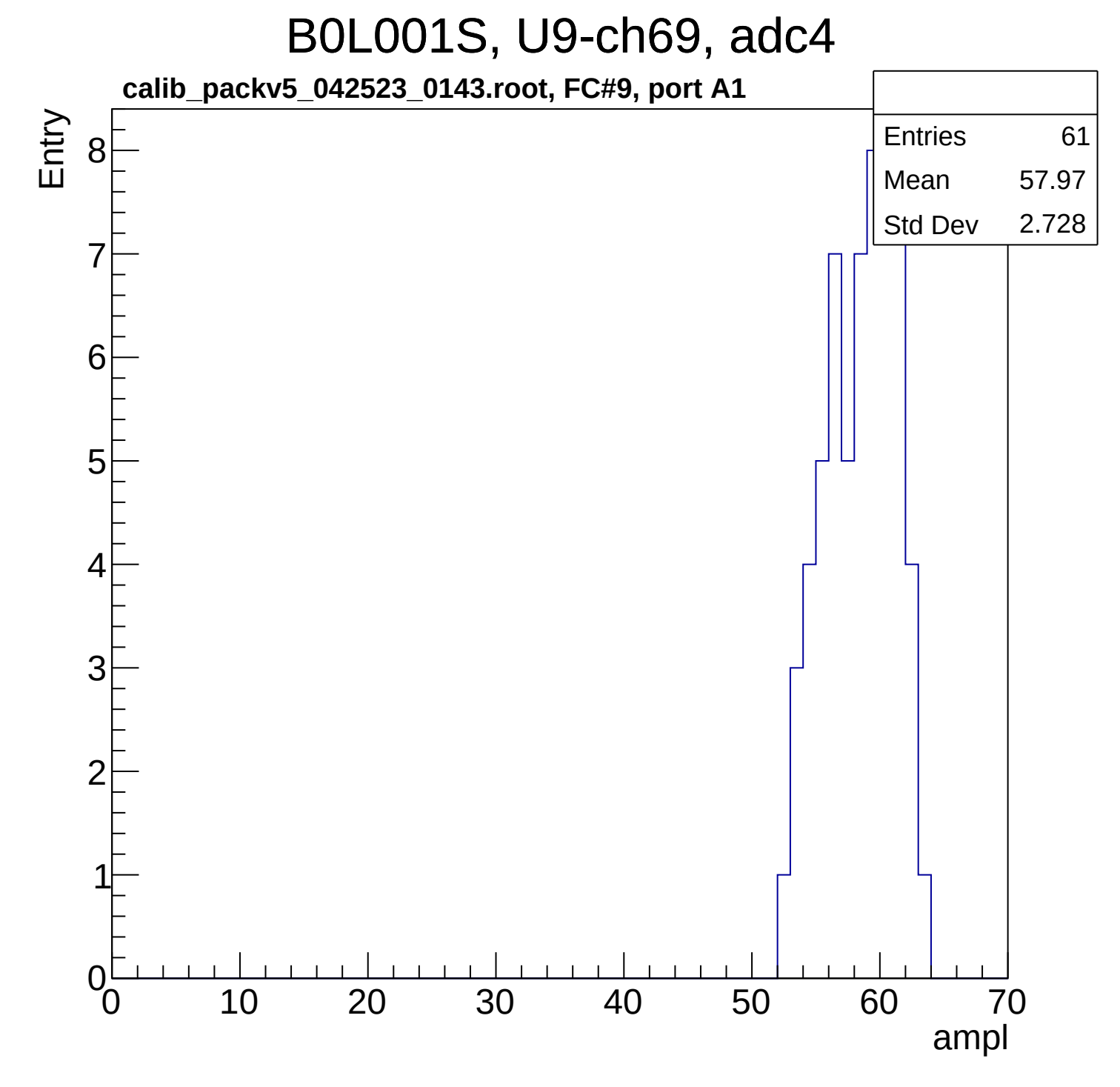

B0L001S, U9-ch69, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 52.44
Std Dev 3.614
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch69, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.97
Std Dev 2.728
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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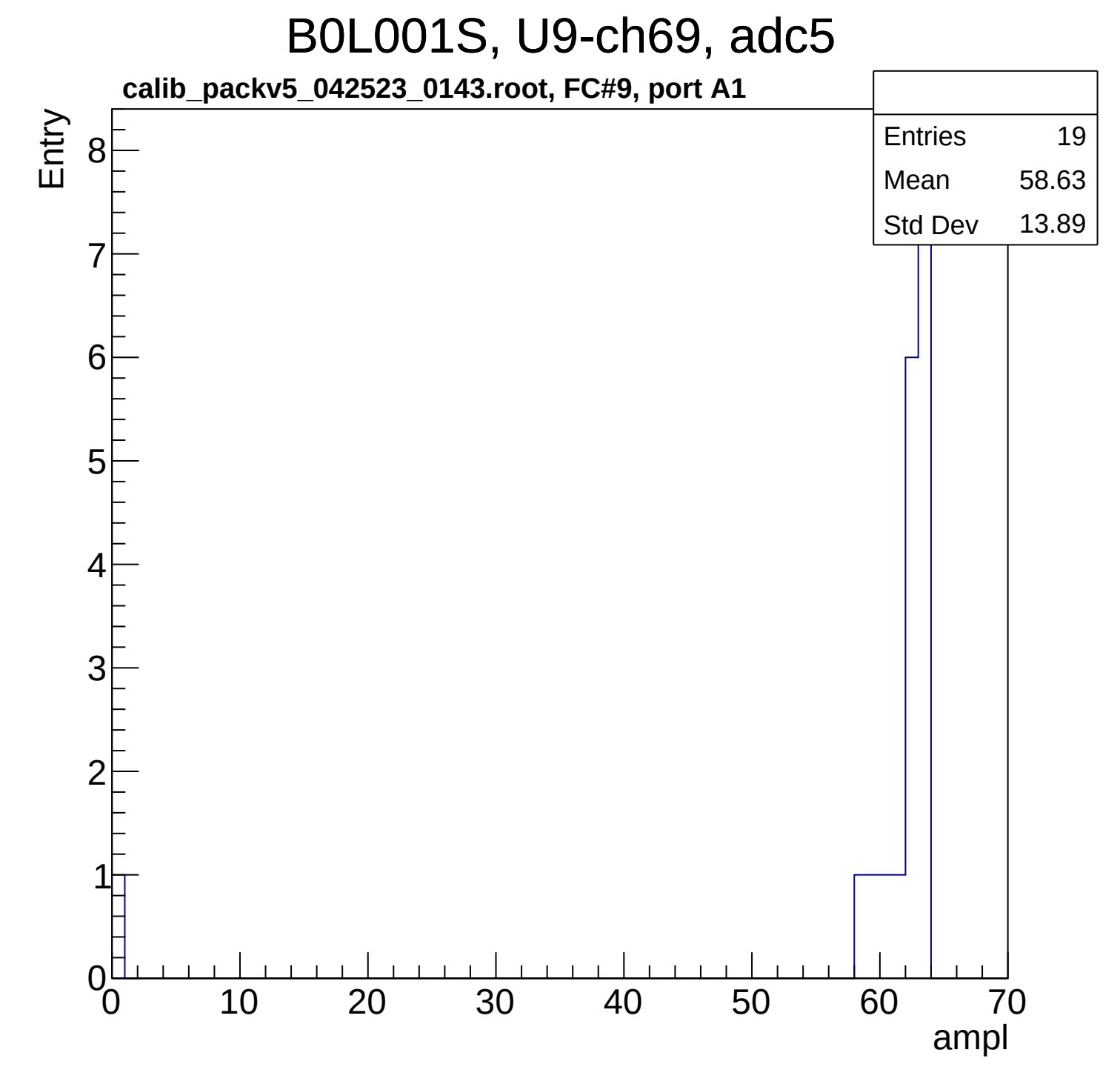
B0L001S, U9-ch69, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 58.63
Std Dev 13.89
Entry
0
1
2
3
4
5
6
7
8
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10
20
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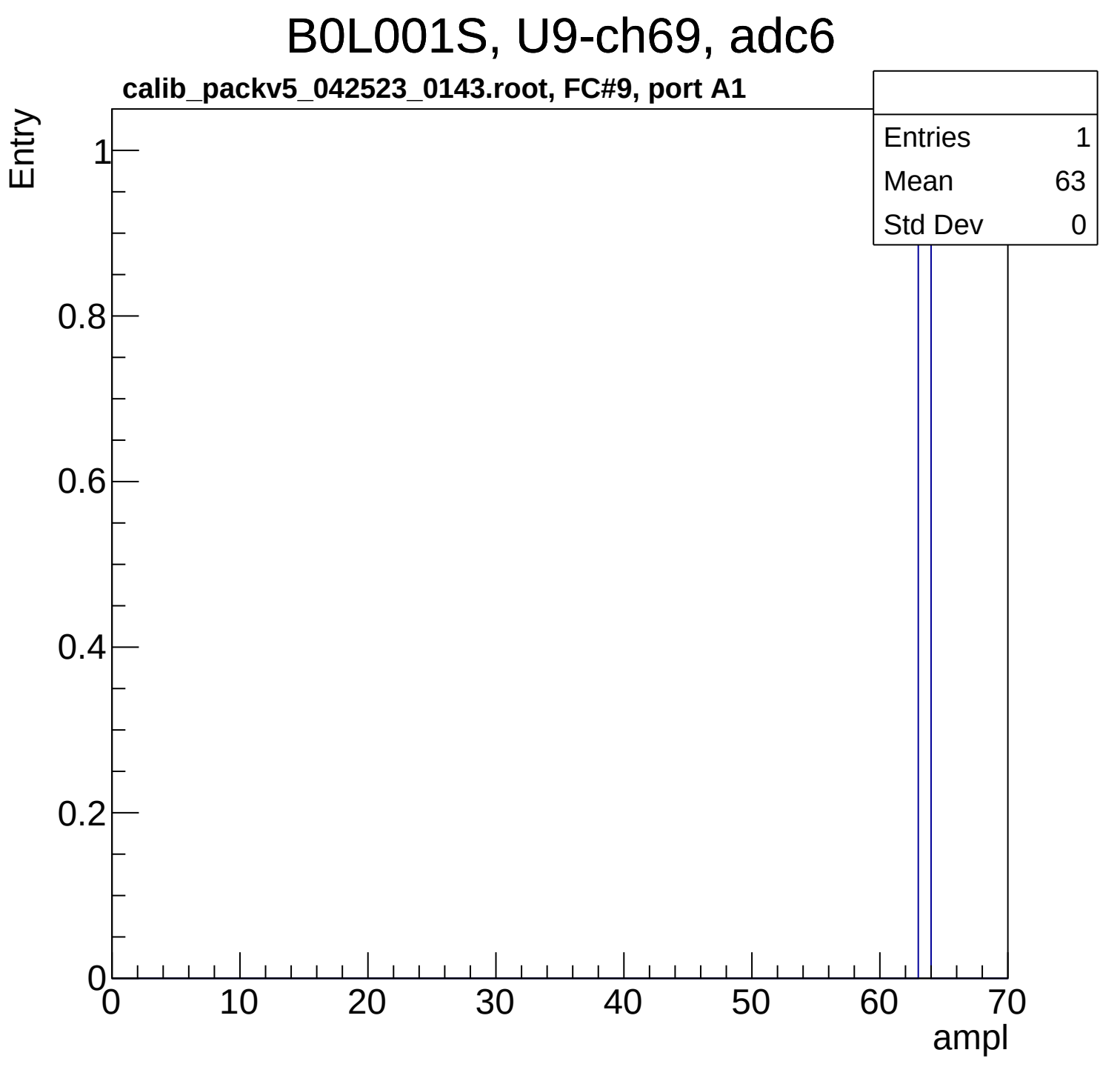
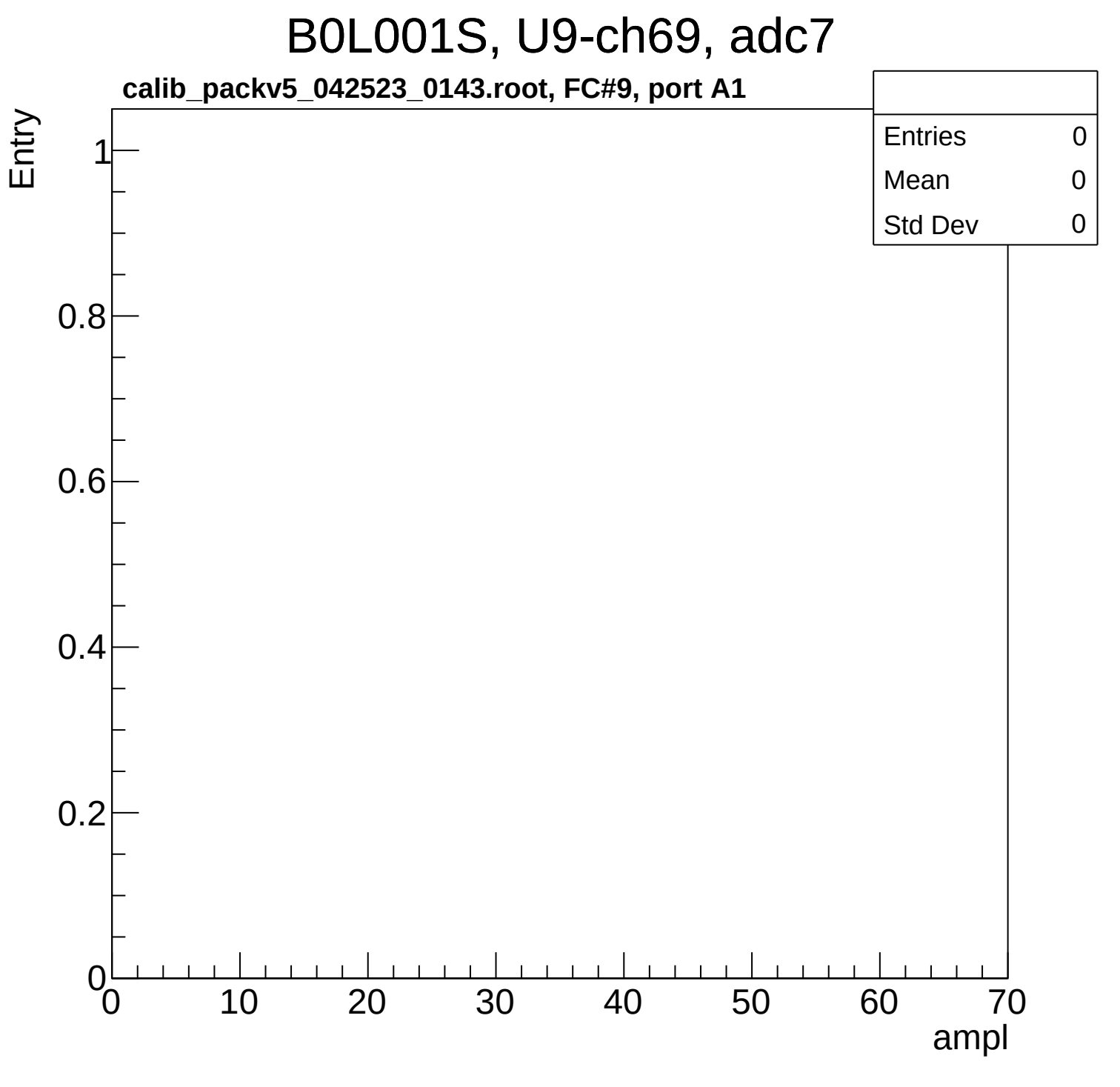
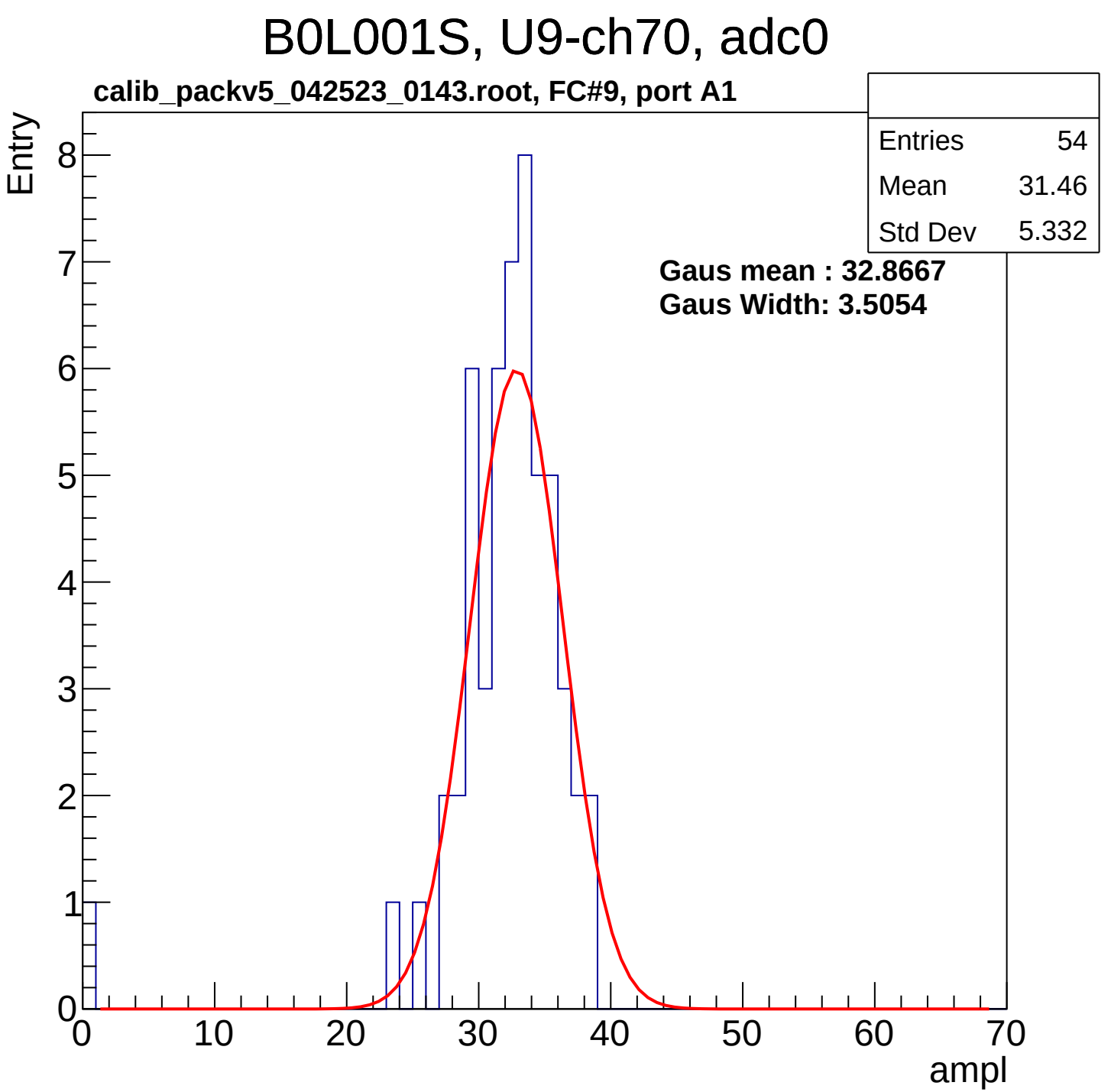
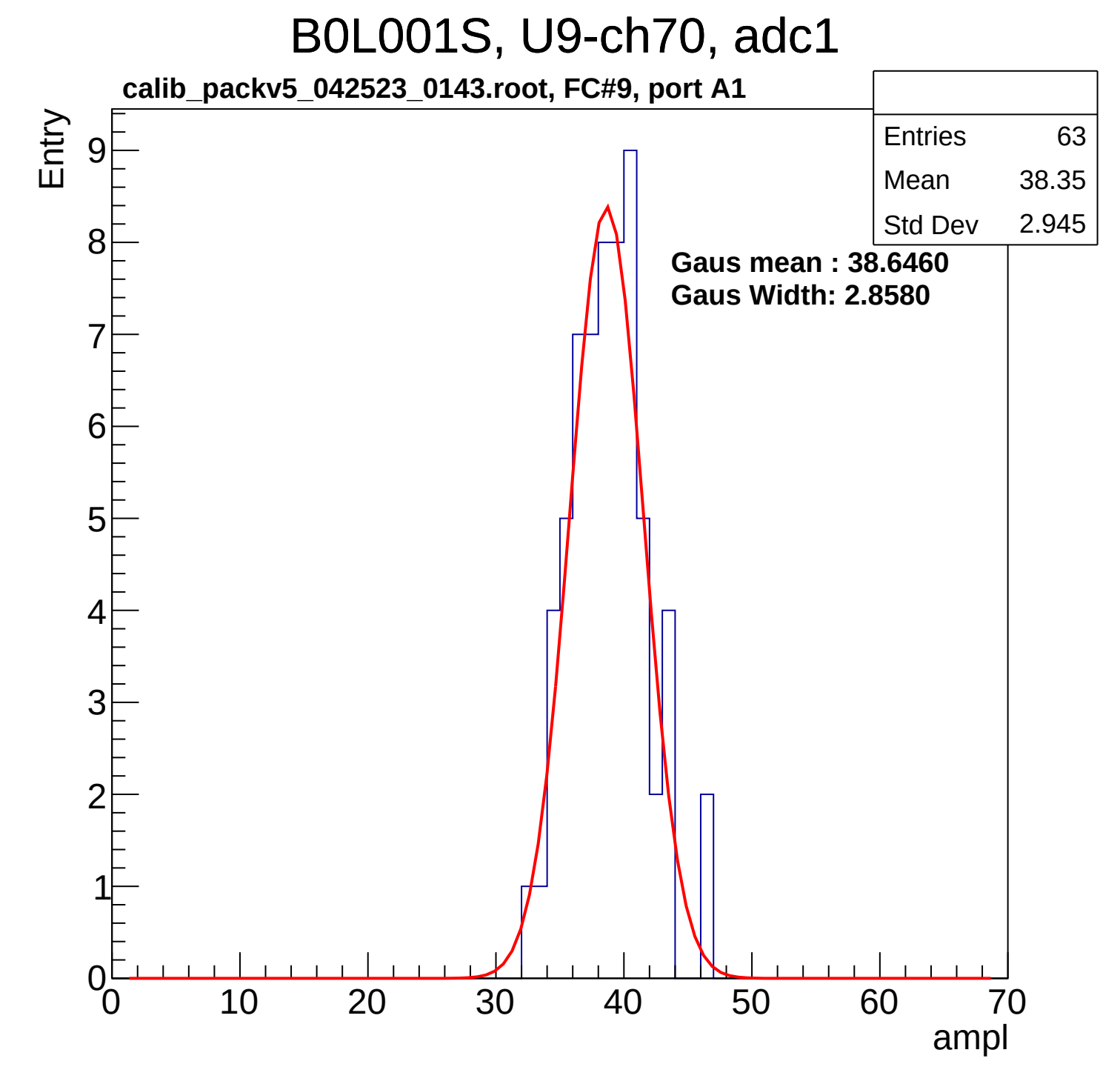
50
60
70
ampl


B0L001S, U9-ch69, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch69, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch70, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 31.46
Std Dev 5.332
Gaus mean : 32.8667
Gaus Width: 3.5054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch70, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.35
Std Dev 2.945
Gaus mean : 38.6460
Gaus Width: 2.8580
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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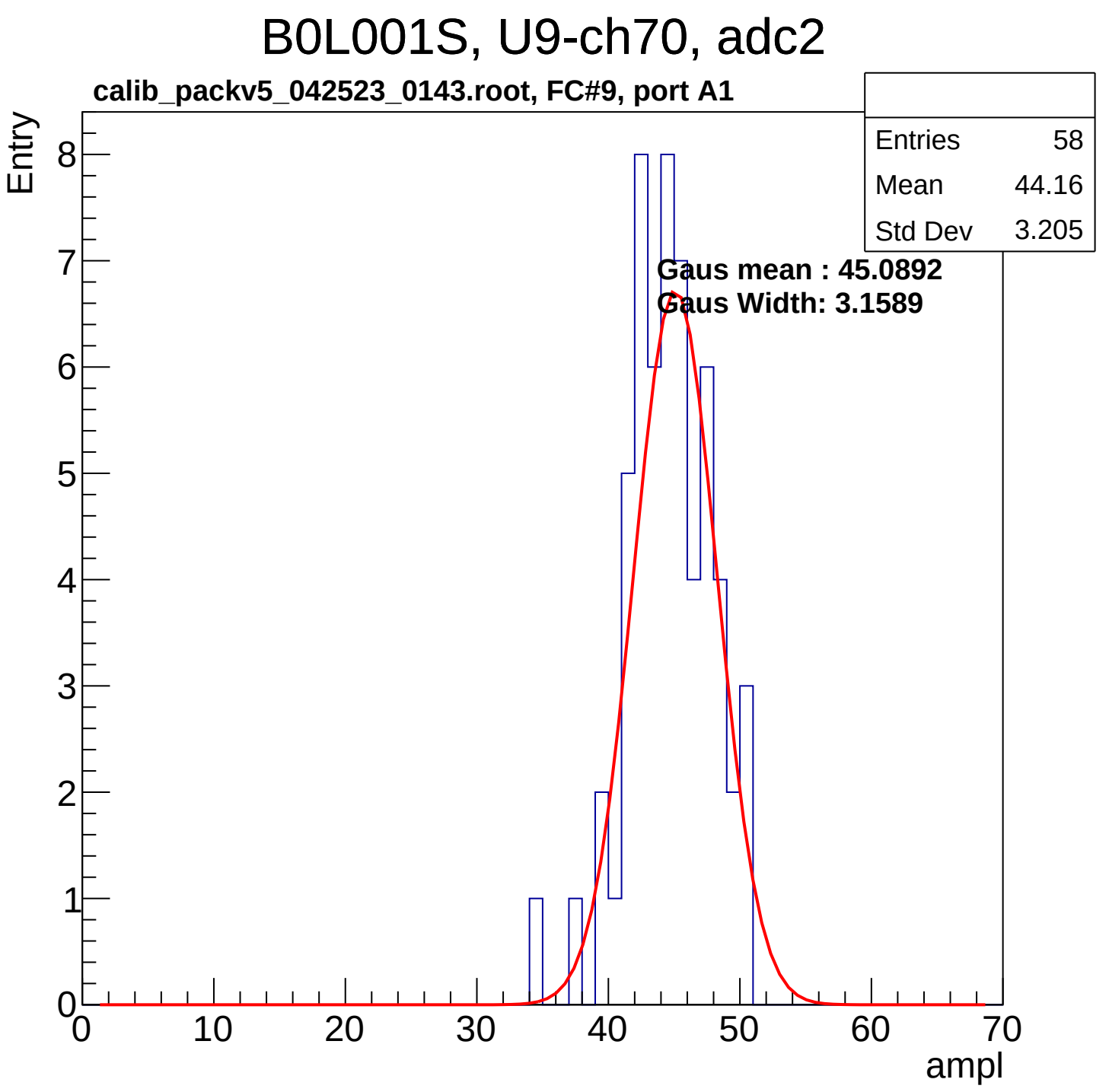
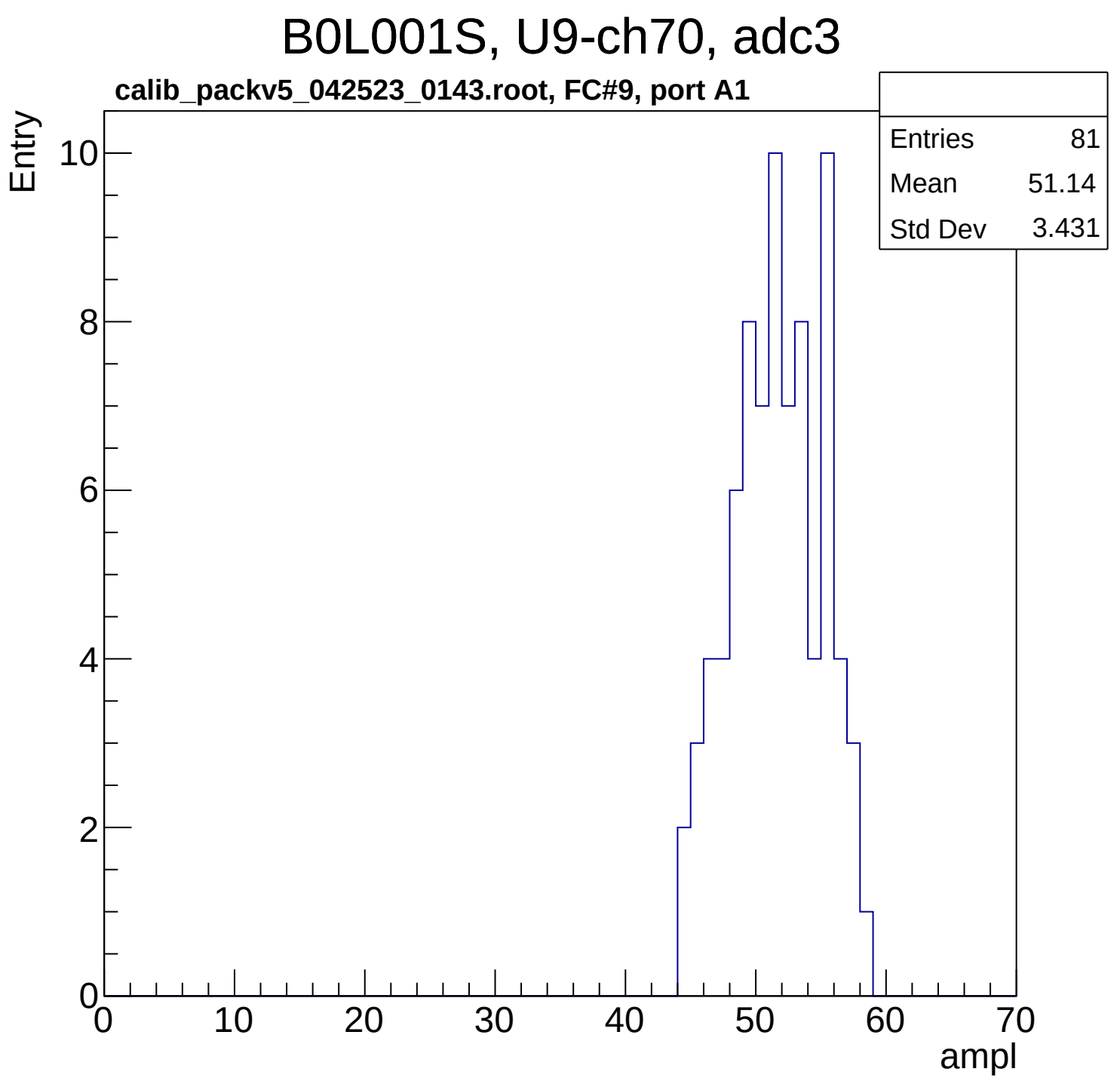
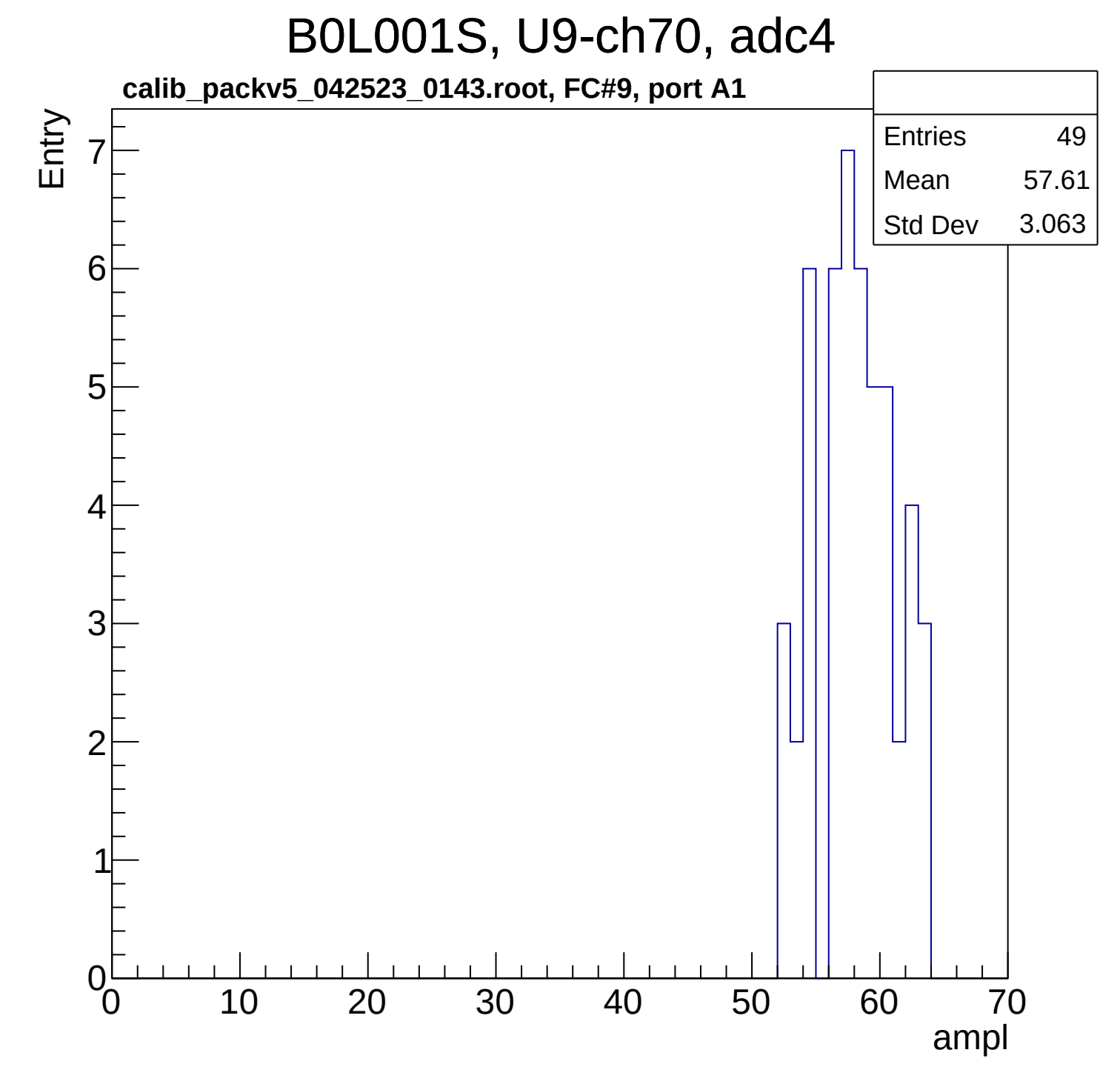
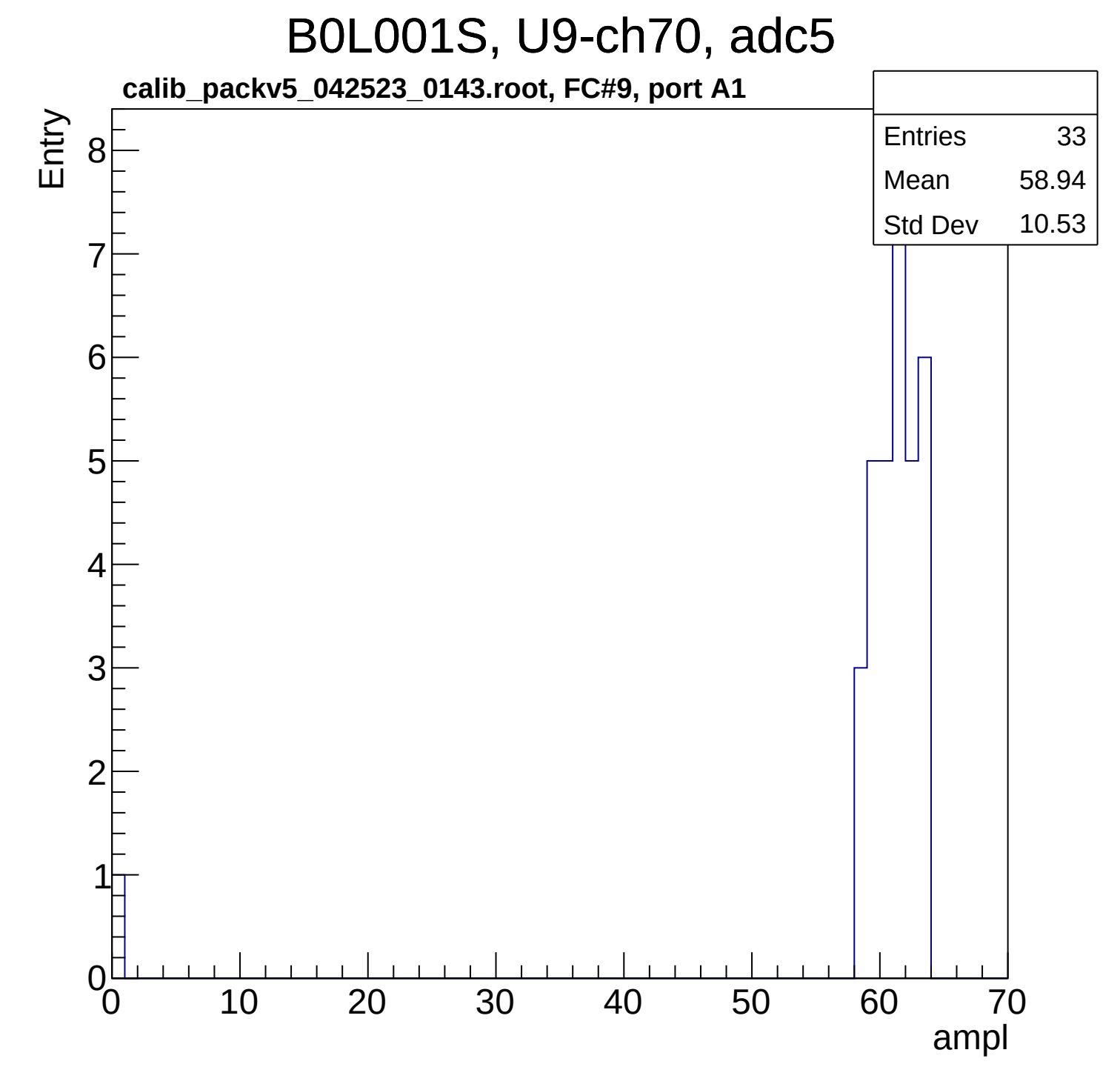
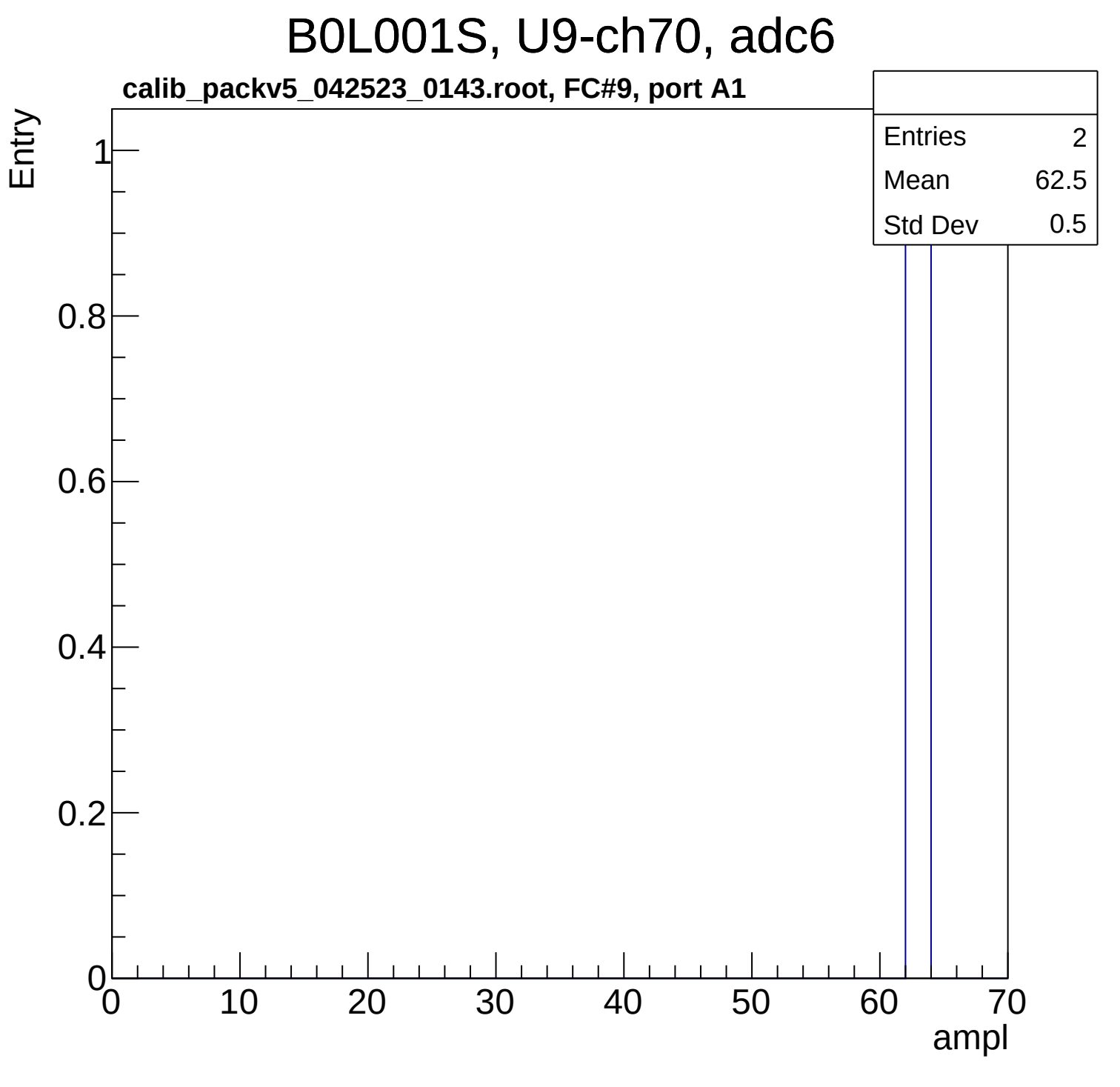
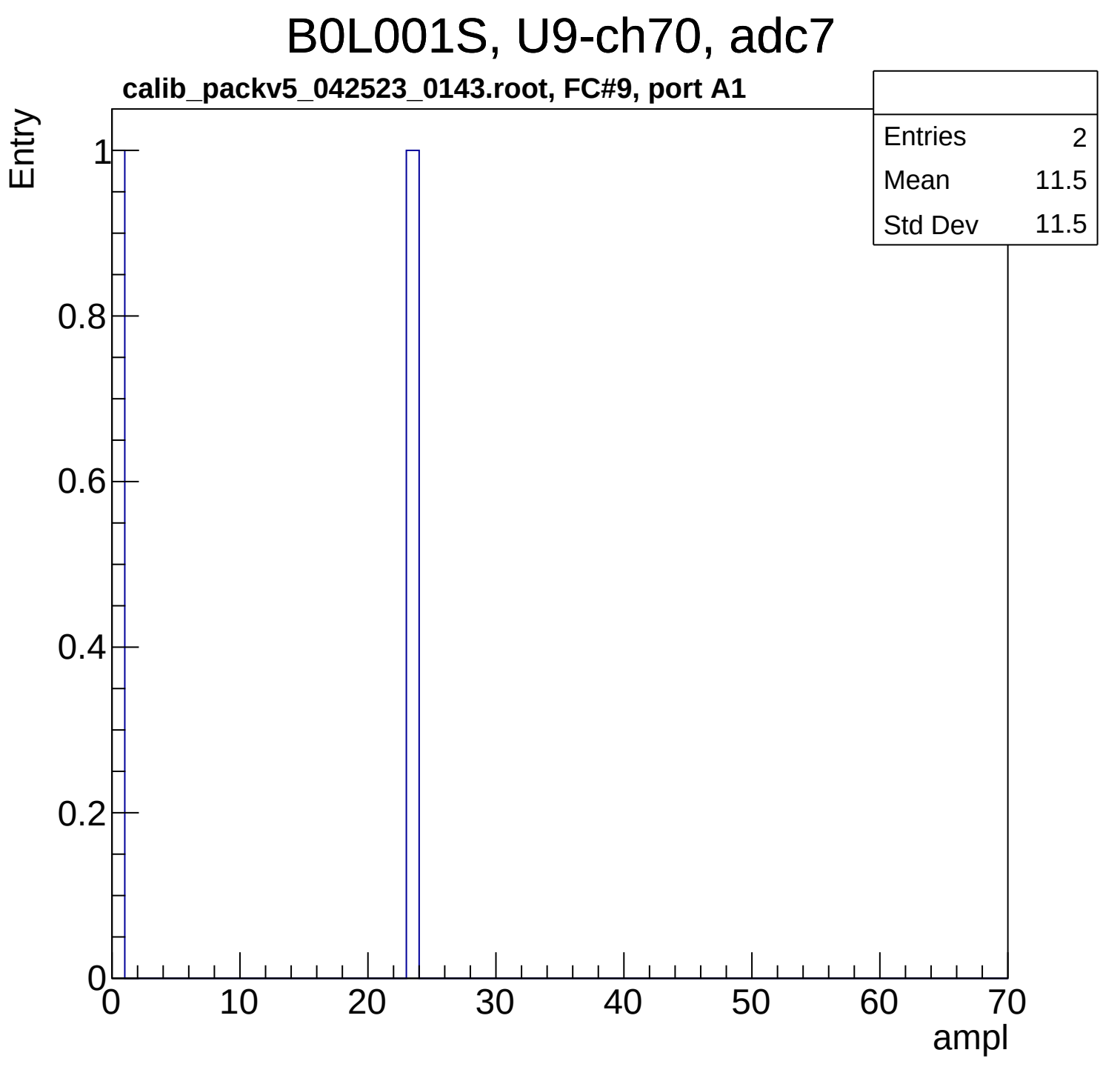

B0L001S, U9-ch70, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.16
Std Dev 3.205
Gaus mean : 45.0892
Gaus Width: 3.1589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

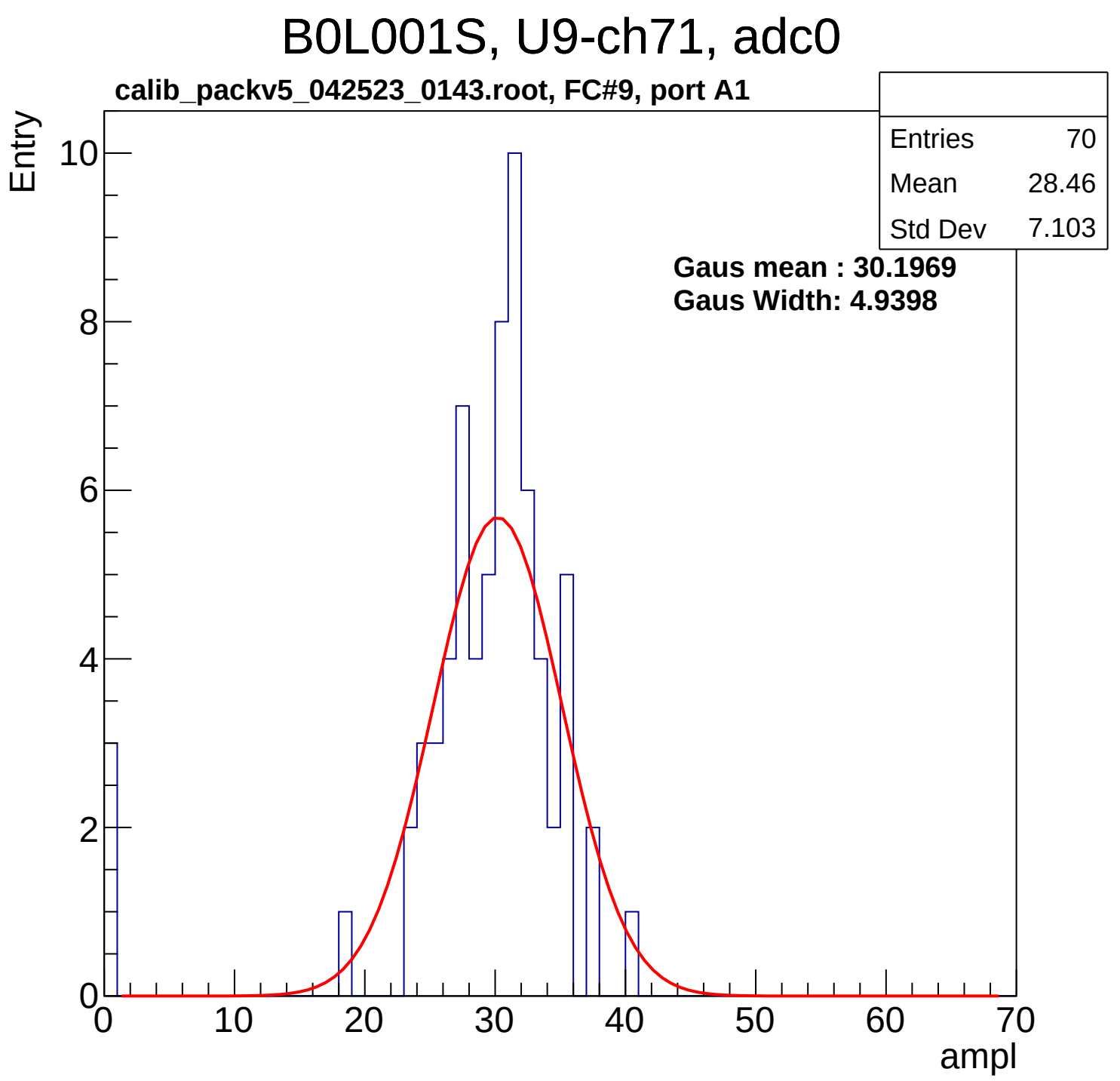

B0L001S, U9-ch70, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 51.14
Std Dev 3.431
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

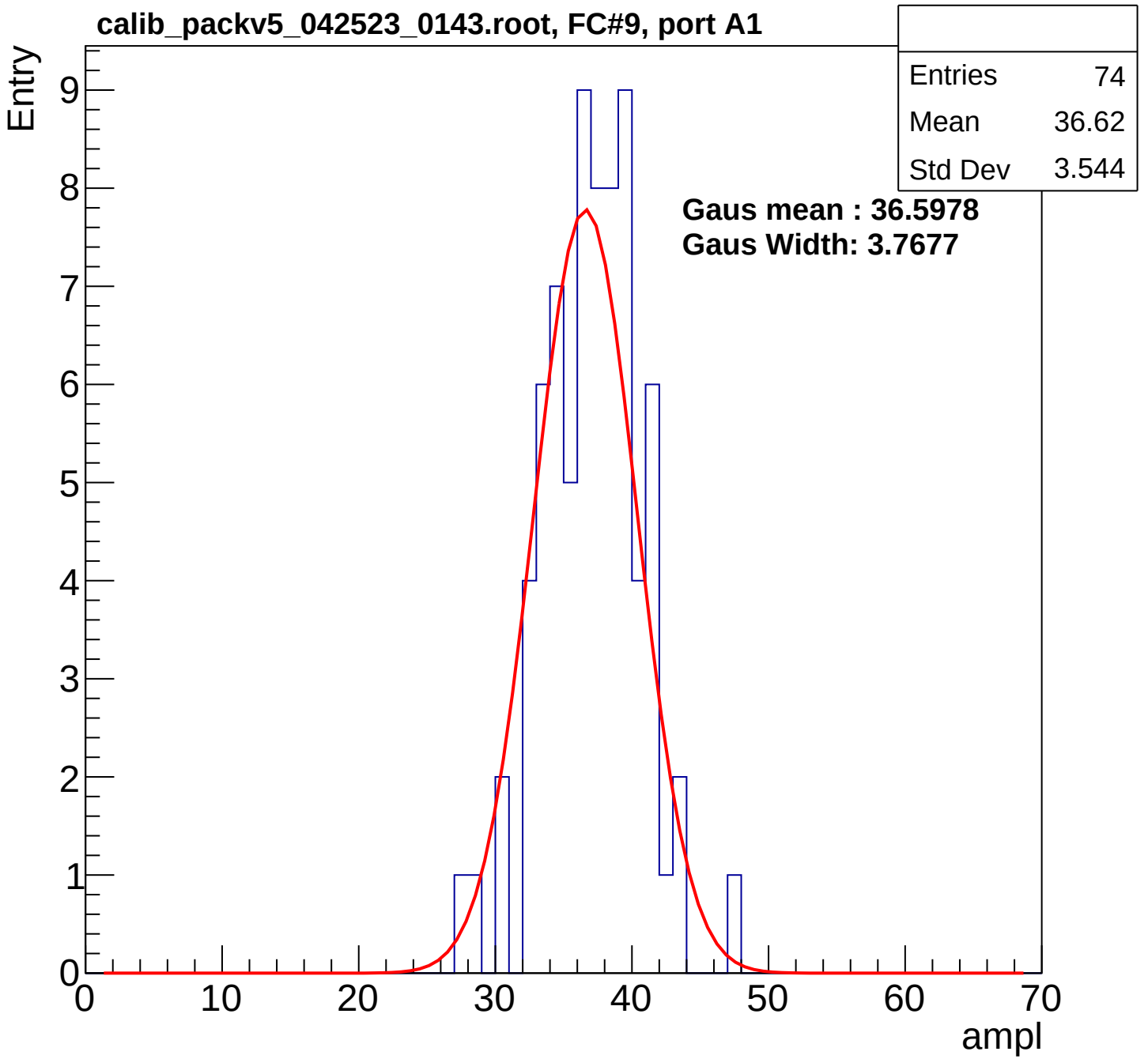

B0L001S, U9-ch70, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.61
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

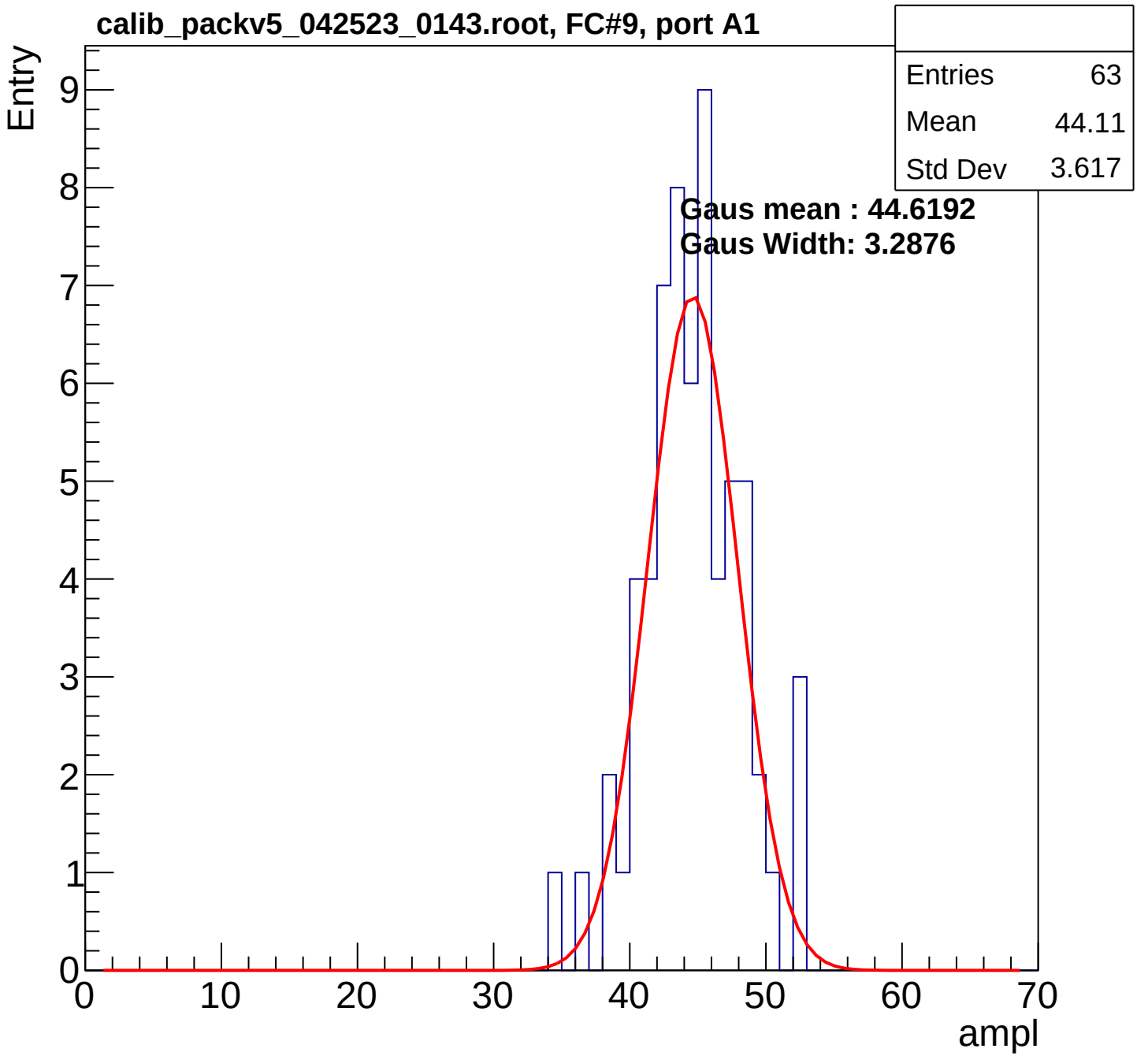

B0L001S, U9-ch70, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.94
Std Dev 10.53
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

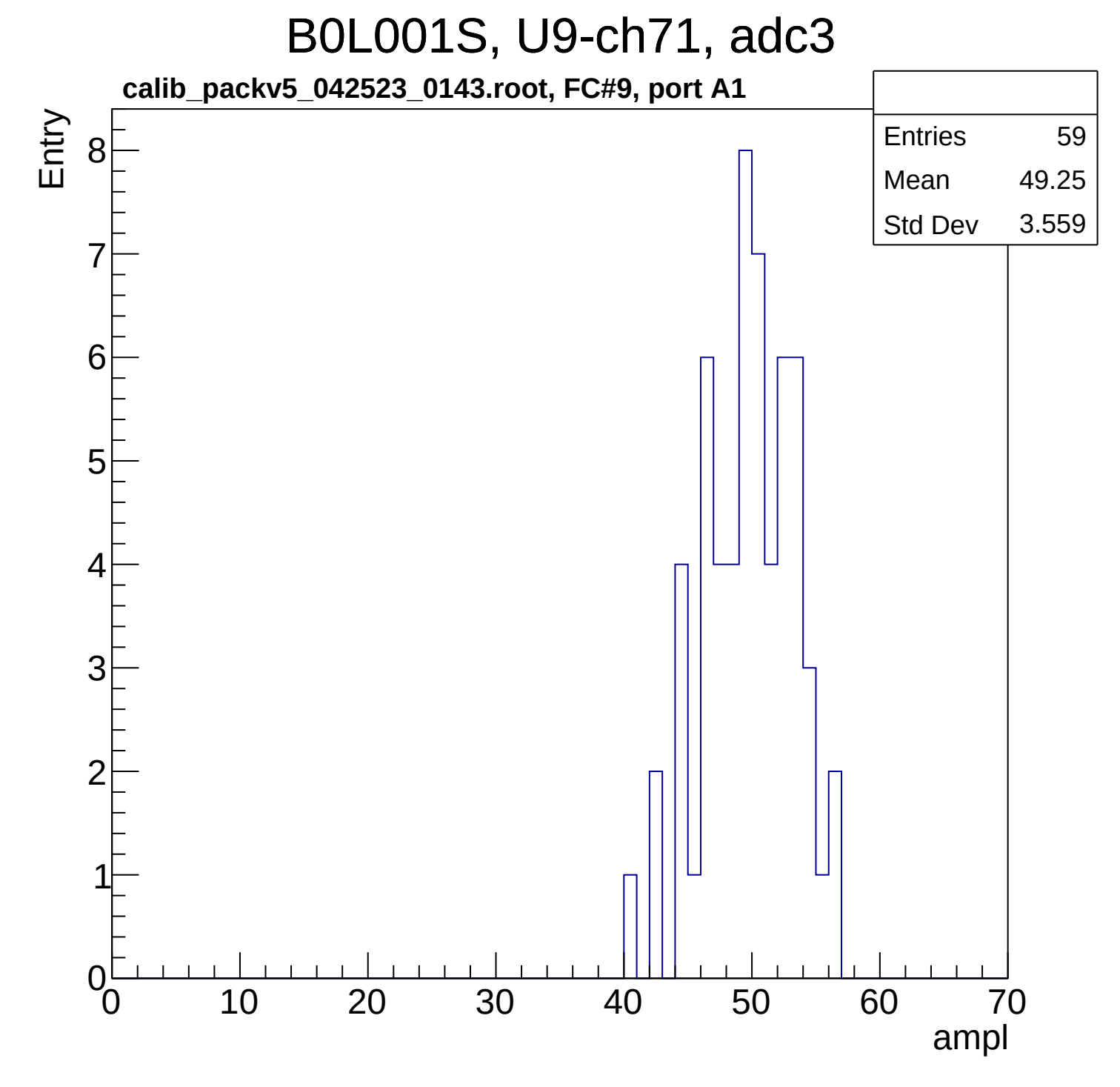

B0L001S, U9-ch70, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

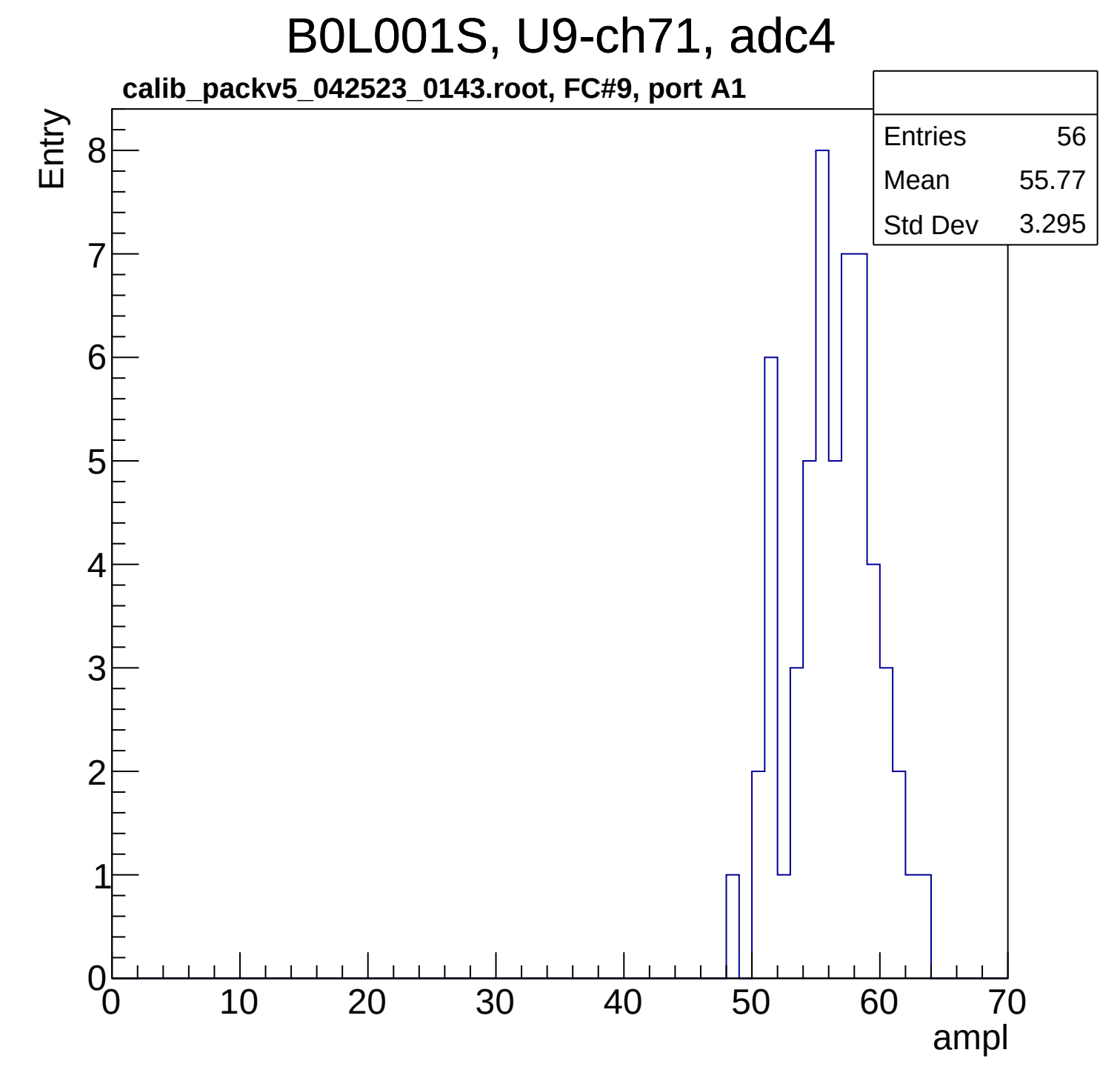

B0L001S, U9-ch70, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch71, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 28.46
Std Dev 7.103
Gaus mean : 30.1969
Gaus Width: 4.9398
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch71, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.62
Std Dev 3.544
Gaus mean : 36.5978
Gaus Width: 3.7677
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch71, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.11
Std Dev 3.617
Gaus mean : 44.6192
Gaus Width: 3.2876
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch71, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 49.25
Std Dev 3.559
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch71, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.77
Std Dev 3.295
Entry
ampl
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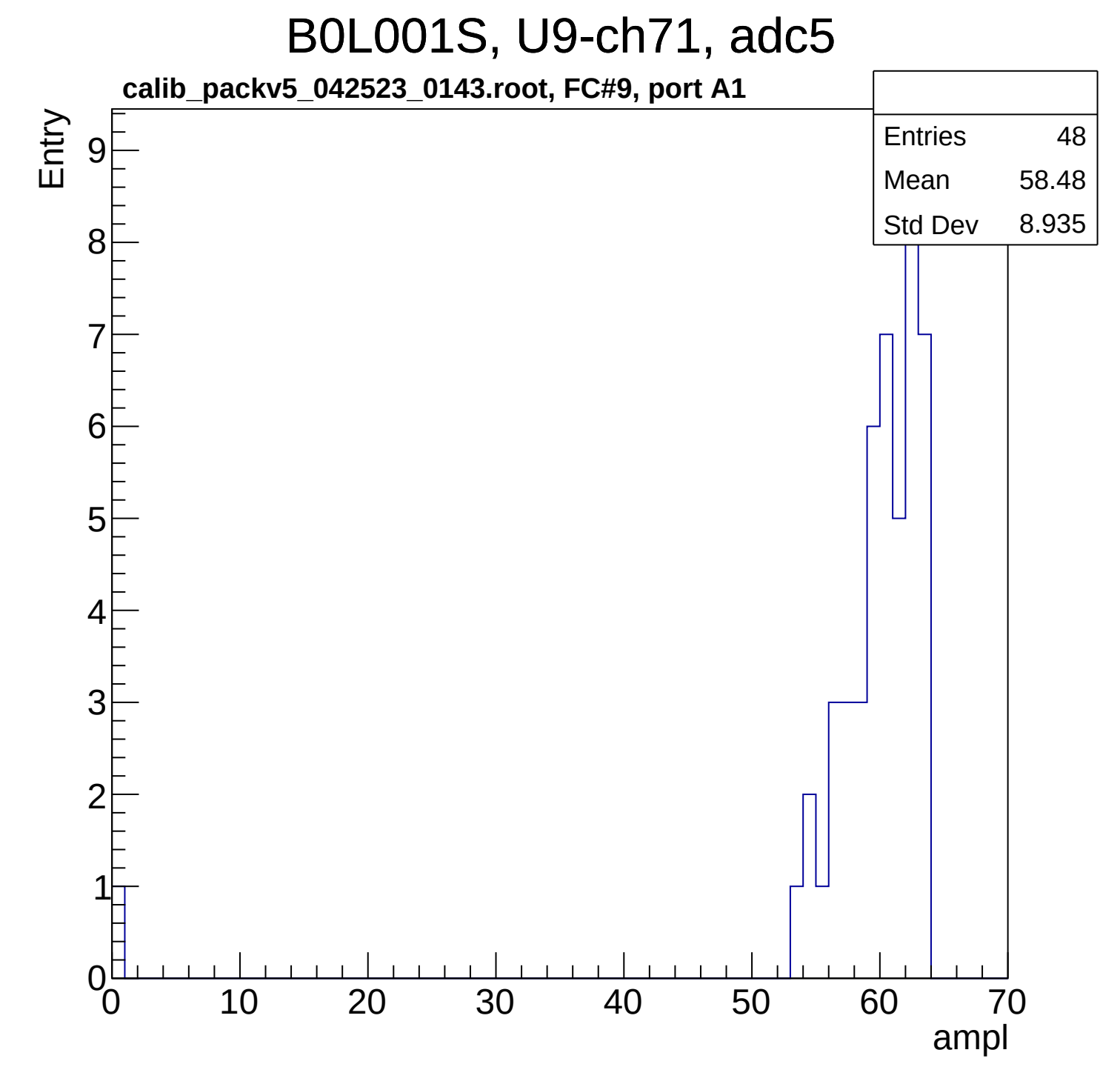
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch71, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.48
Std Dev 8.935
Entry
ampl
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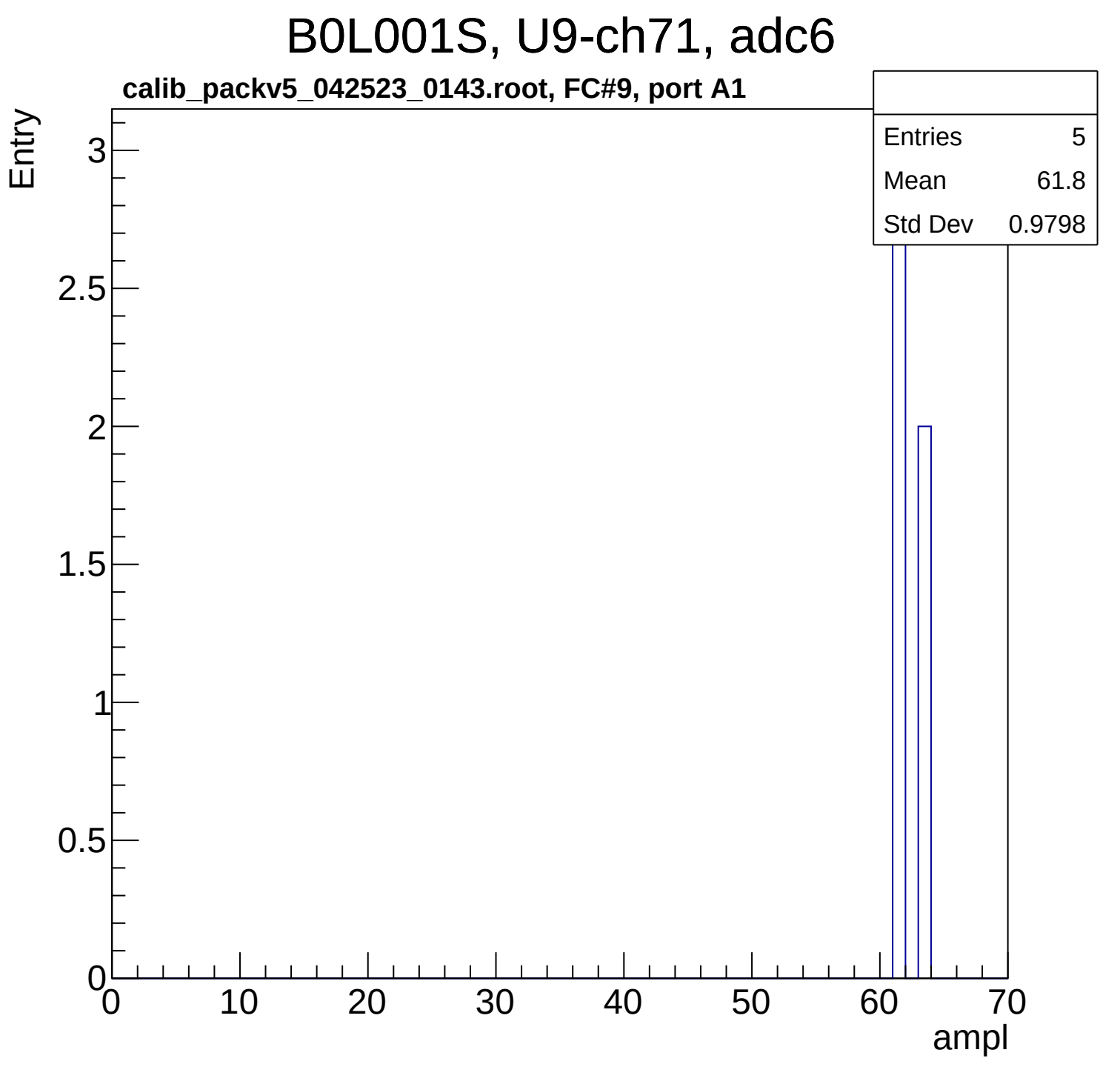
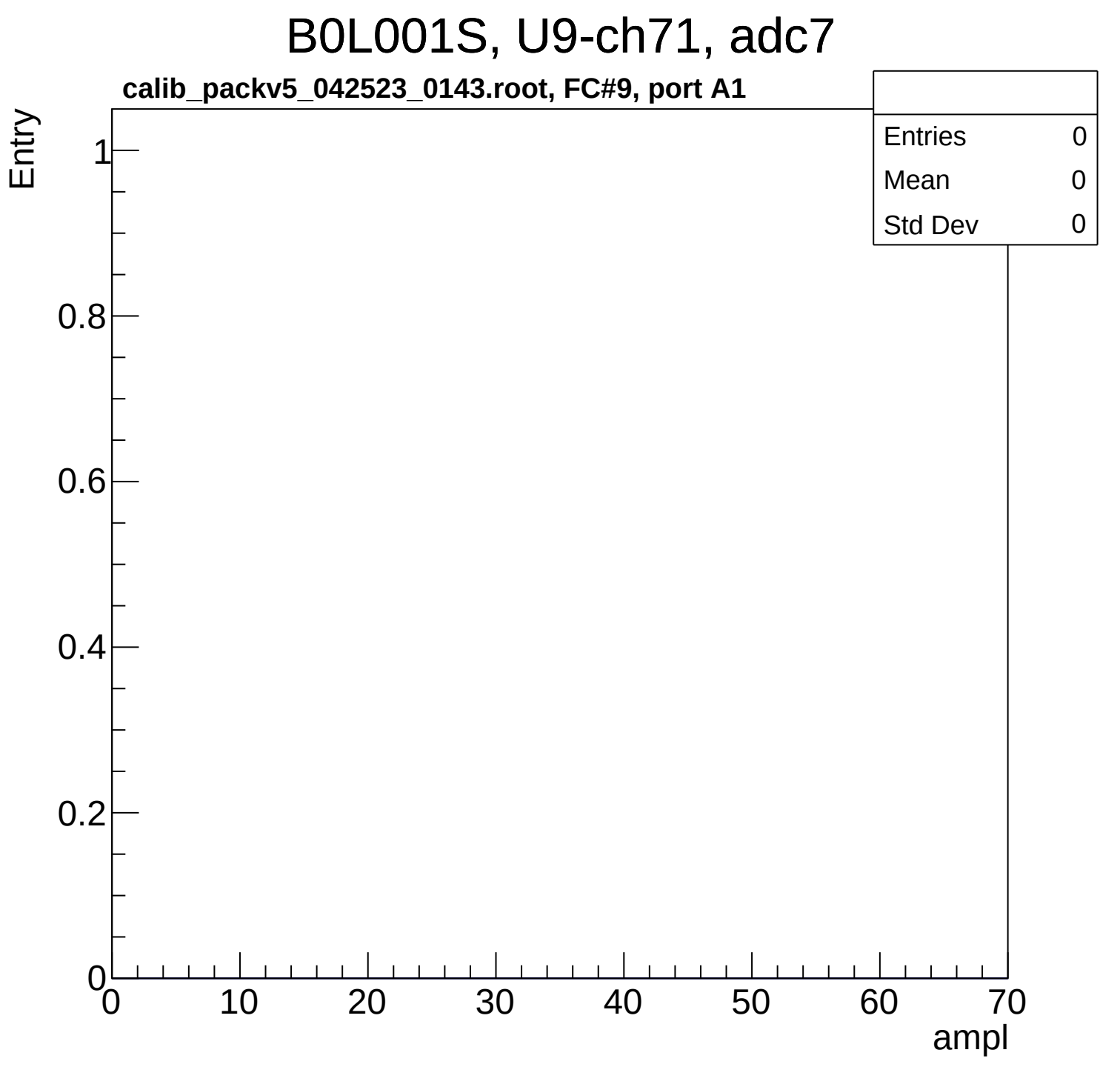
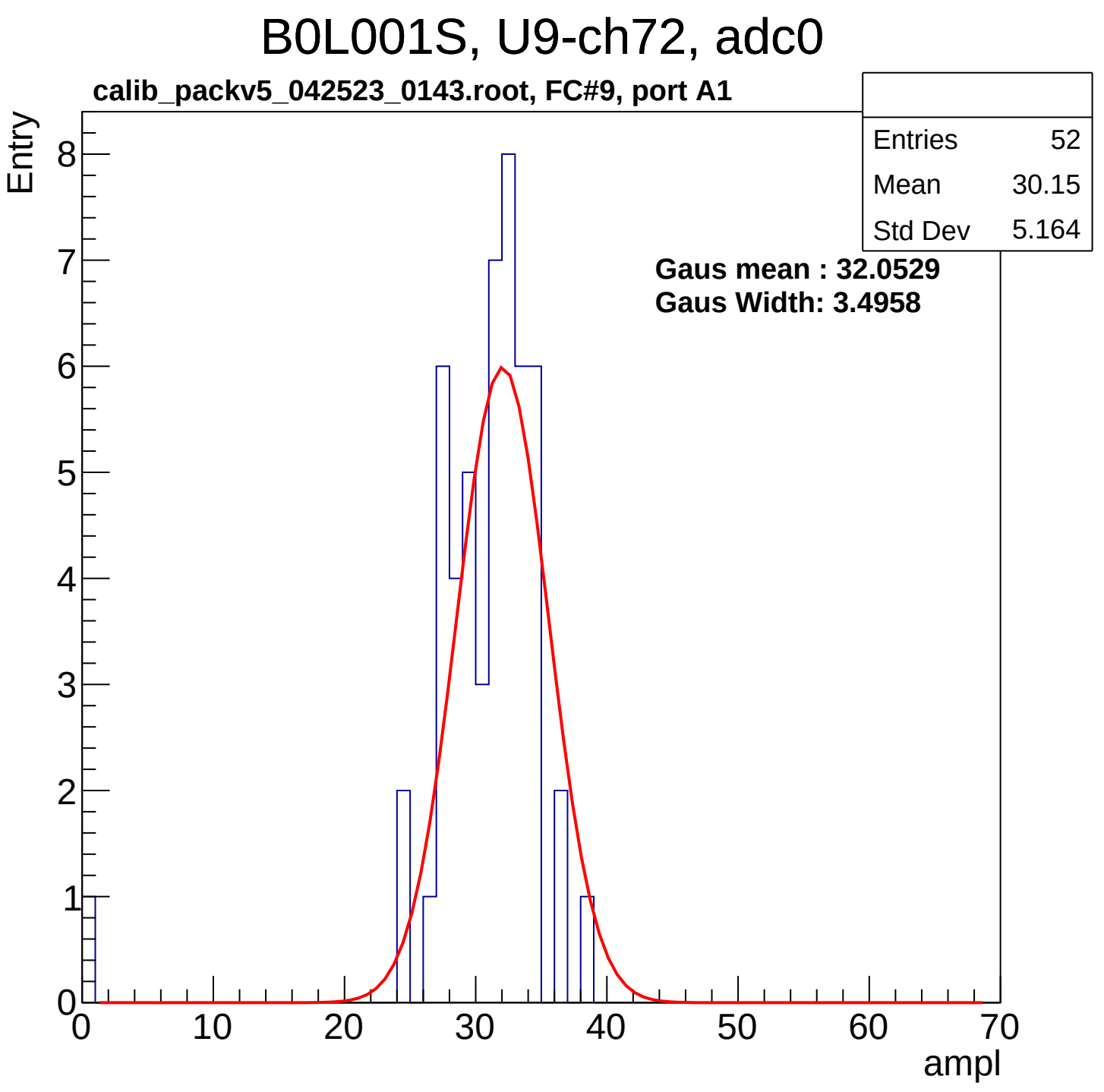
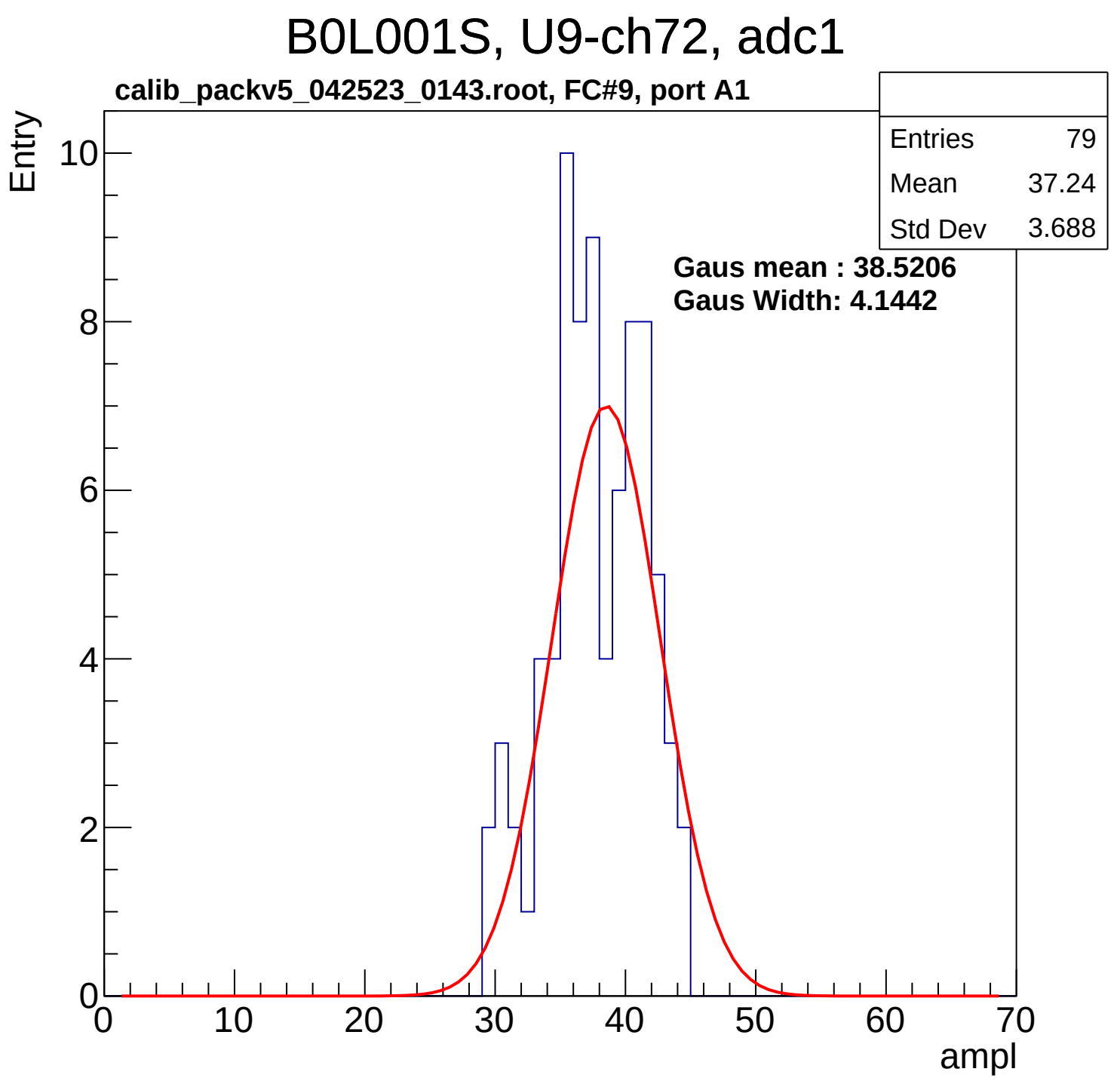
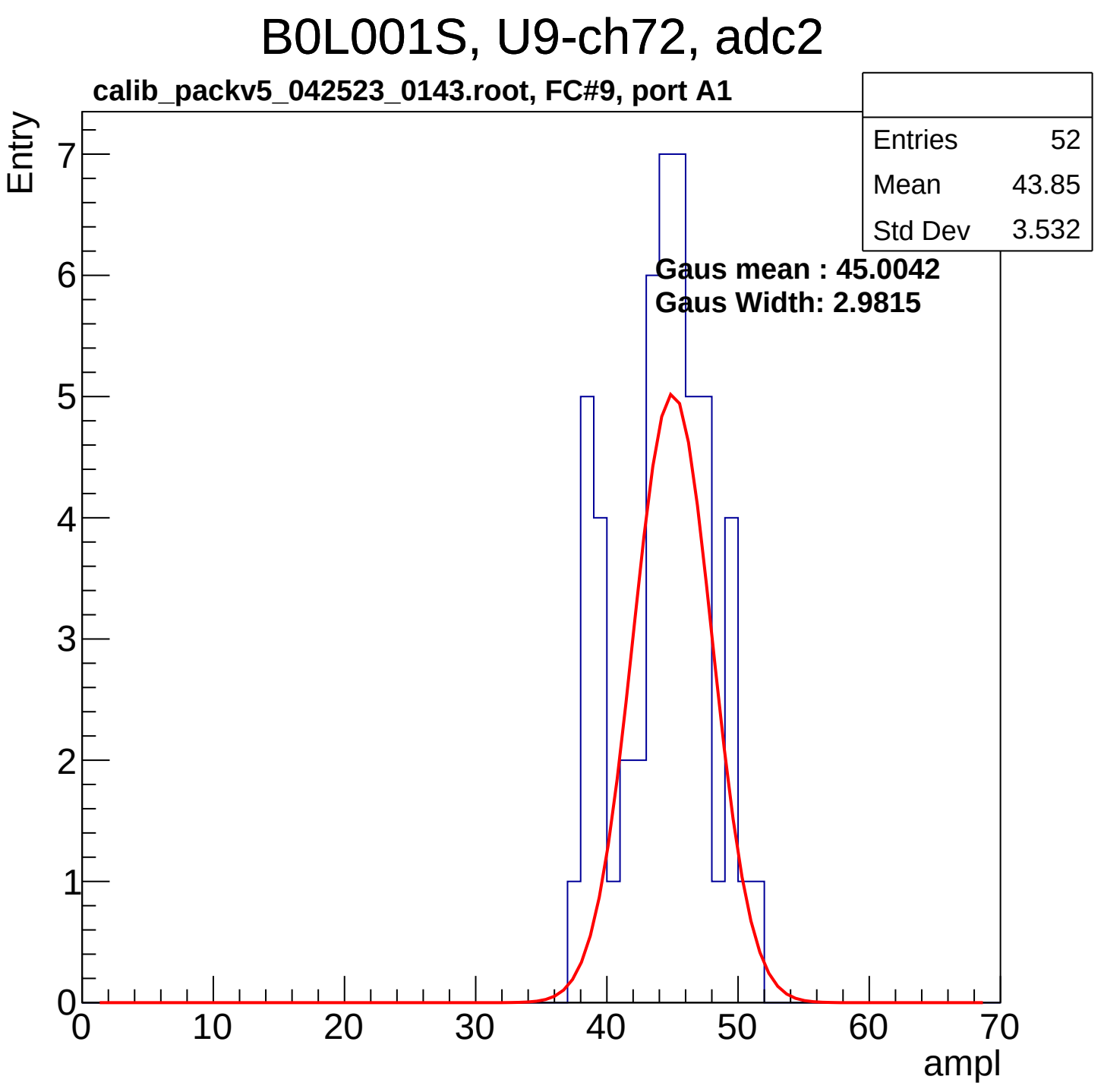
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

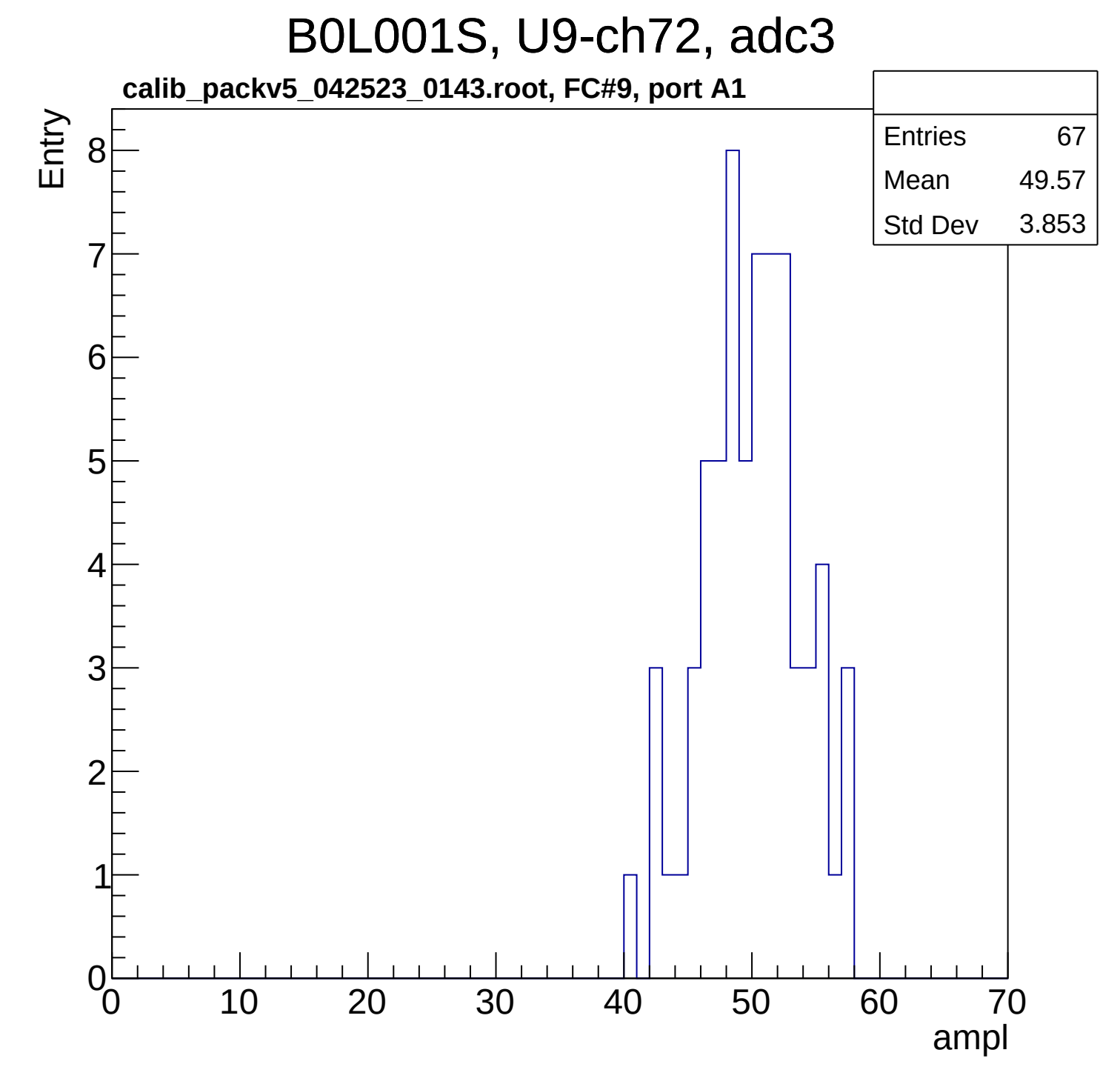

B0L001S, U9-ch71, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.8
Std Dev 0.9798
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch71, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch72, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 30.15
Std Dev 5.164
Gaus mean : 32.0529
Gaus Width: 3.4958
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch72, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 37.24
Std Dev 3.688
Gaus mean : 38.5206
Gaus Width: 4.1442
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch72, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 43.85
Std Dev 3.532
Gaus mean : 45.0042
Gaus Width: 2.9815
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch72, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 49.57
Std Dev 3.853
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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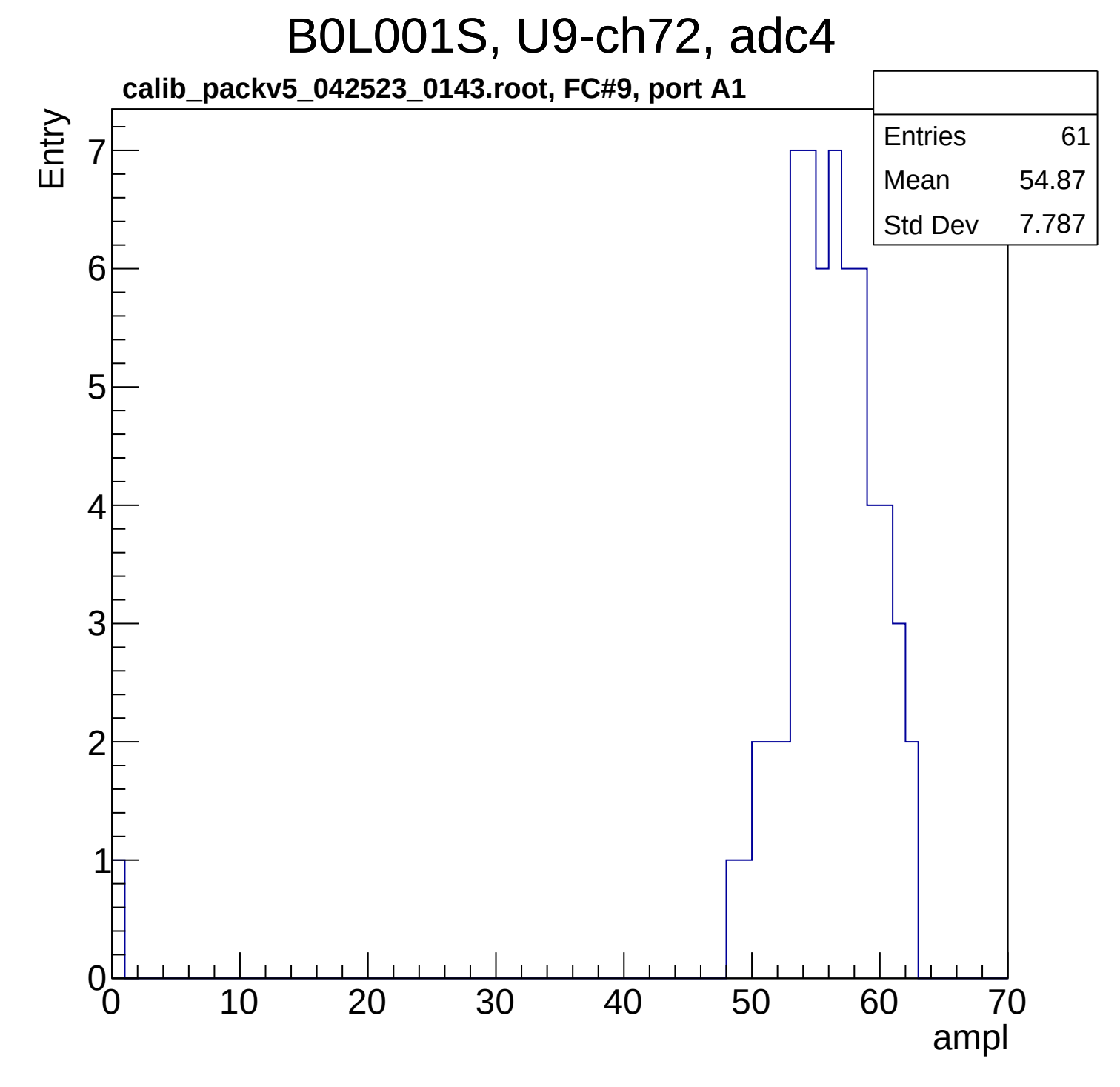
40
50
60
70


B0L001S, U9-ch72, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 54.87
Std Dev 7.787
Entry
ampl
0
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2
3
4
5
6
7
0
10
20
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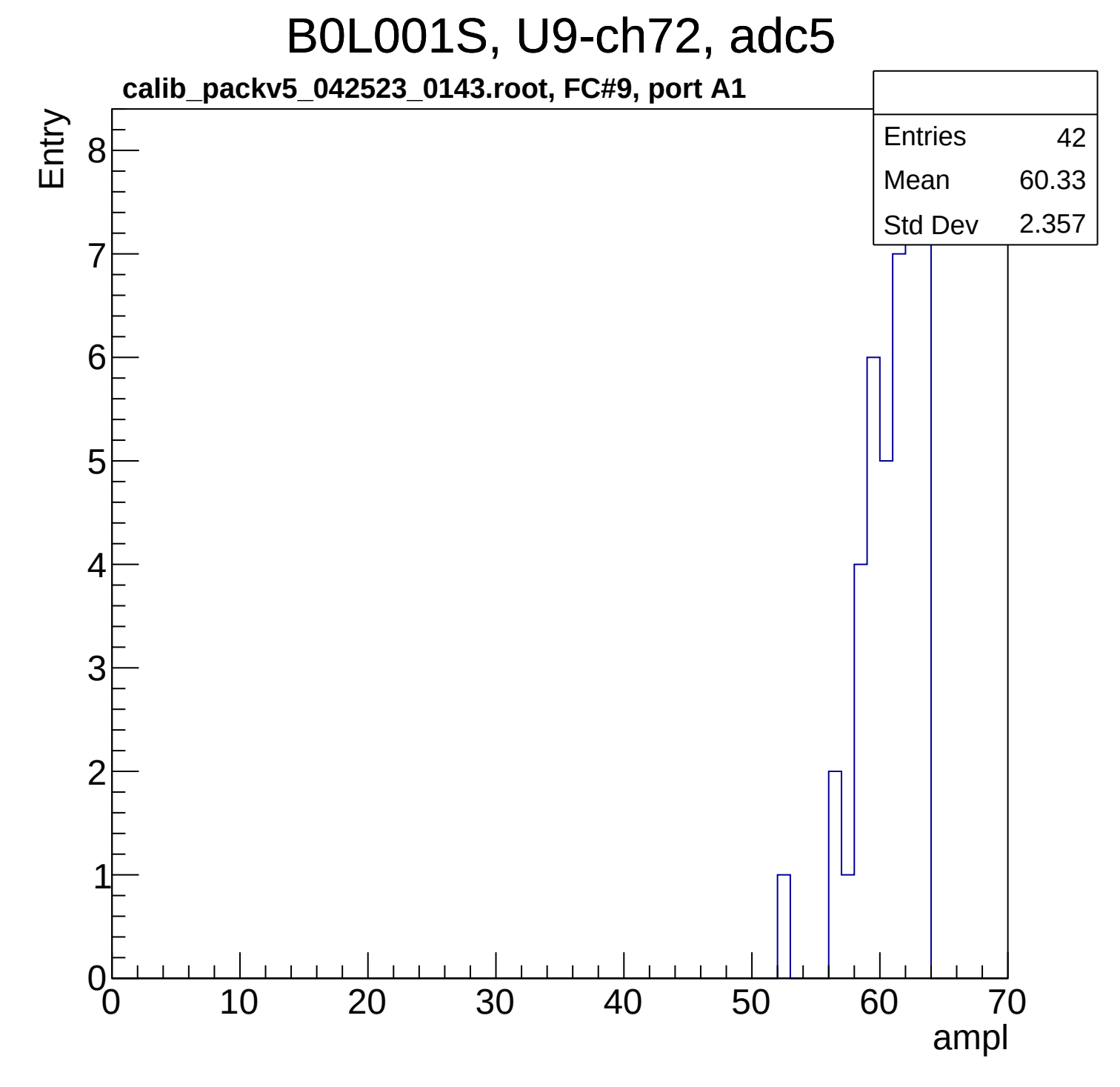
40
50
60
70


B0L001S, U9-ch72, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 60.33
Std Dev 2.357
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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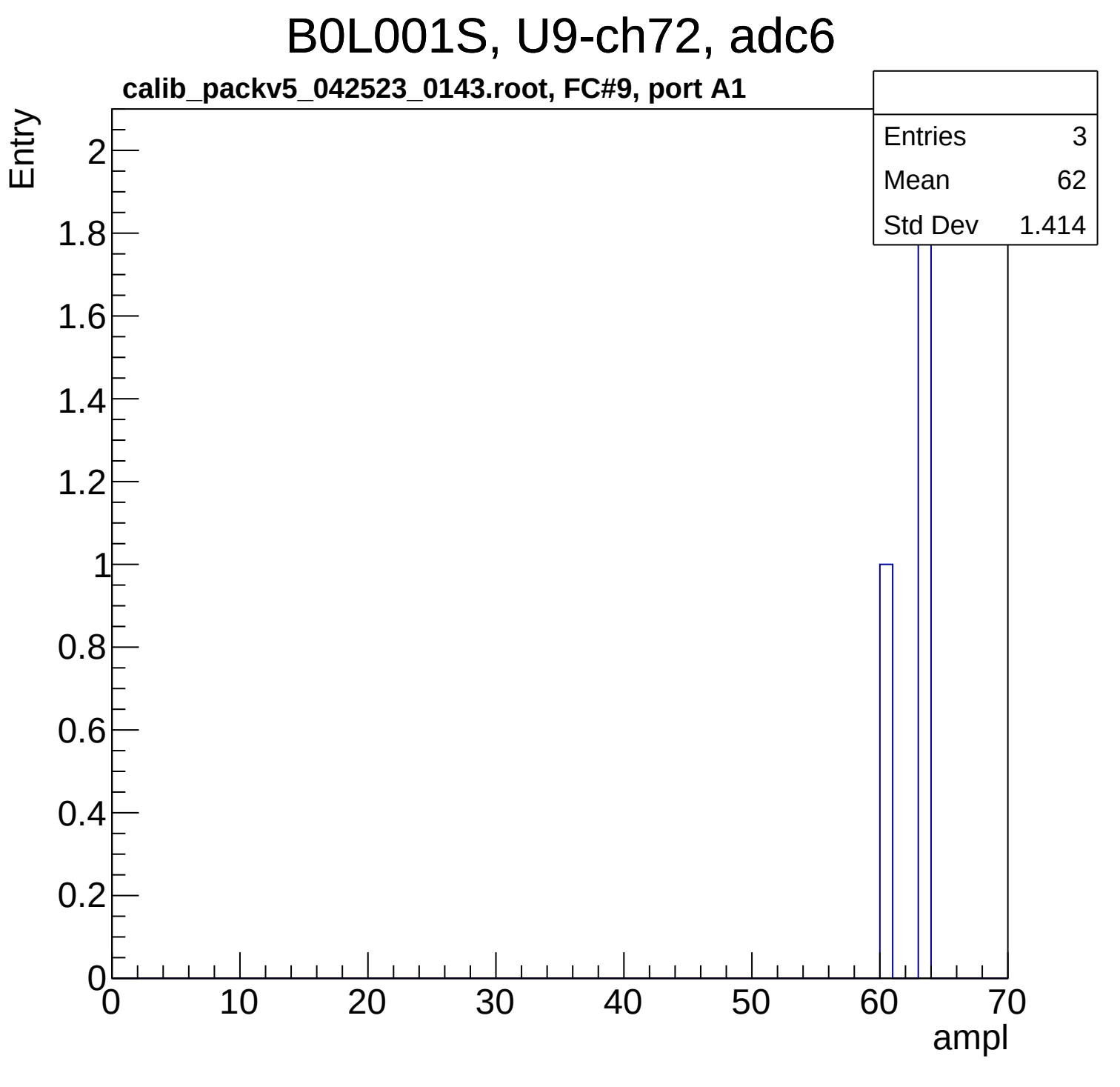
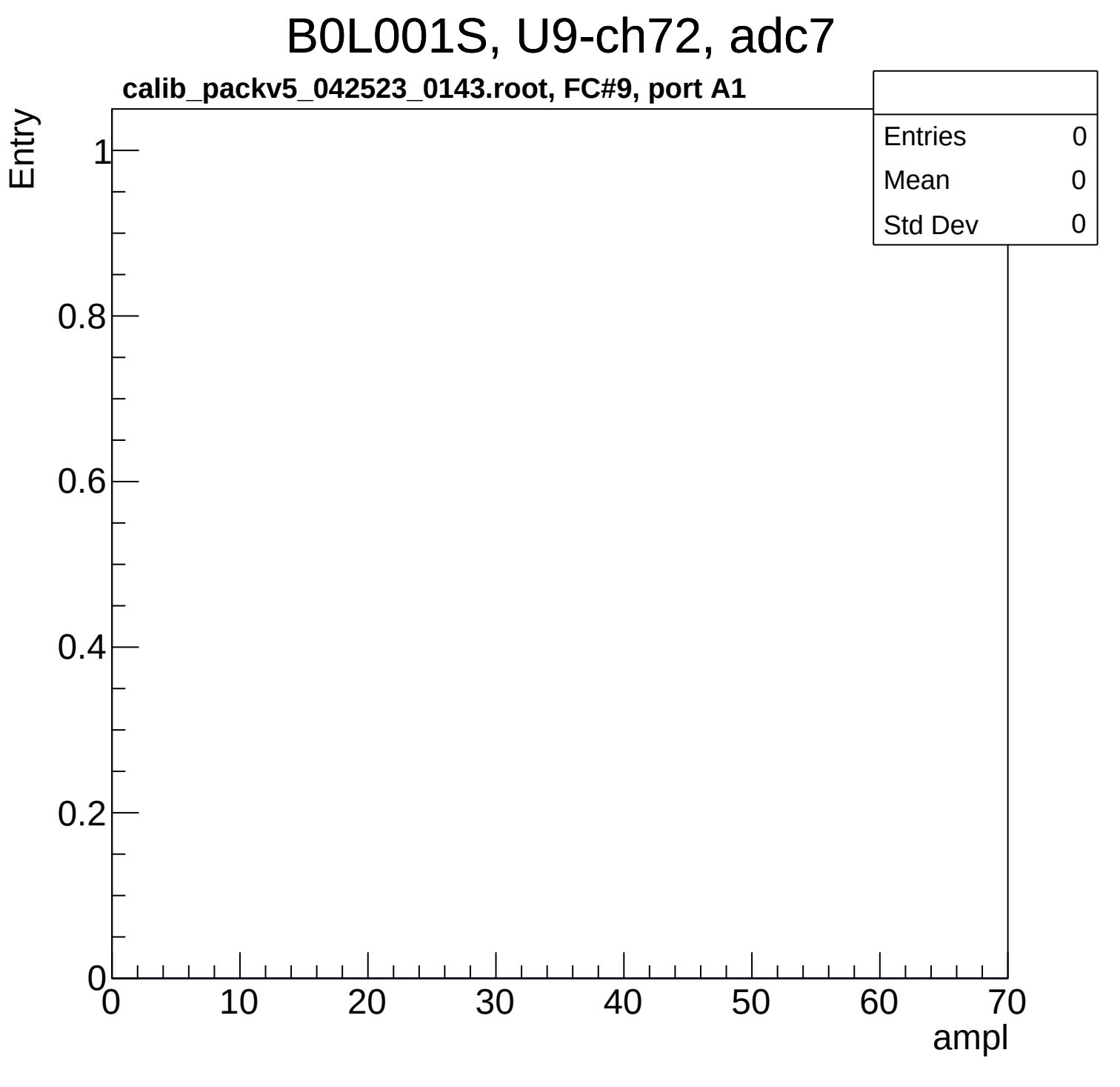

B0L001S, U9-ch72, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch72, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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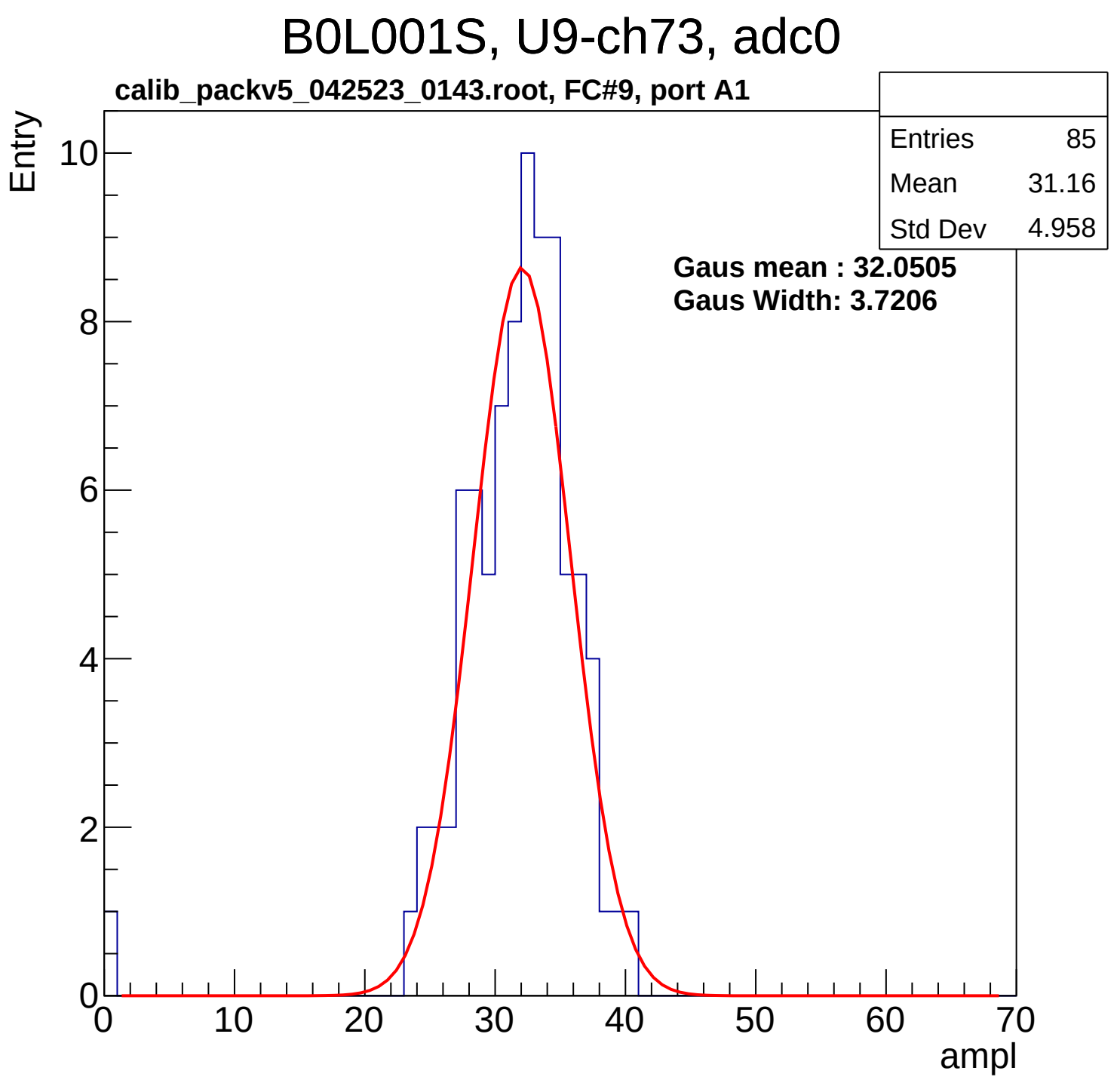
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch73, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 31.16
Std Dev 4.958
Gaus mean : 32.0505
Gaus Width: 3.7206
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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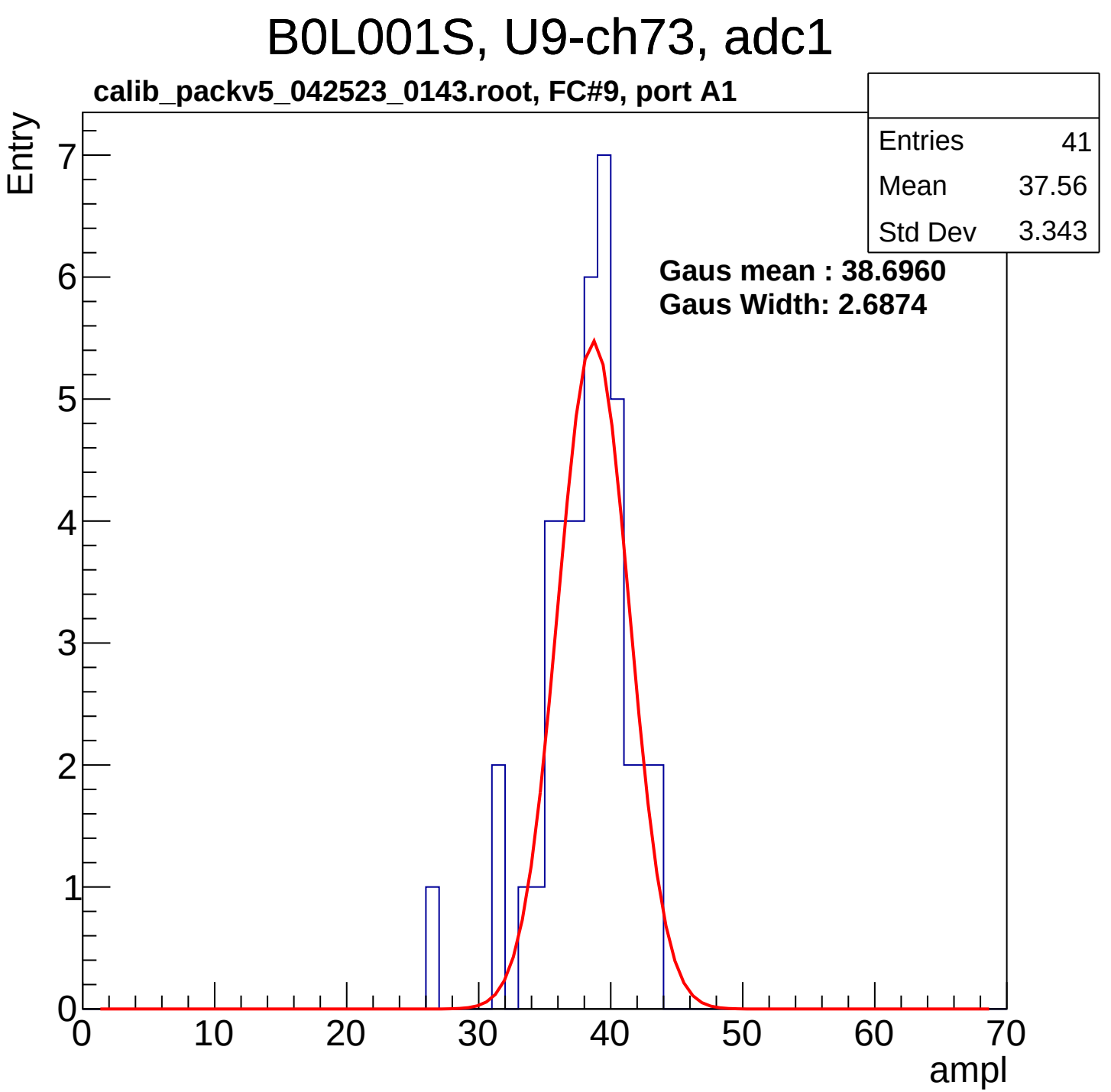
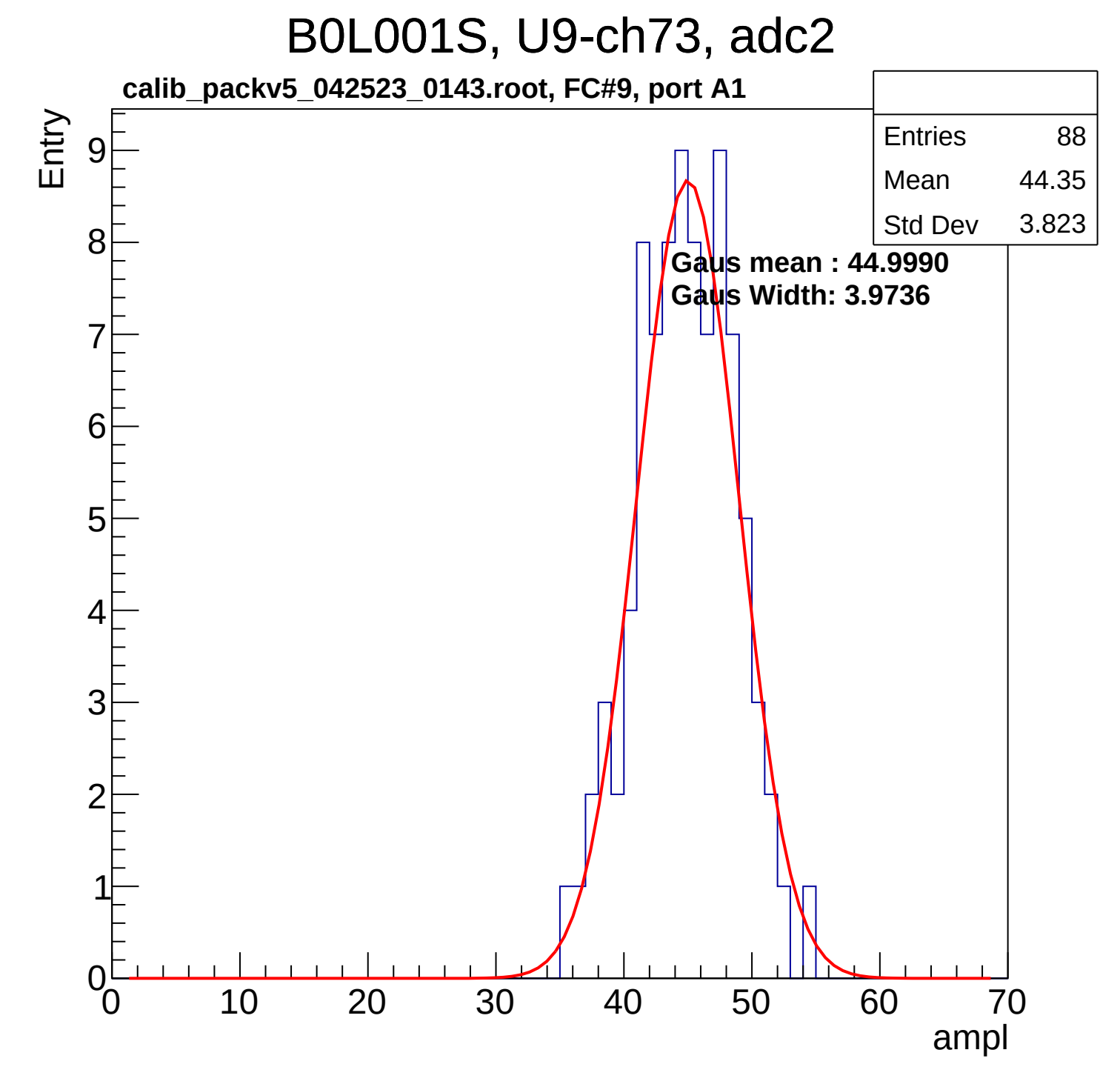
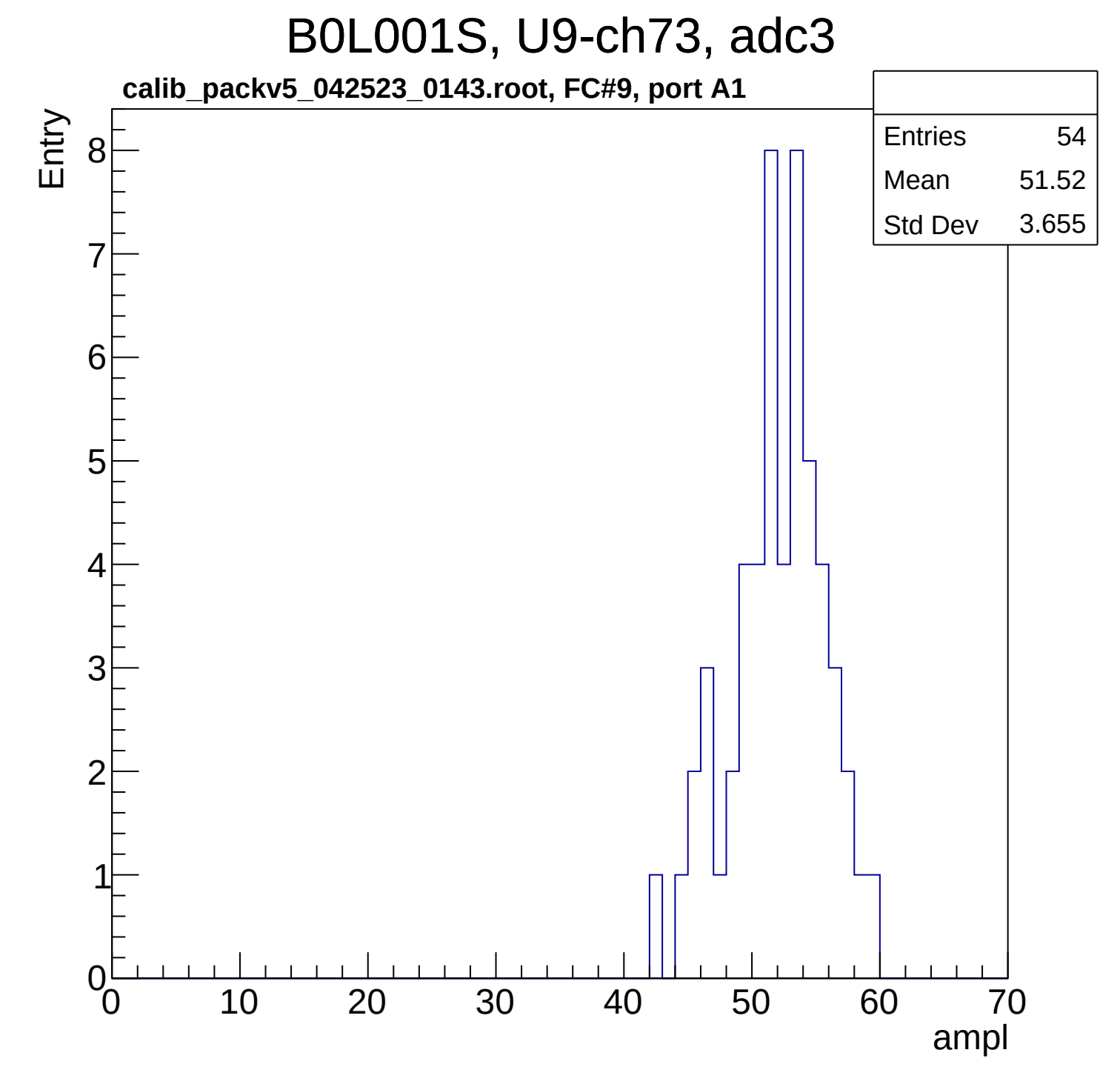

B0L001S, U9-ch73, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 37.56
Std Dev 3.343
Gaus mean : 38.6960
Gaus Width: 2.6874
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch73, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 88
Mean 44.35
Std Dev 3.823
Gaus mean : 44.9990
Gaus Width: 3.9736
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch73, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 51.52
Std Dev 3.655
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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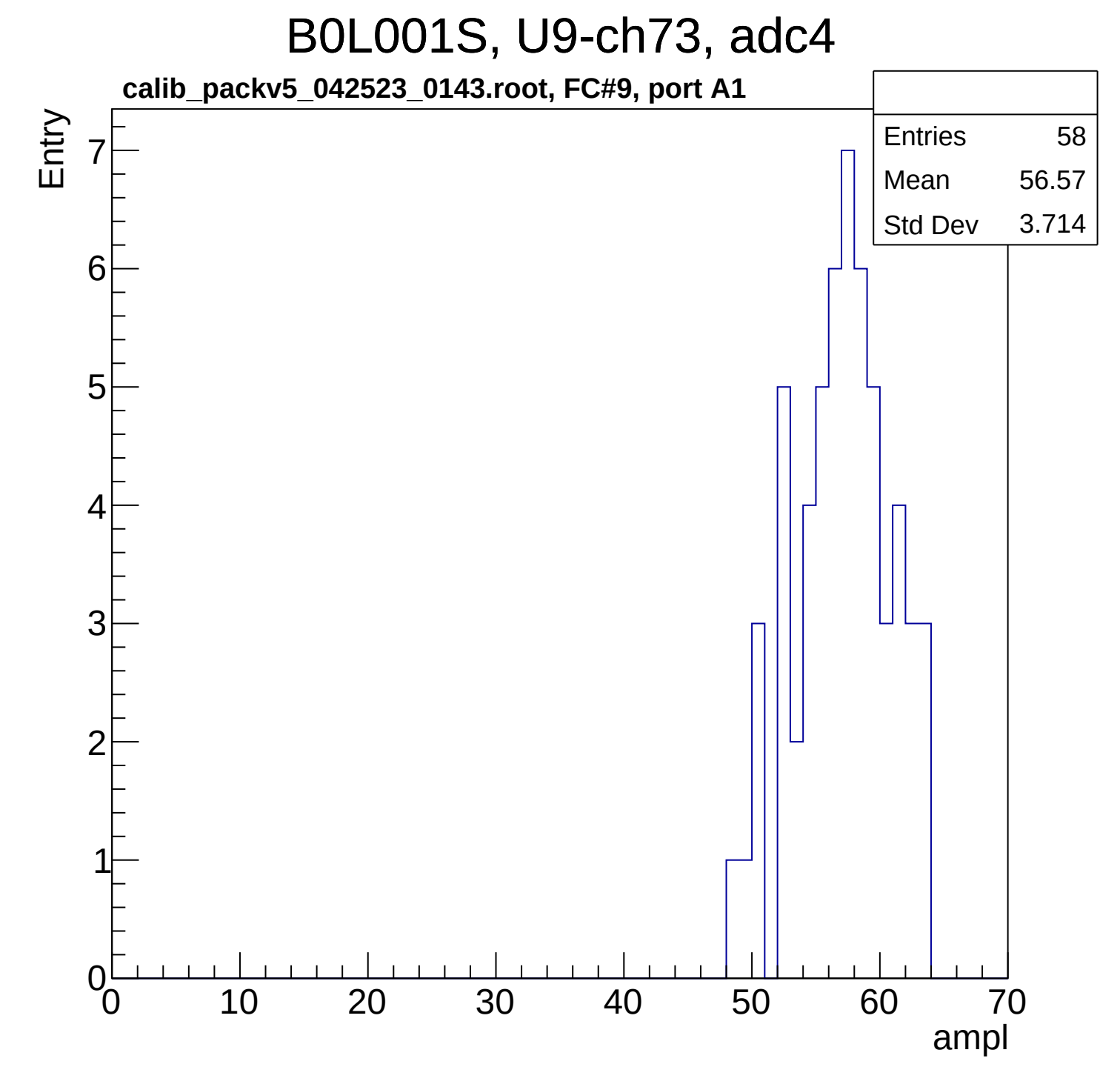
B0L001S, U9-ch73, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.57
Std Dev 3.714
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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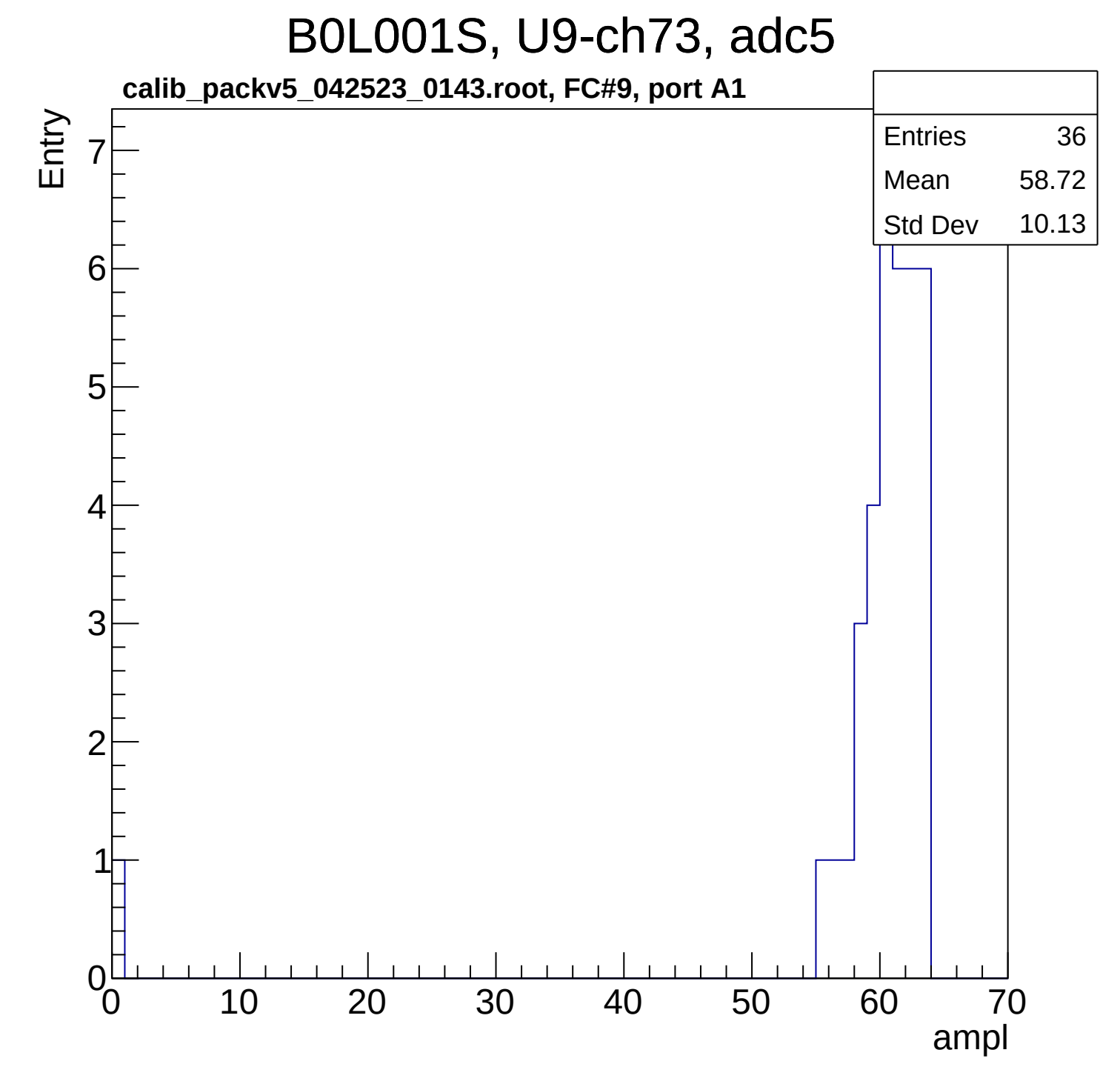
B0L001S, U9-ch73, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.72
Std Dev 10.13
Entry
0
1
2
3
4
5
6
7
0
10
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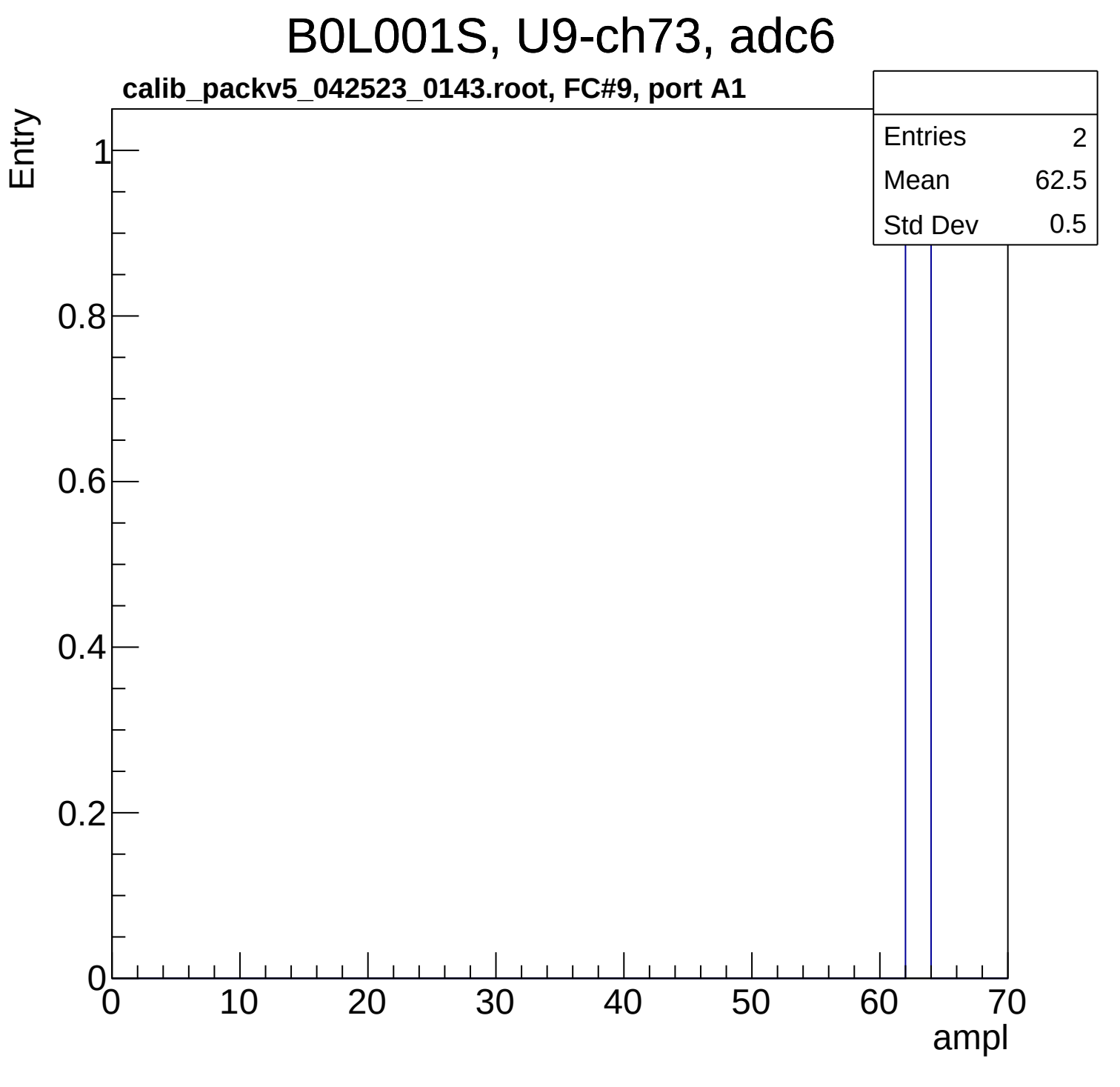
30
40
50
60
70
ampl


B0L001S, U9-ch73, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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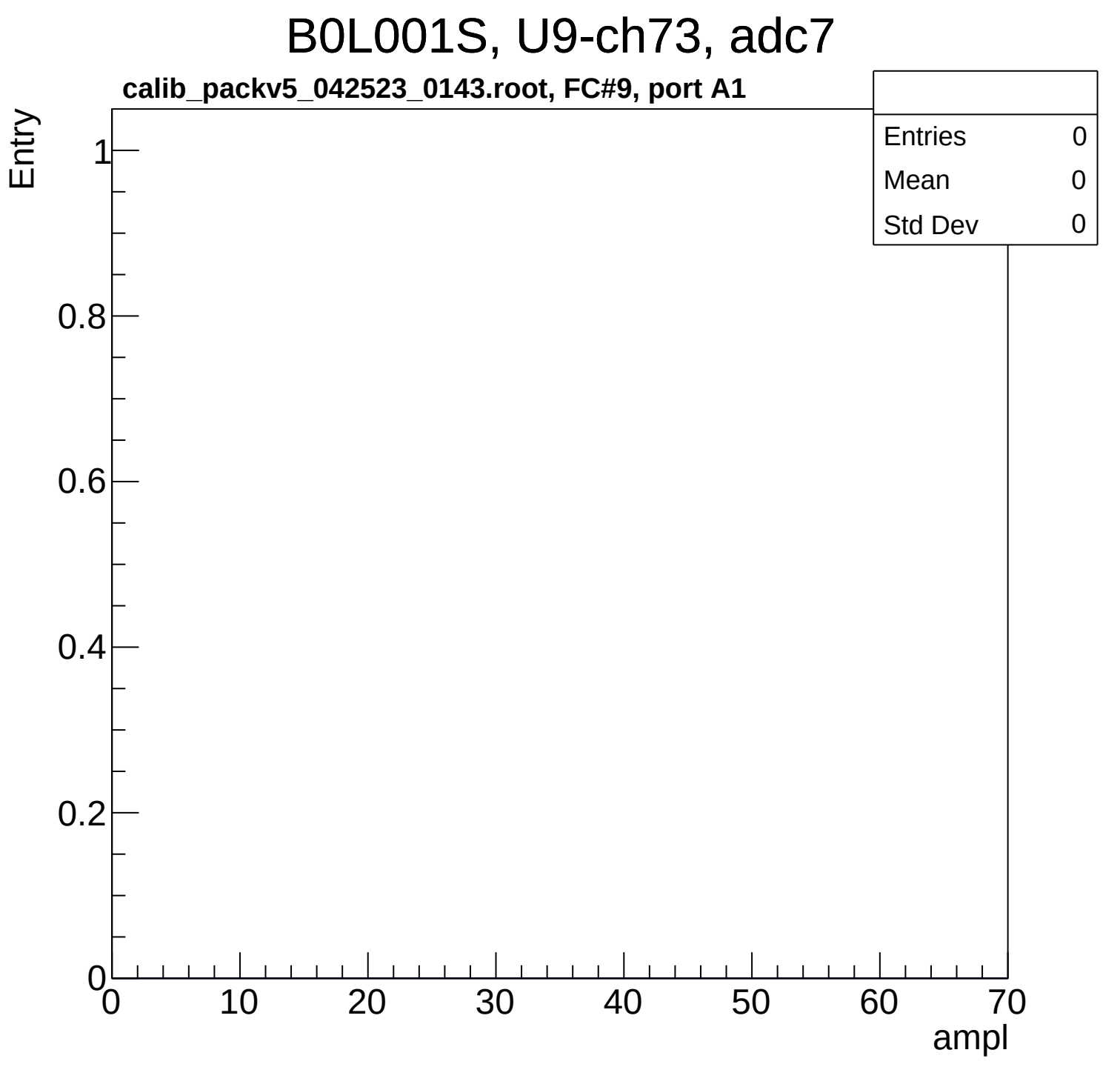
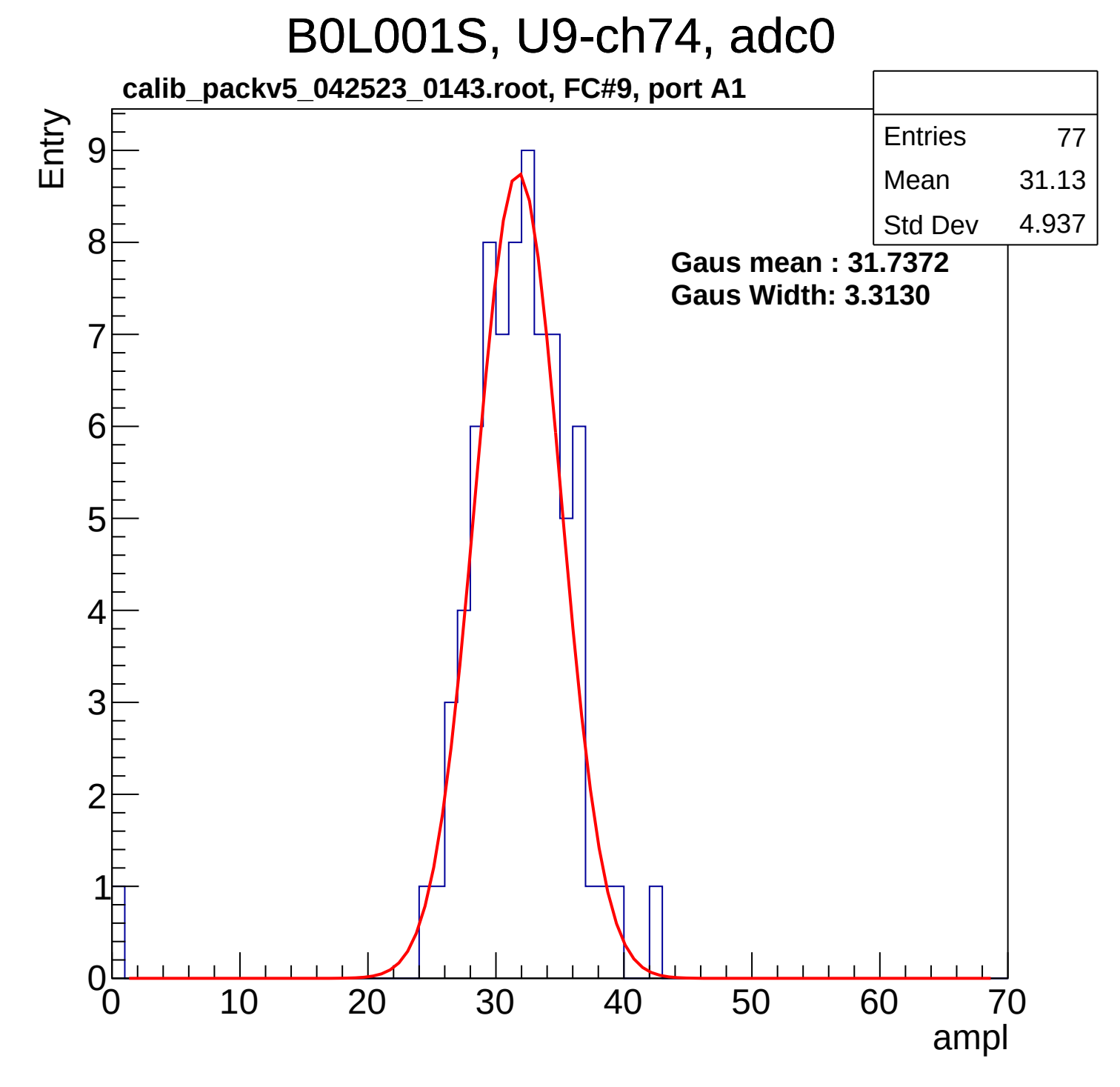
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch73, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch74, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 31.13
Std Dev 4.937
Gaus mean : 31.7372
Gaus Width: 3.3130
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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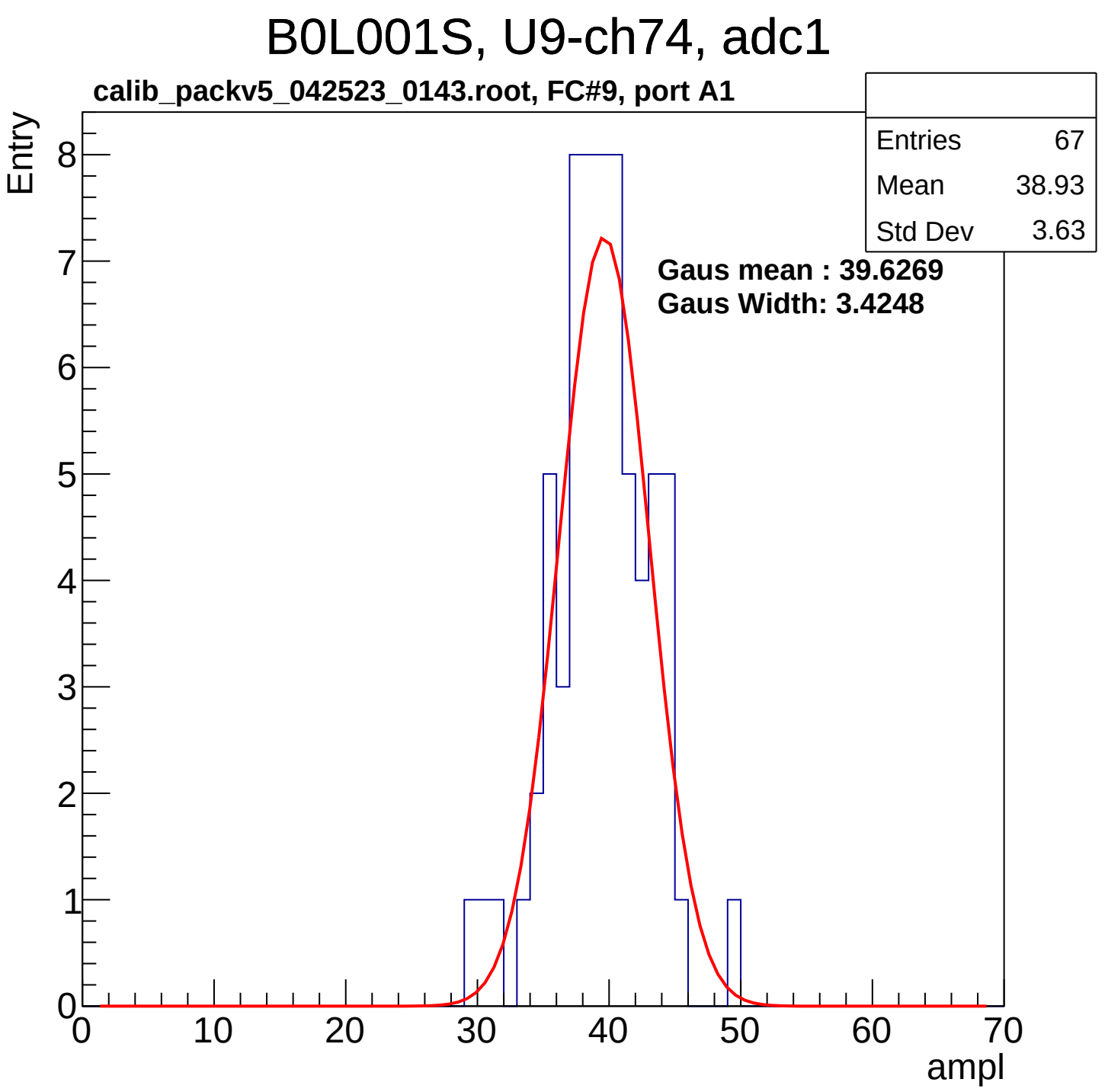
B0L001S, U9-ch74, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.93
Std Dev 3.63
Gaus mean : 39.6269
Gaus Width: 3.4248
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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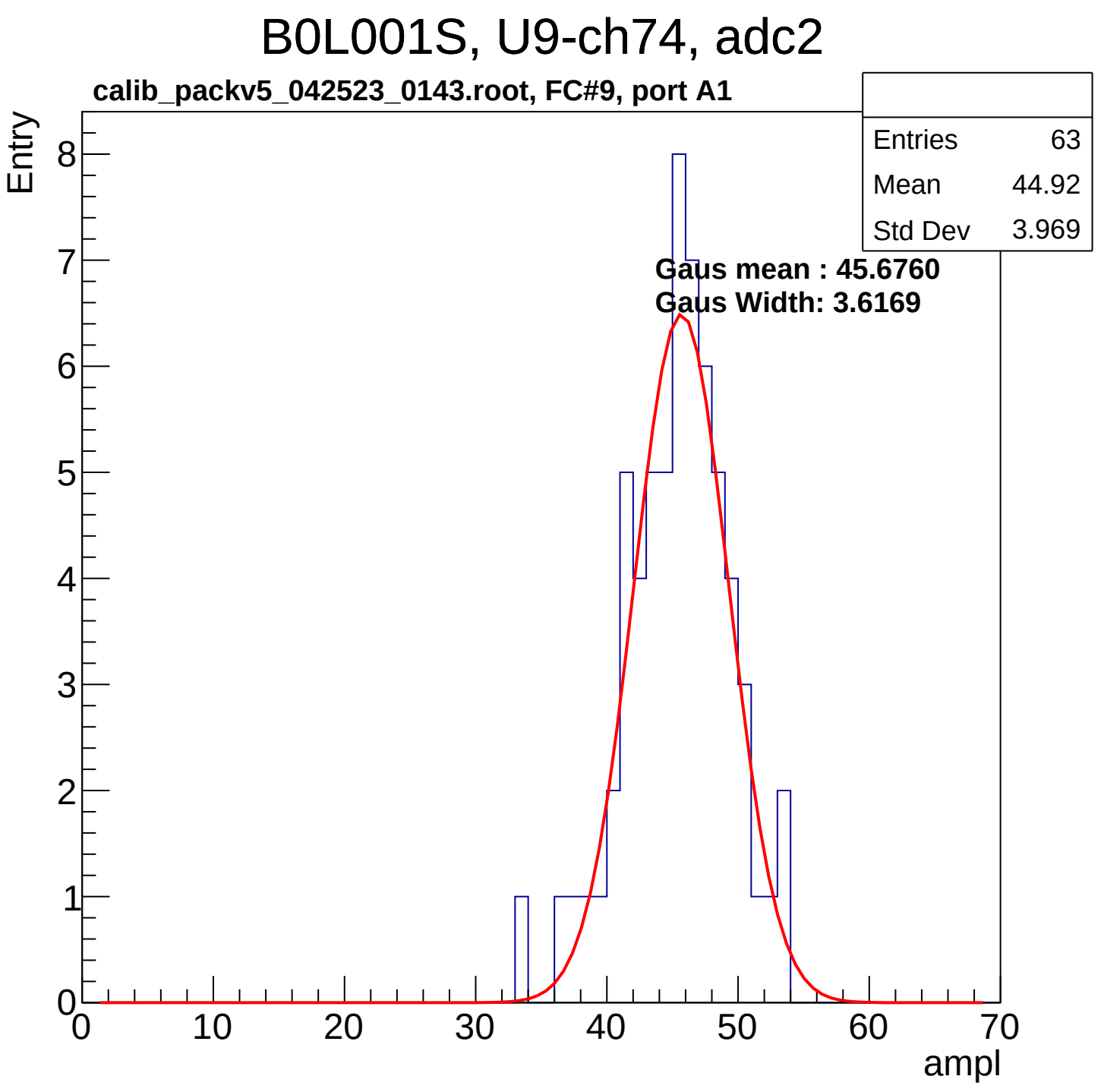
B0L001S, U9-ch74, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 44.92
Std Dev 3.969
Gaus mean : 45.6760
Gaus Width: 3.6169
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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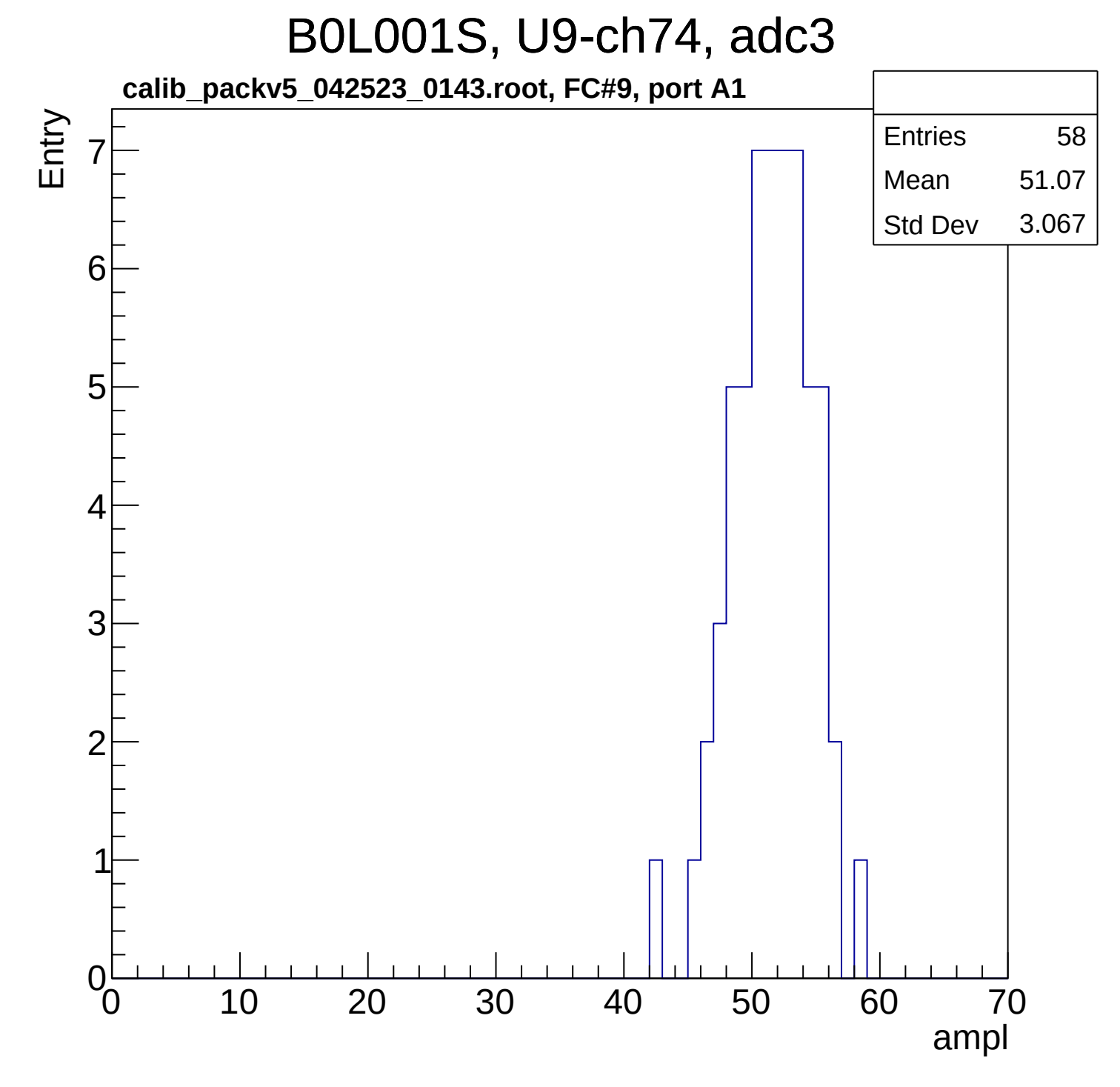
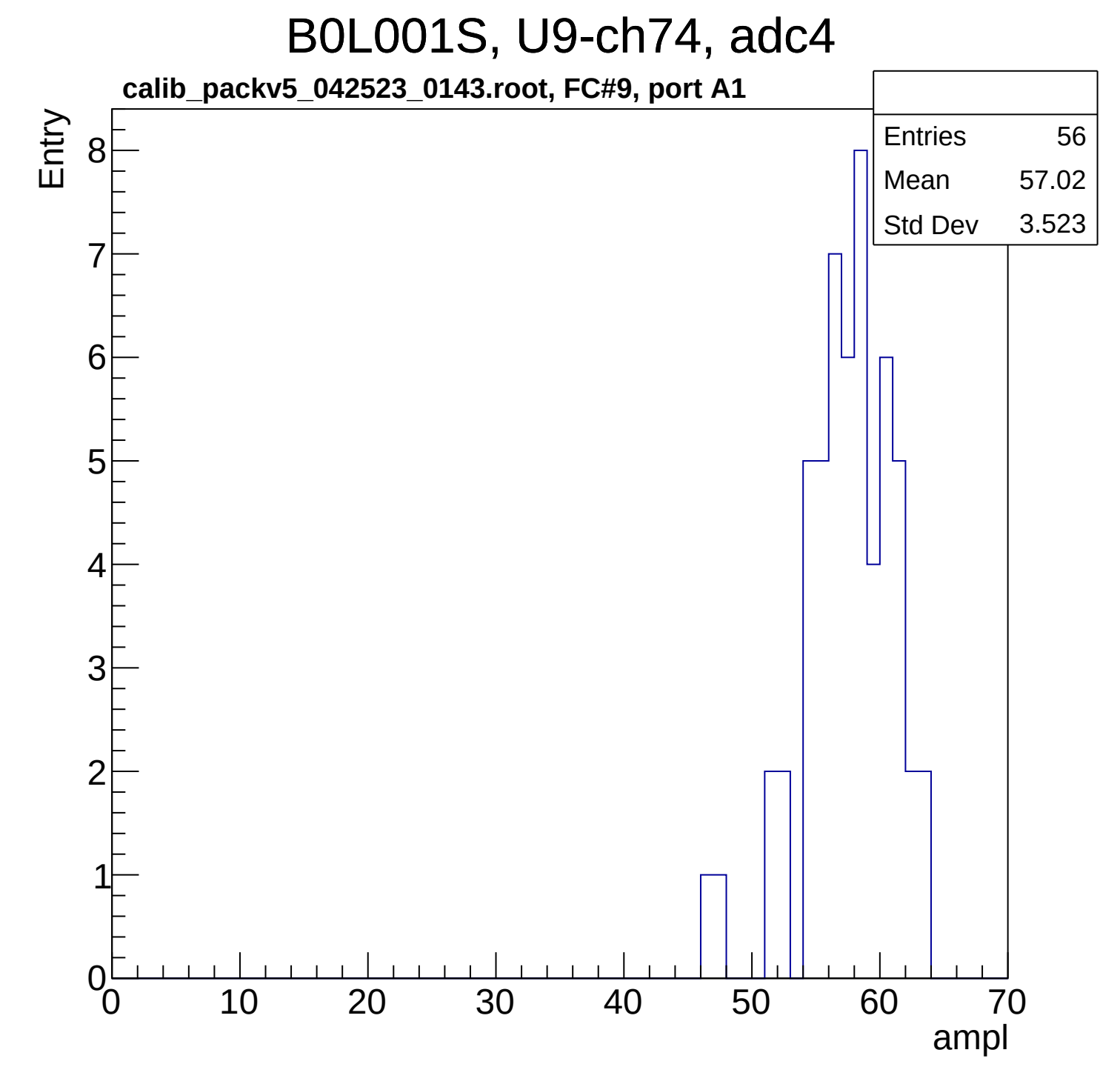

B0L001S, U9-ch74, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 51.07
Std Dev 3.067
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch74, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.02
Std Dev 3.523
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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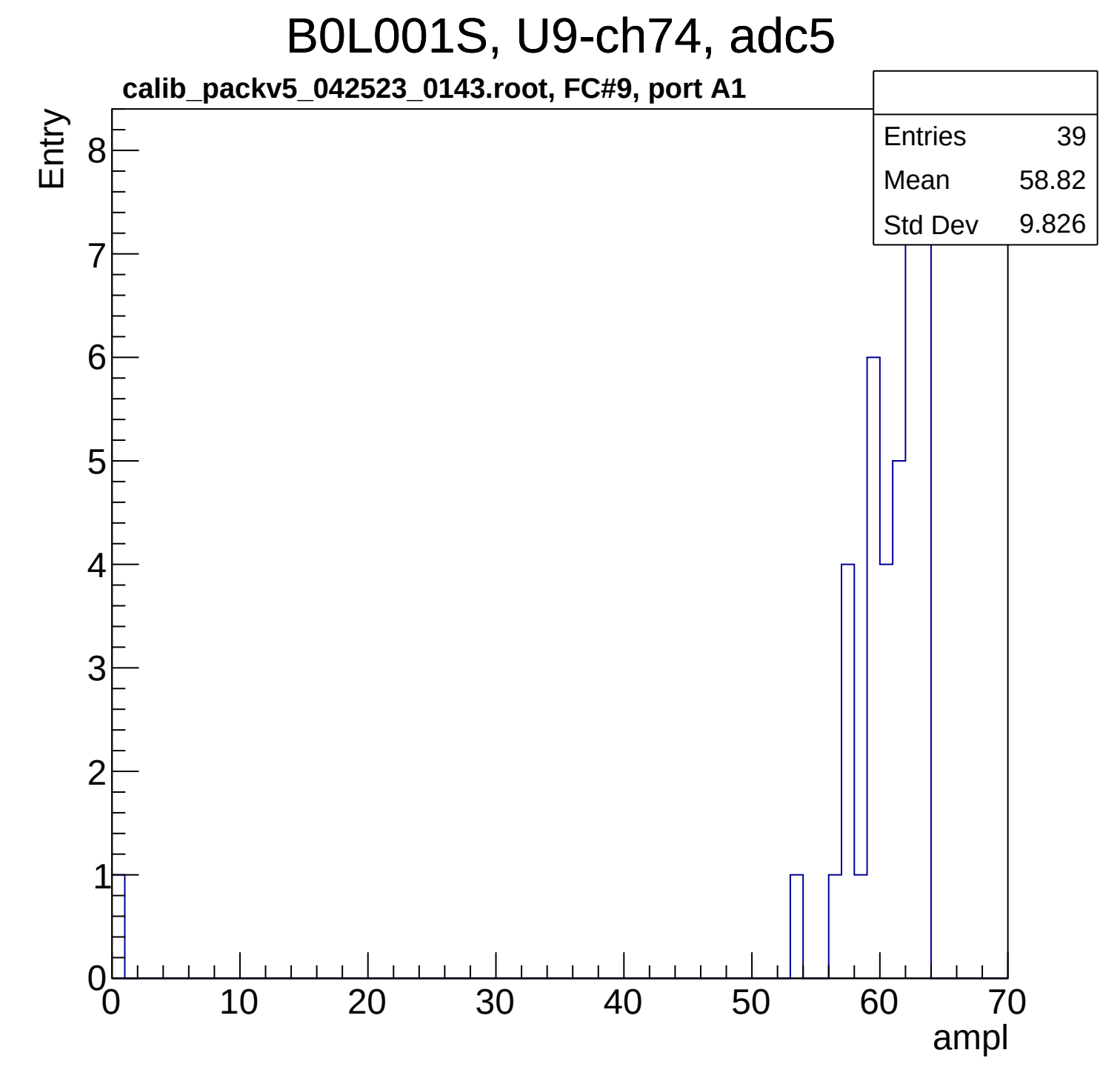
B0L001S, U9-ch74, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.82
Std Dev 9.826
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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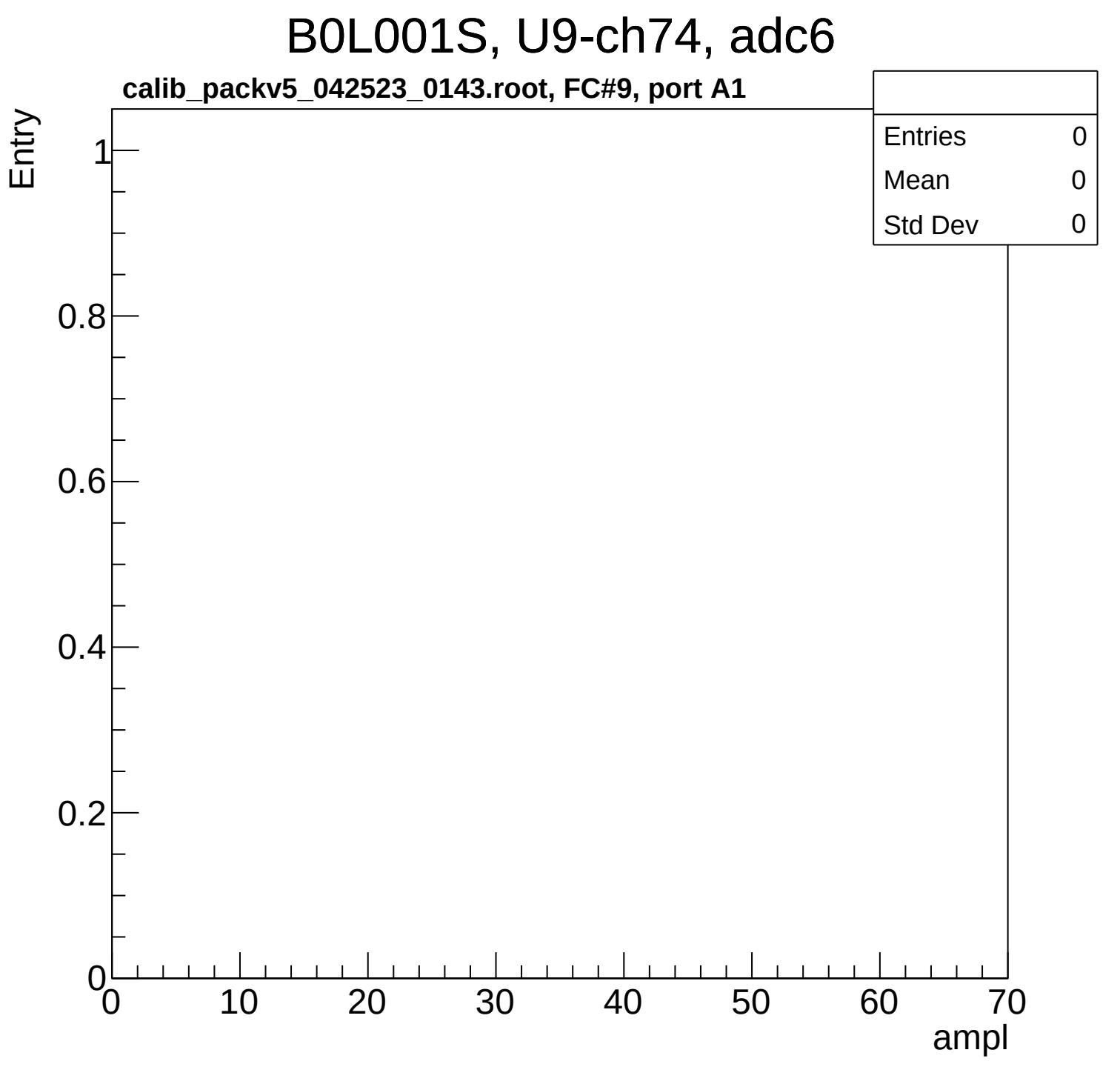
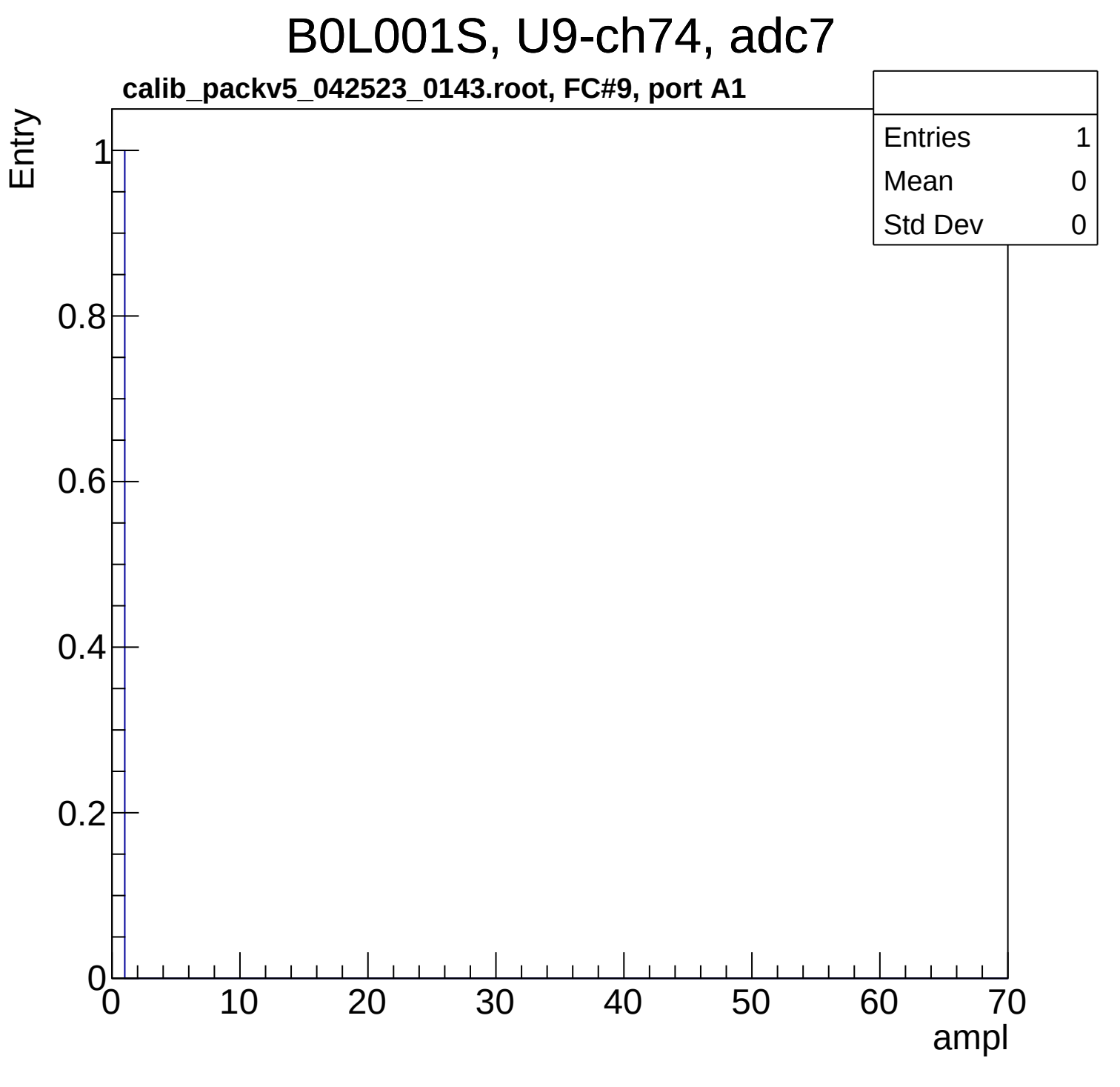
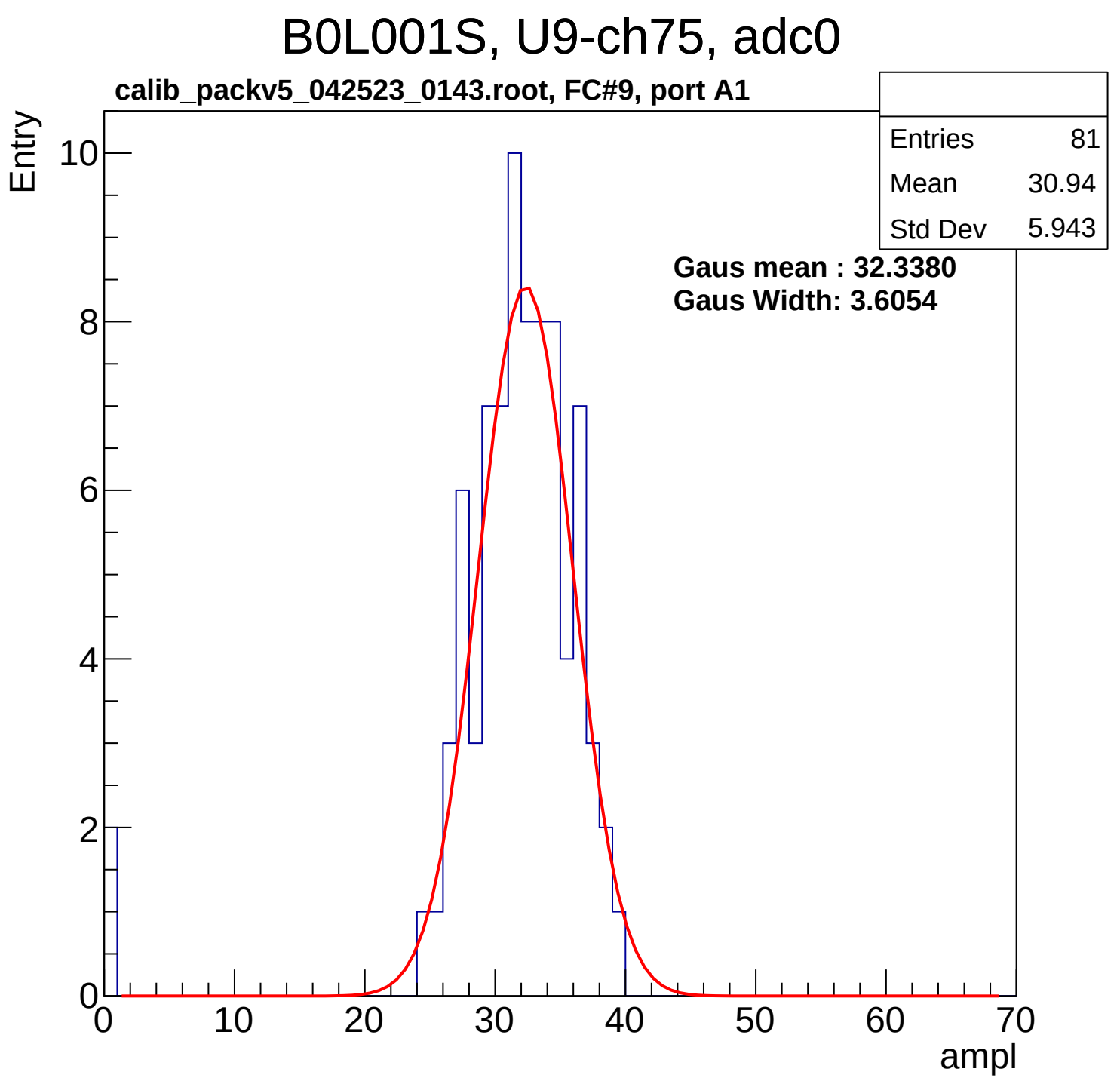
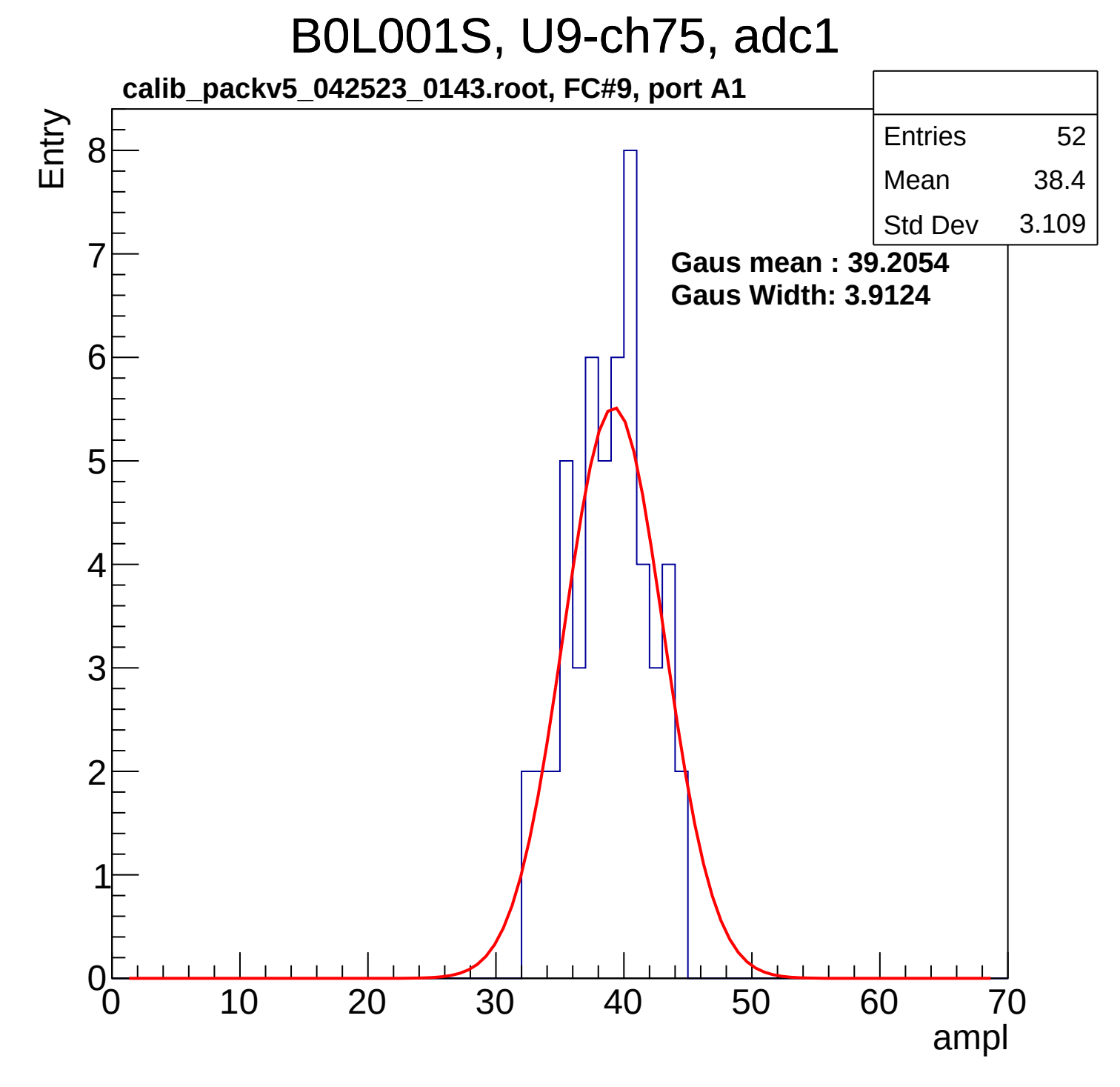

B0L001S, U9-ch74, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch74, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch75, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 30.94
Std Dev 5.943
Gaus mean : 32.3380
Gaus Width: 3.6054
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch75, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 38.4
Std Dev 3.109
Gaus mean : 39.2054
Gaus Width: 3.9124
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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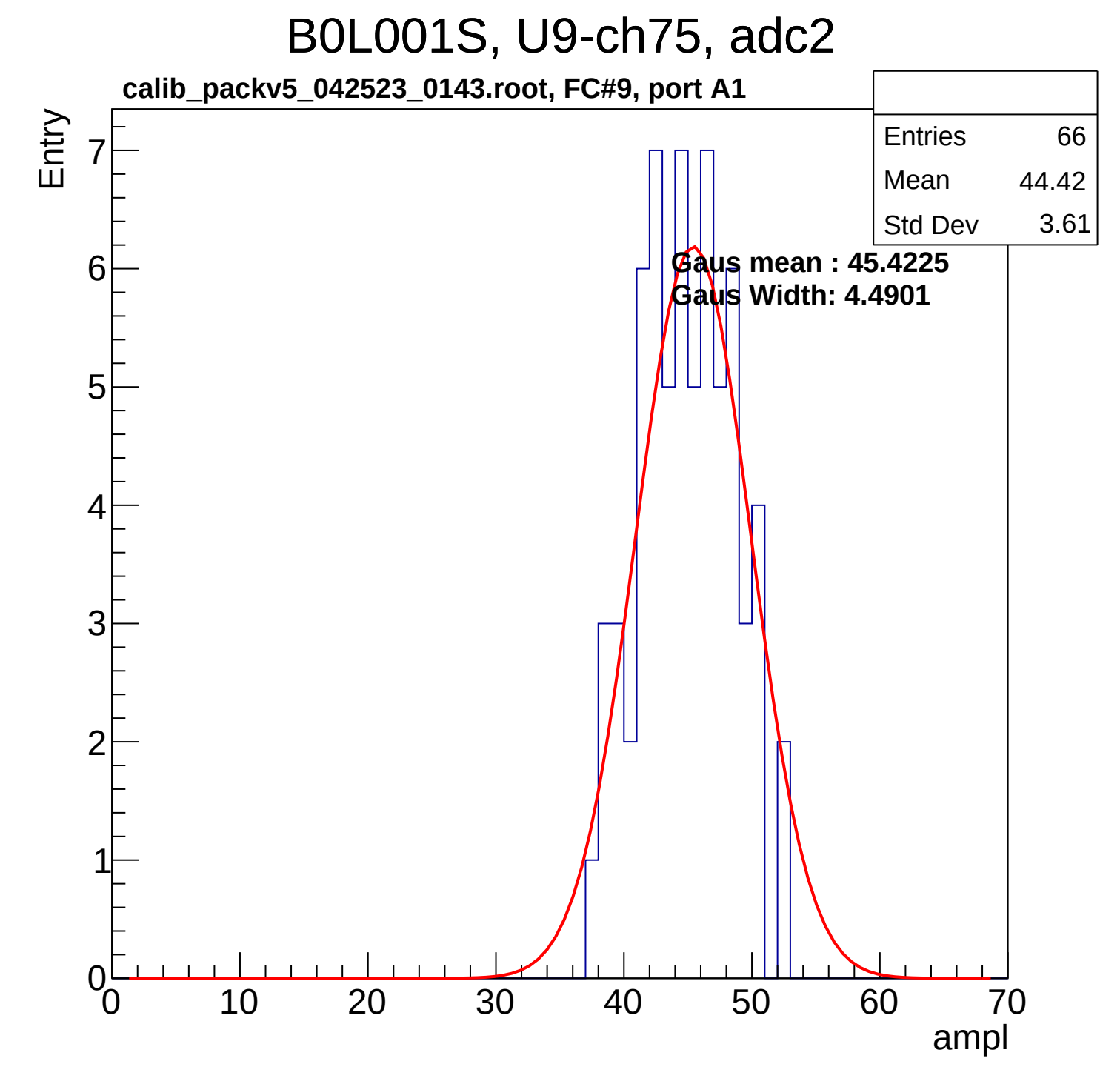
B0L001S, U9-ch75, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 44.42
Std Dev 3.61
Gaus mean : 45.4225
Gaus Width: 4.4901
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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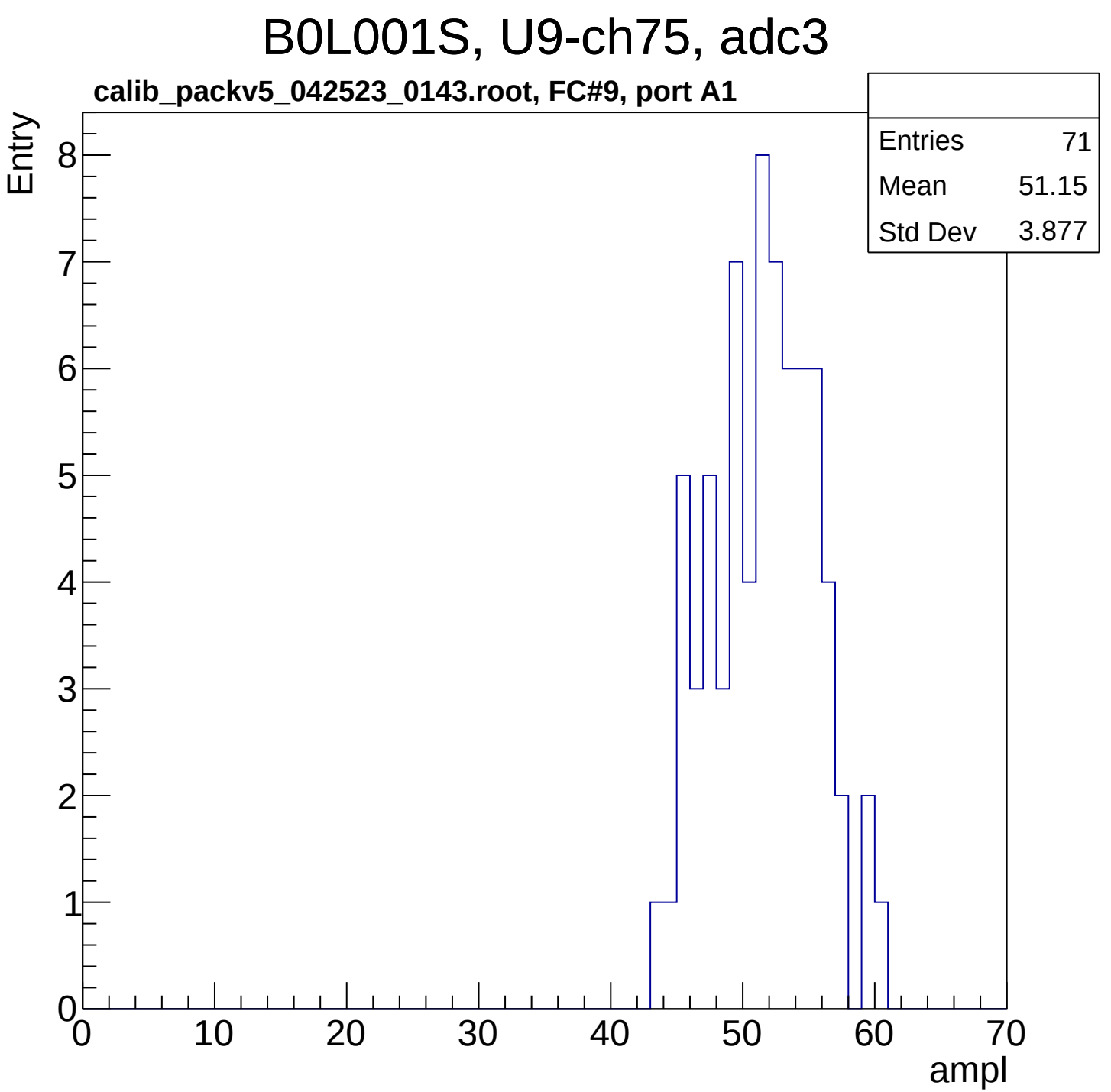
B0L001S, U9-ch75, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.15
Std Dev 3.877
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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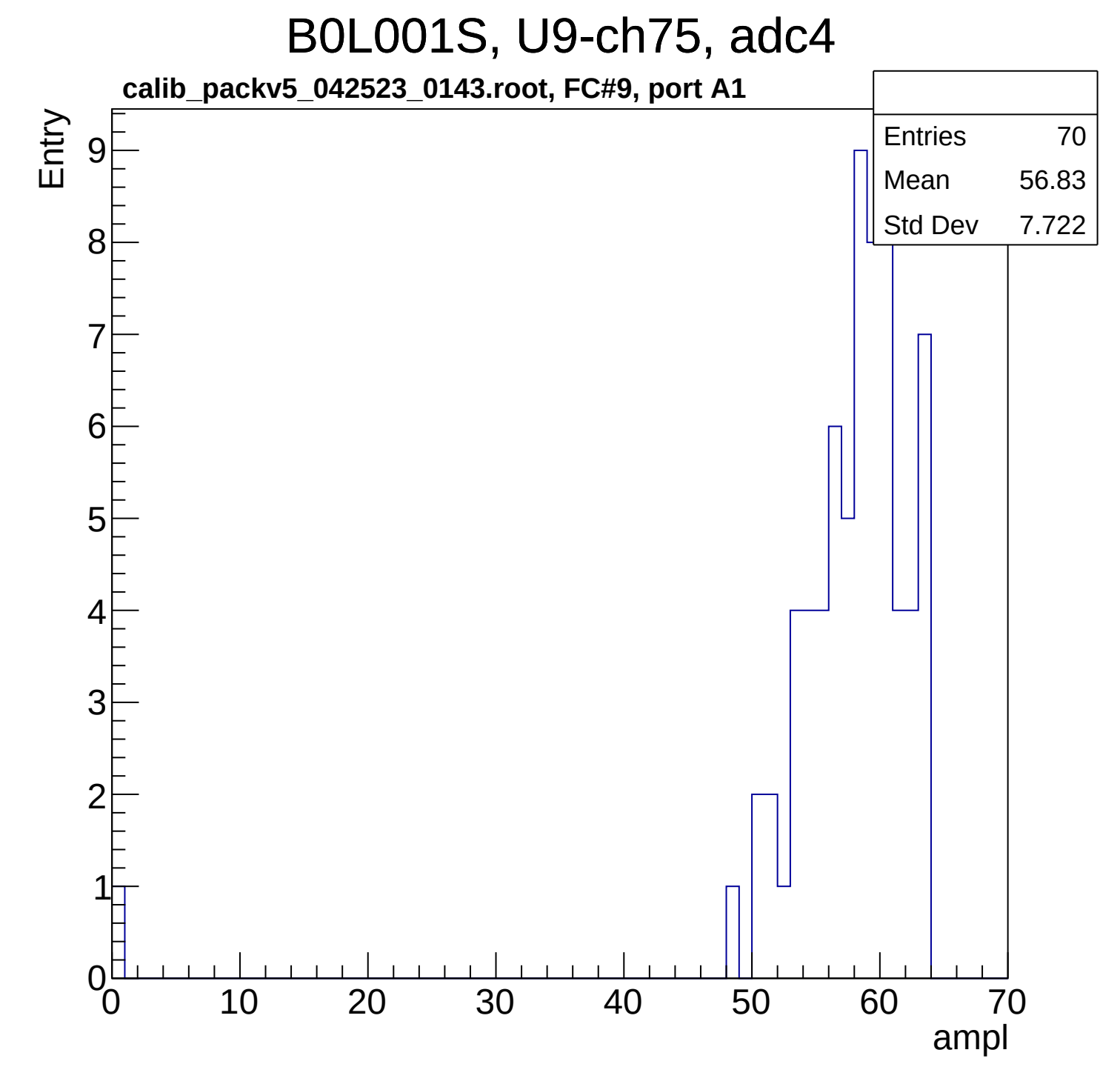
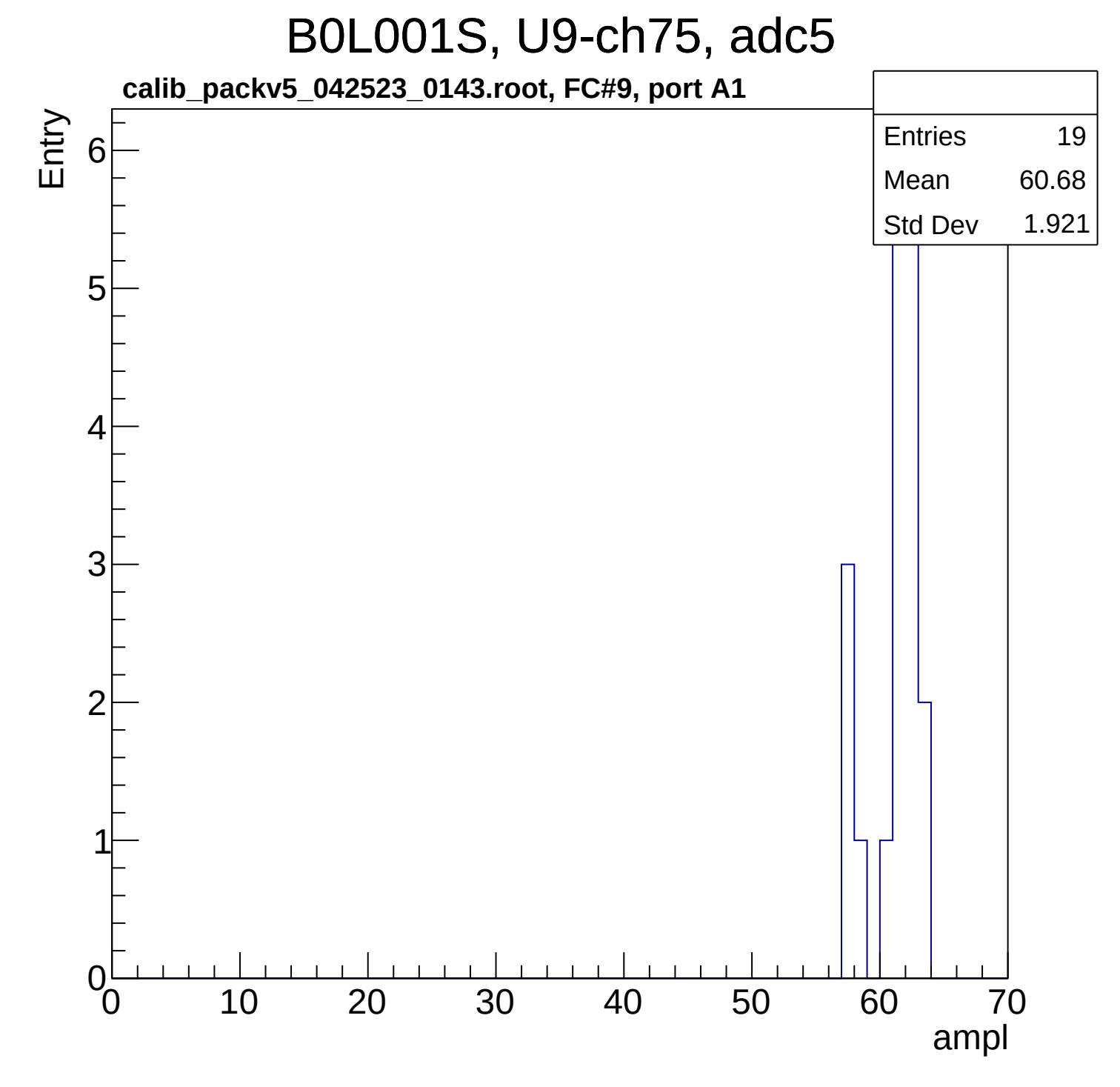
40
50
60
70


B0L001S, U9-ch75, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 56.83
Std Dev 7.722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch75, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 19
Mean 60.68
Std Dev 1.921
Entry
0
1
2
3
4
5
6
0
10
20
30
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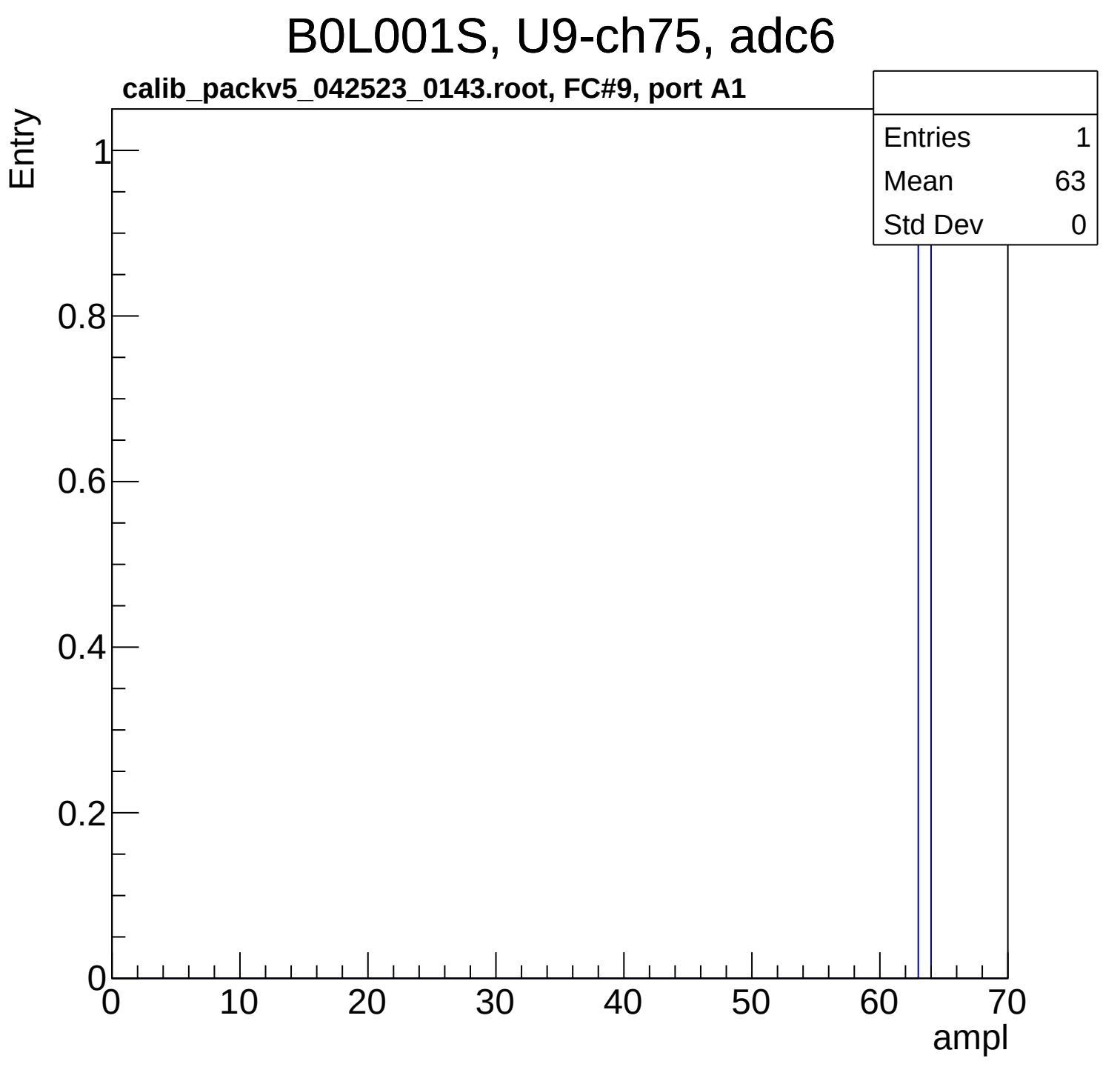
50
60
70
ampl


B0L001S, U9-ch75, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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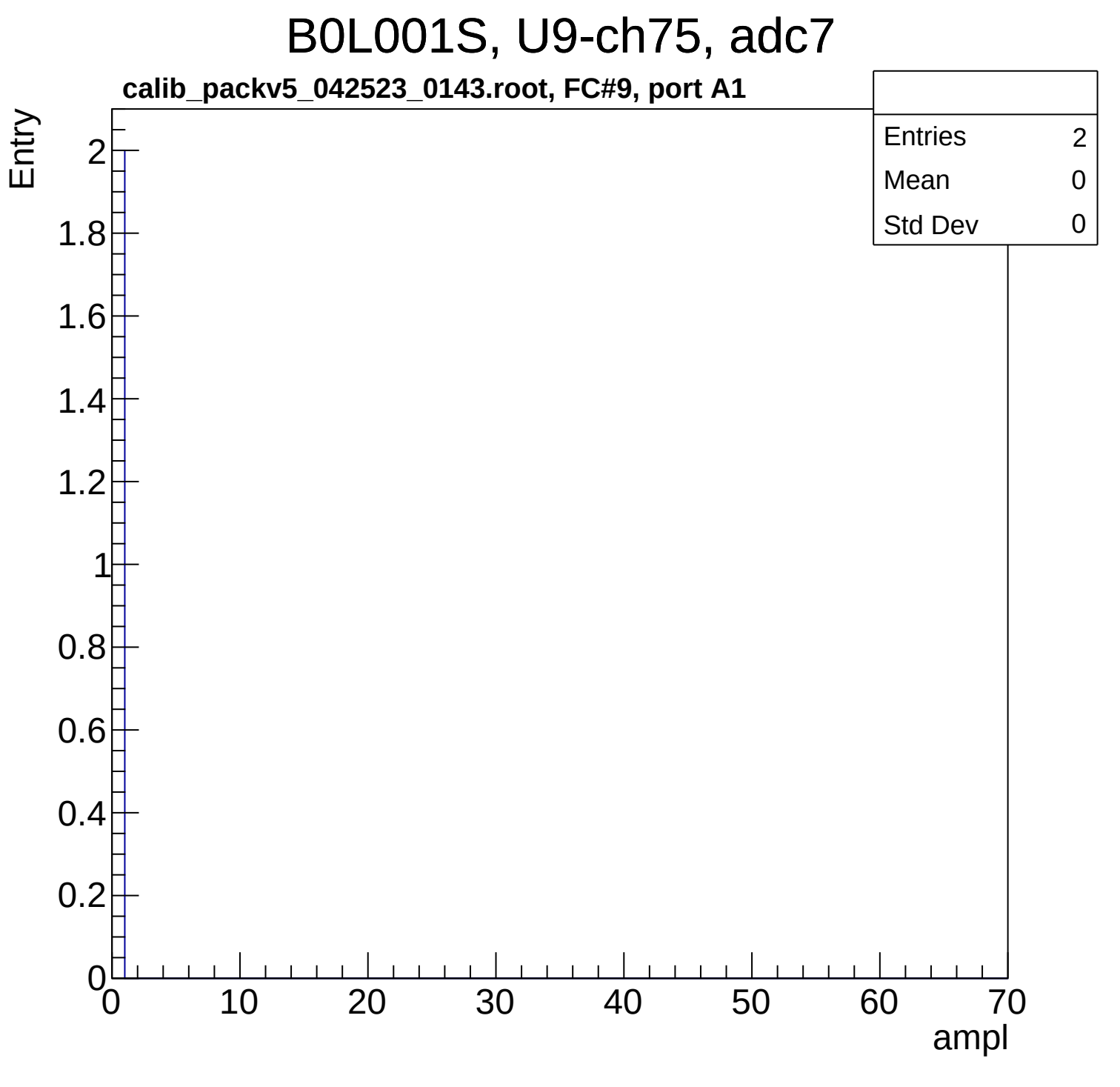
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch75, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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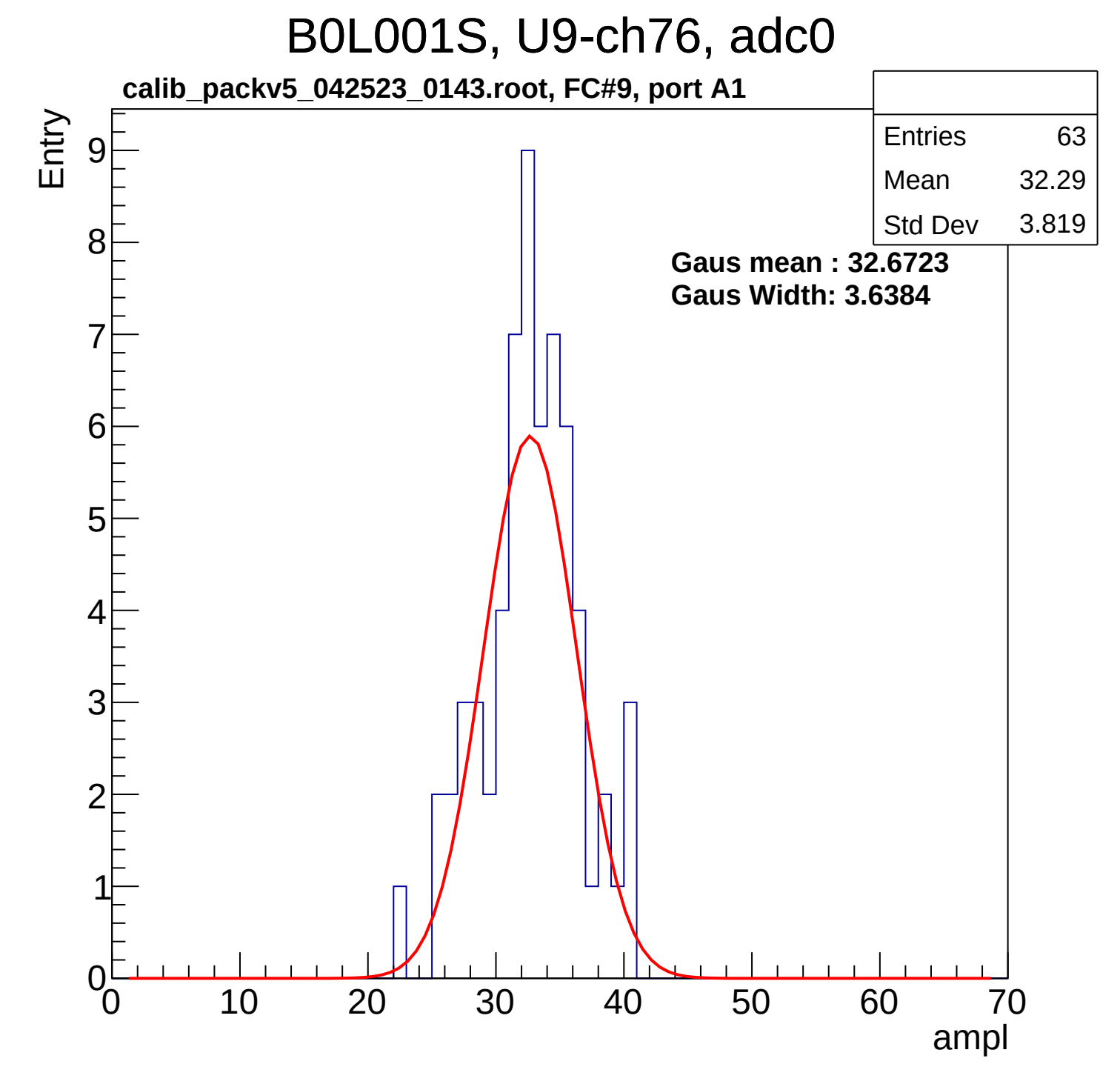
B0L001S, U9-ch76, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 32.29
Std Dev 3.819
Gaus mean : 32.6723
Gaus Width: 3.6384
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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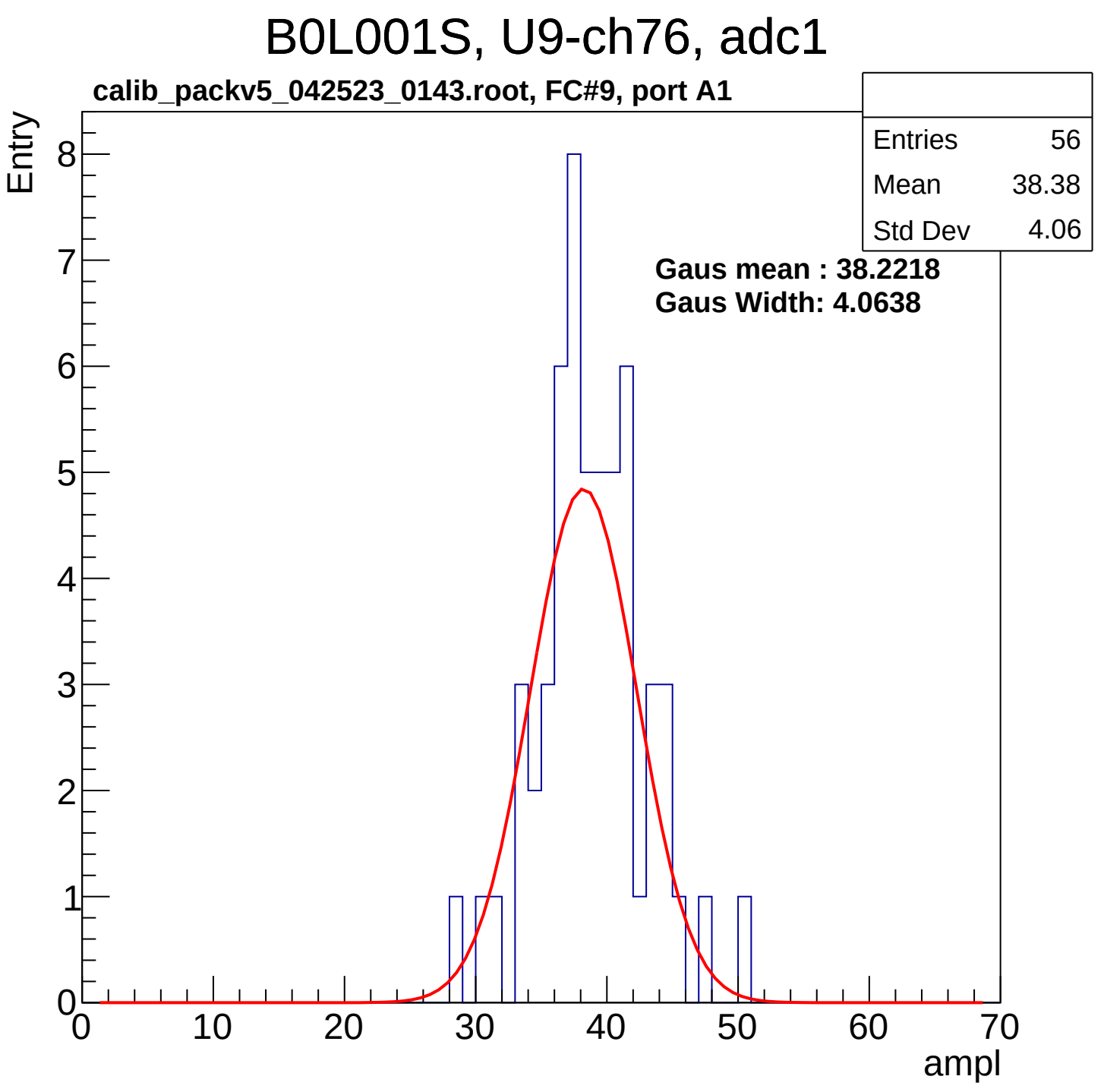
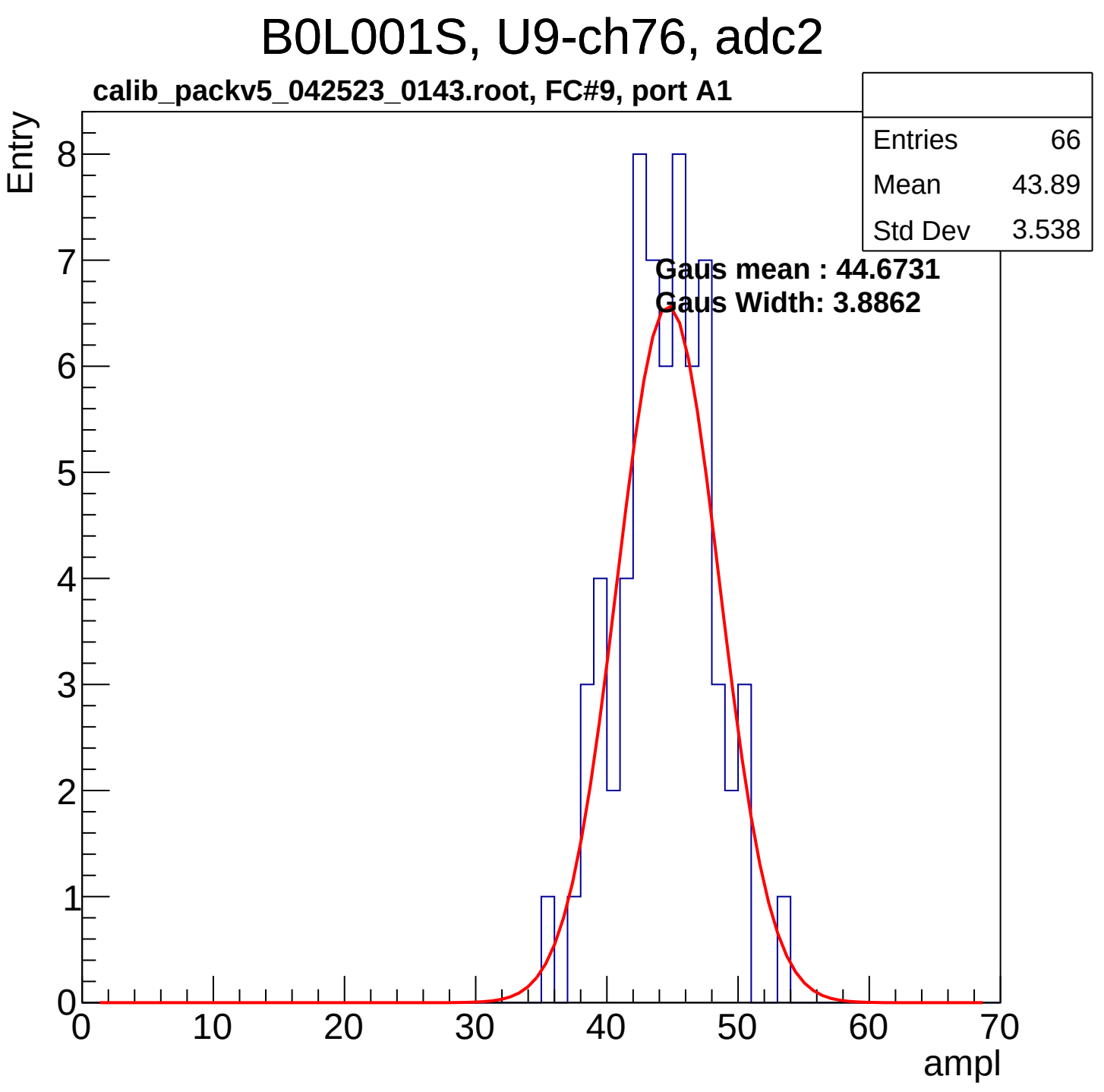
60
70


B0L001S, U9-ch76, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 38.38
Std Dev 4.06
Gaus mean : 38.2218
Gaus Width: 4.0638
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch76, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.89
Std Dev 3.538
Gaus mean : 44.6731
Gaus Width: 3.8862
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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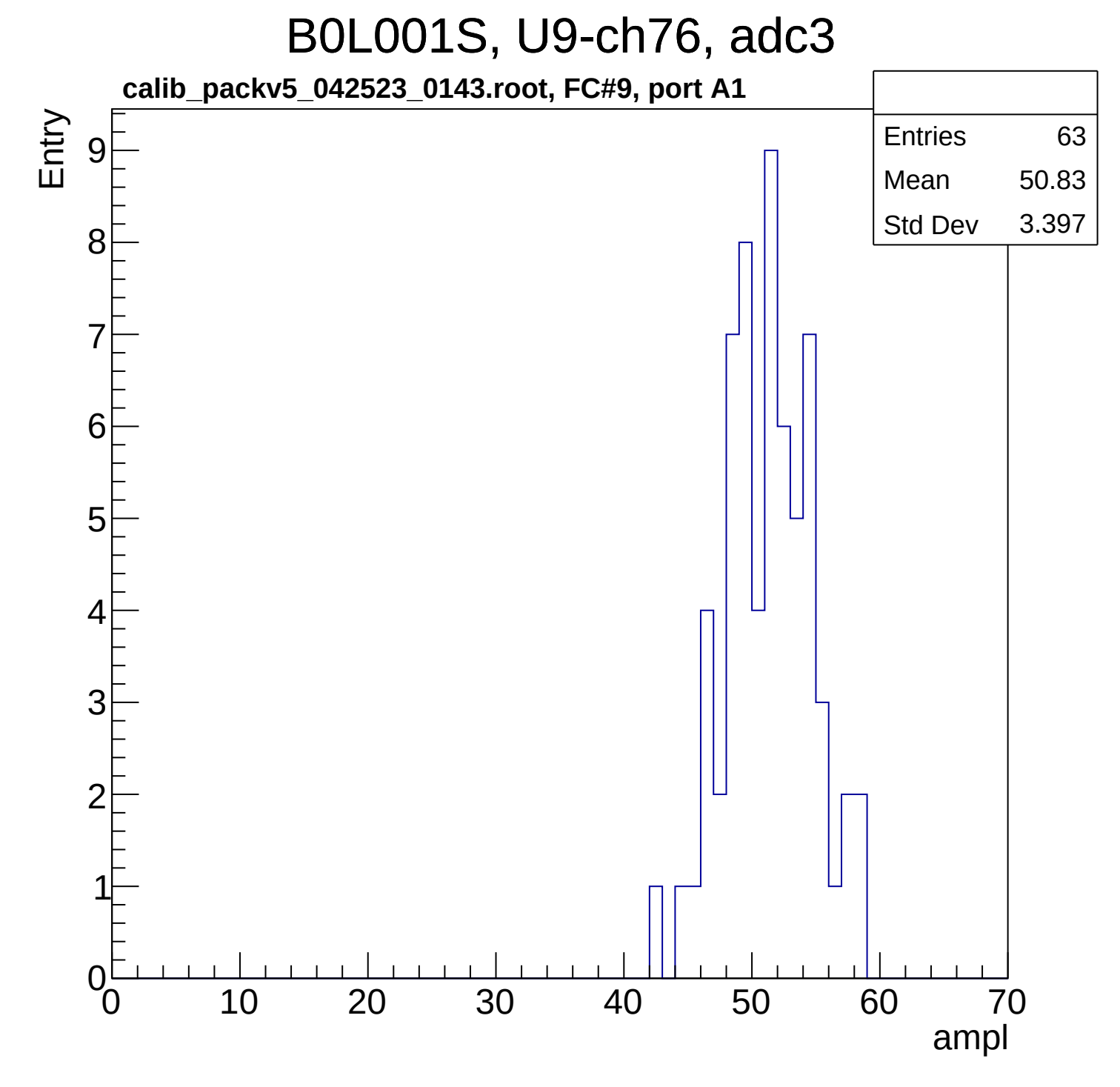
B0L001S, U9-ch76, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.83
Std Dev 3.397
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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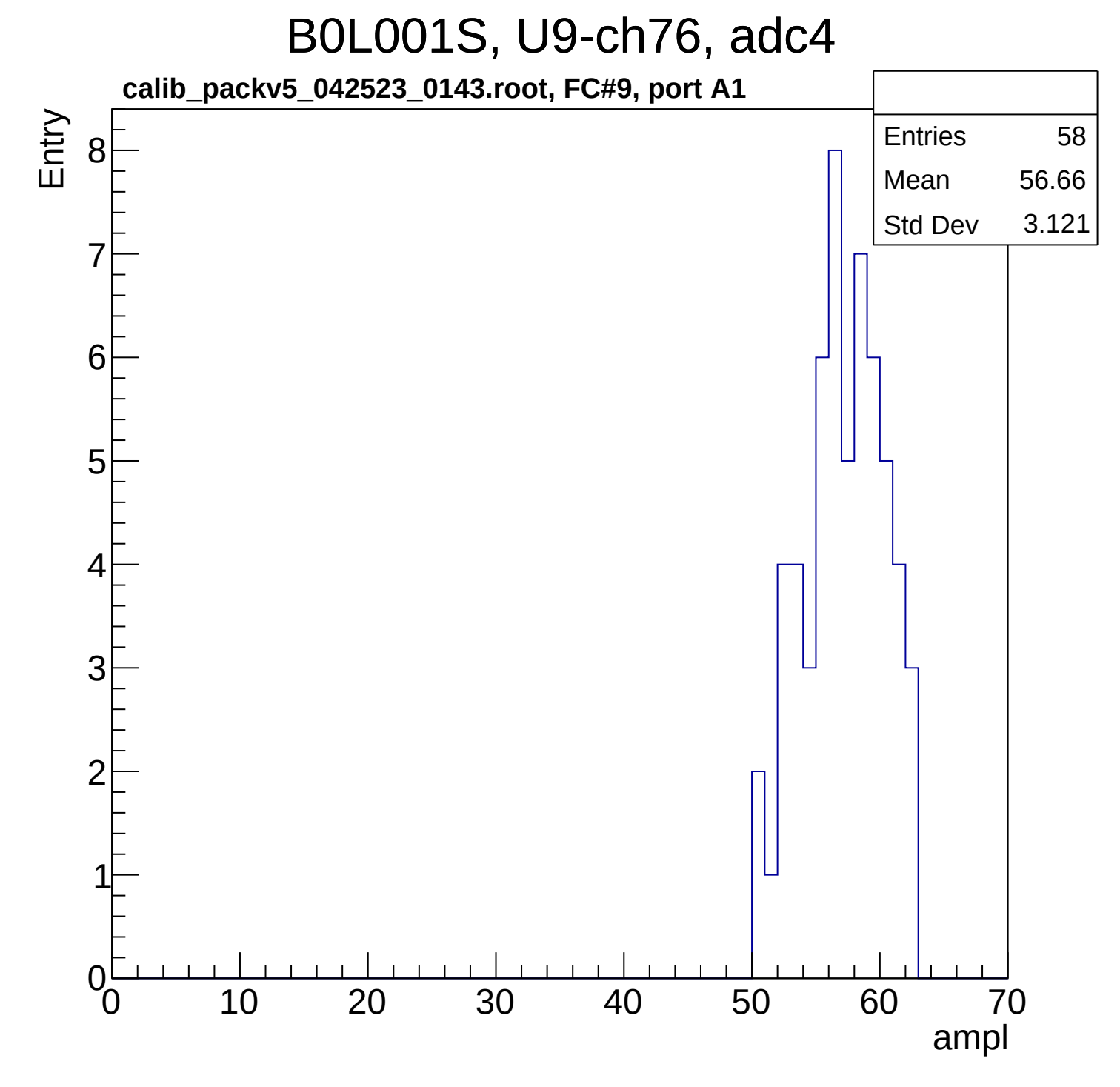
B0L001S, U9-ch76, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.66
Std Dev 3.121
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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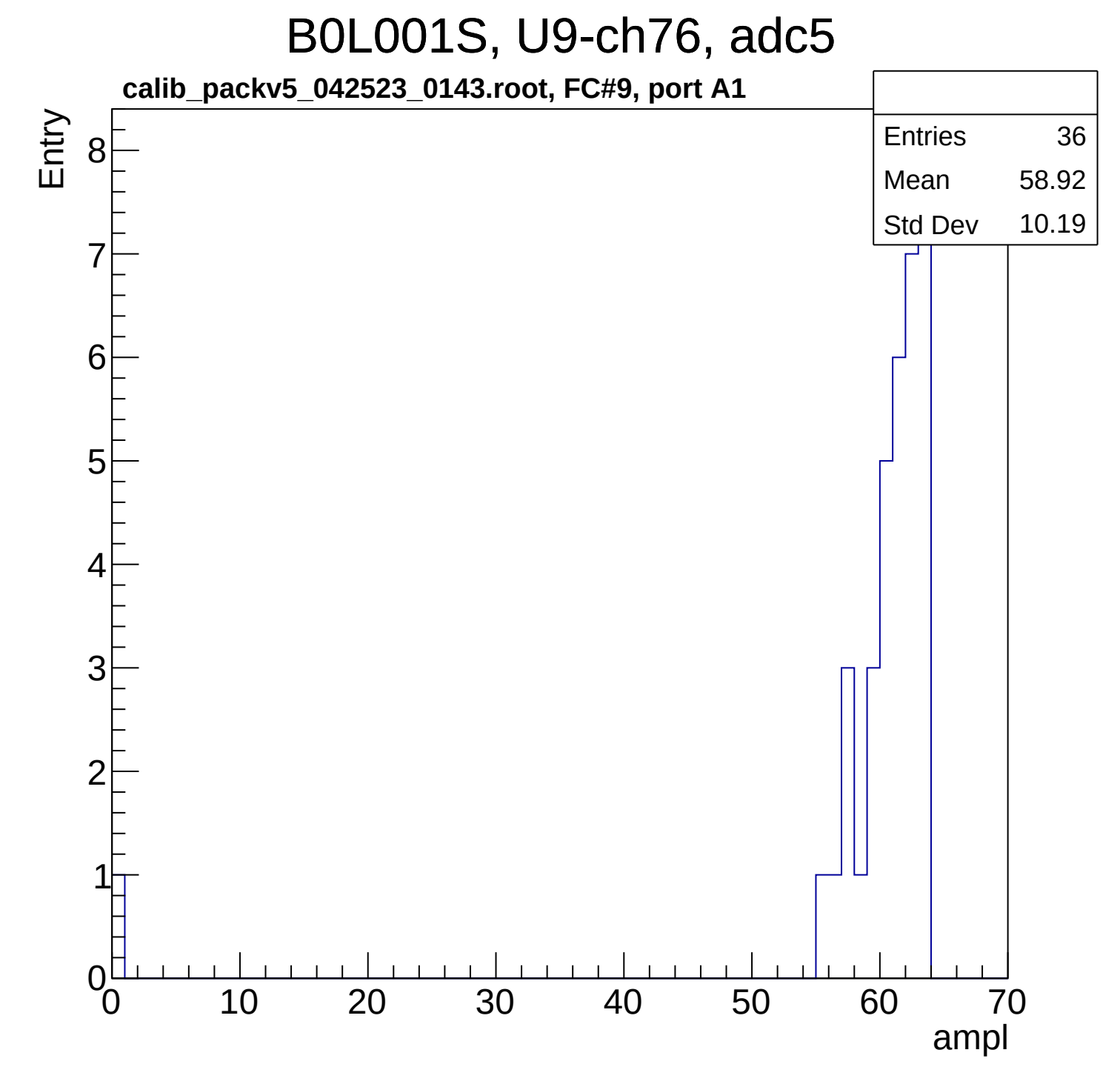
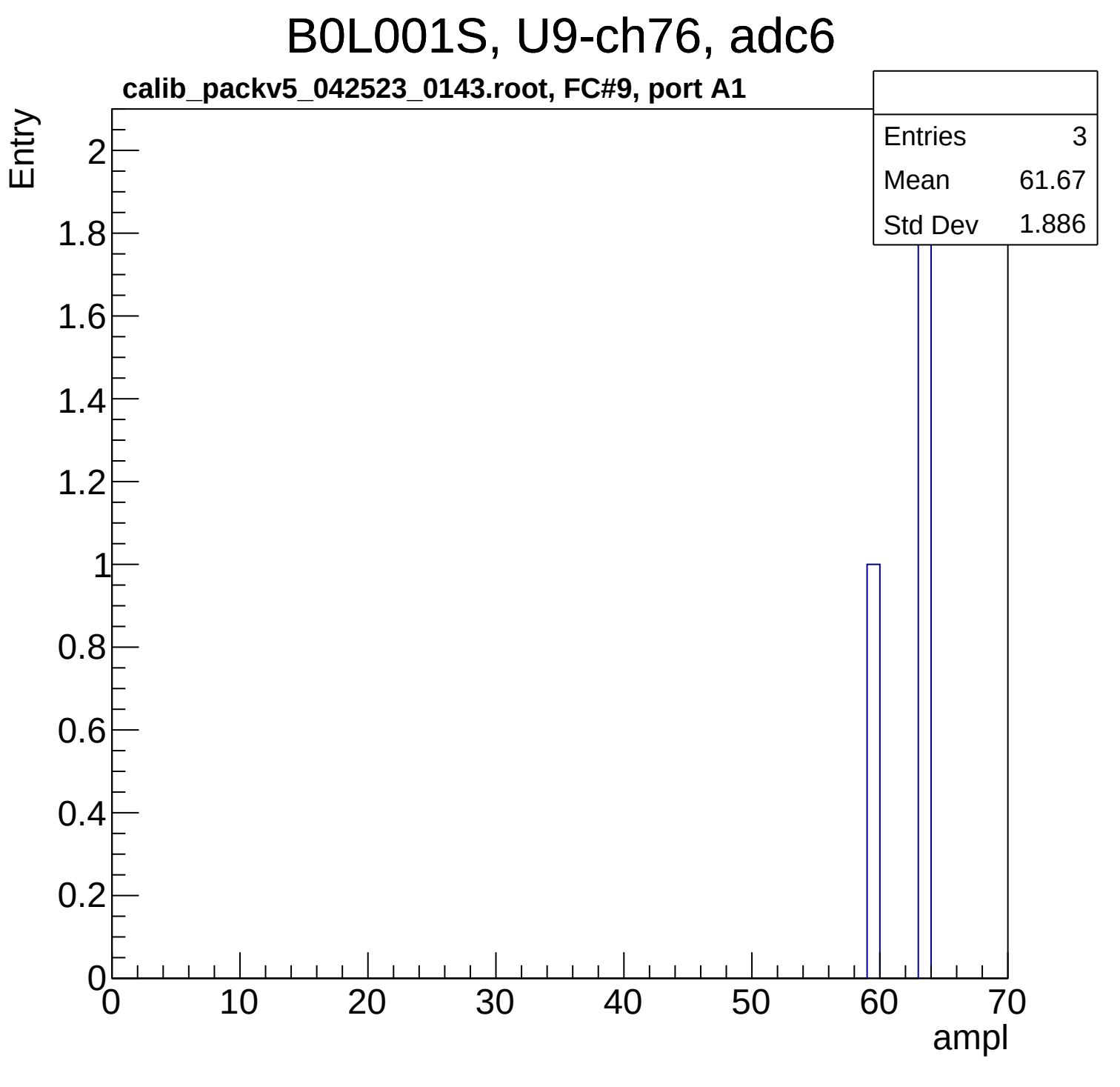
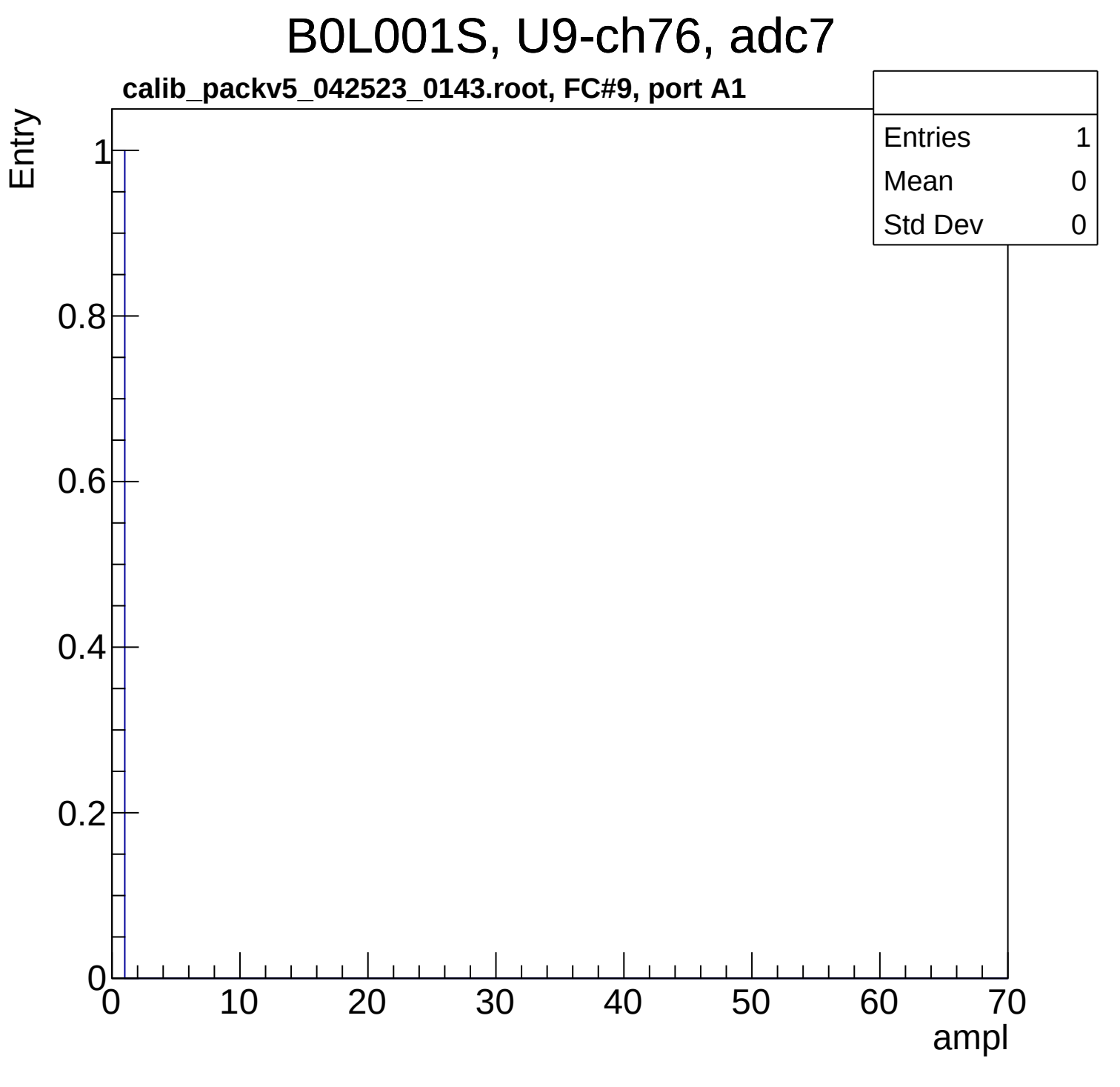

B0L001S, U9-ch76, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.92
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch76, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch76, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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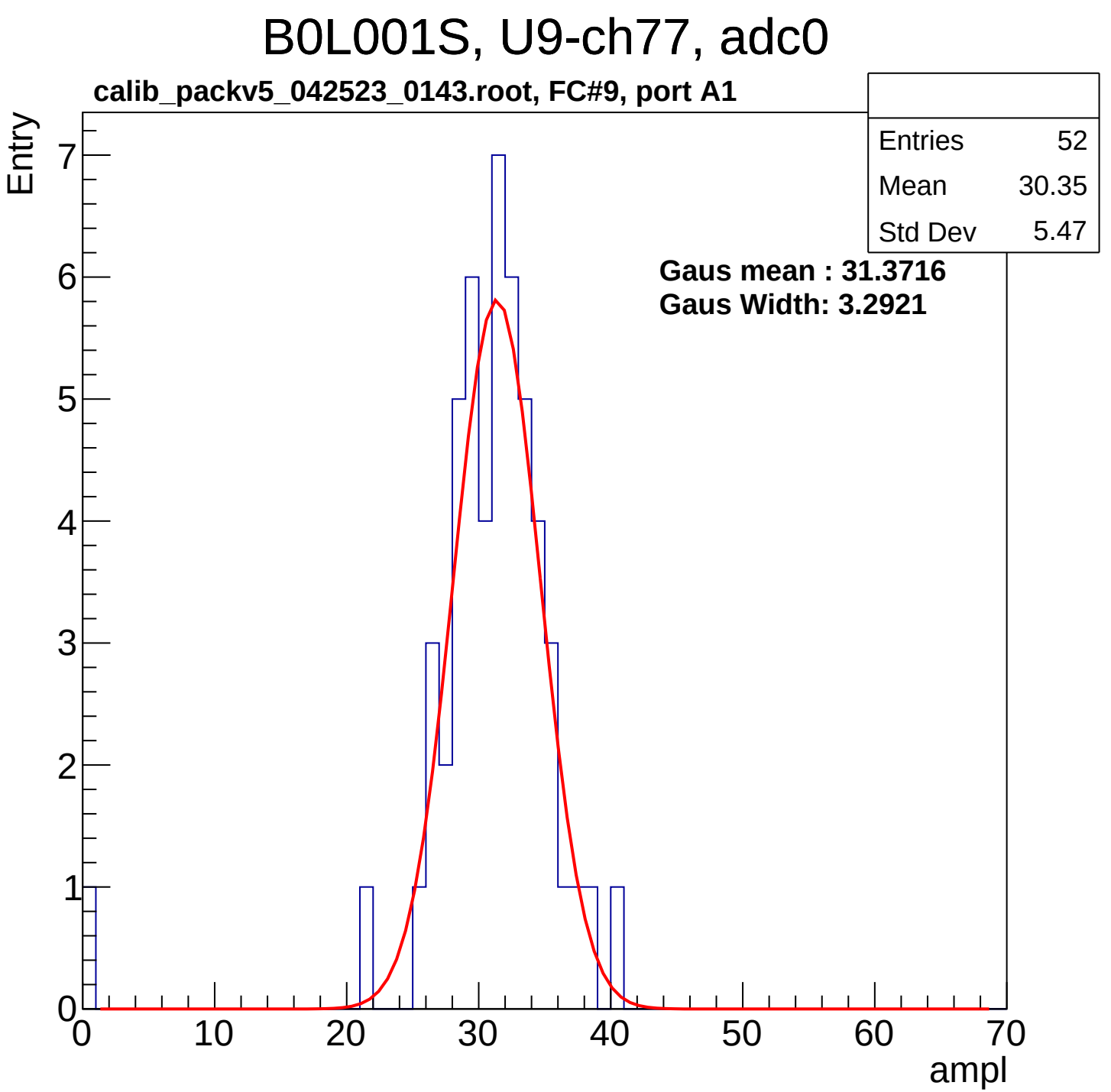
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch77, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 30.35
Std Dev 5.47
Gaus mean : 31.3716
Gaus Width: 3.2921
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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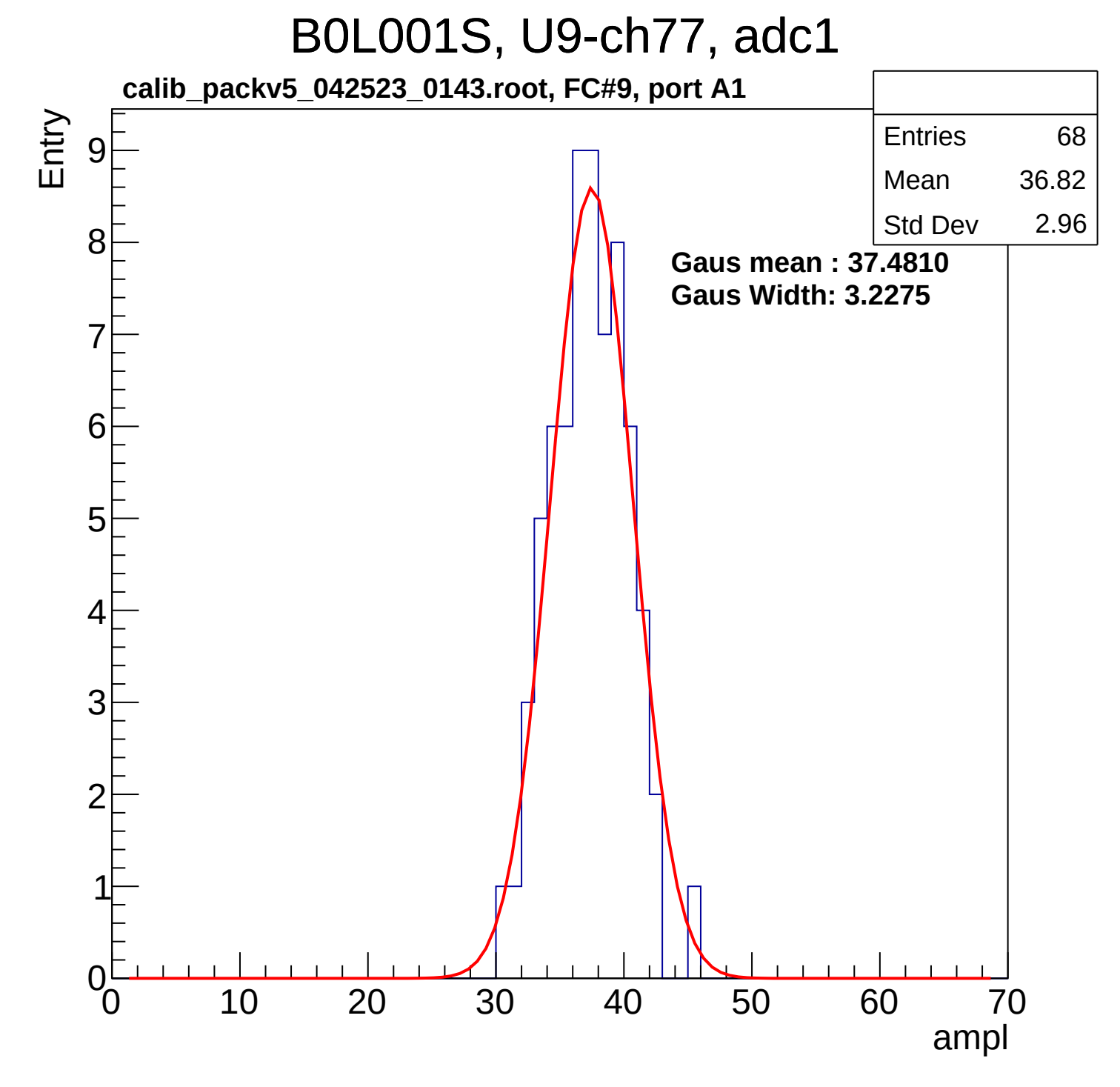
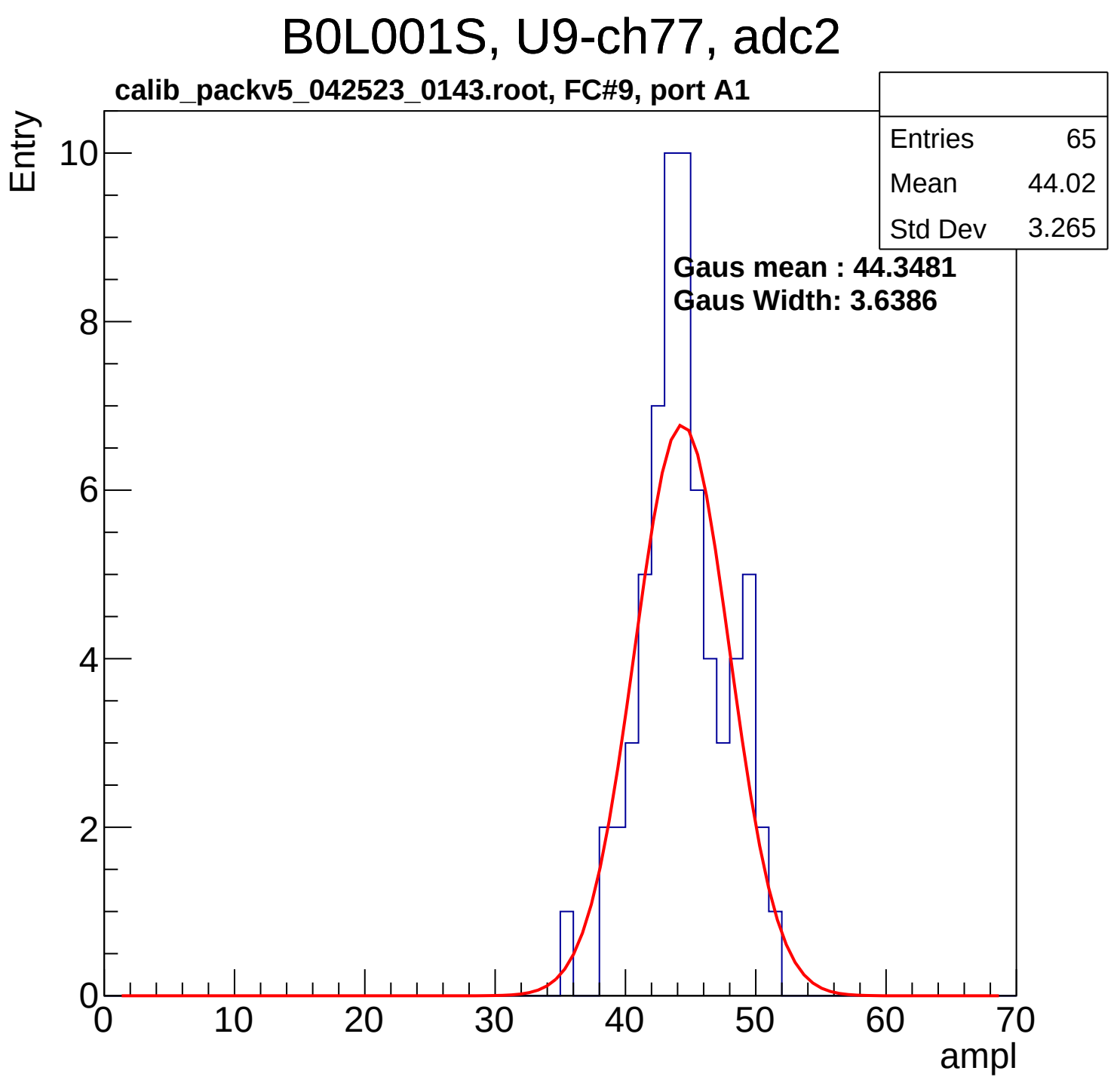

B0L001S, U9-ch77, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.82
Std Dev 2.96
Gaus mean : 37.4810
Gaus Width: 3.2275
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U9-ch77, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.02
Std Dev 3.265
Gaus mean : 44.3481
Gaus Width: 3.6386
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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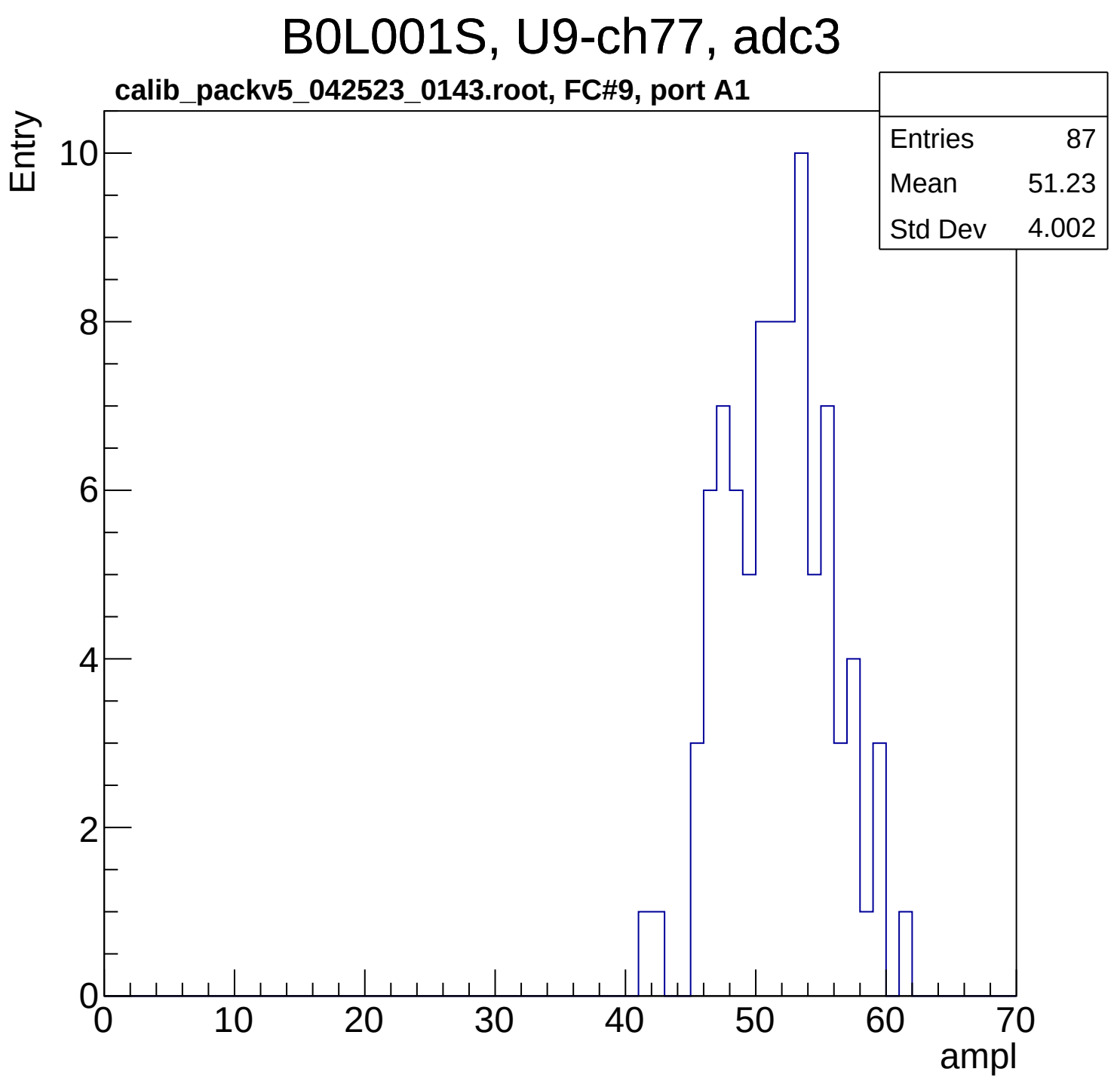
B0L001S, U9-ch77, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 51.23
Std Dev 4.002
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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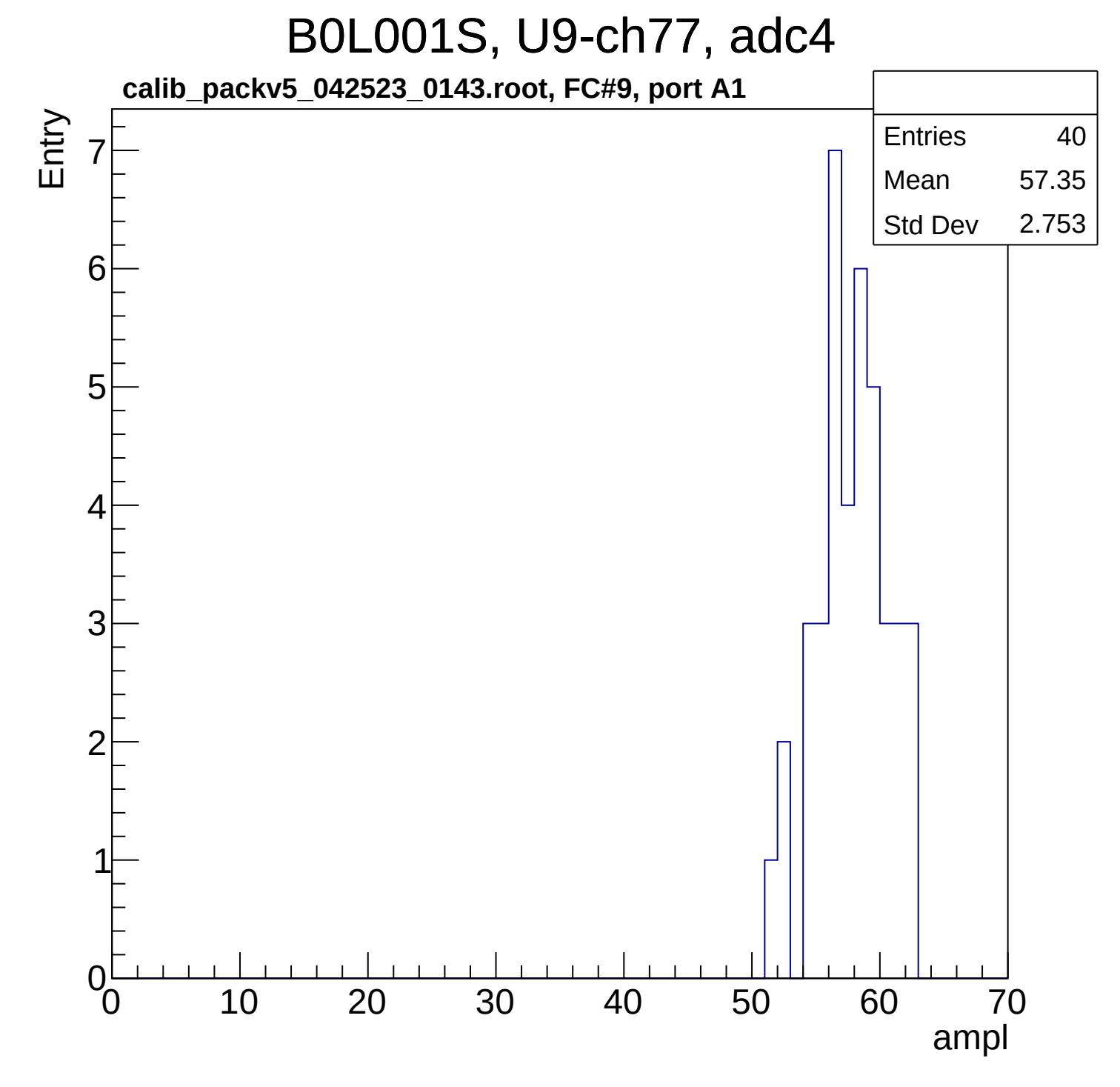
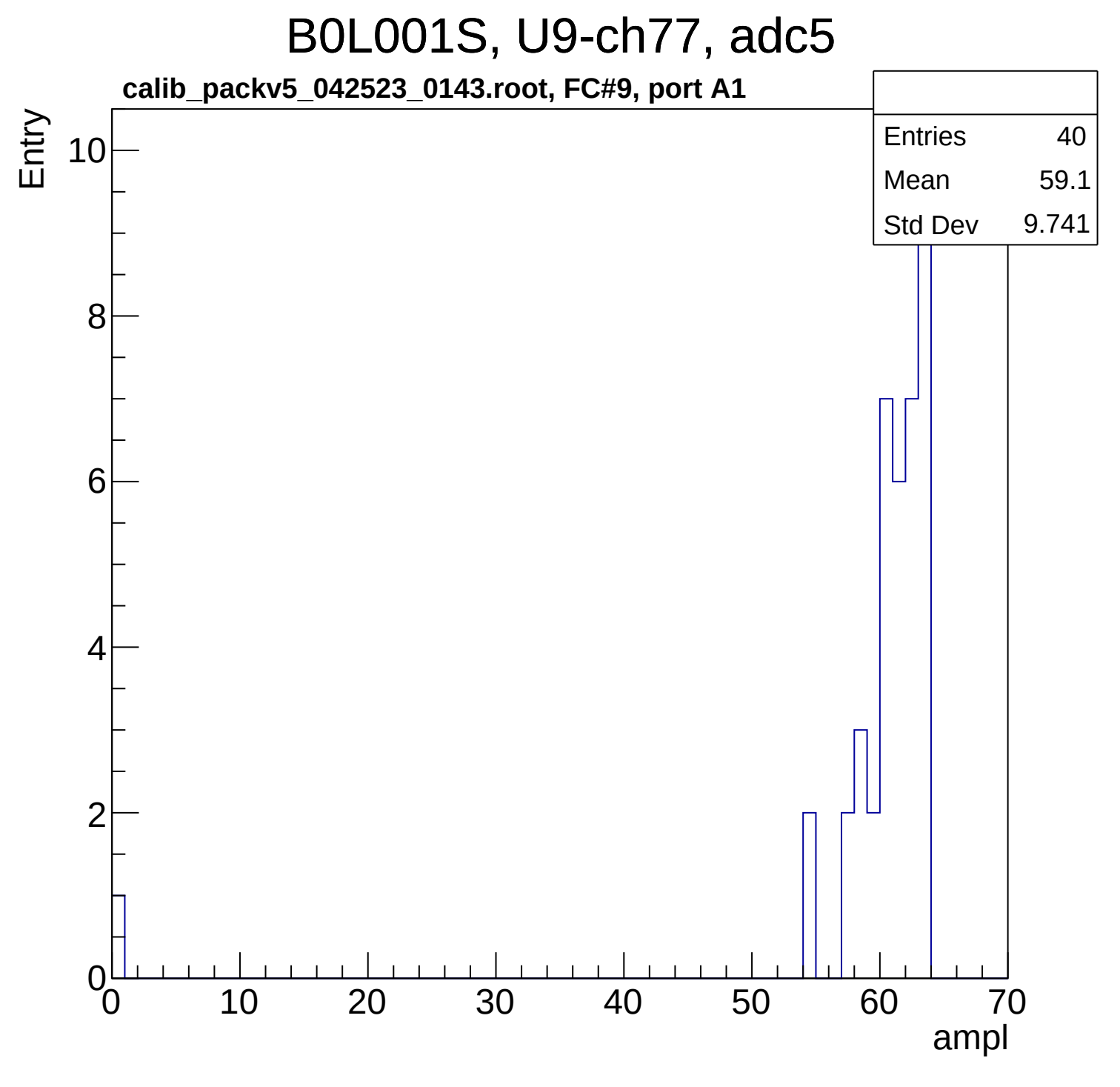
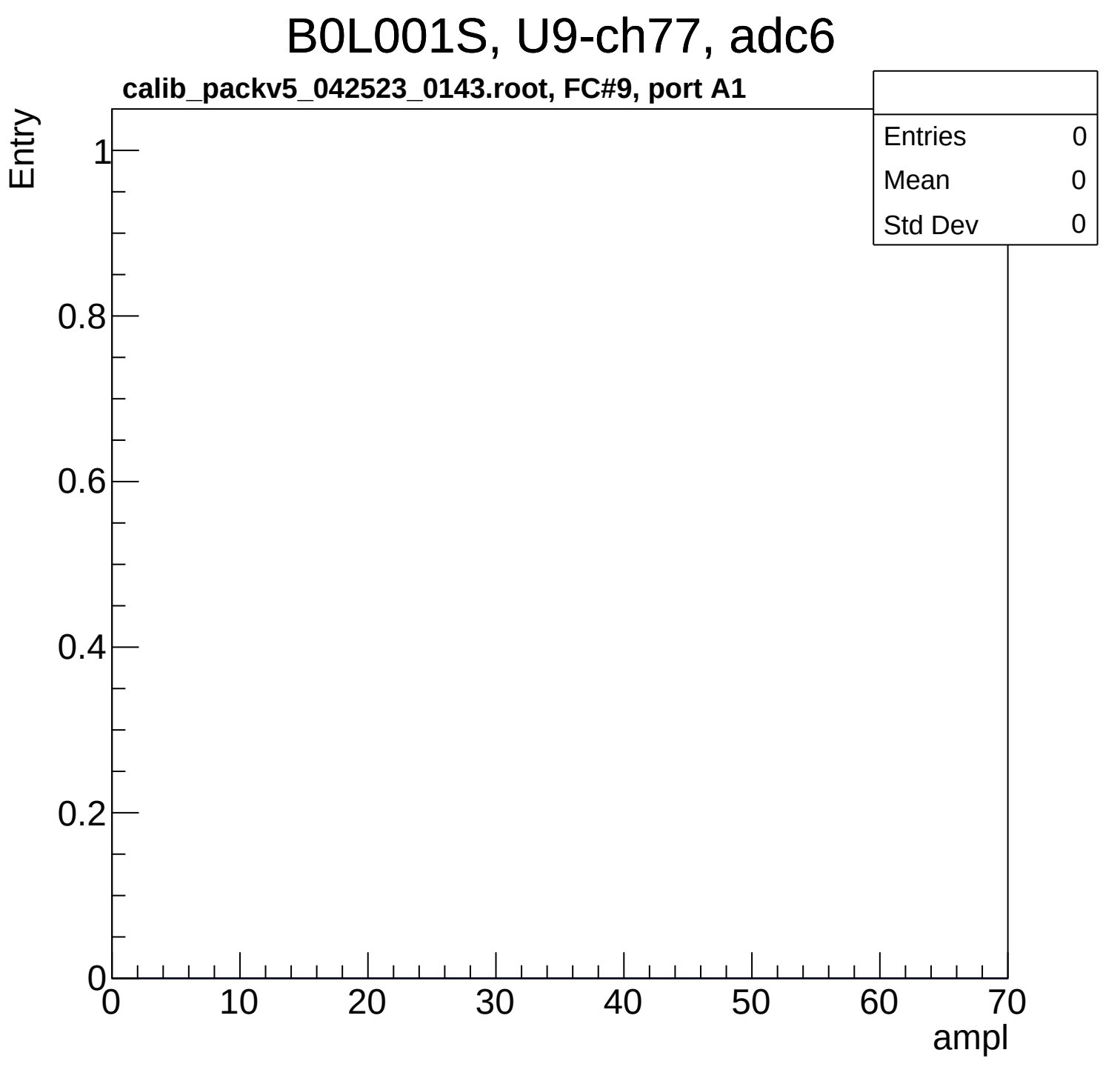
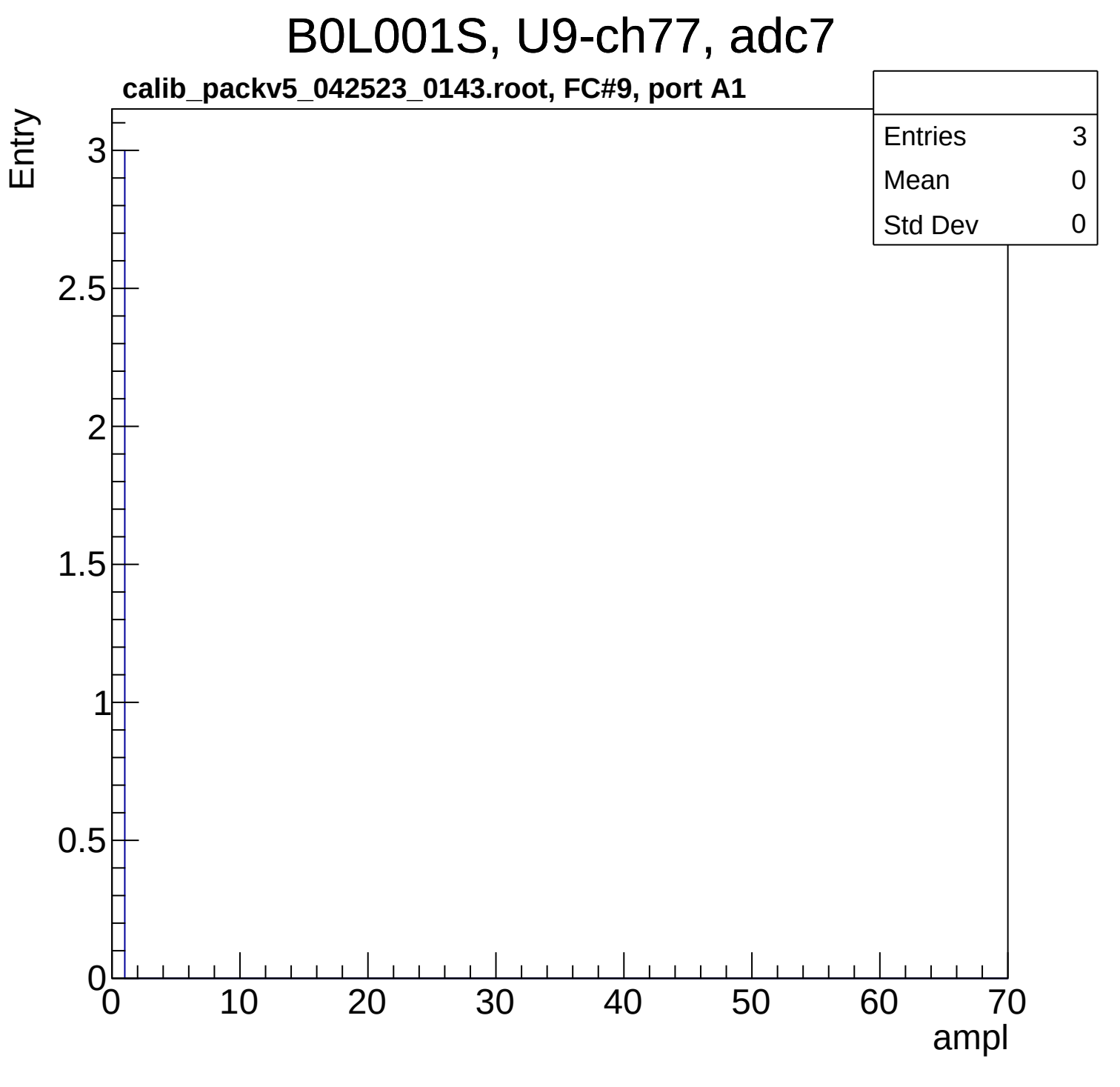
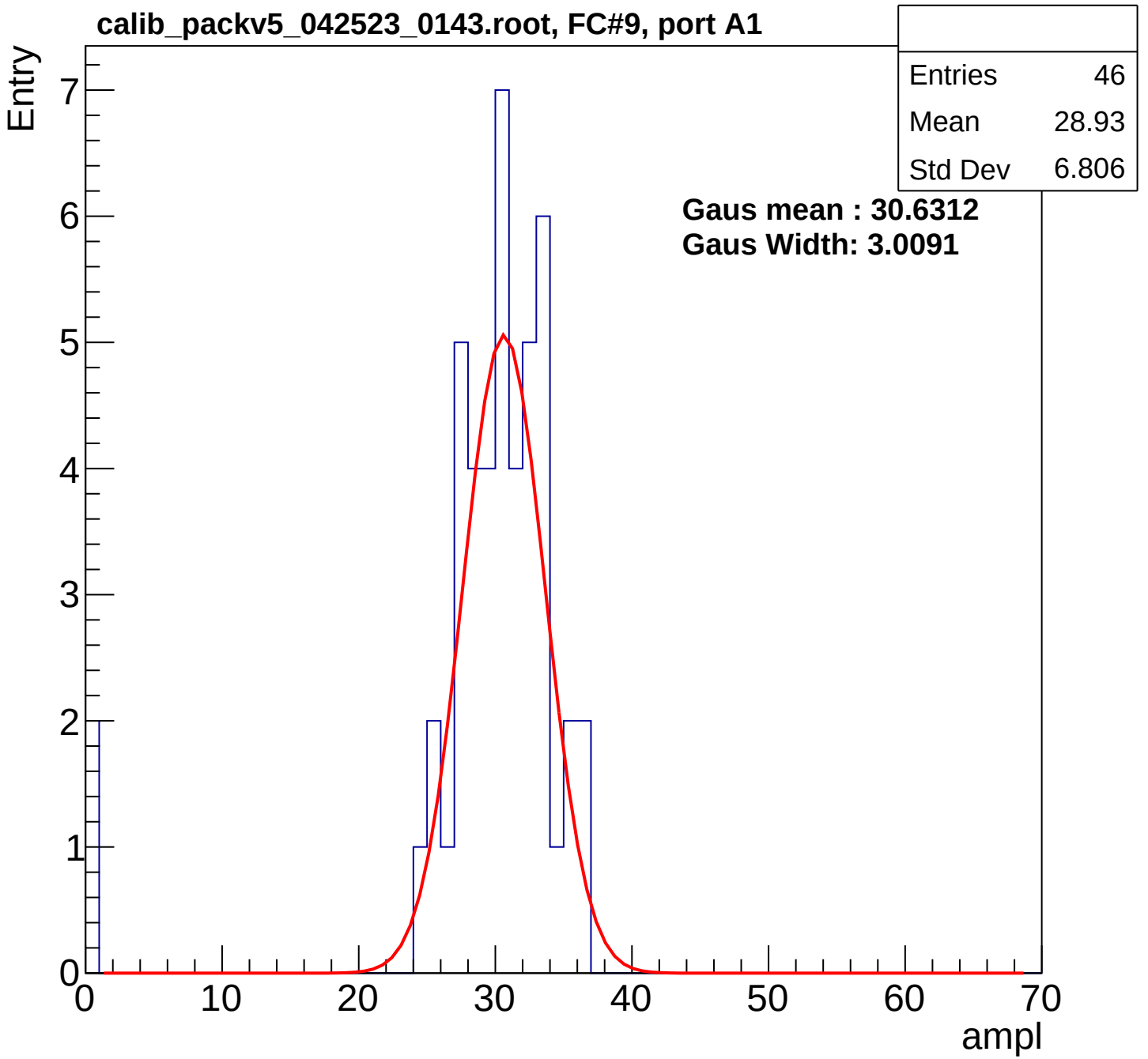
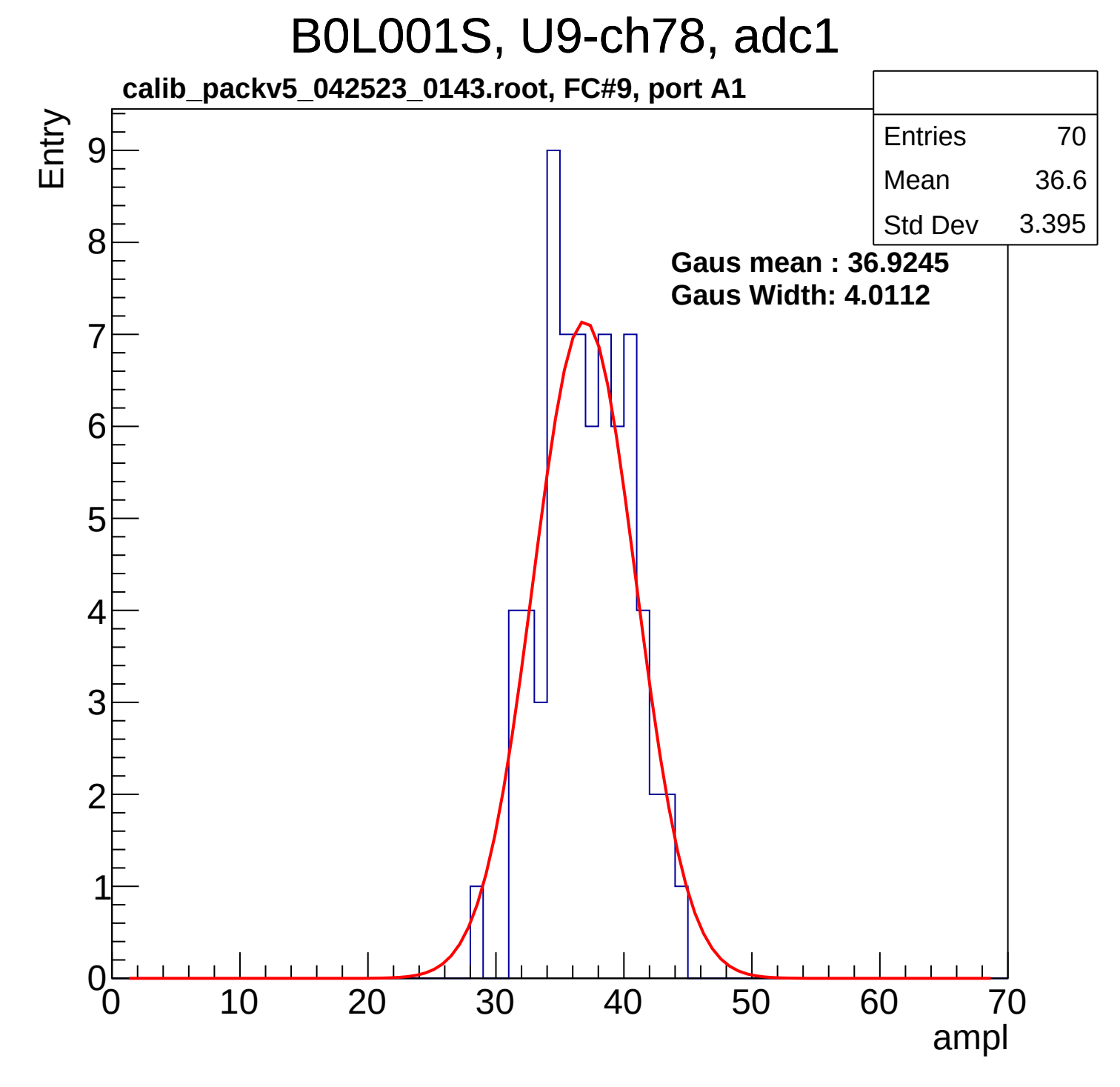

B0L001S, U9-ch77, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 57.35
Std Dev 2.753
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch77, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.1
Std Dev 9.741
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch77, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch77, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch78, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 28.93
Std Dev 6.806
Gaus mean : 30.6312
Gaus Width: 3.0091
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch78, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.6
Std Dev 3.395
Gaus mean : 36.9245
Gaus Width: 4.0112
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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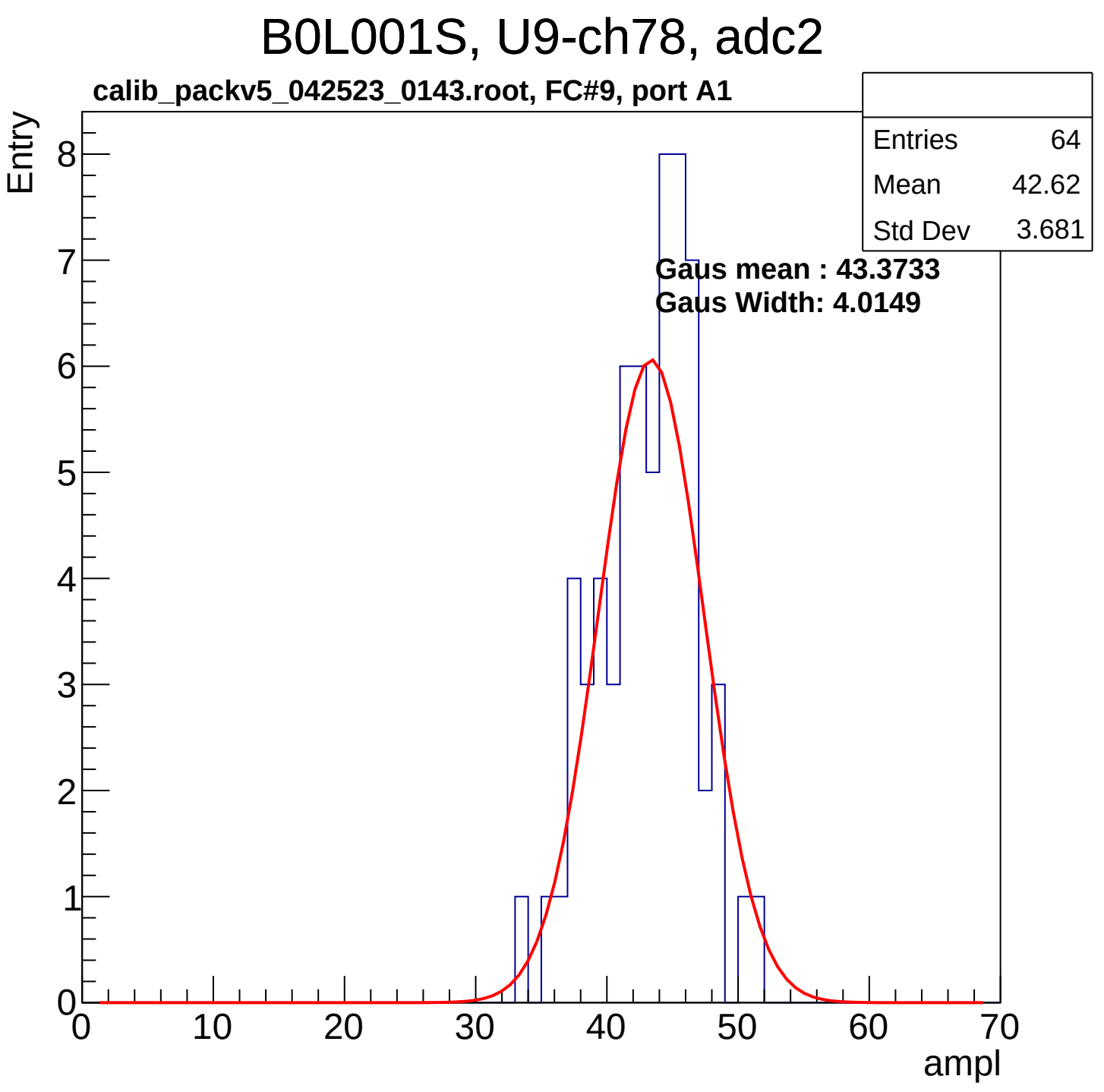
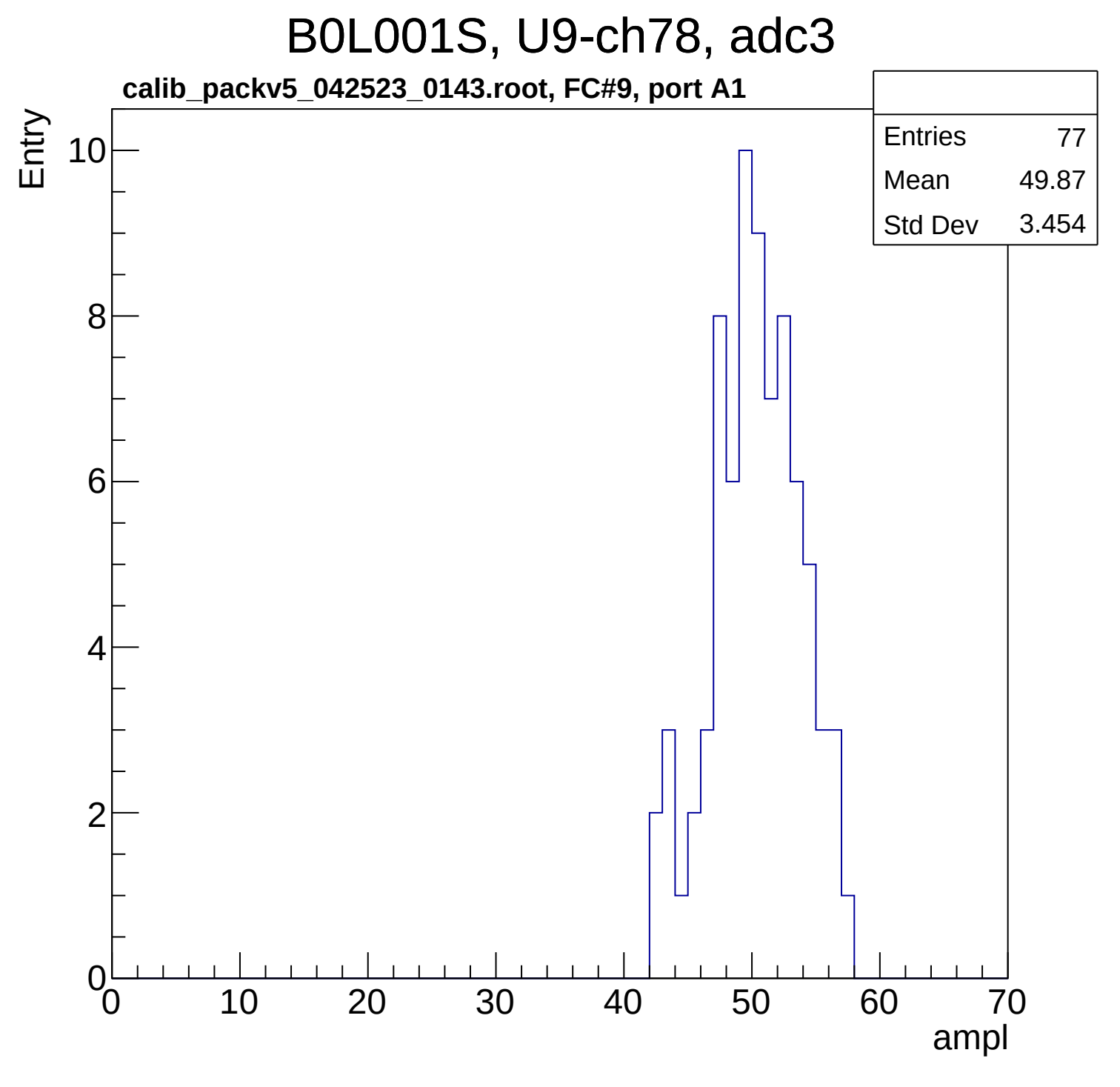
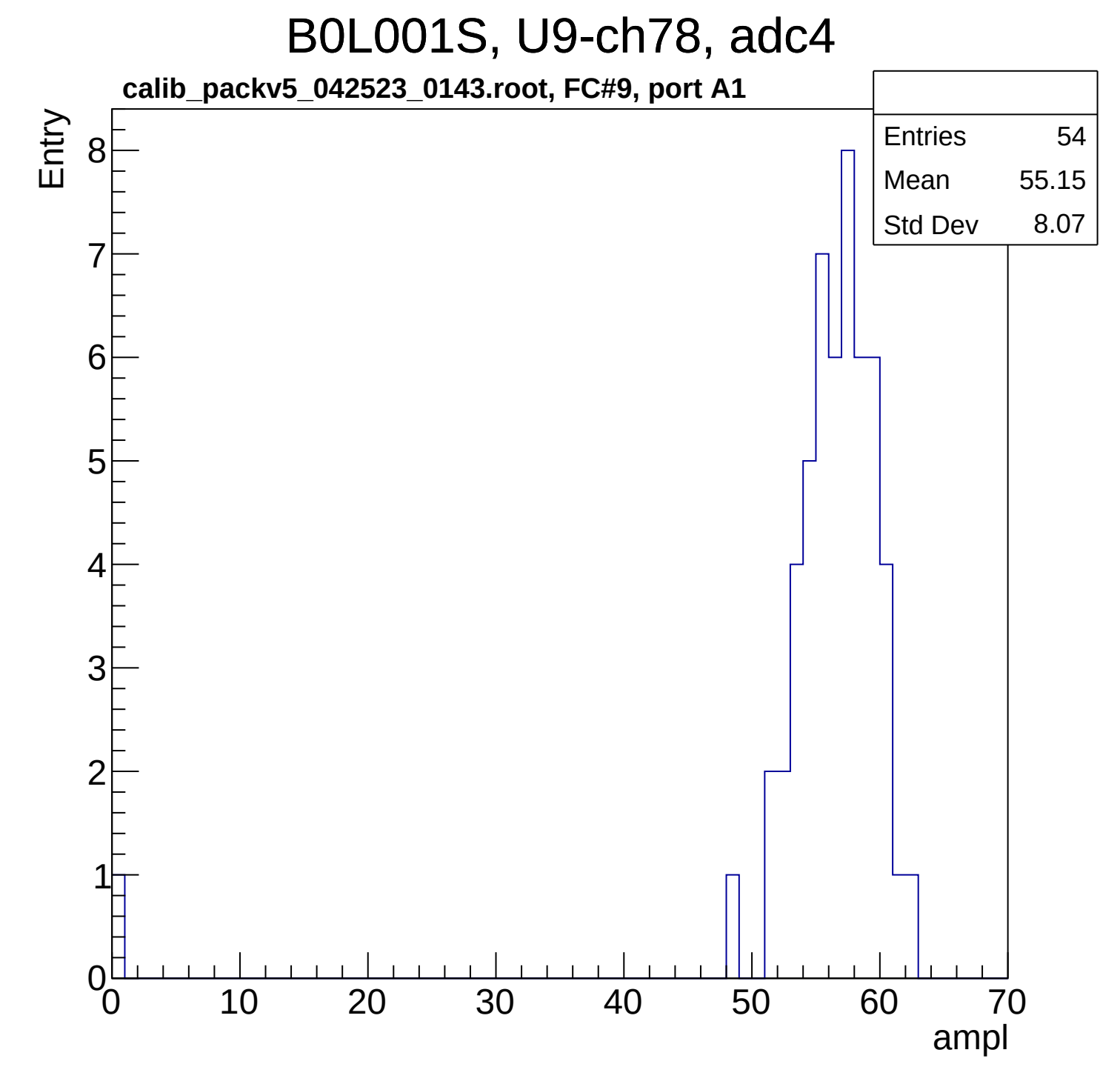
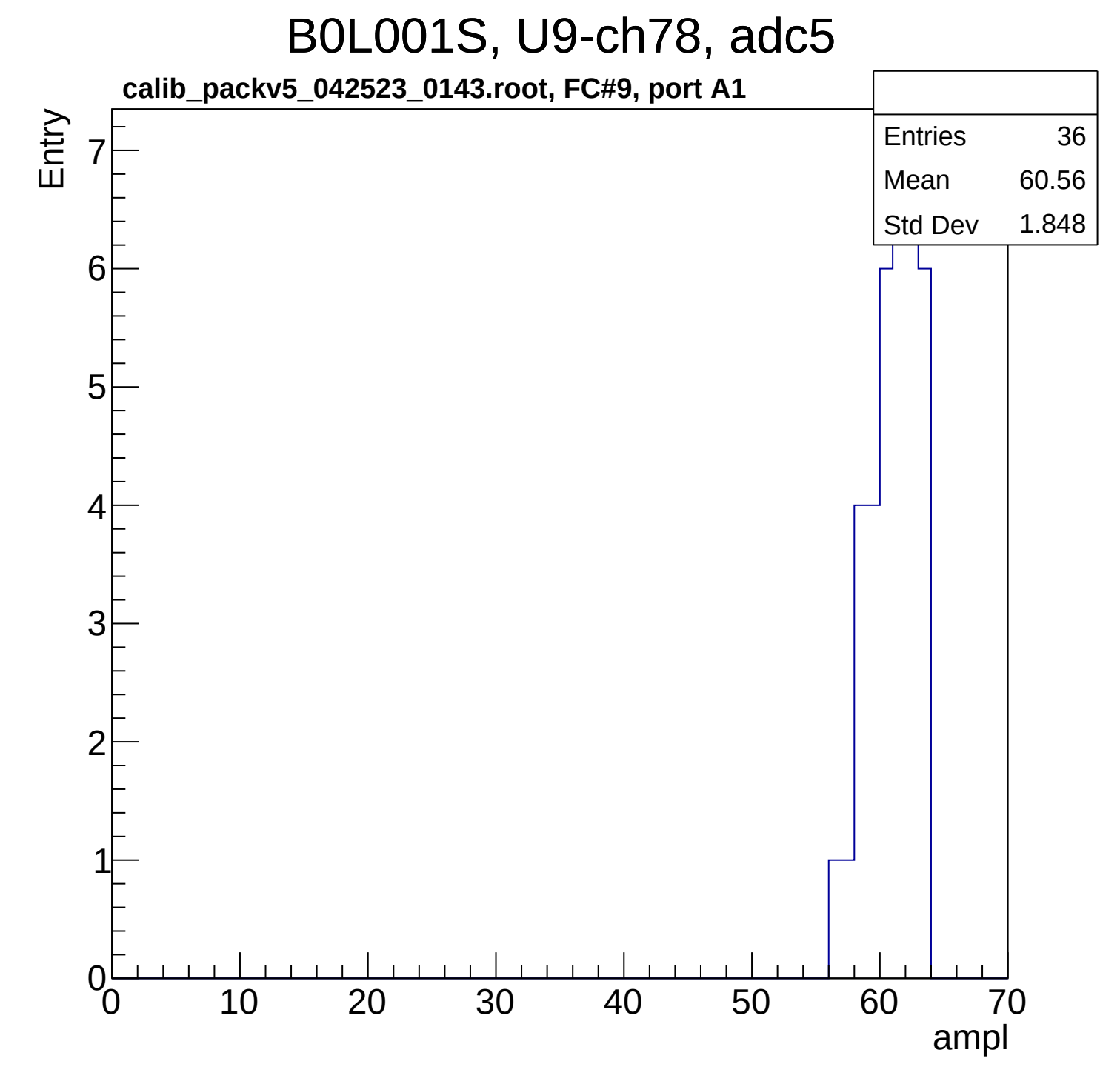
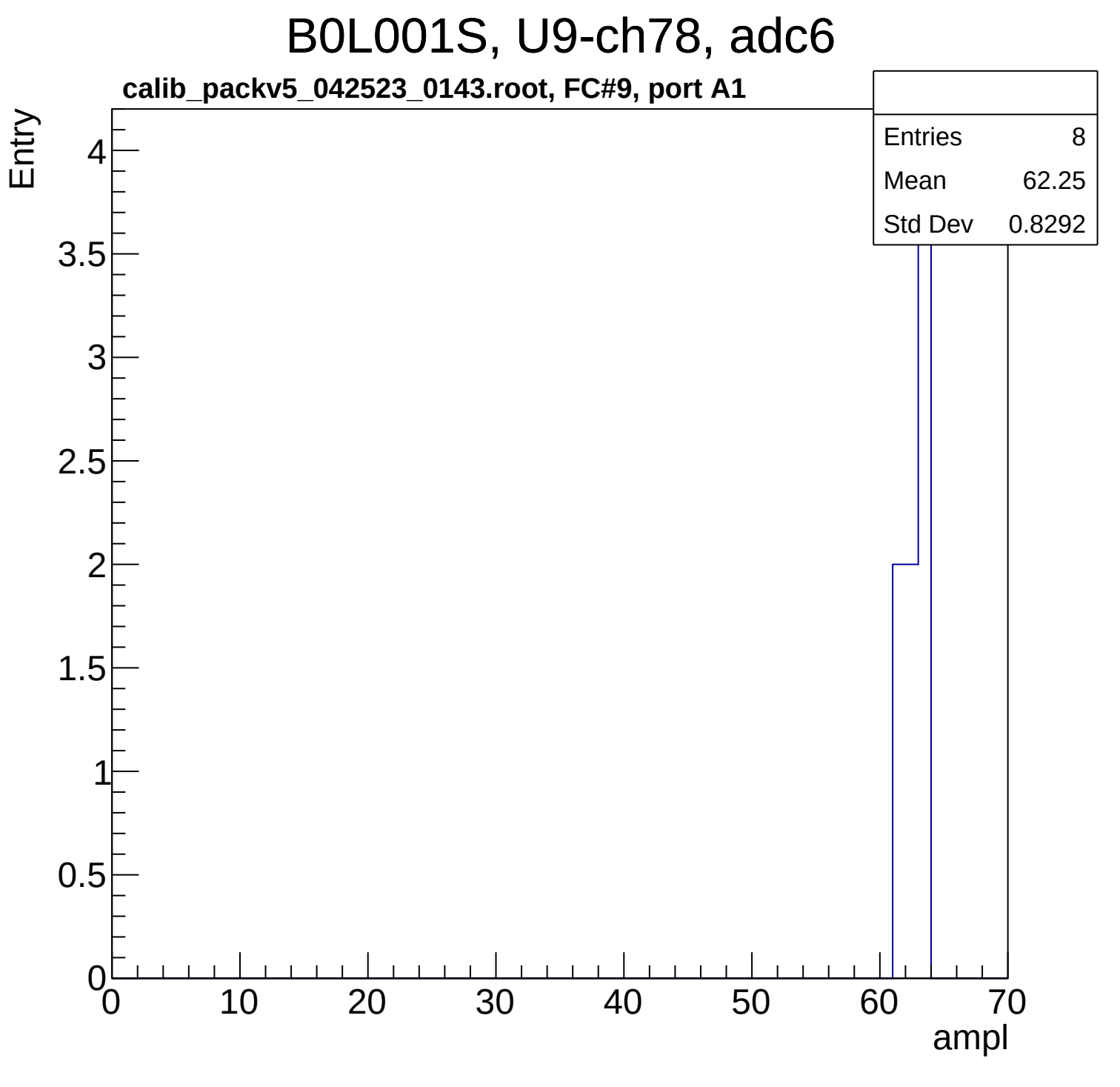
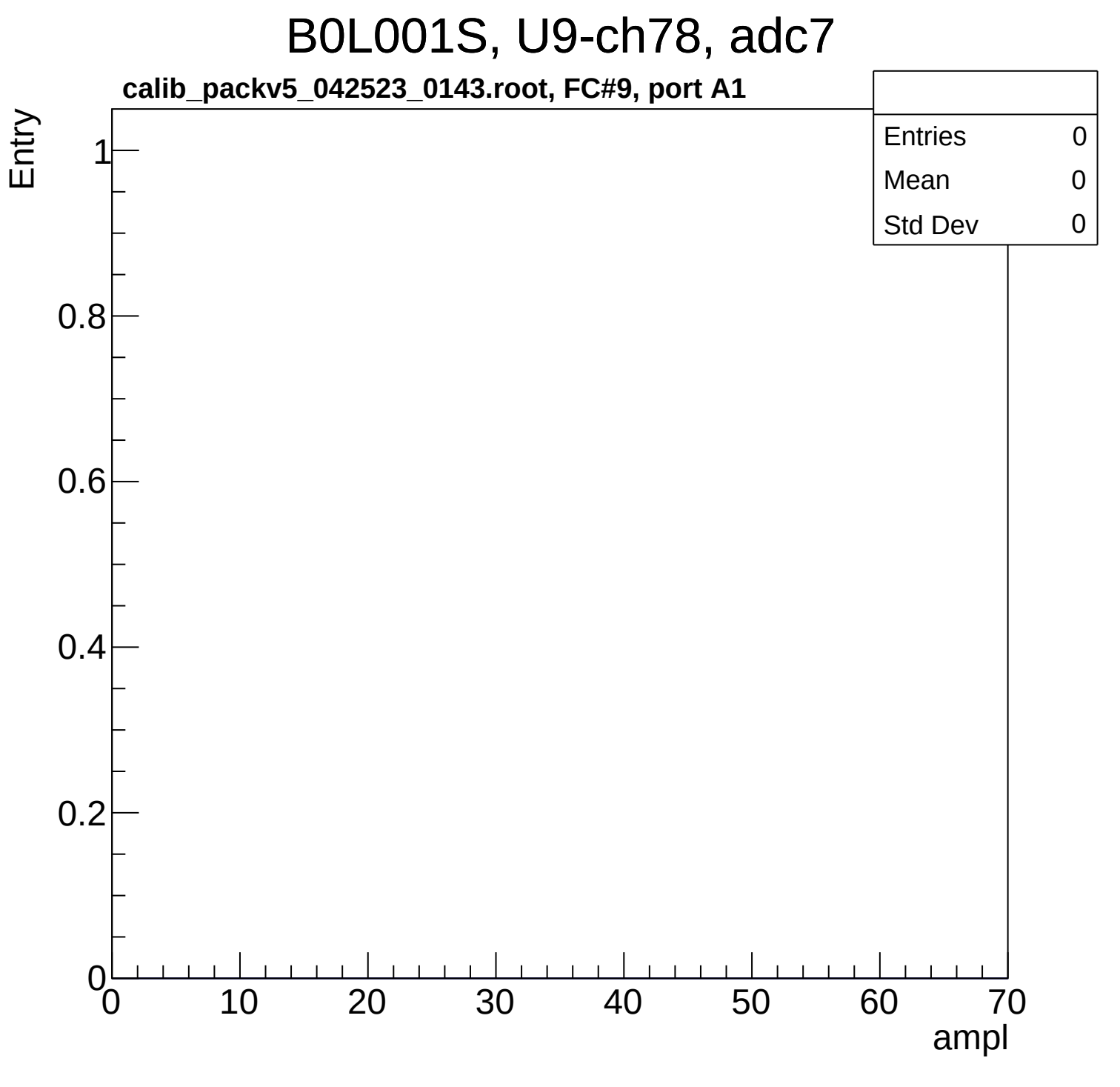

B0L001S, U9-ch78, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 42.62
Std Dev 3.681
Gaus mean : 43.3733
Gaus Width: 4.0149
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

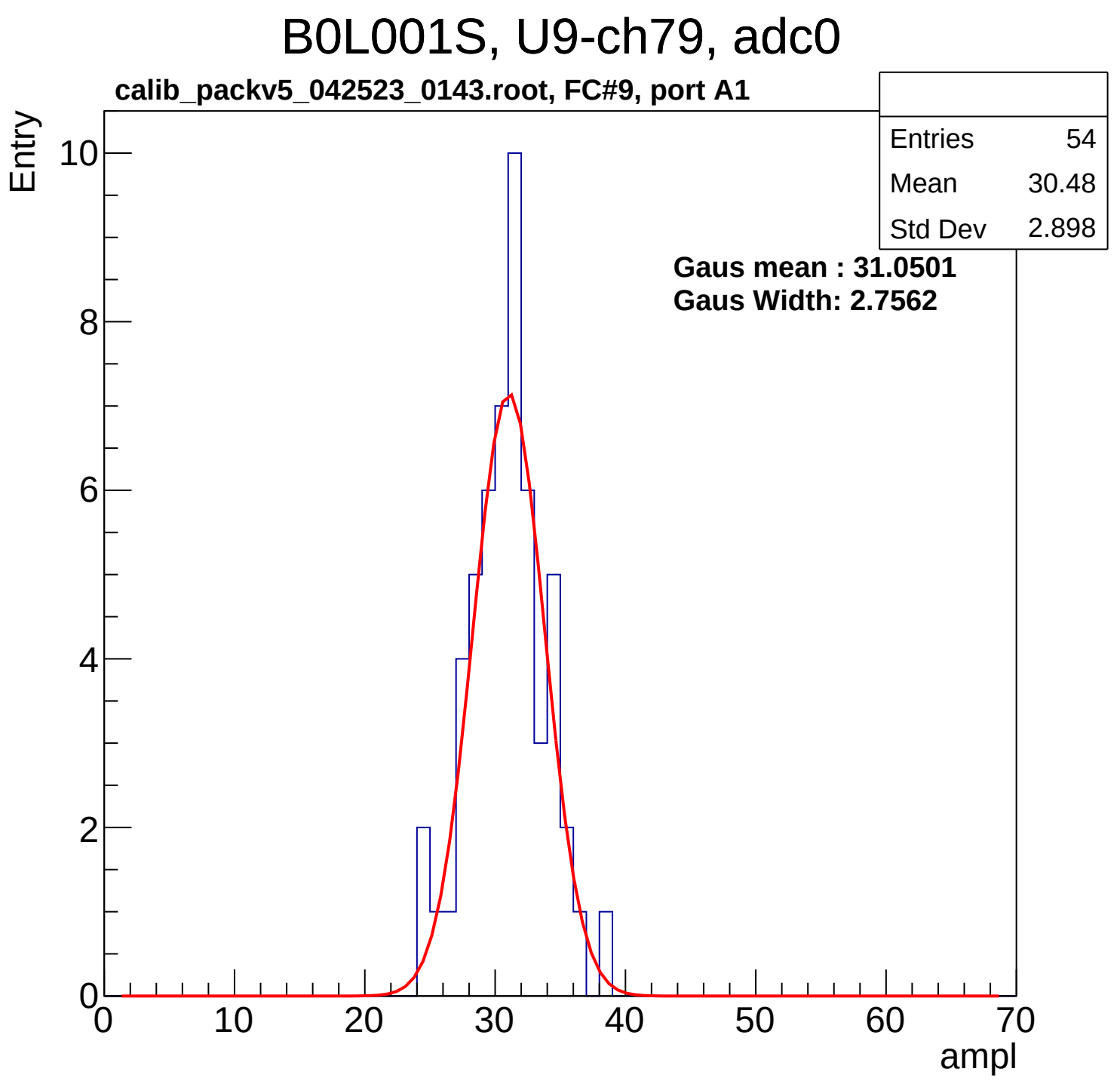

B0L001S, U9-ch78, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 49.87
Std Dev 3.454
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

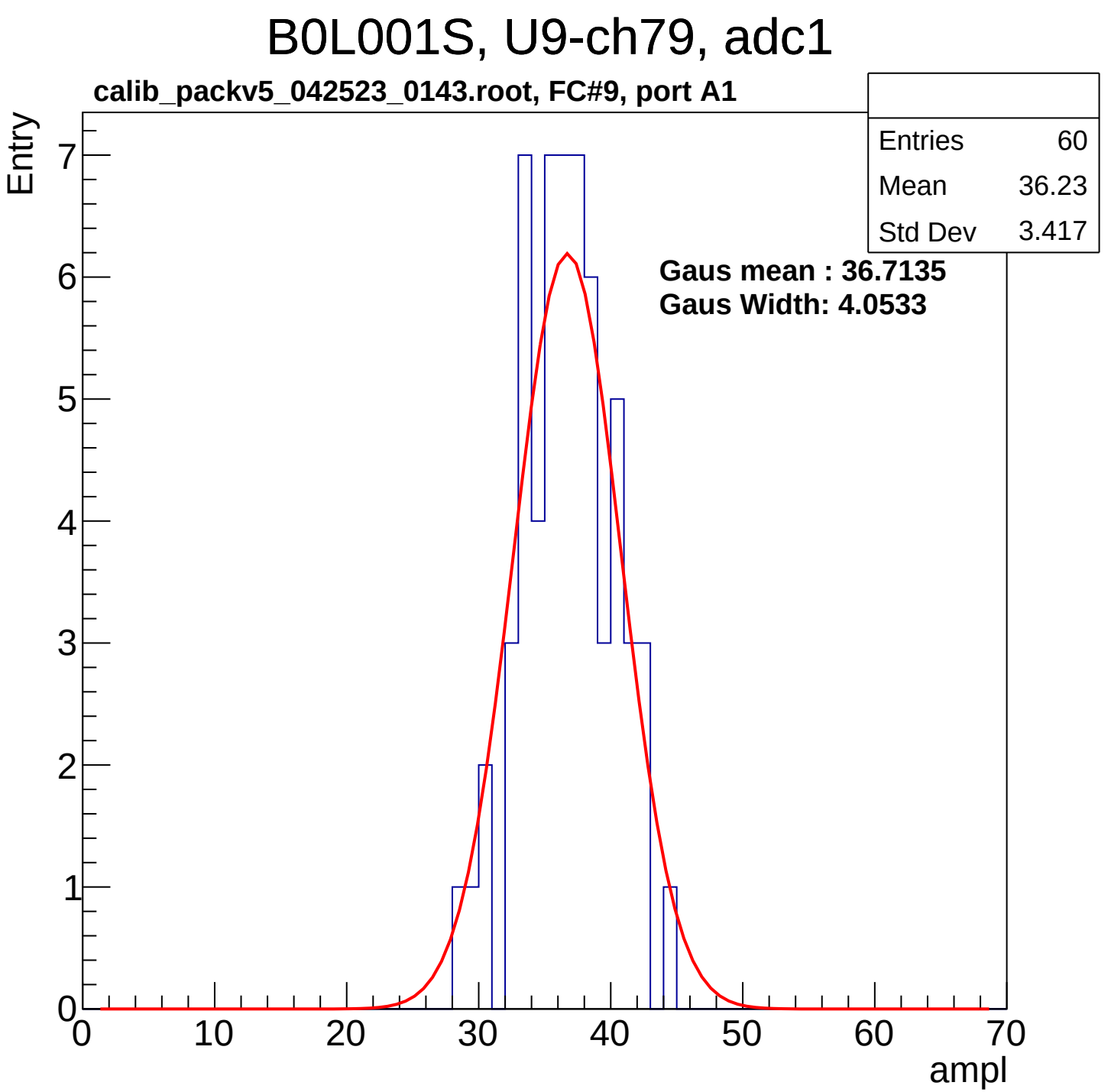

B0L001S, U9-ch78, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 55.15
Std Dev 8.07
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

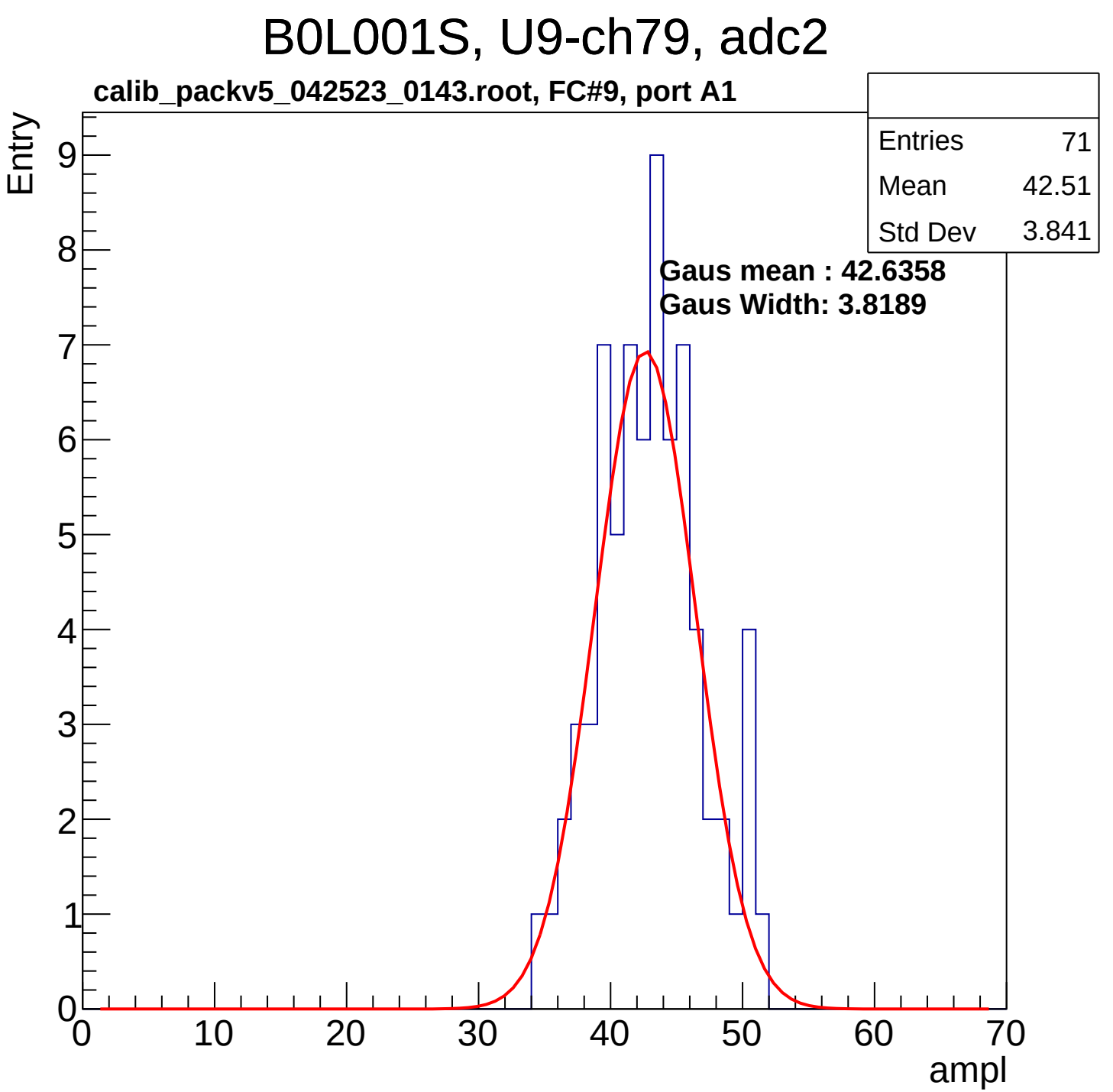

B0L001S, U9-ch78, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 60.56
Std Dev 1.848
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch78, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U9-ch78, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch79, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 30.48
Std Dev 2.898
Gaus mean : 31.0501
Gaus Width: 2.7562
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch79, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 36.23
Std Dev 3.417
Gaus mean : 36.7135
Gaus Width: 4.0533
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch79, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 42.51
Std Dev 3.841
Gaus mean : 42.6358
Gaus Width: 3.8189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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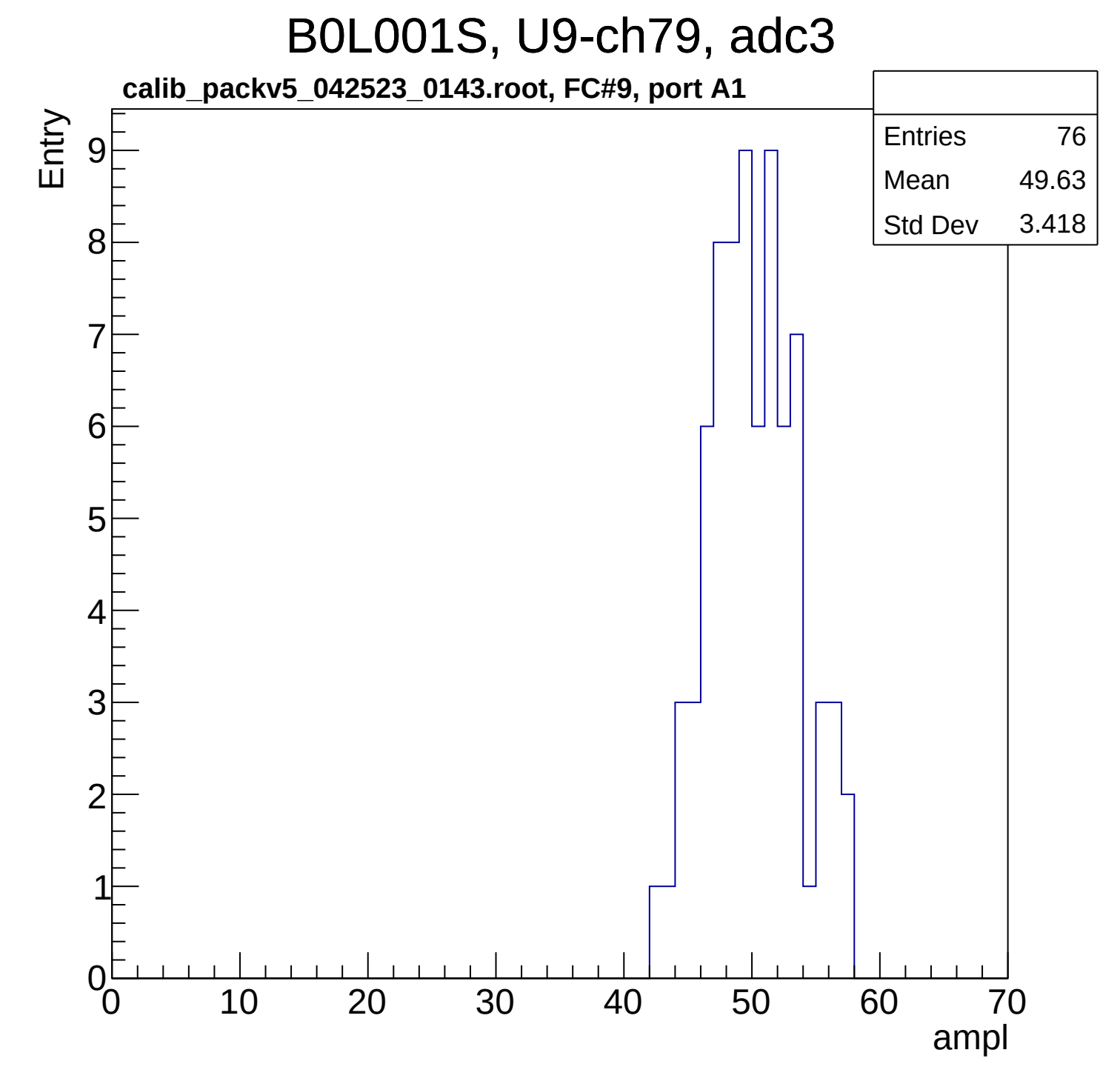
B0L001S, U9-ch79, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 49.63
Std Dev 3.418
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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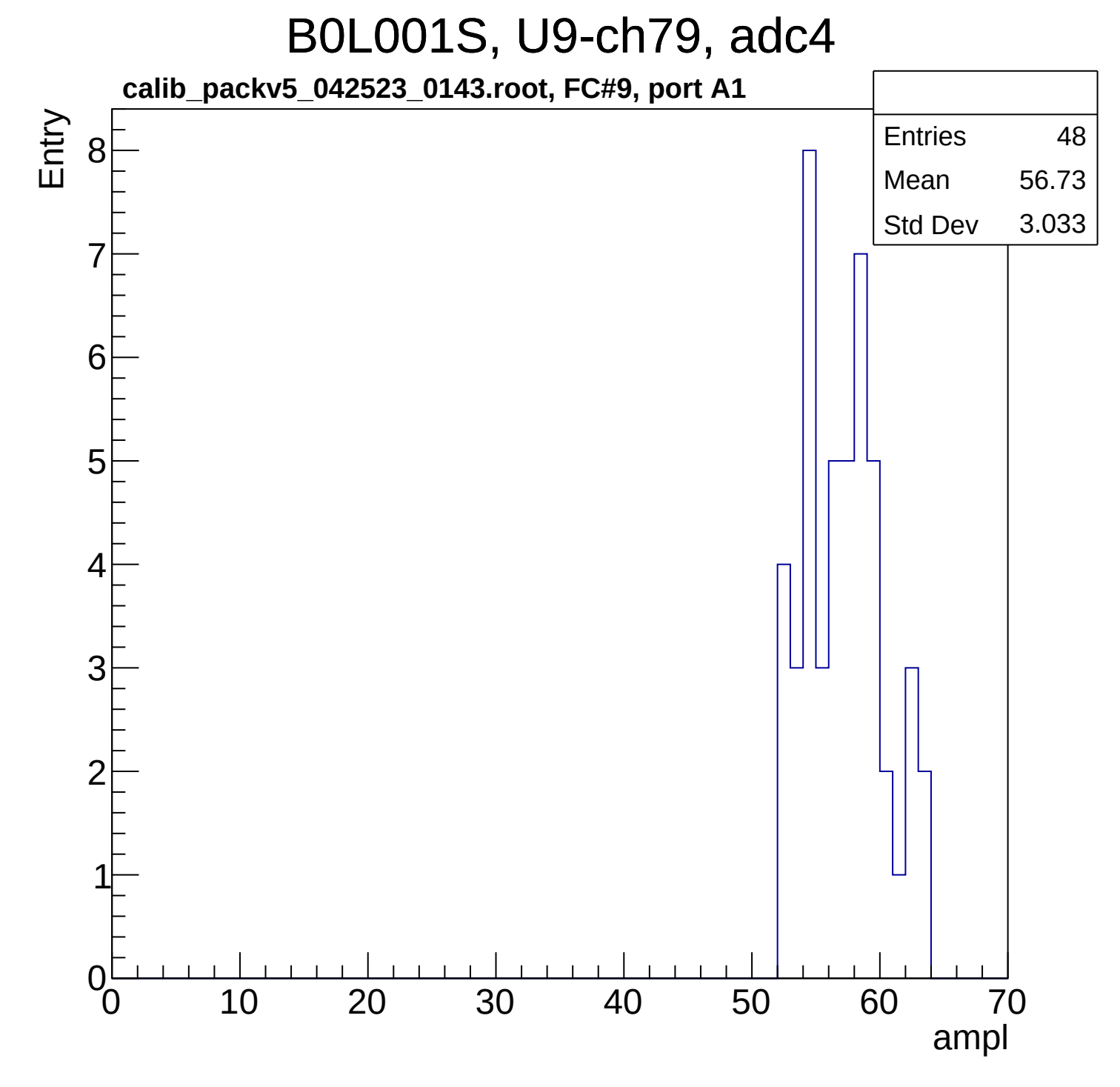
B0L001S, U9-ch79, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 56.73
Std Dev 3.033
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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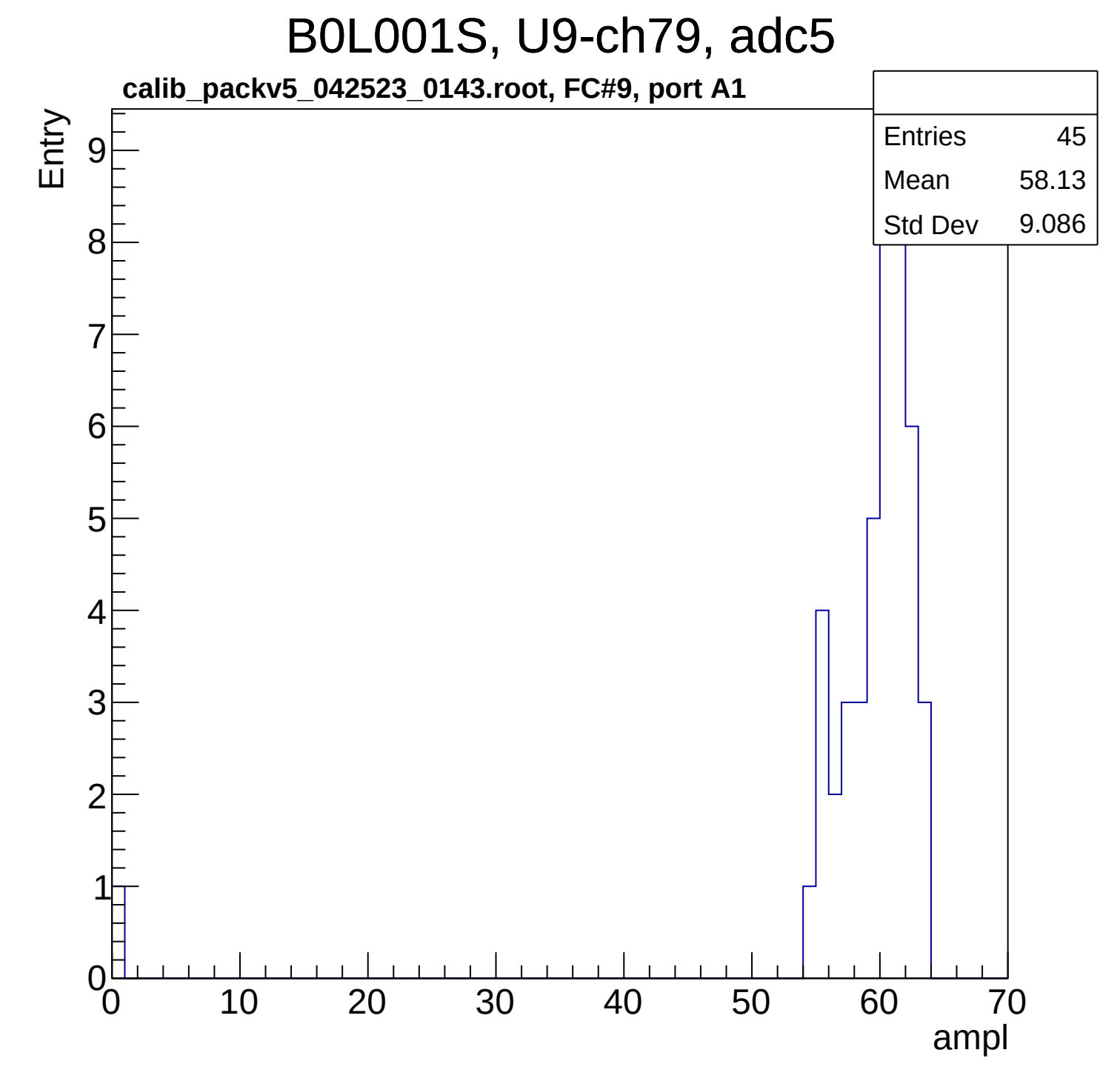
B0L001S, U9-ch79, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.13
Std Dev 9.086
Entry
ampl
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3
4
5
6
7
8
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0
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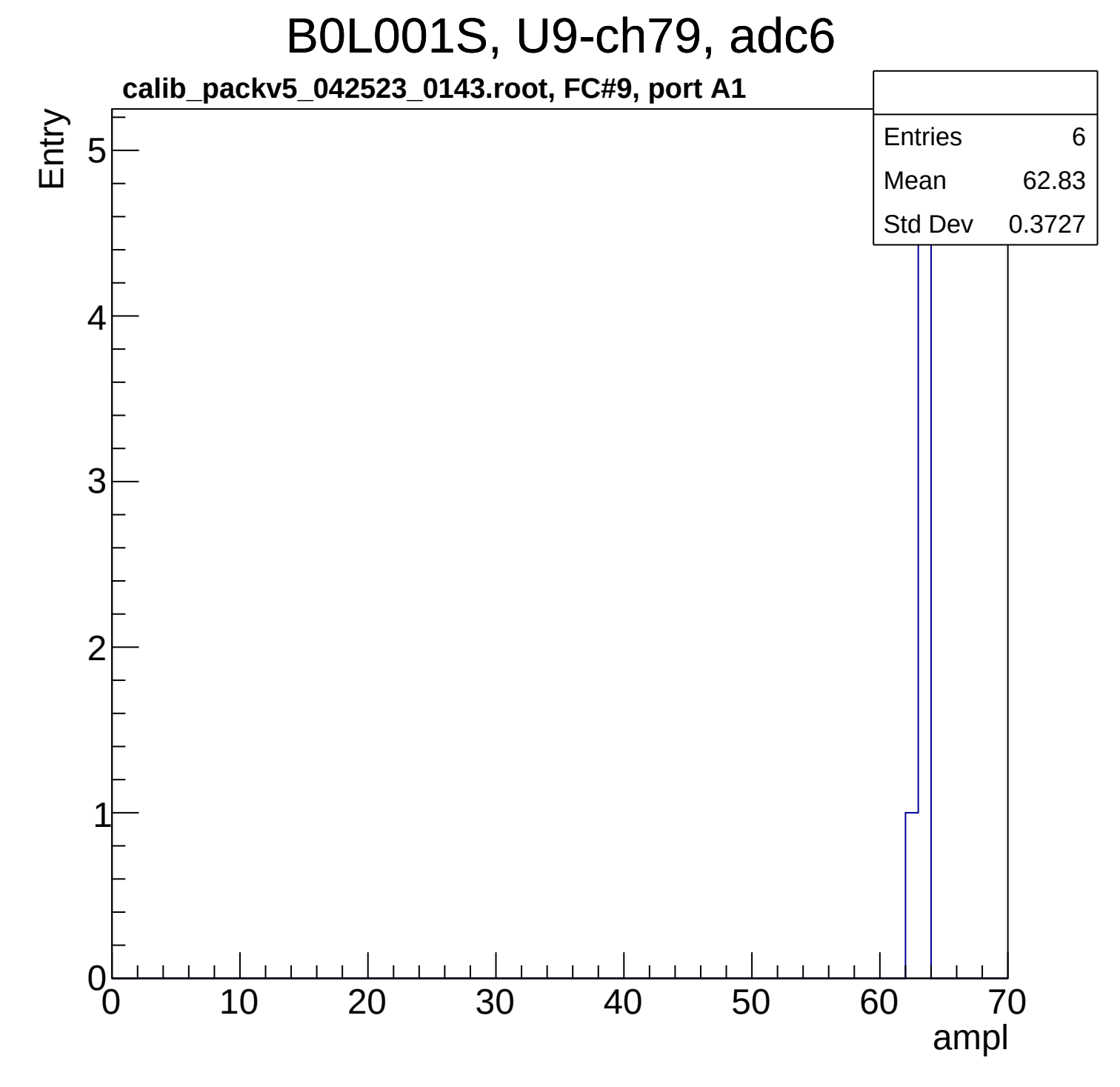
B0L001S, U9-ch79, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.83
Std Dev 0.3727
Entry
0
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2
3
4
5
0
10
20
30
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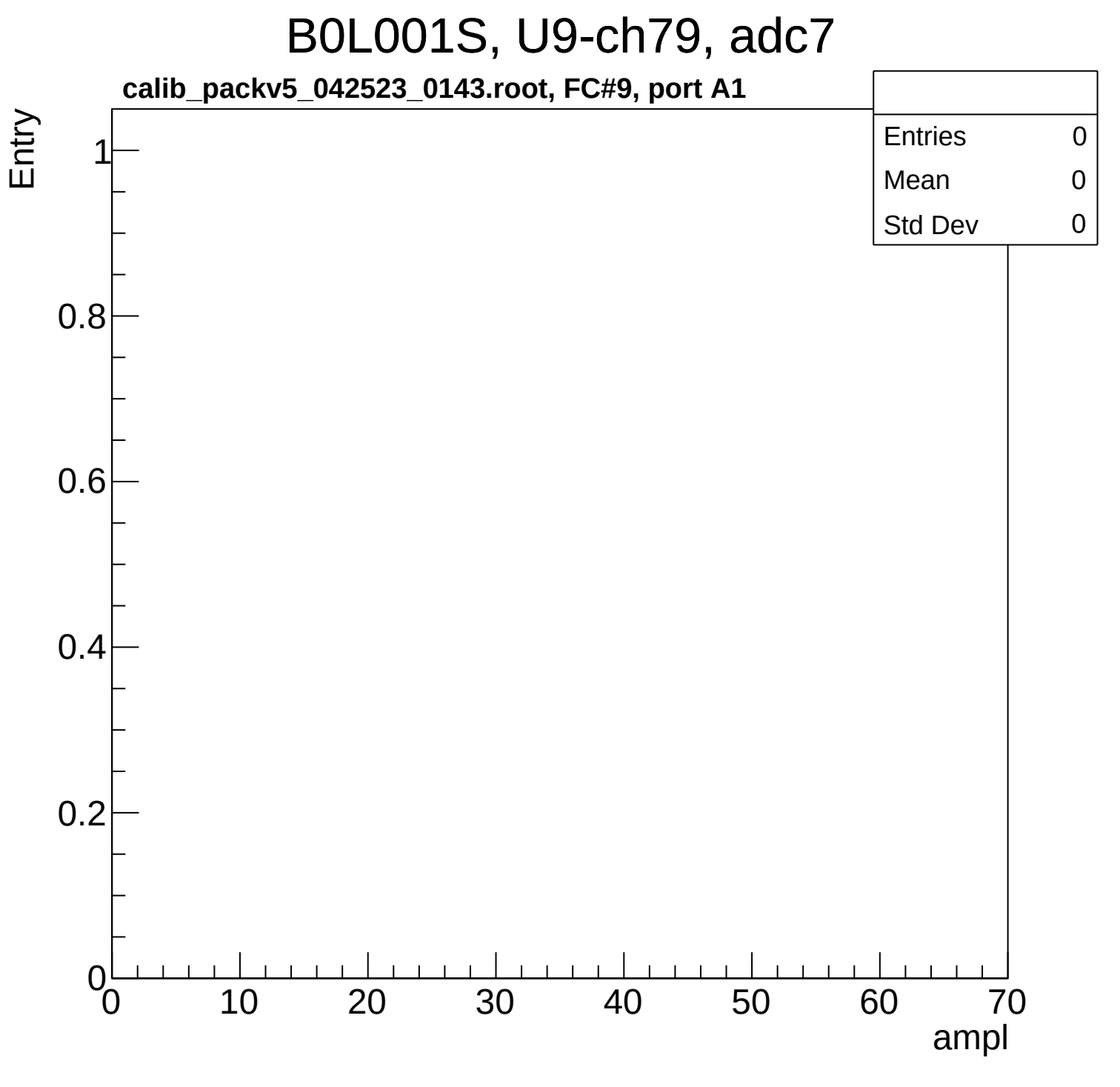
50
60
70
ampl


B0L001S, U9-ch79, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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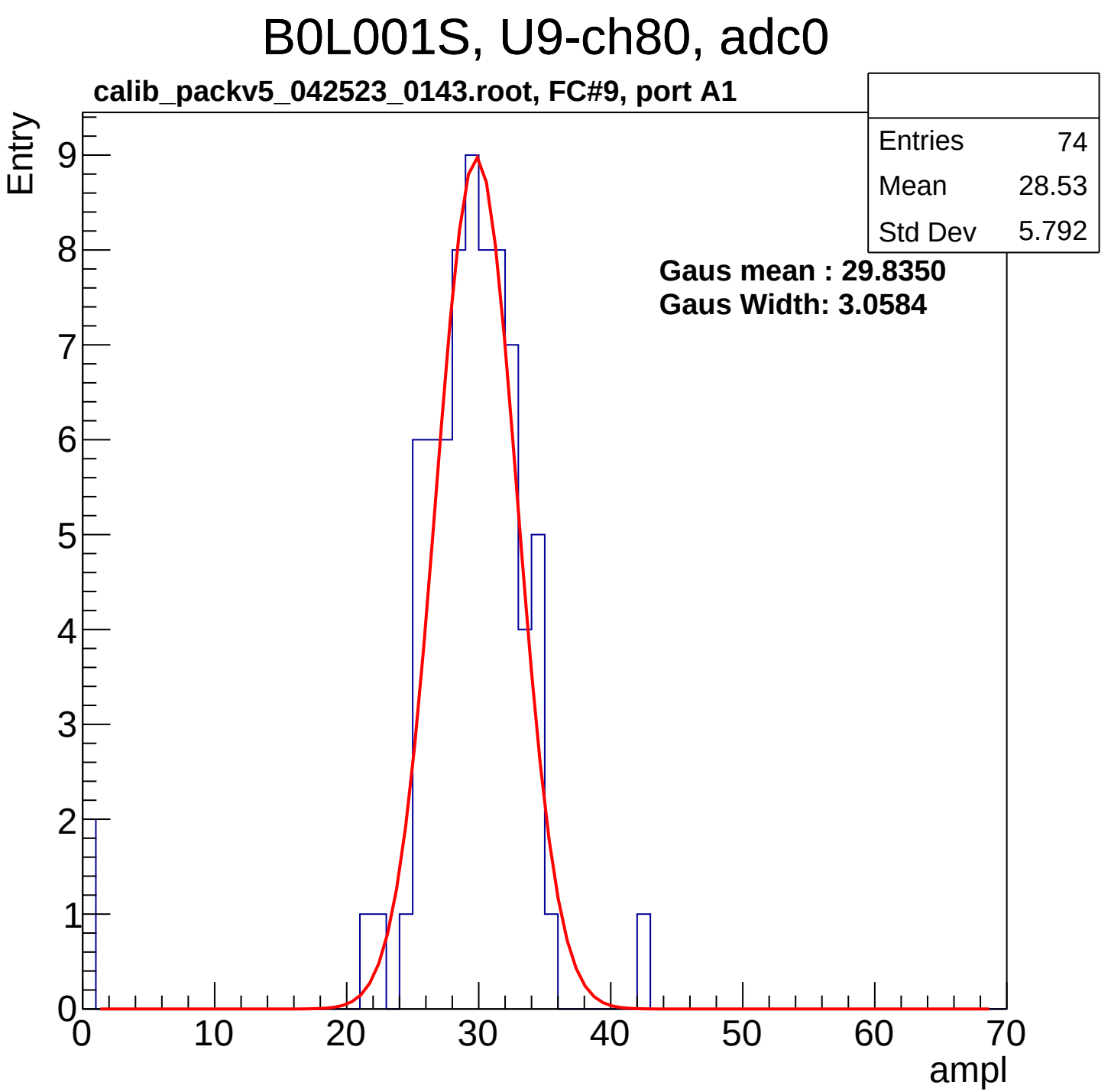
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch80, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 28.53
Std Dev 5.792
Gaus mean : 29.8350
Gaus Width: 3.0584
Entry
ampl
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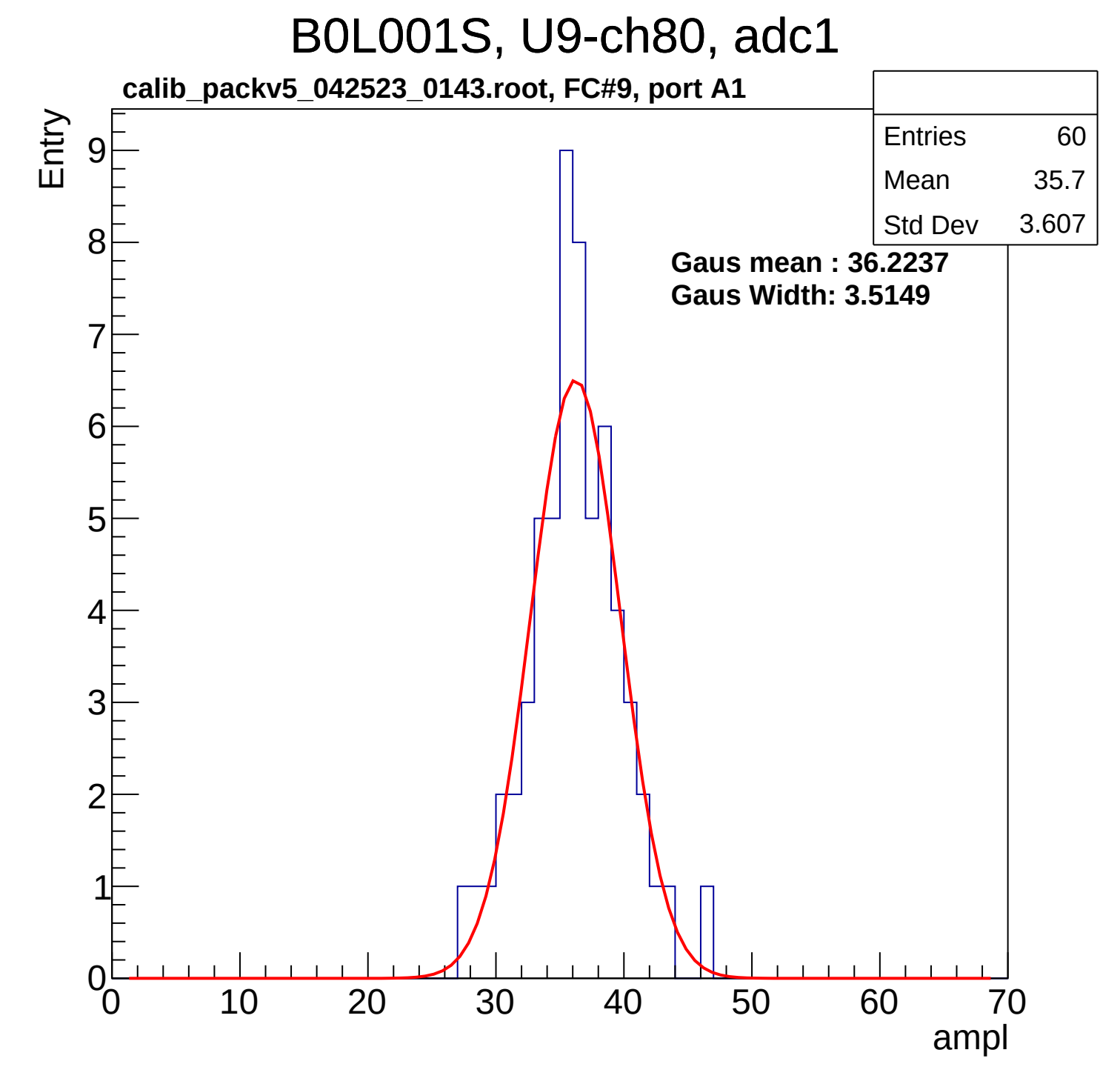
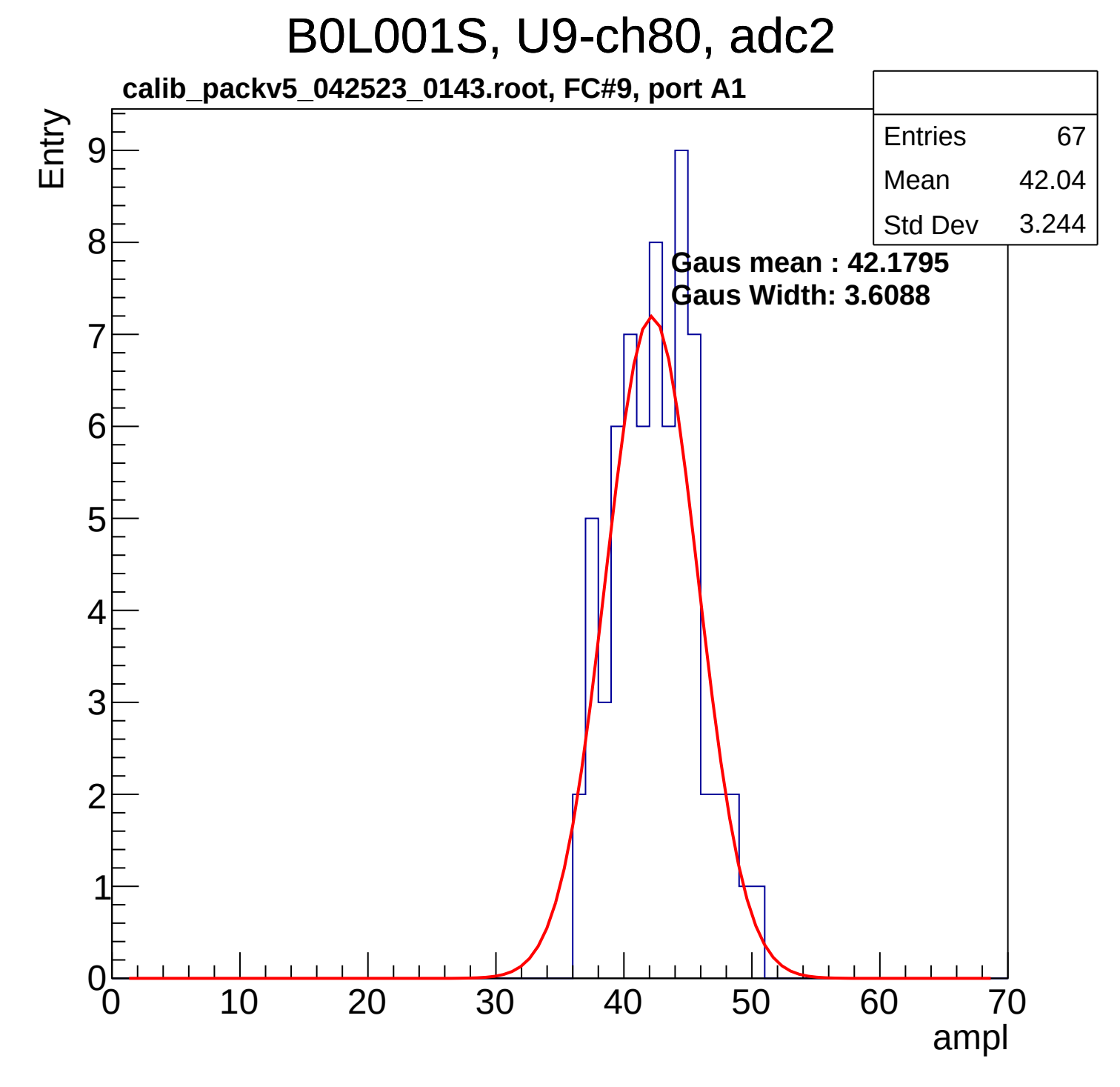
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch80, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 35.7
Std Dev 3.607
Gaus mean : 36.2237
Gaus Width: 3.5149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch80, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 42.04
Std Dev 3.244
Gaus mean : 42.1795
Gaus Width: 3.6088
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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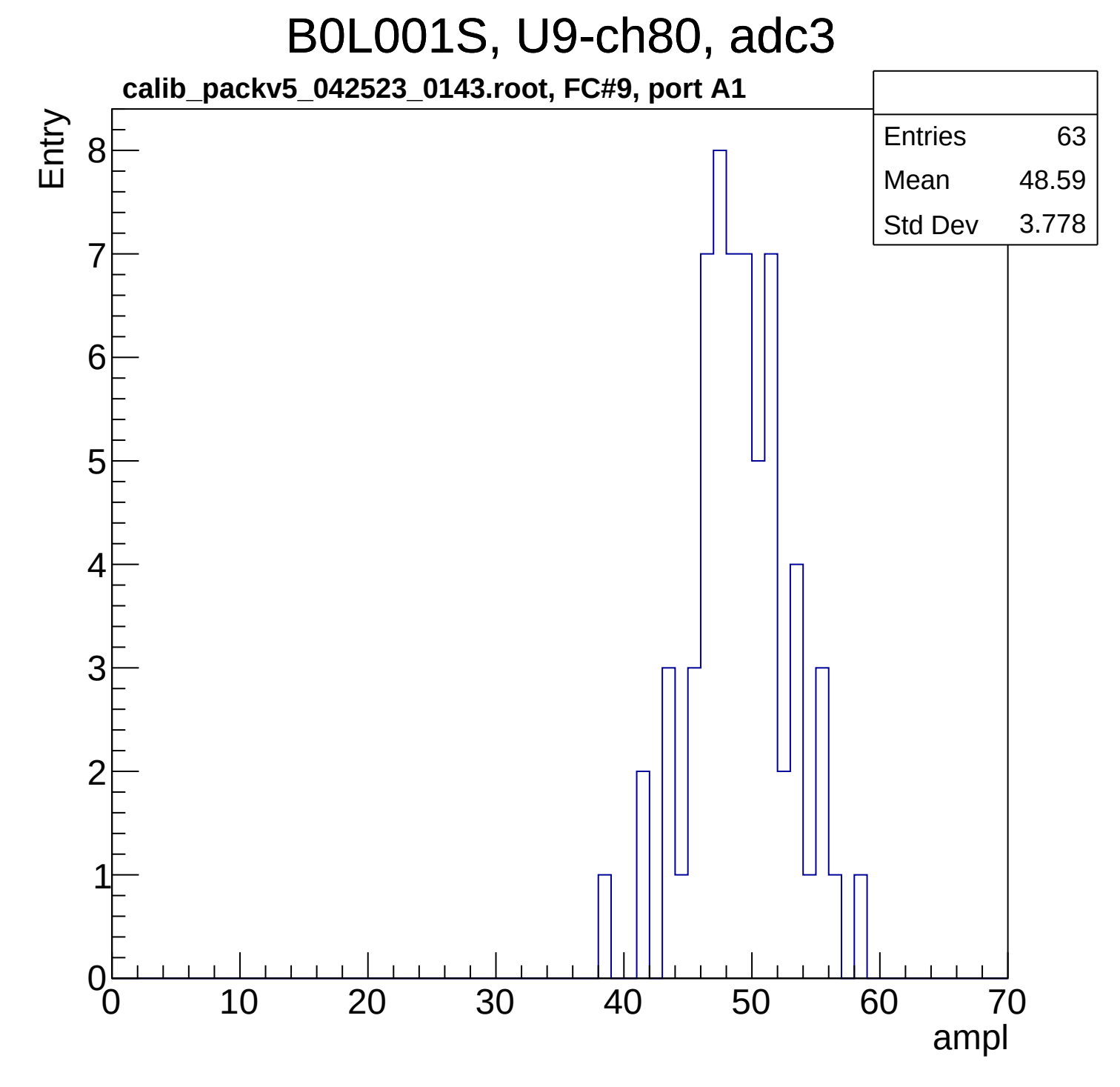
B0L001S, U9-ch80, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 48.59
Std Dev 3.778
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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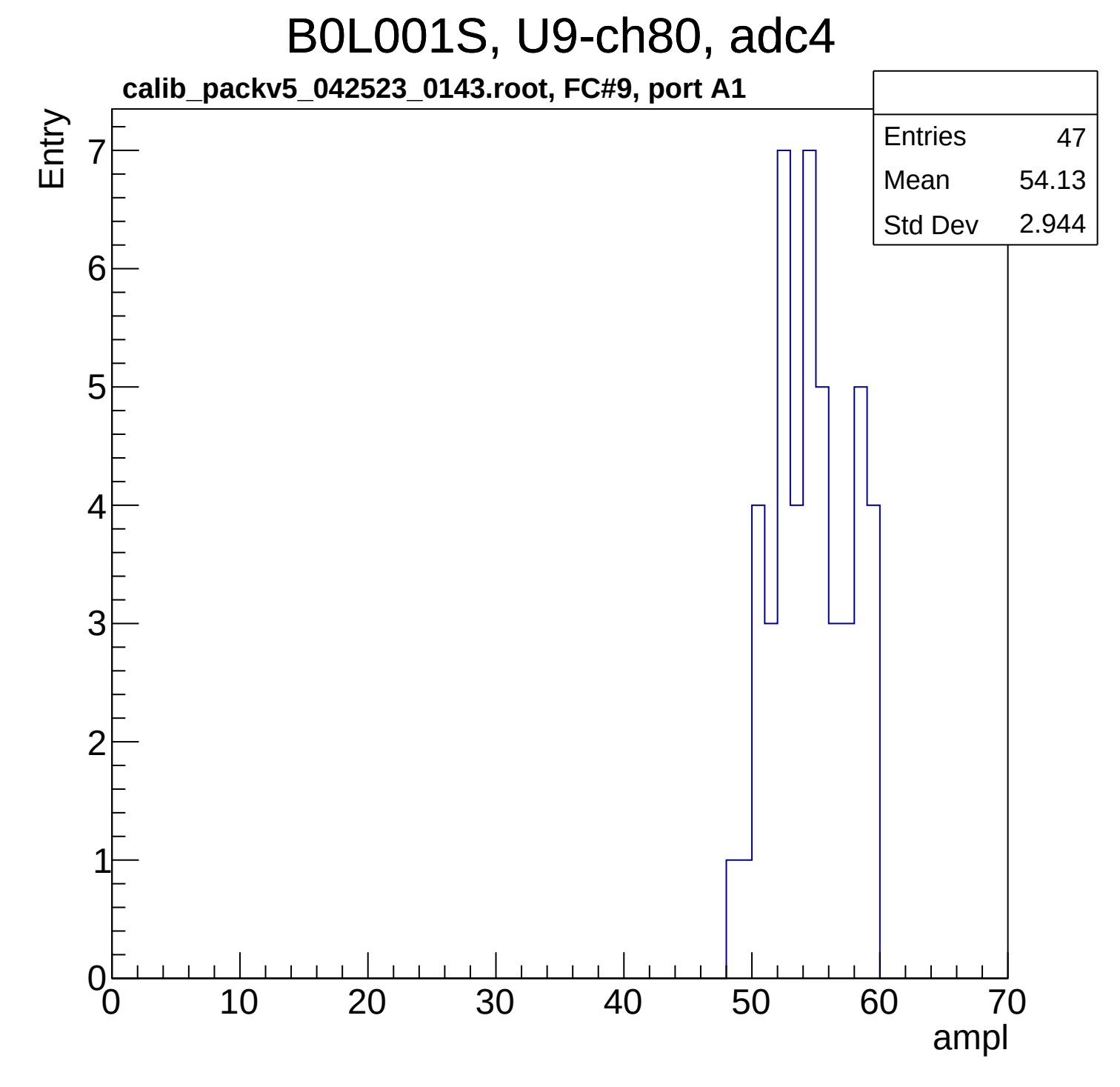
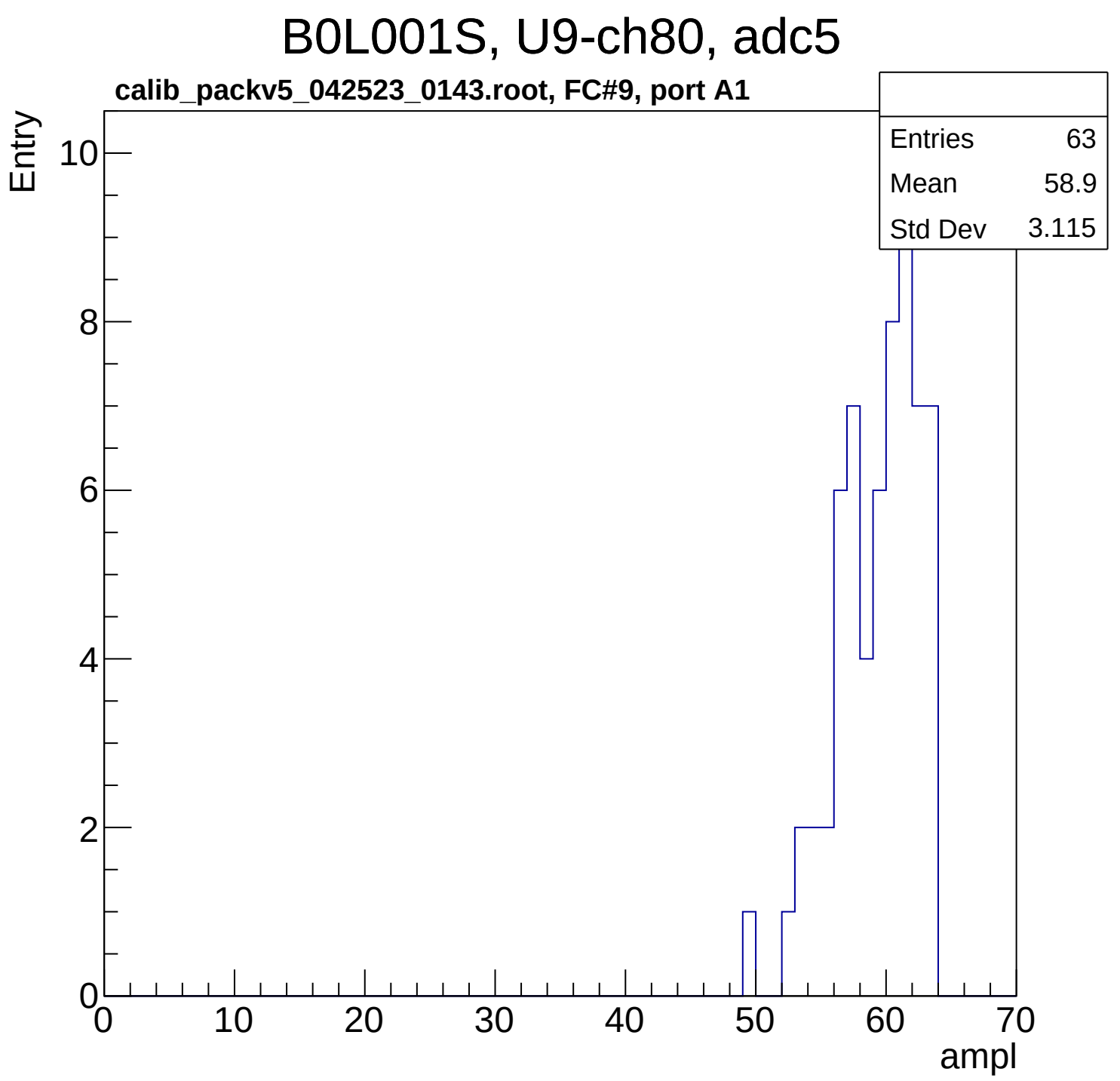
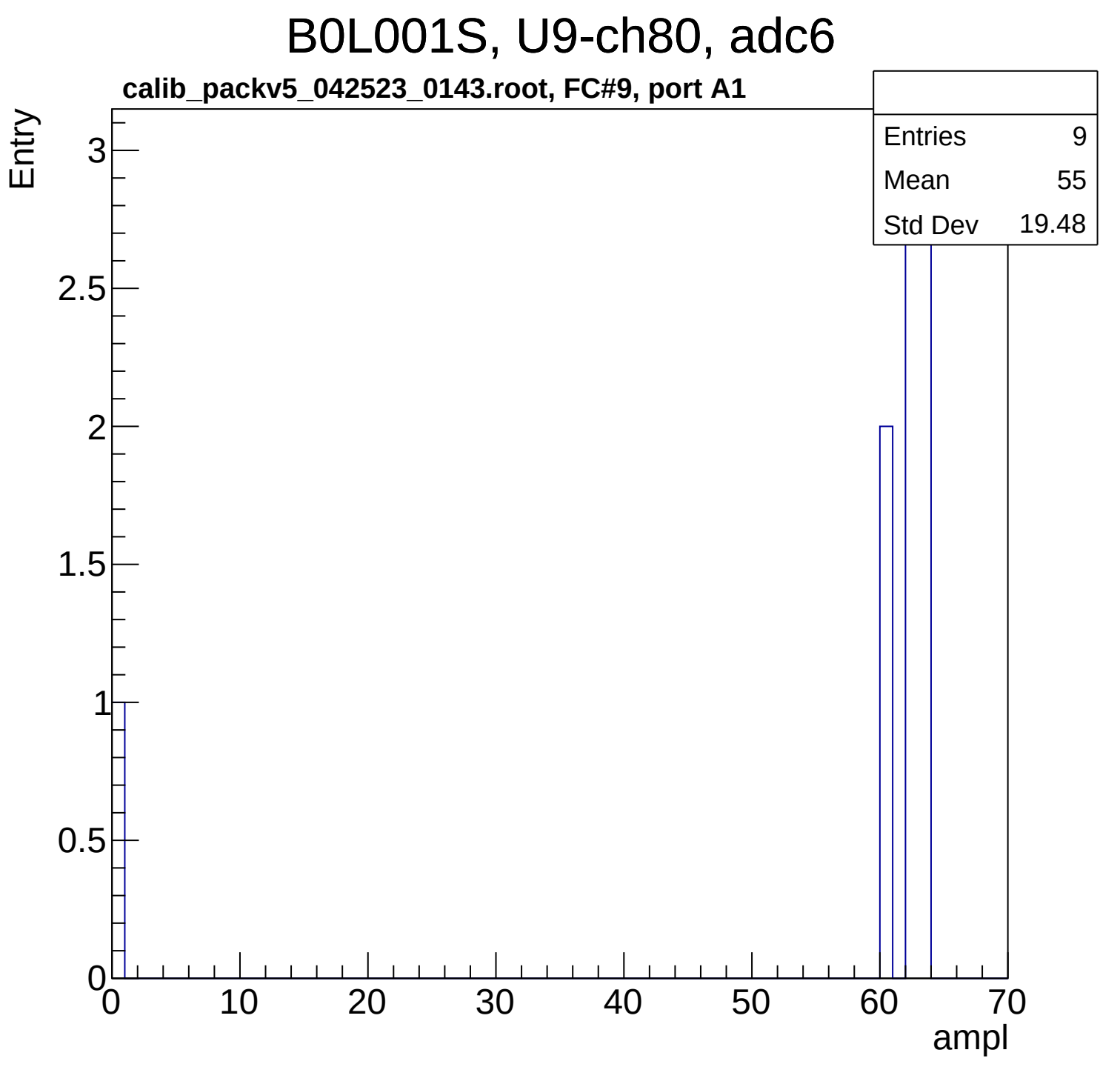

B0L001S, U9-ch80, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 54.13
Std Dev 2.944
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch80, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 58.9
Std Dev 3.115
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch80, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 55
Std Dev 19.48
Entry
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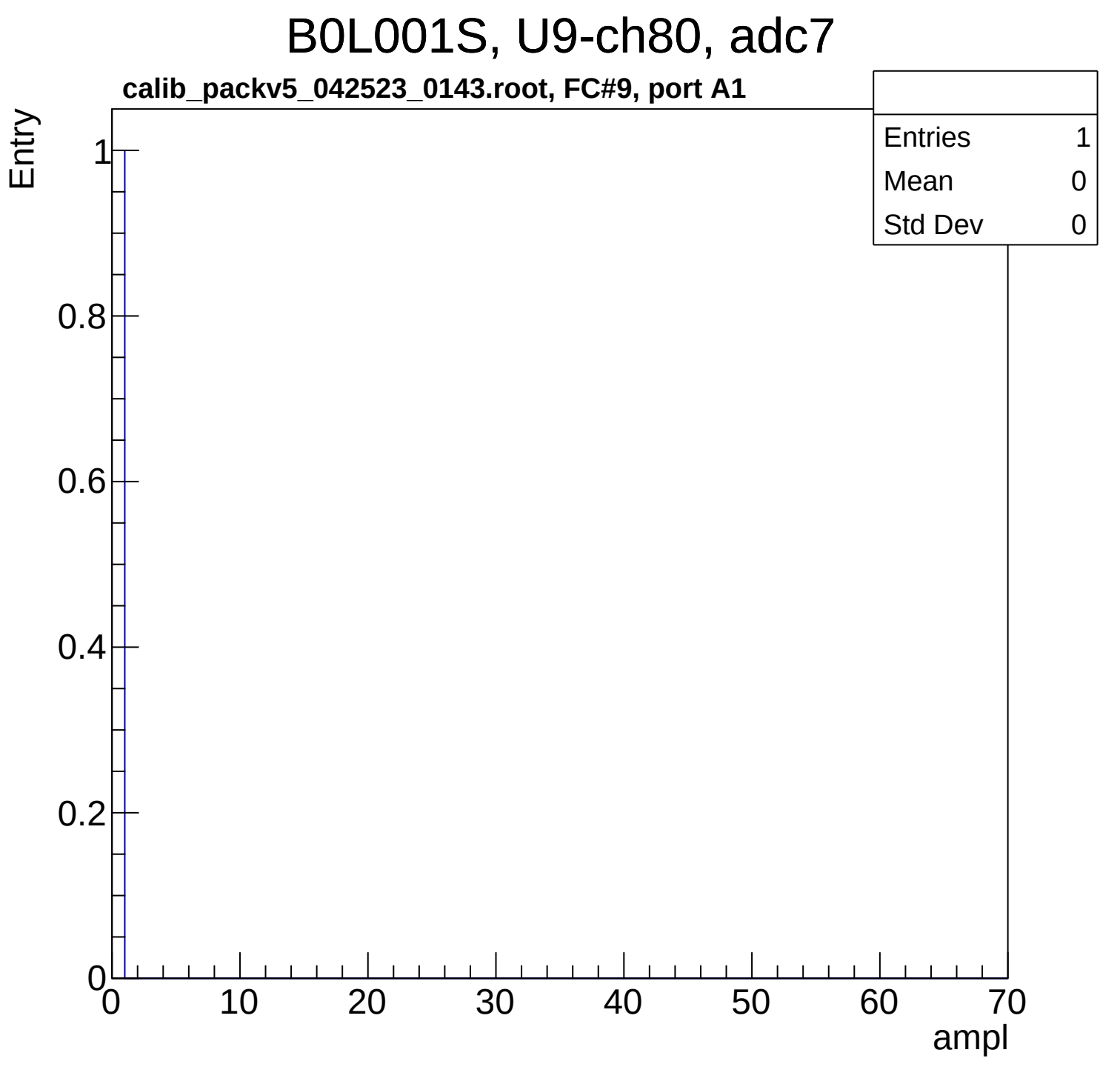
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch80, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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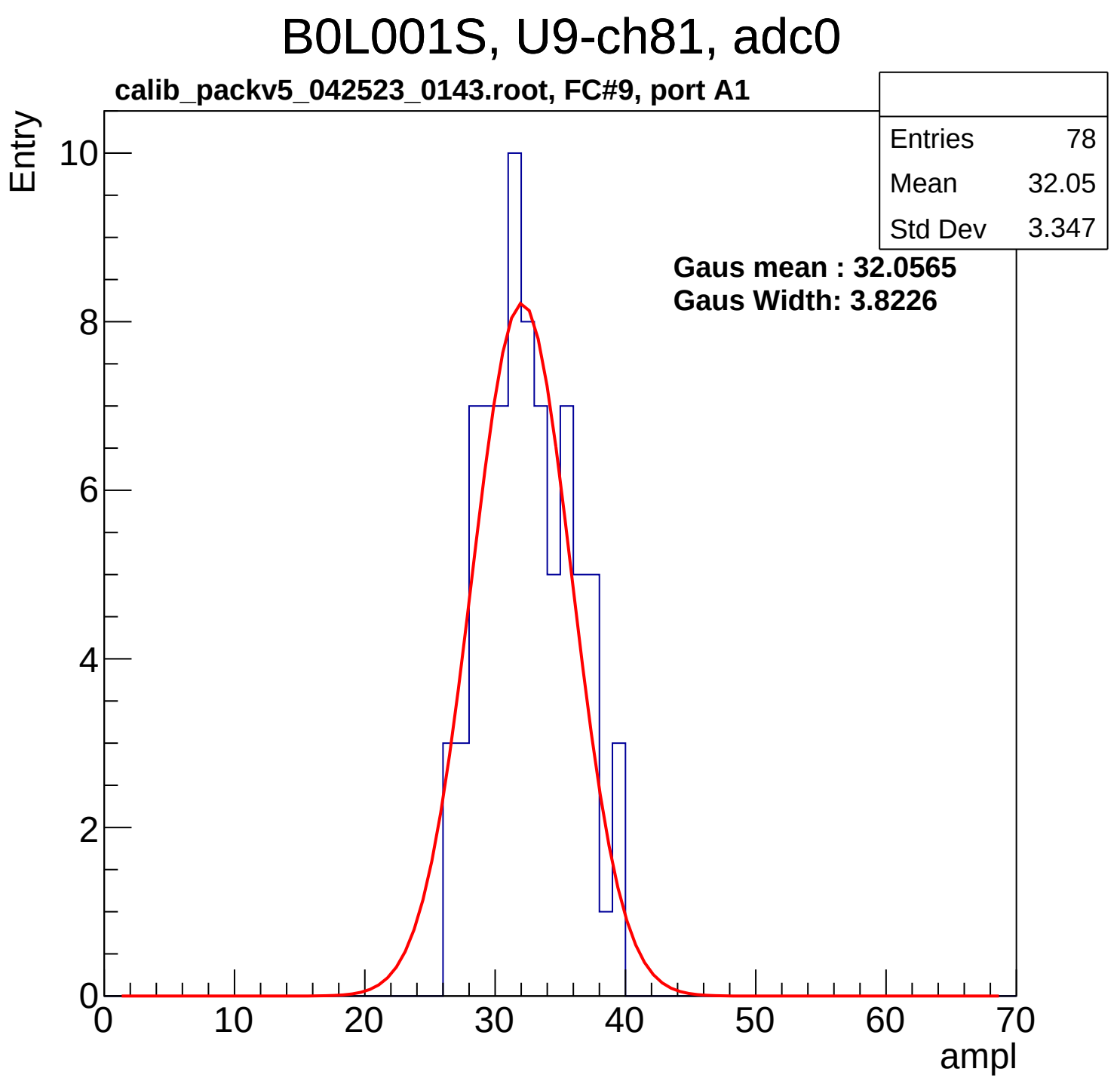
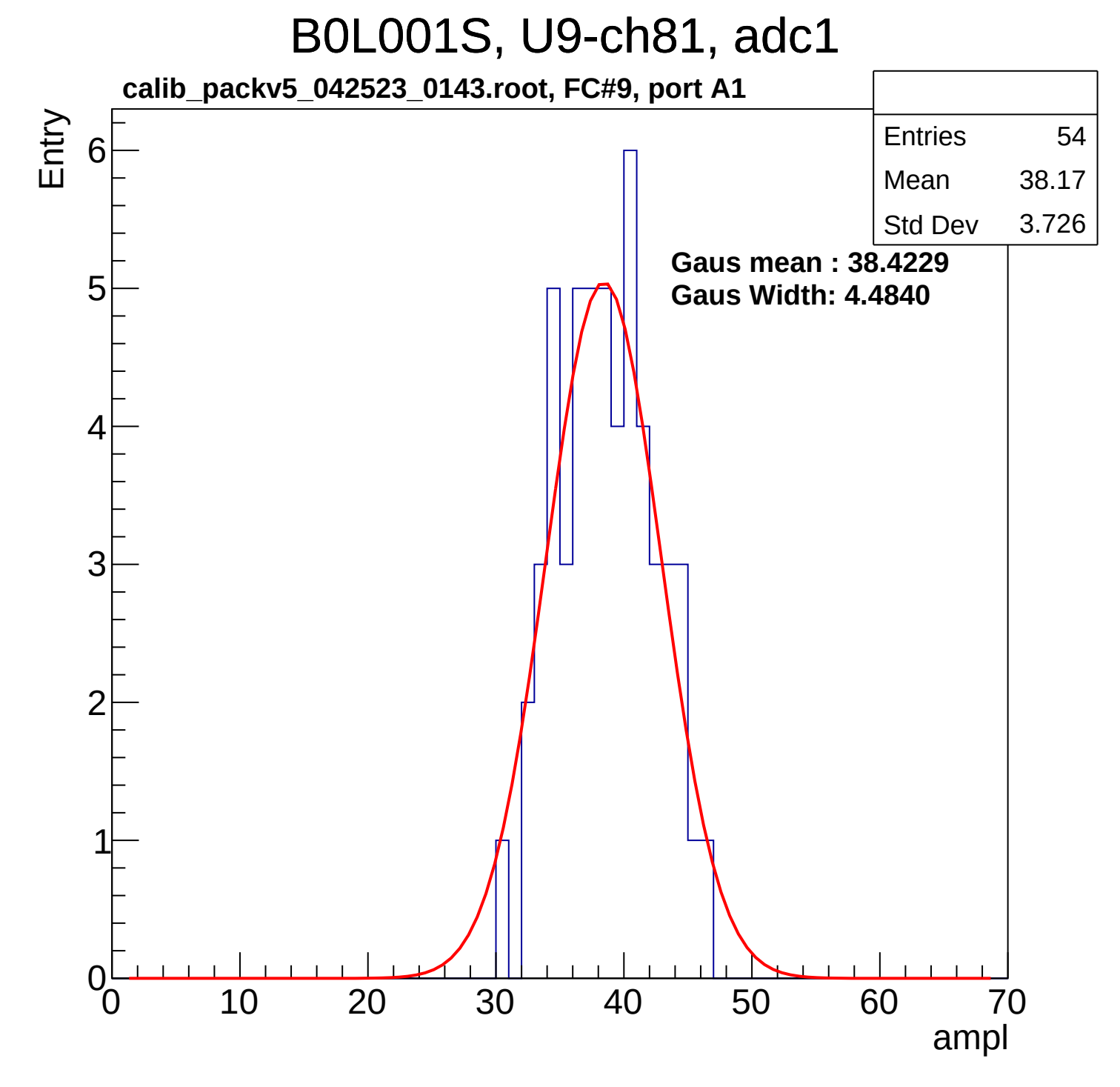
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch81, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 32.05
Std Dev 3.347
Gaus mean : 32.0565
Gaus Width: 3.8226
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch81, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 38.17
Std Dev 3.726
Gaus mean : 38.4229
Gaus Width: 4.4840
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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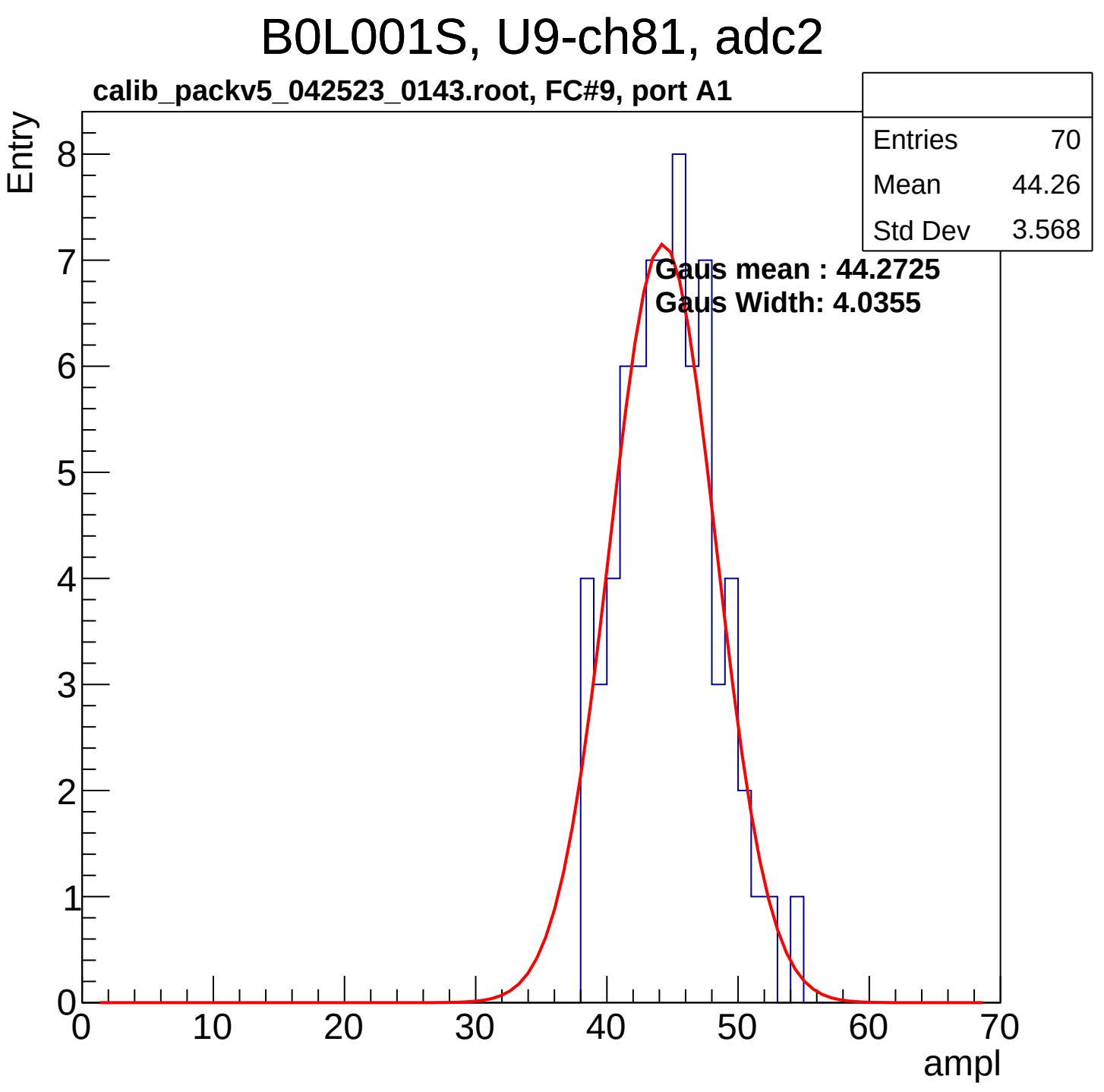
B0L001S, U9-ch81, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.26
Std Dev 3.568
Gaus mean : 44.2725
Gaus Width: 4.0355
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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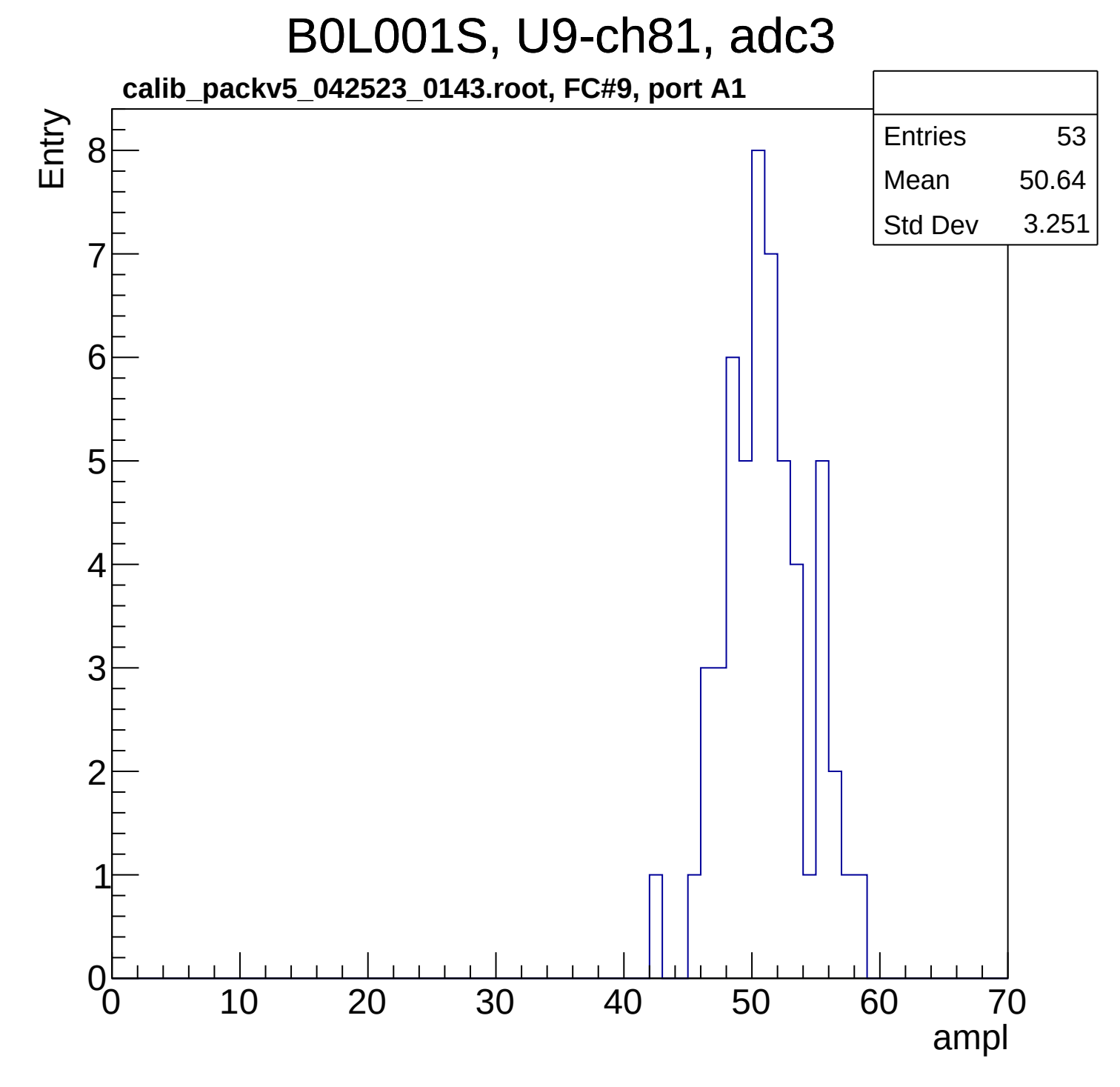
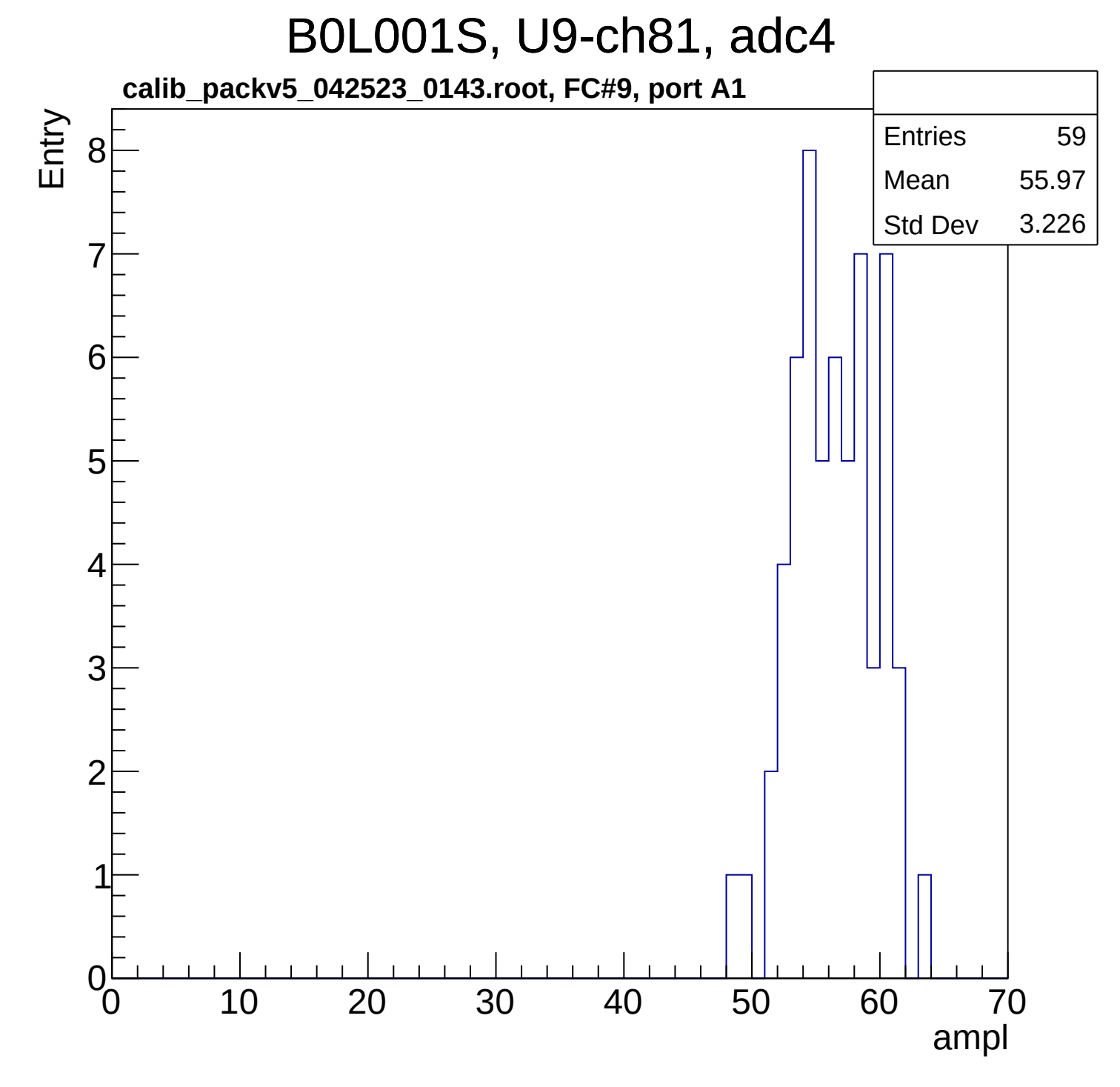
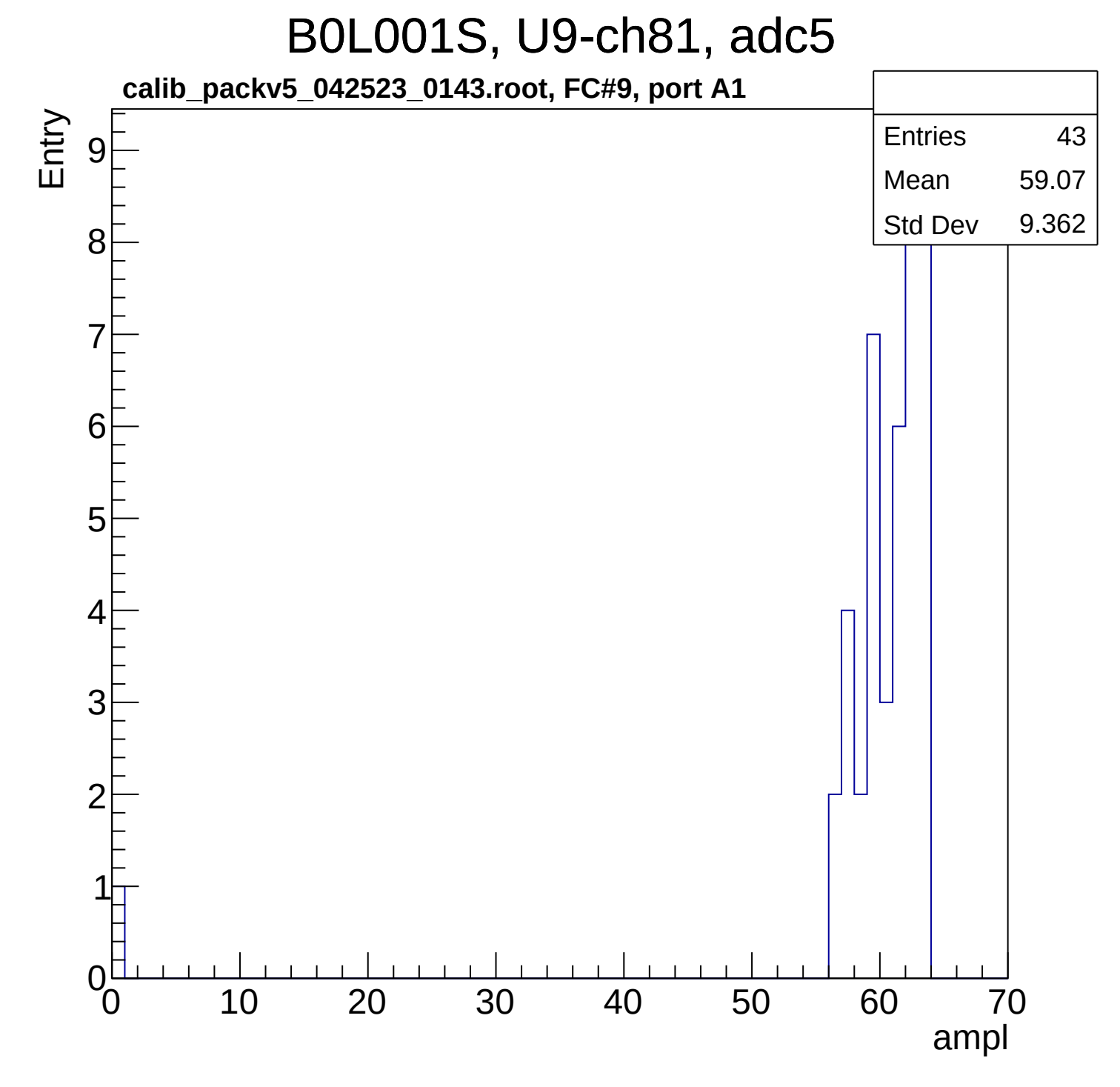

B0L001S, U9-ch81, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 50.64
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch81, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.97
Std Dev 3.226
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch81, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.07
Std Dev 9.362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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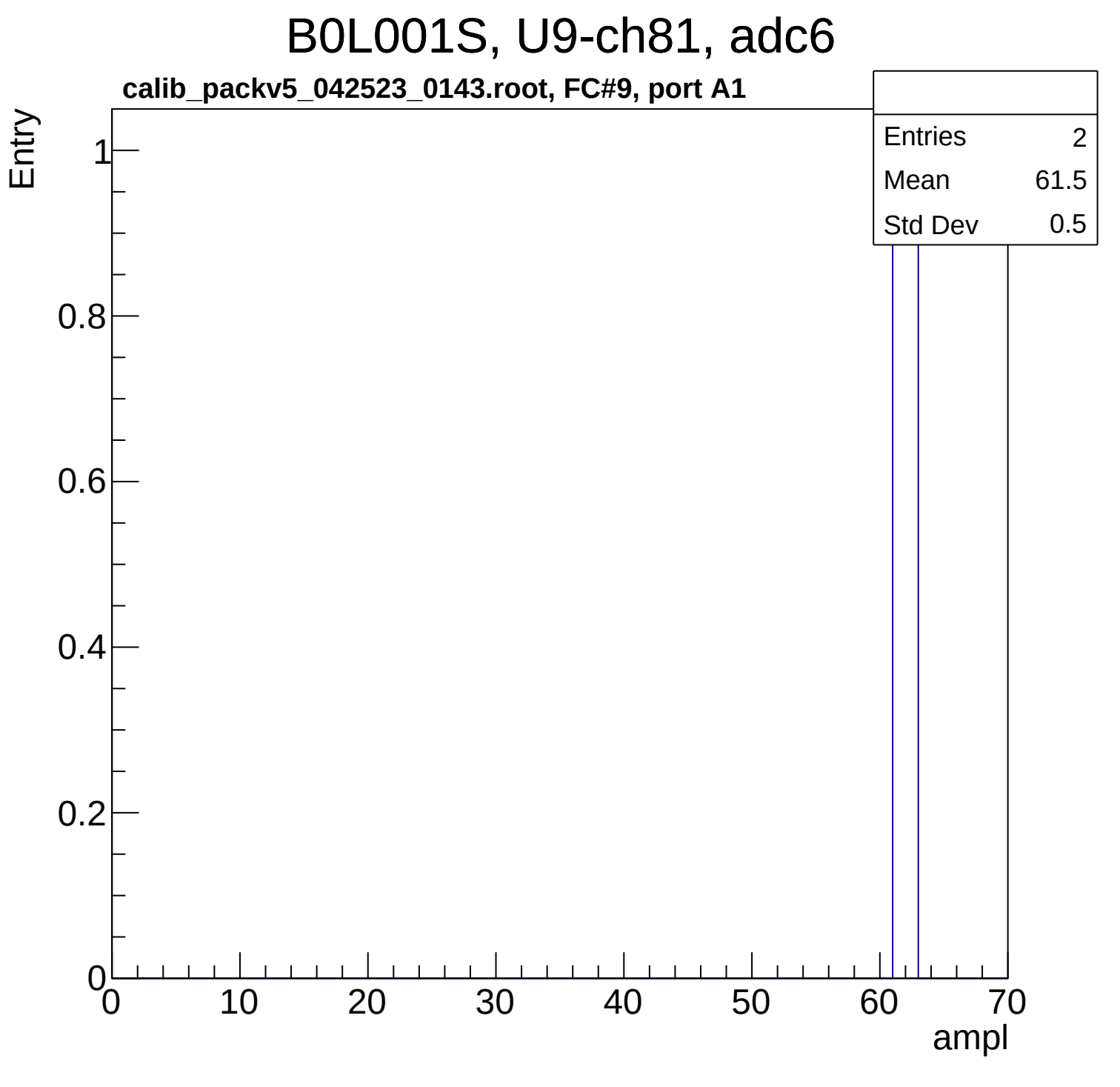
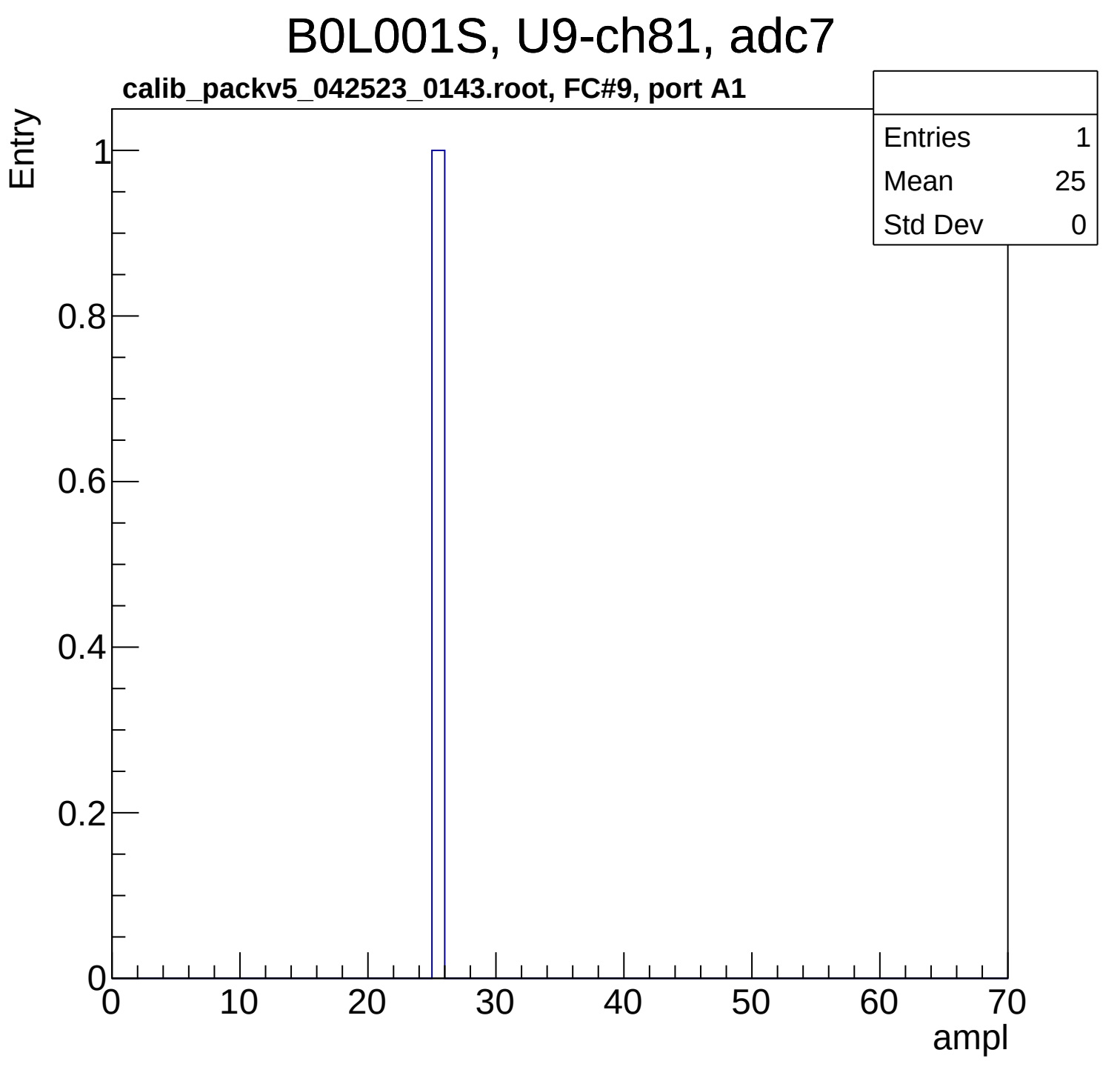

B0L001S, U9-ch81, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch81, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 25
Std Dev 0
Entry
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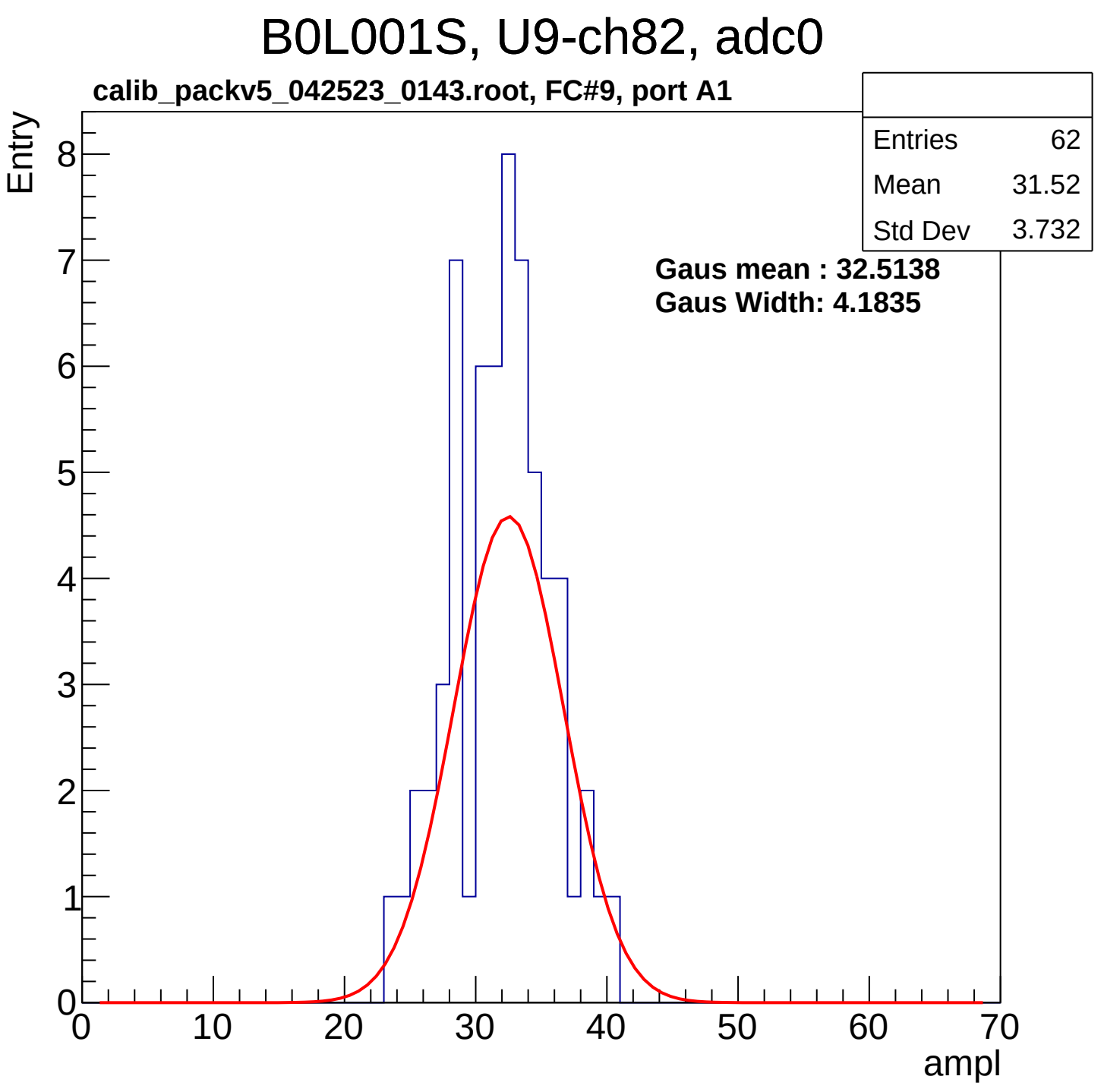
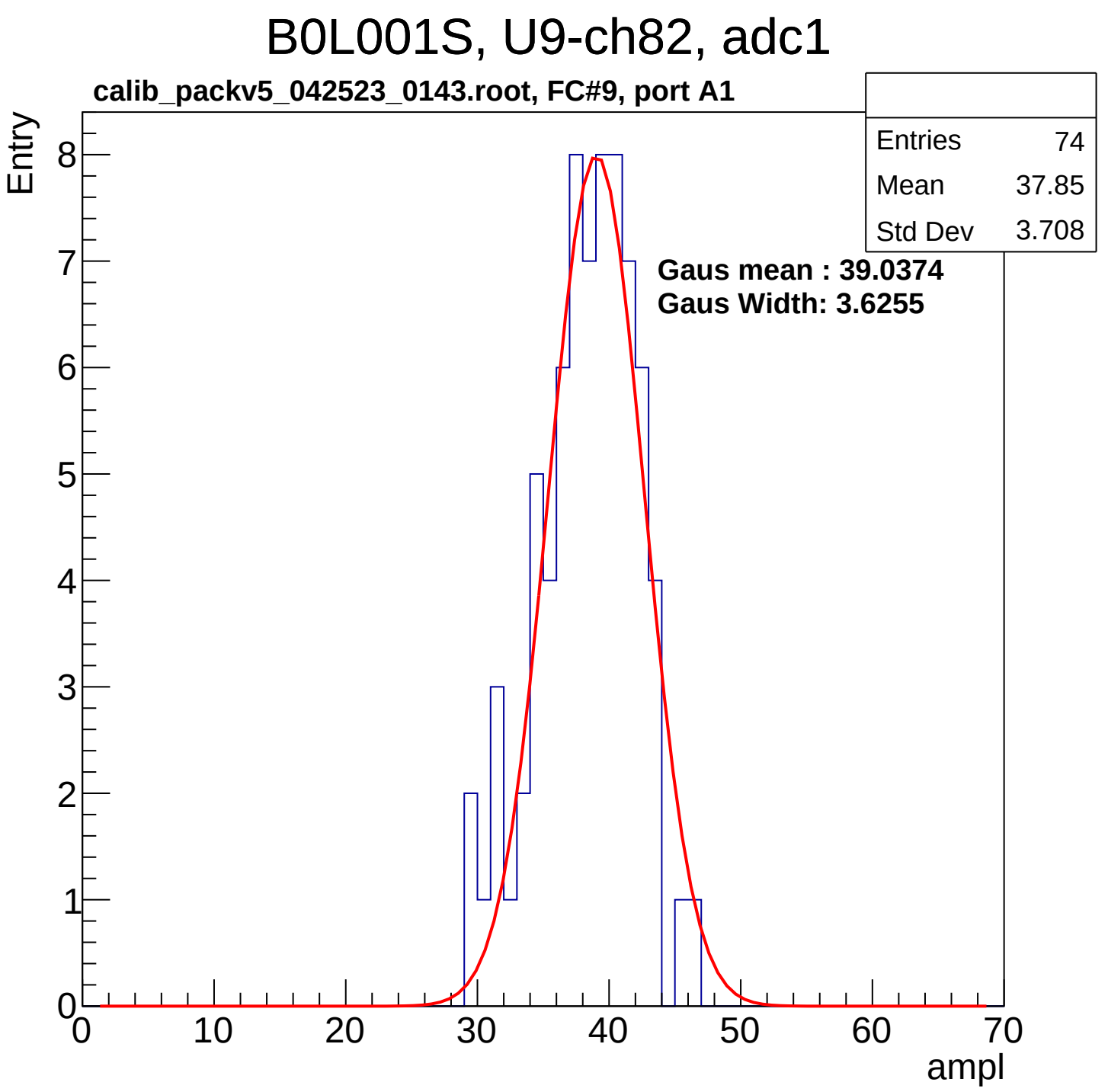
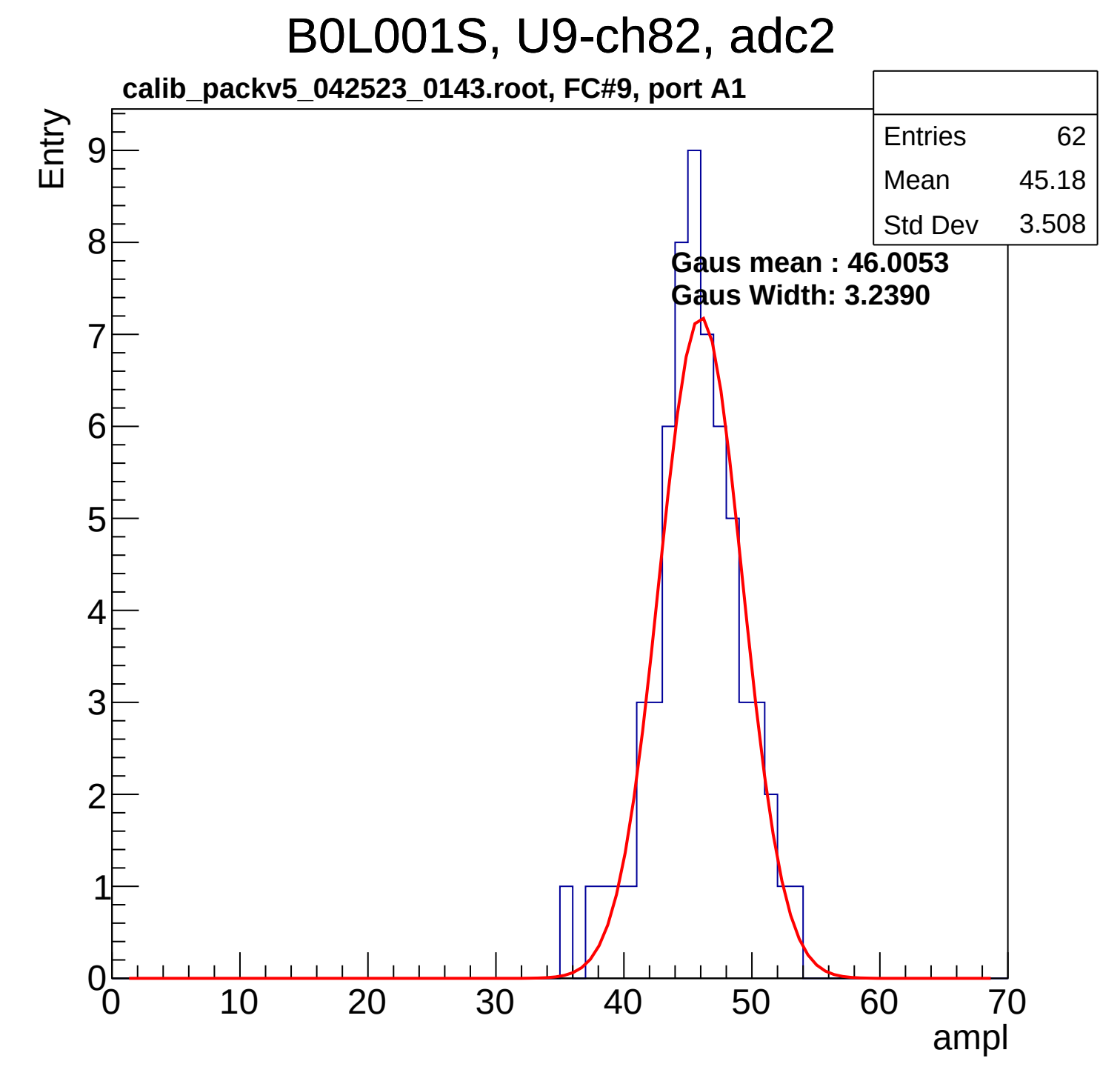
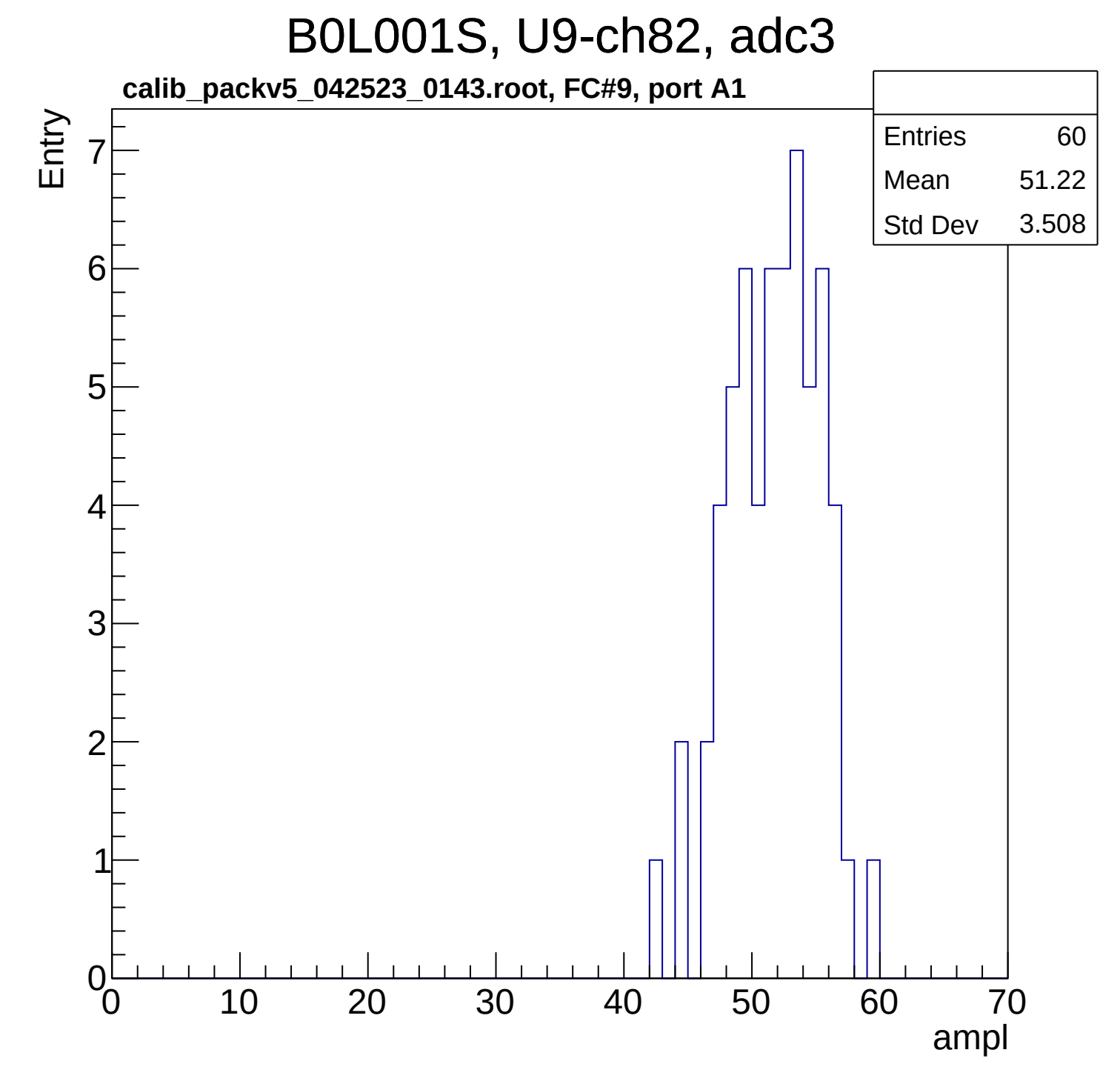
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch82, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.52
Std Dev 3.732
Gaus mean : 32.5138
Gaus Width: 4.1835
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch82, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.85
Std Dev 3.708
Gaus mean : 39.0374
Gaus Width: 3.6255
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch82, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 45.18
Std Dev 3.508
Gaus mean : 46.0053
Gaus Width: 3.2390
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch82, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.22
Std Dev 3.508
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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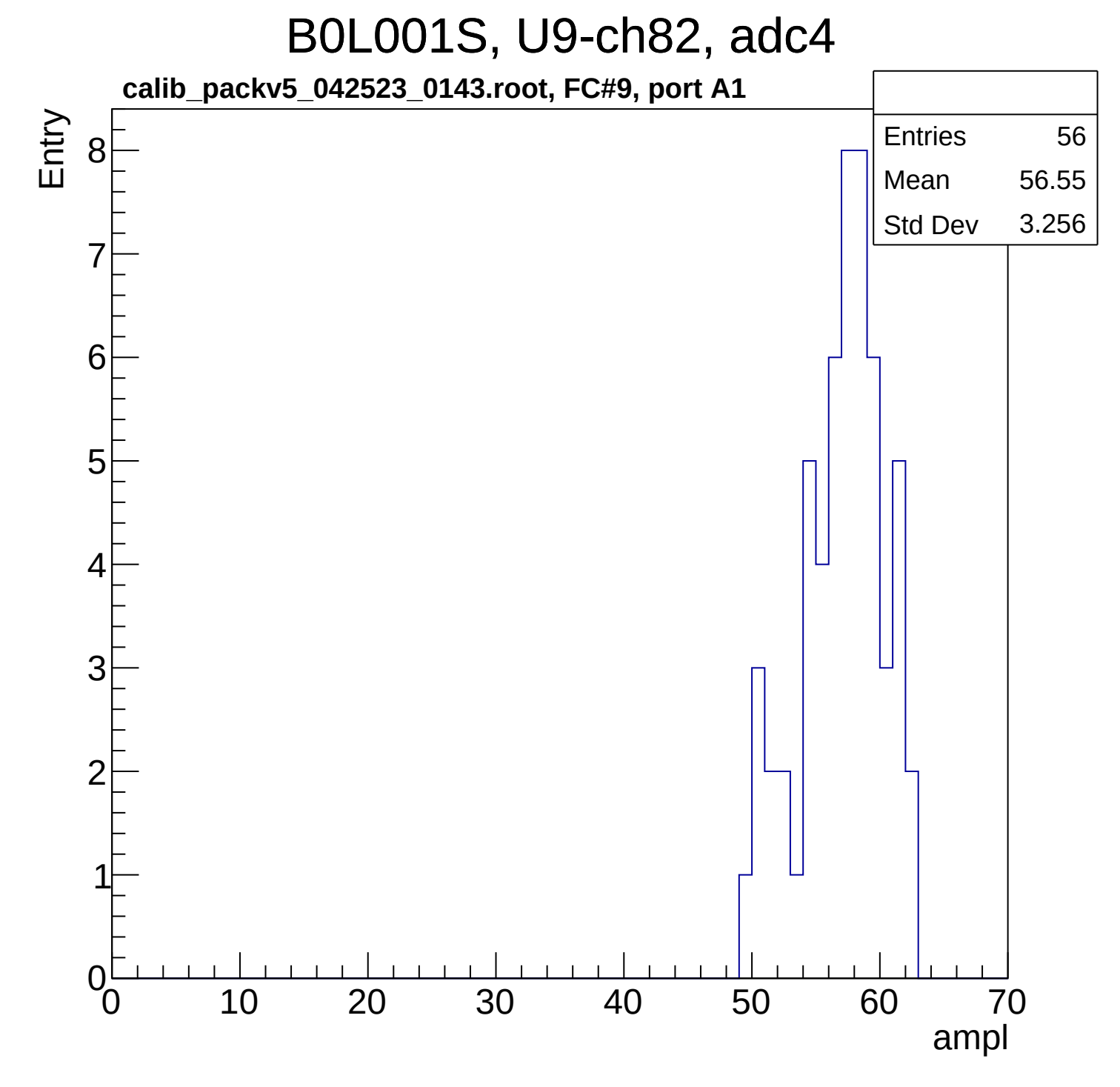
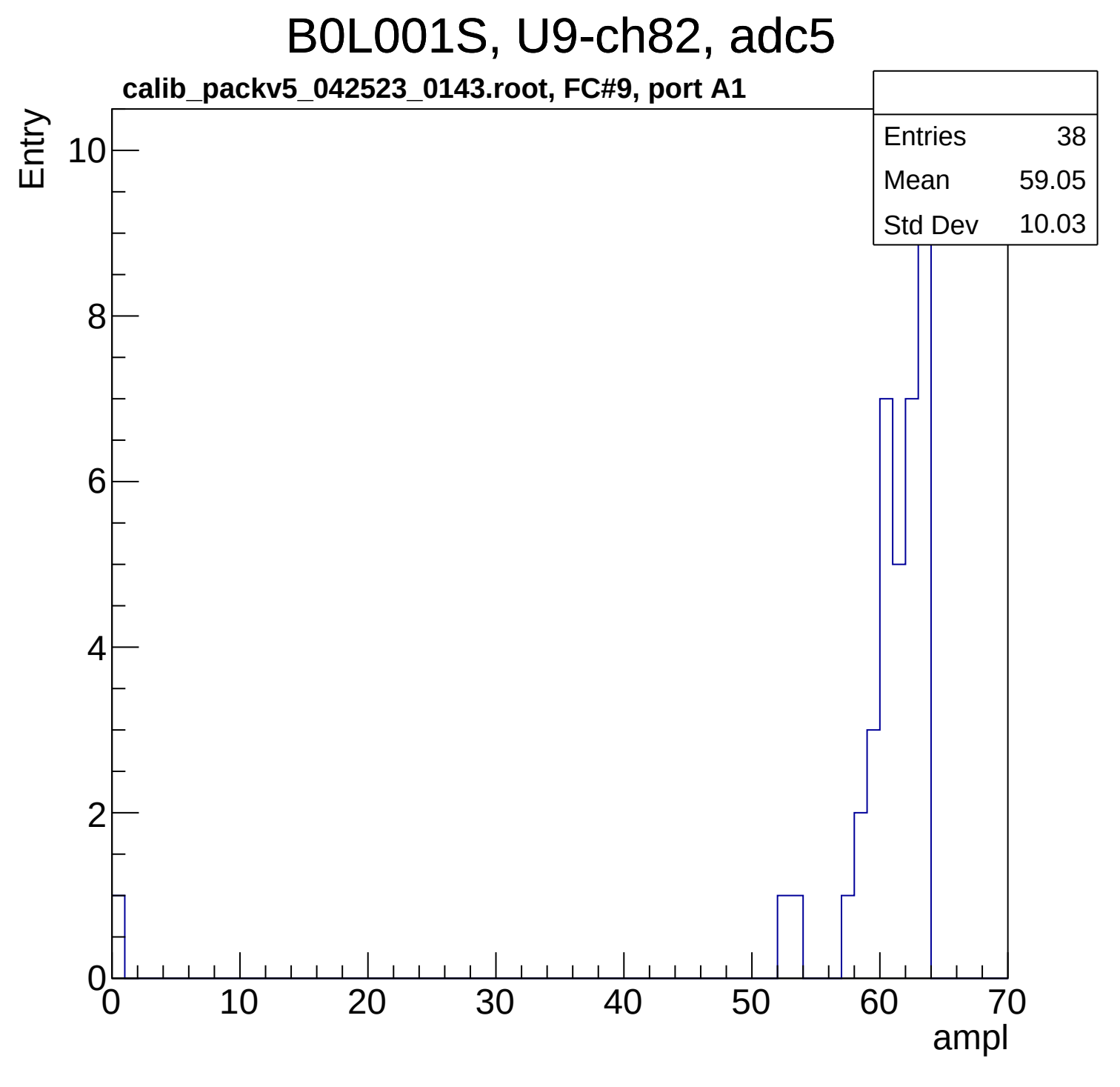
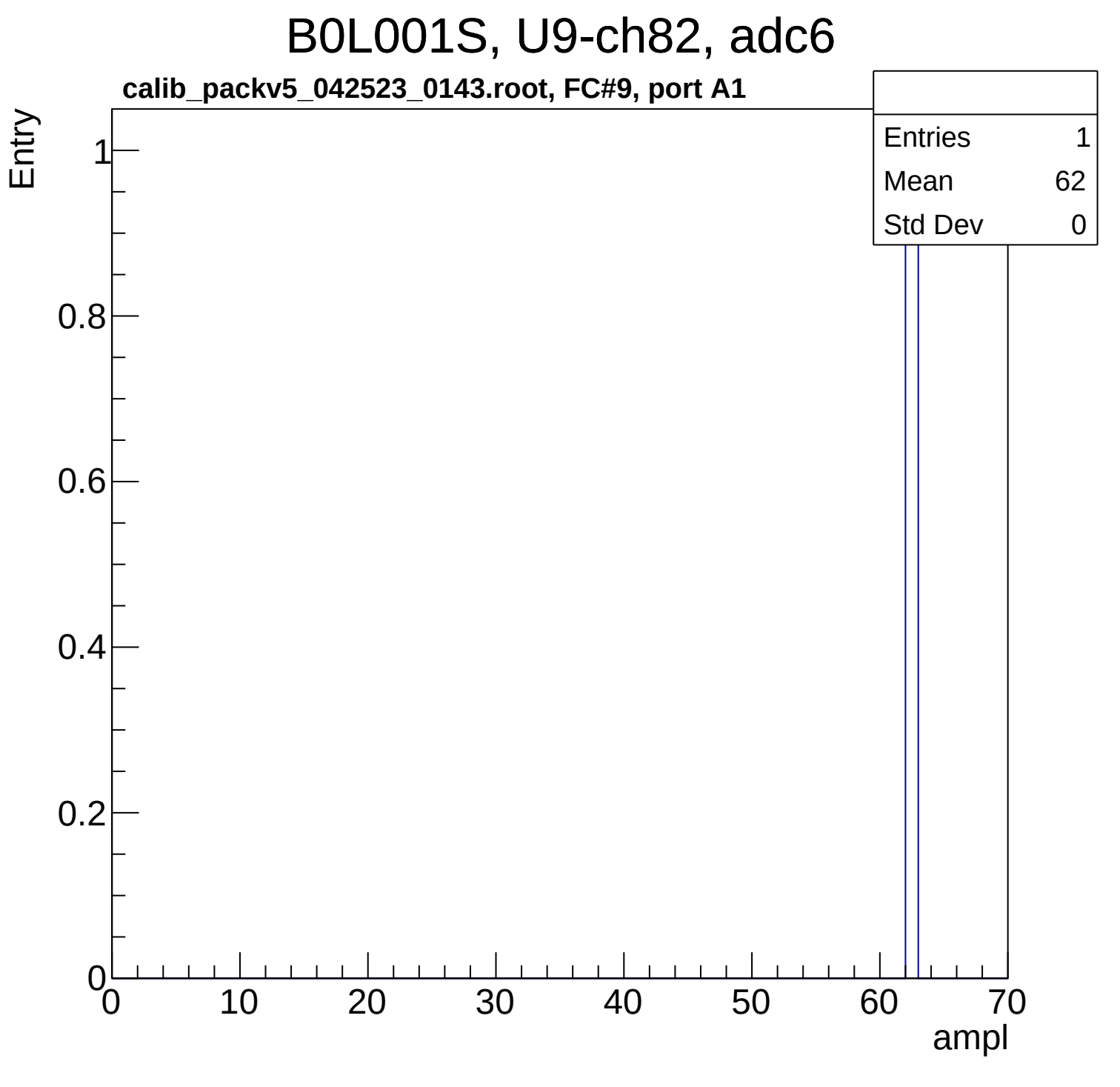
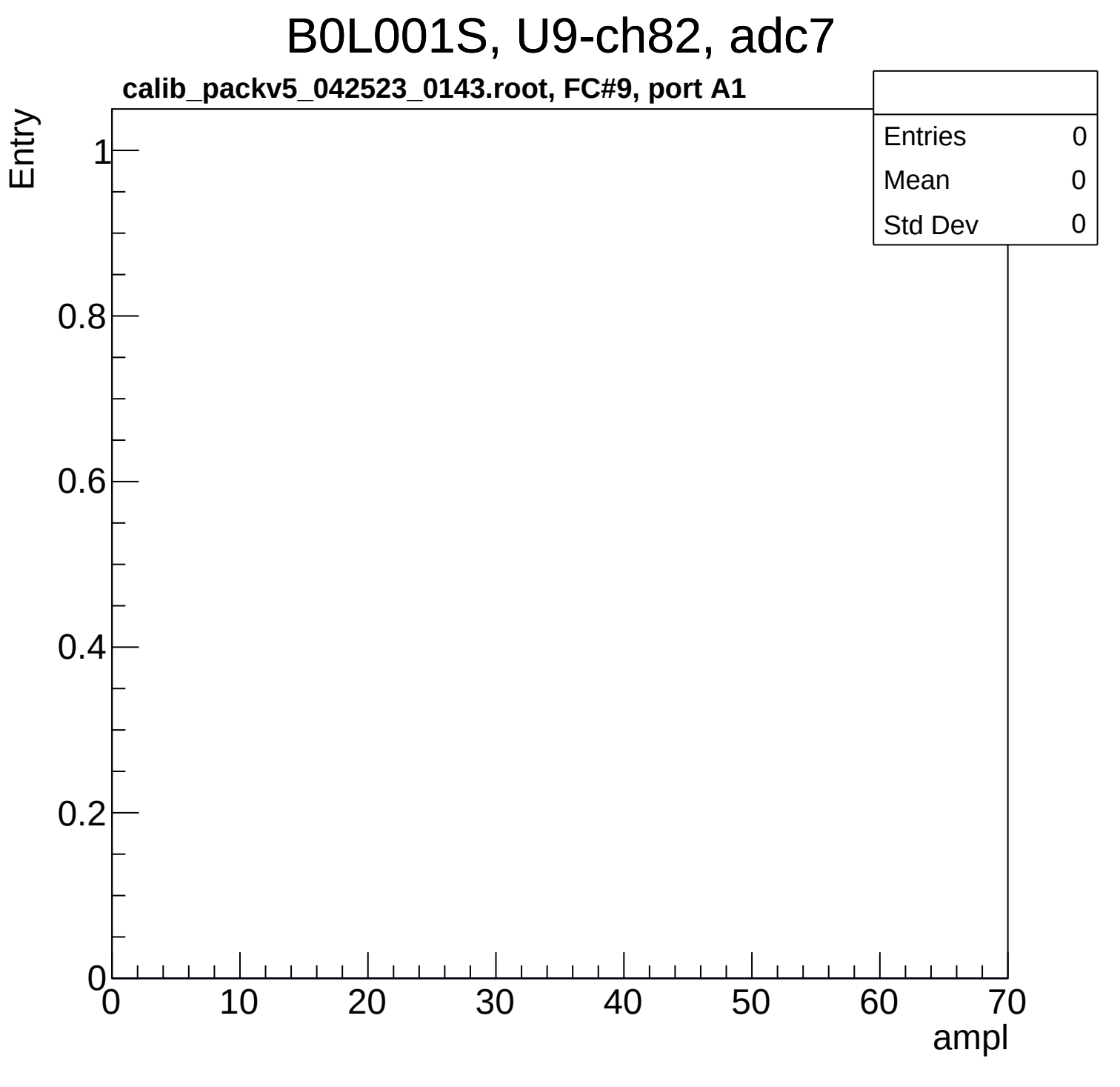
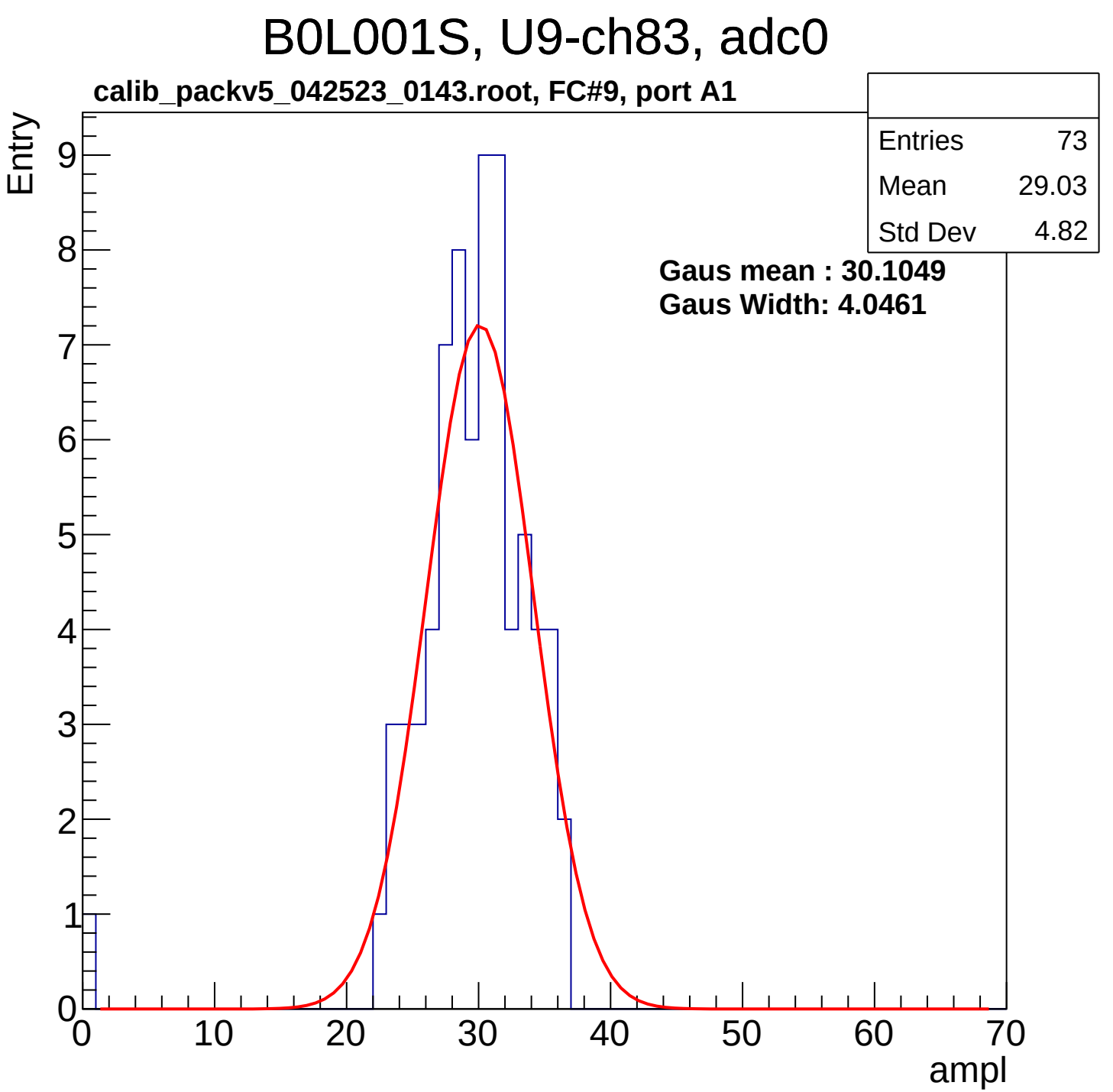
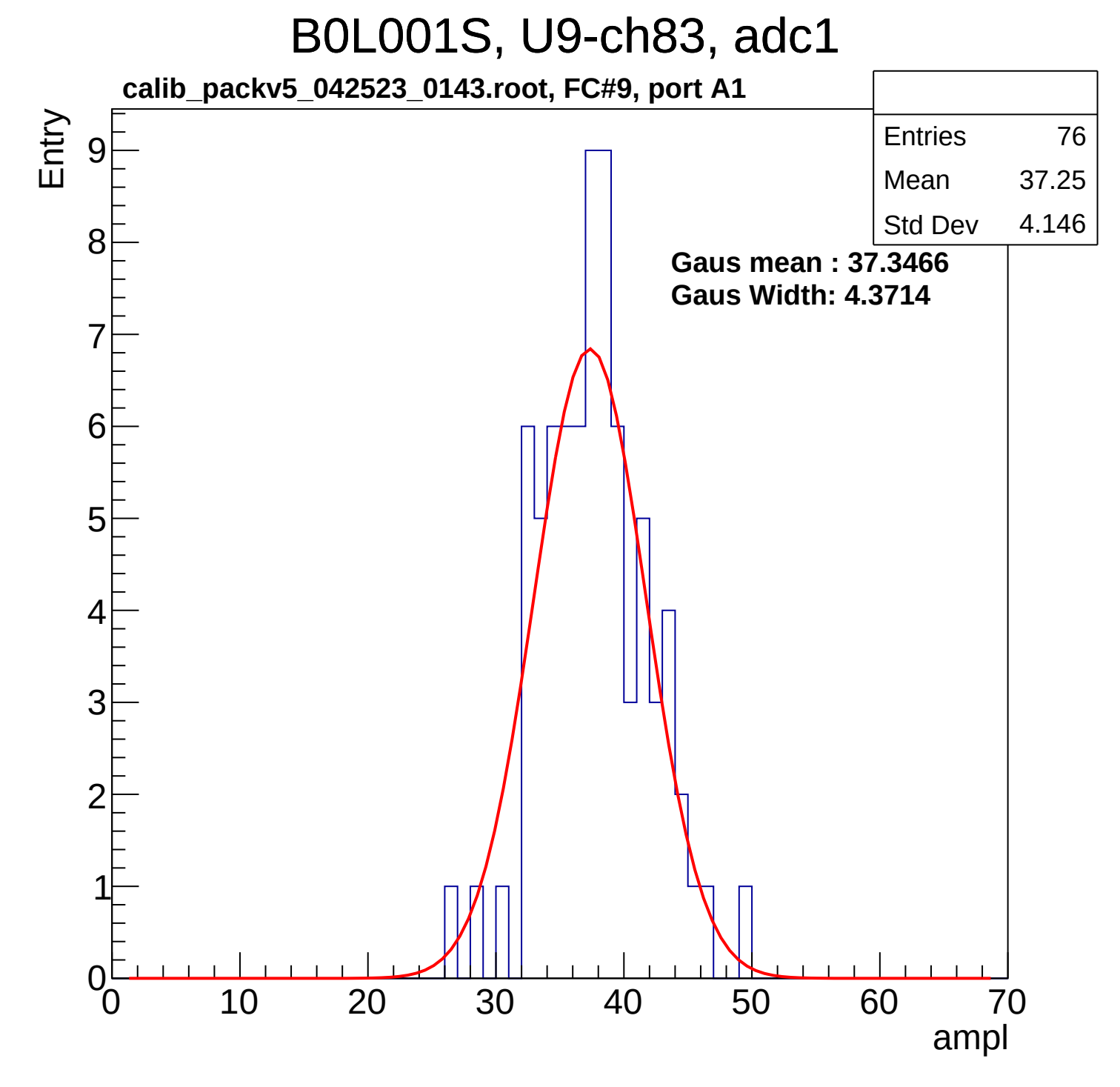

B0L001S, U9-ch82, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 56.55
Std Dev 3.256
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

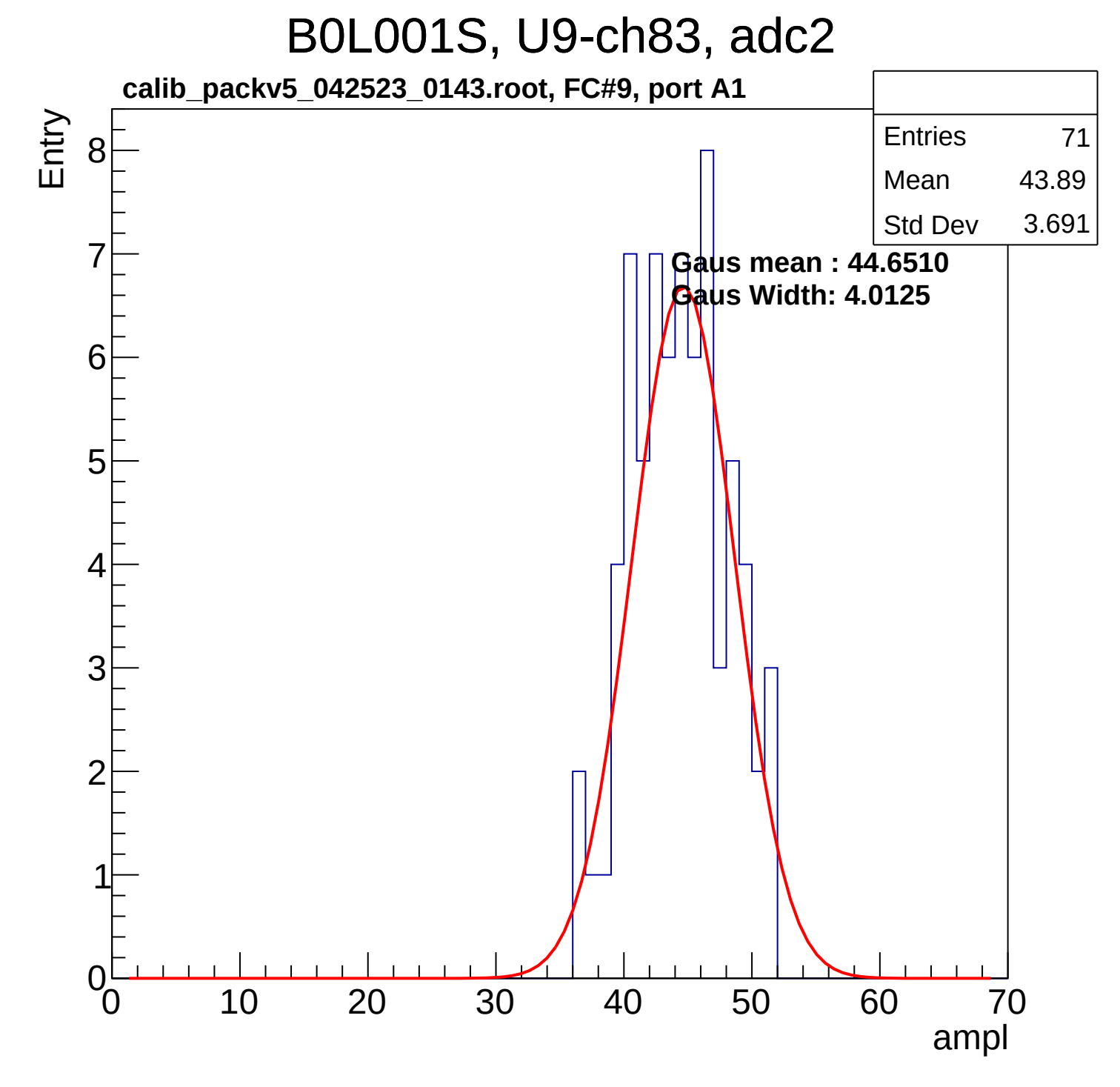

B0L001S, U9-ch82, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 59.05
Std Dev 10.03
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

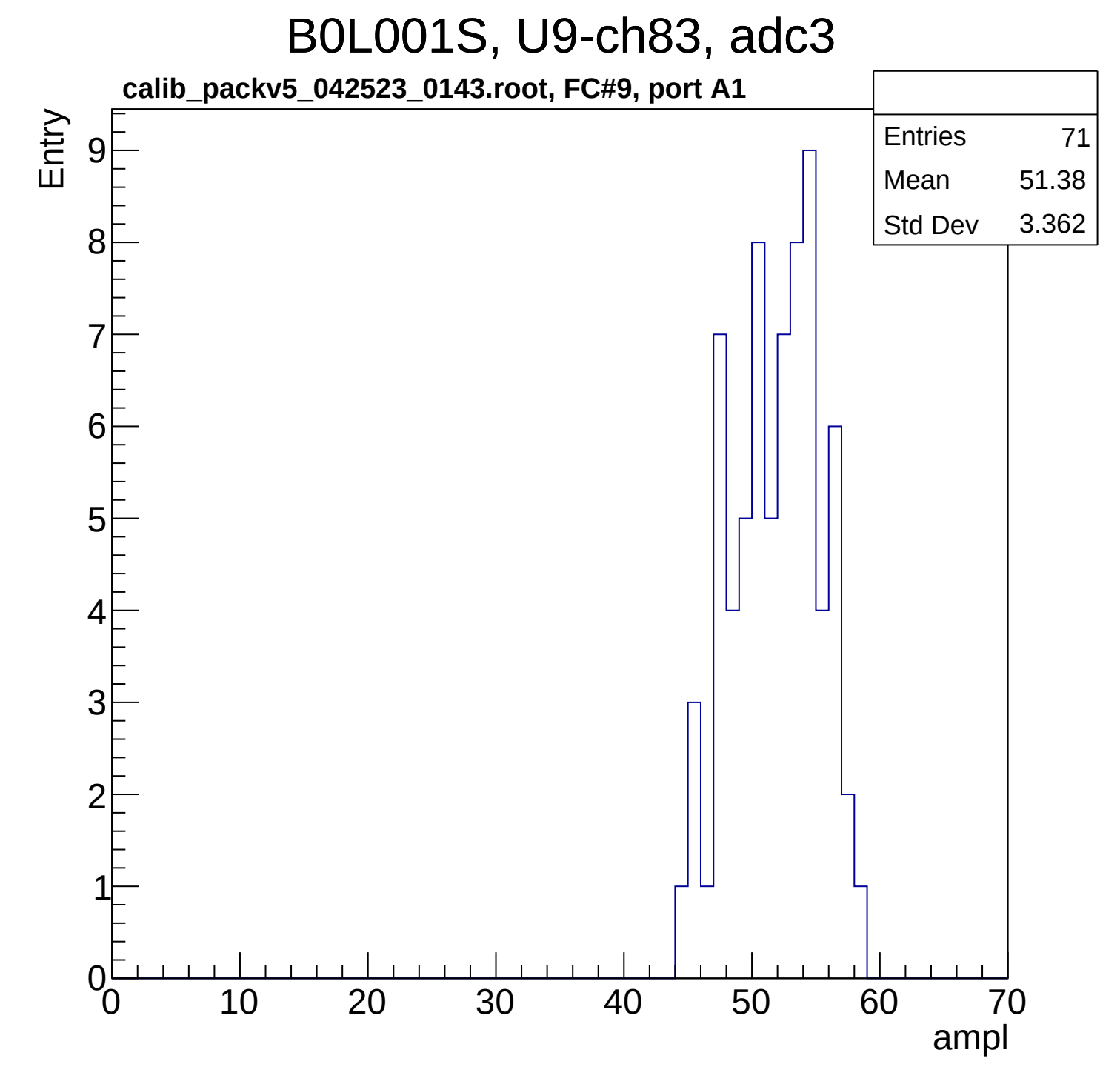

B0L001S, U9-ch82, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch82, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch83, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.03
Std Dev 4.82
Gaus mean : 30.1049
Gaus Width: 4.0461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch83, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.25
Std Dev 4.146
Gaus mean : 37.3466
Gaus Width: 4.3714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch83, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 43.89
Std Dev 3.691
Gaus mean : 44.6510
Gaus Width: 4.0125
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch83, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.38
Std Dev 3.362
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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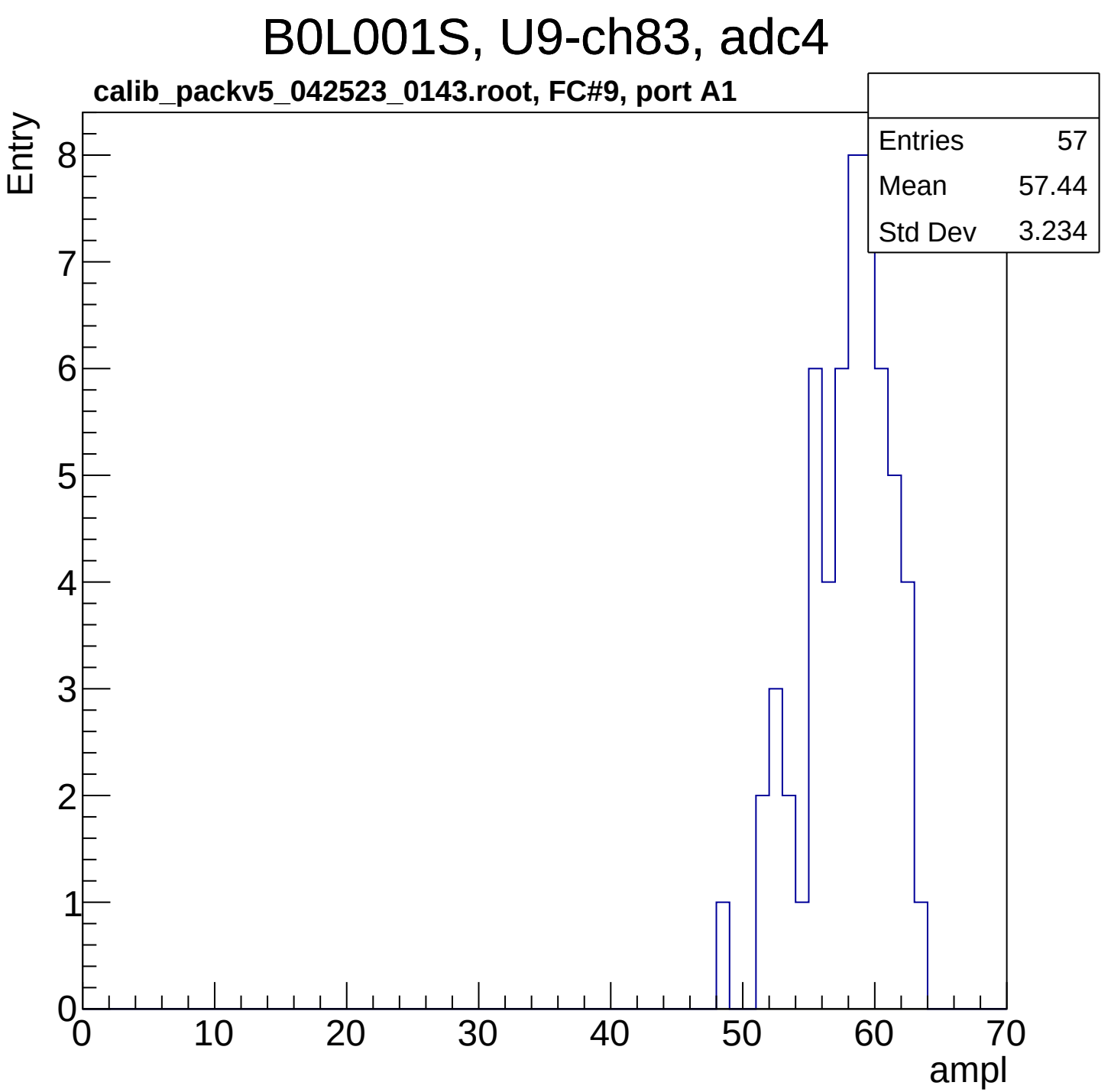
B0L001S, U9-ch83, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.44
Std Dev 3.234
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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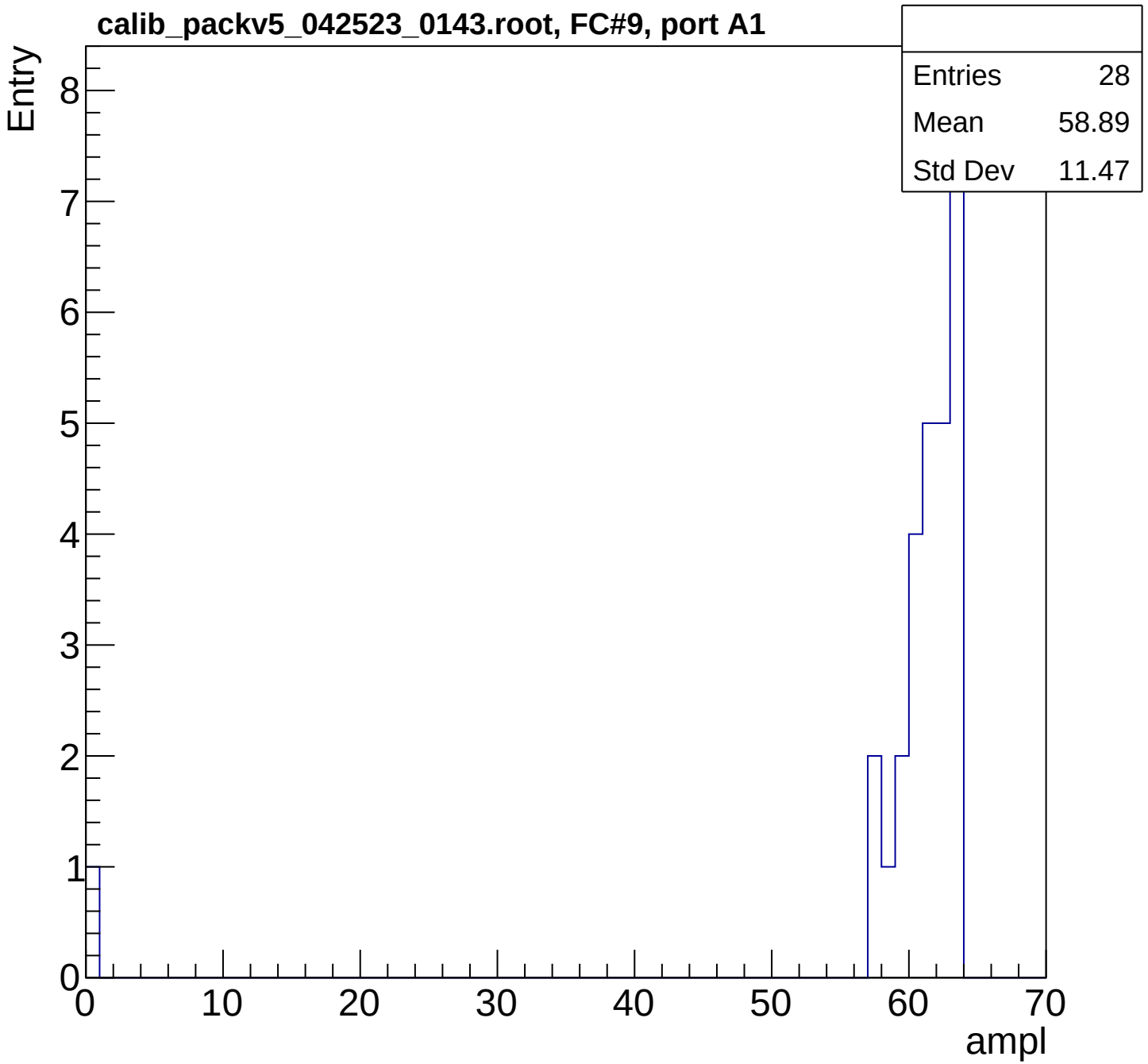
B0L001S, U9-ch83, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.89
Std Dev 11.47
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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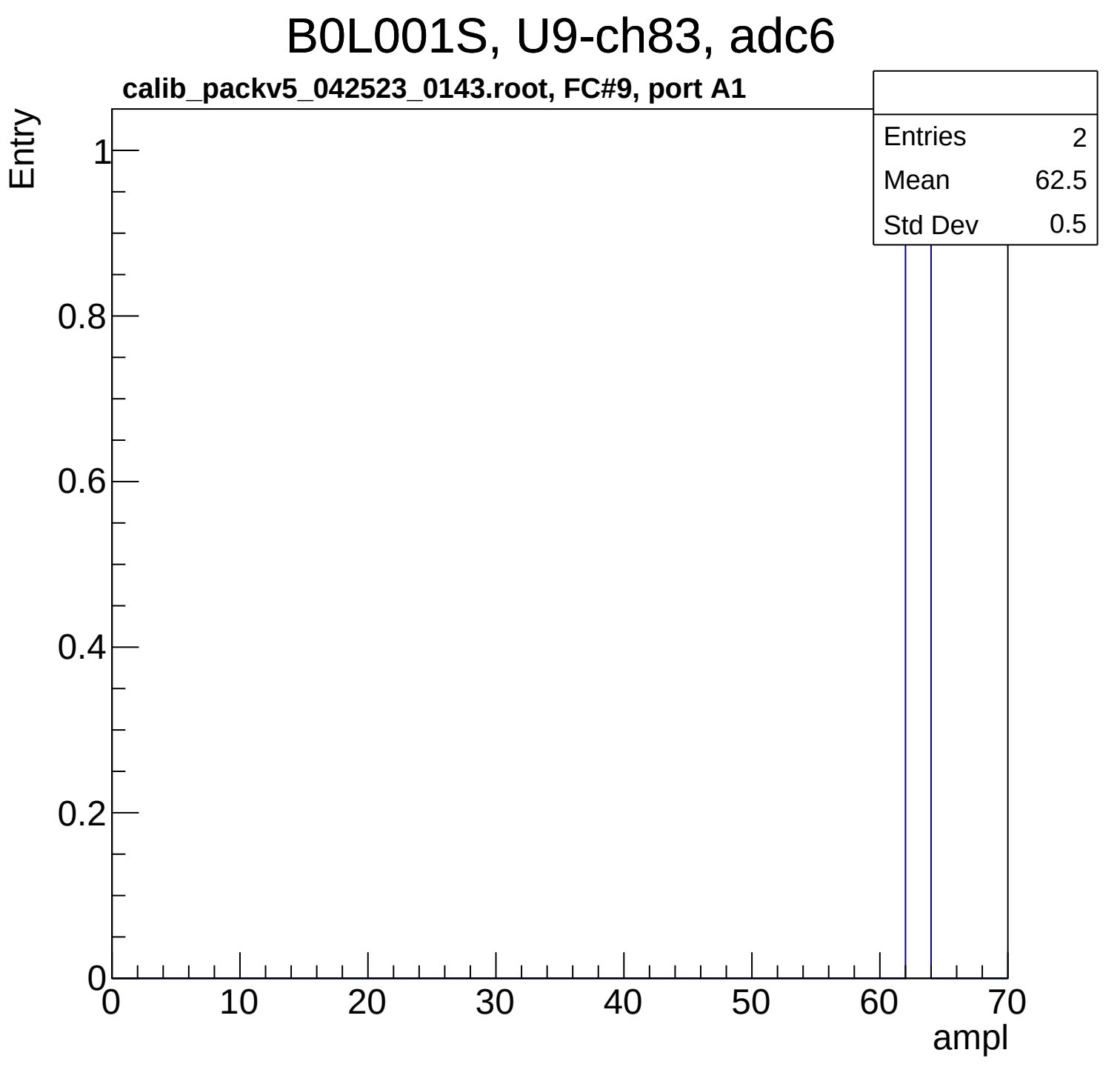
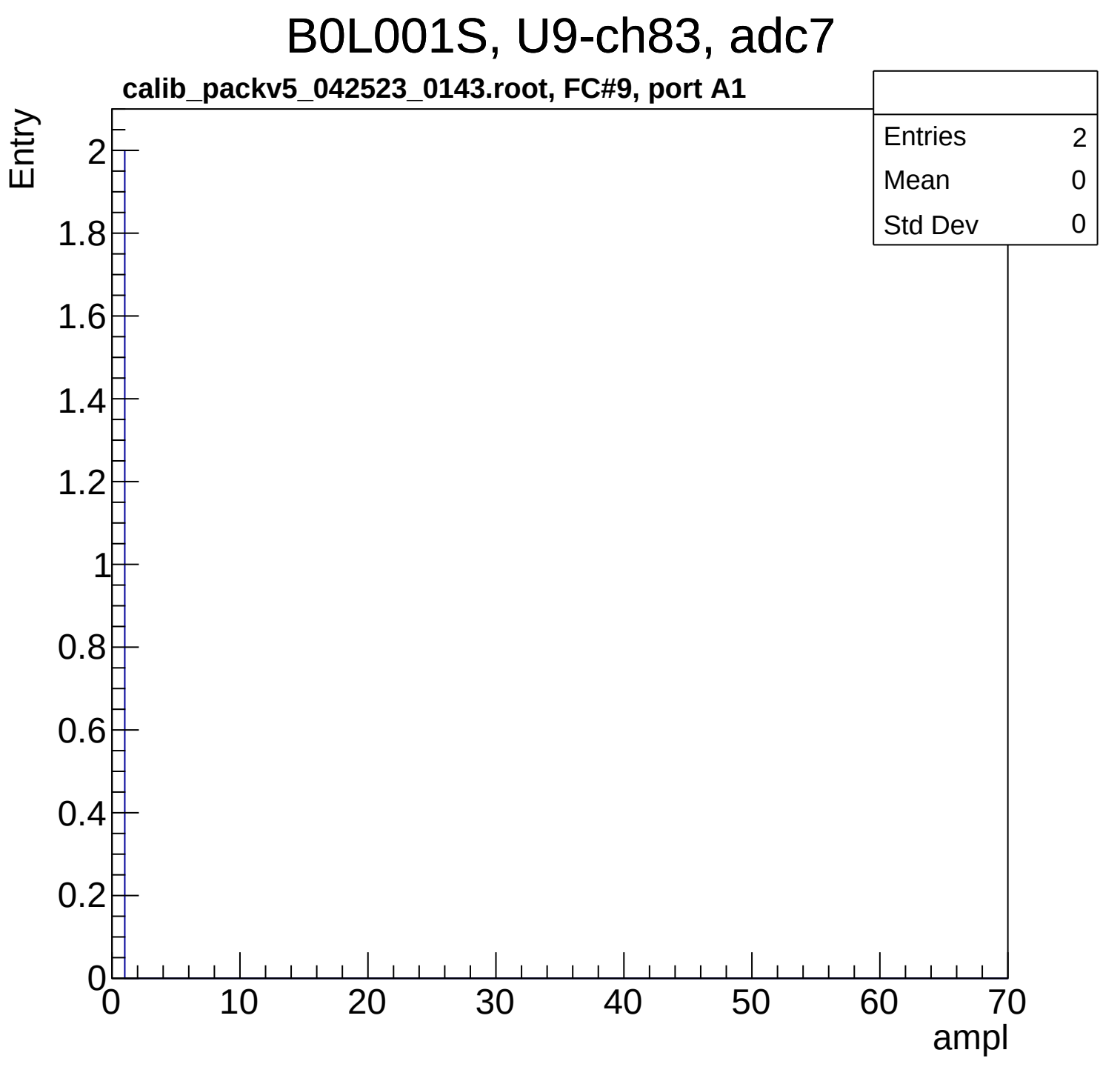

B0L001S, U9-ch83, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch83, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
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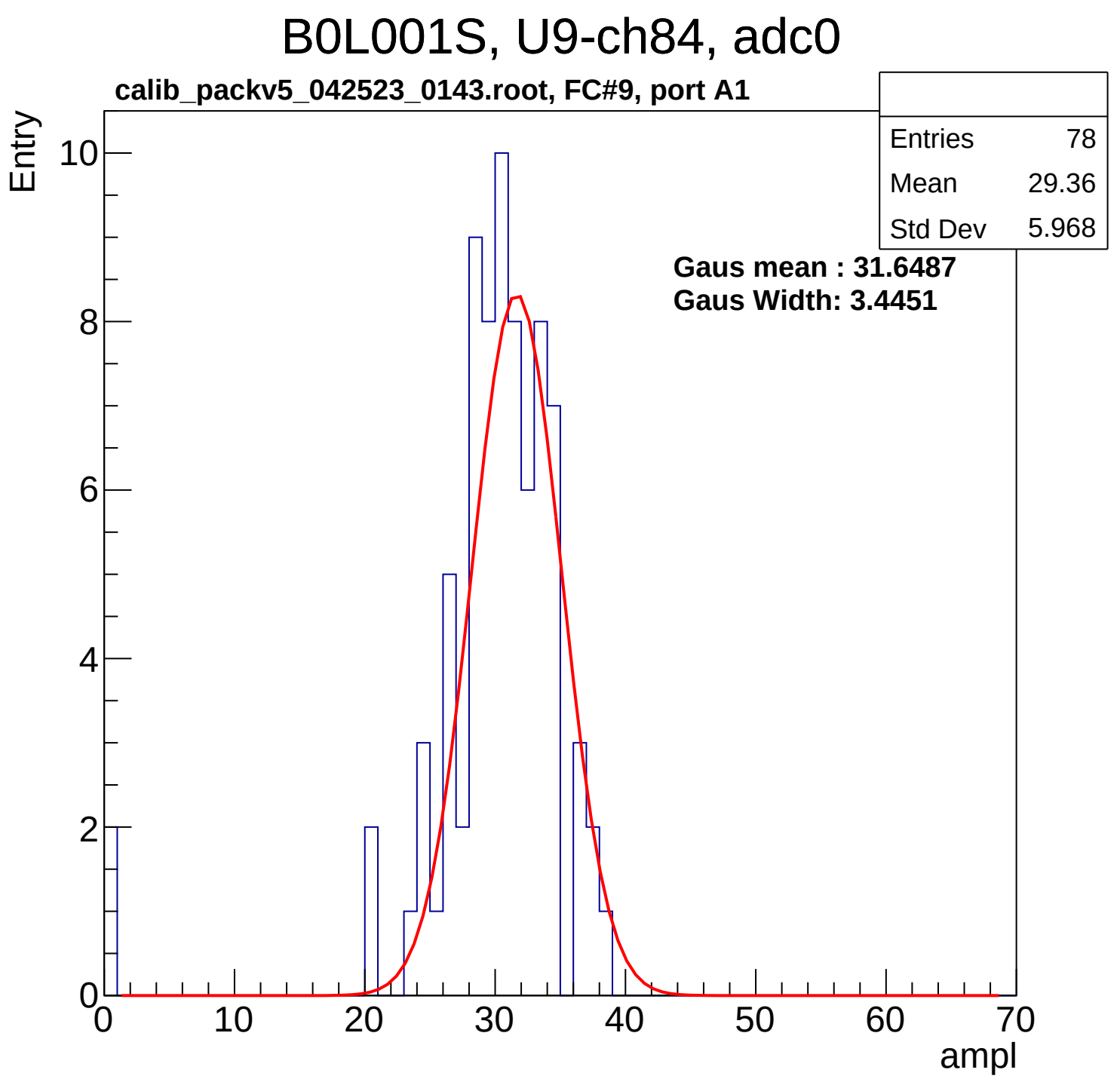
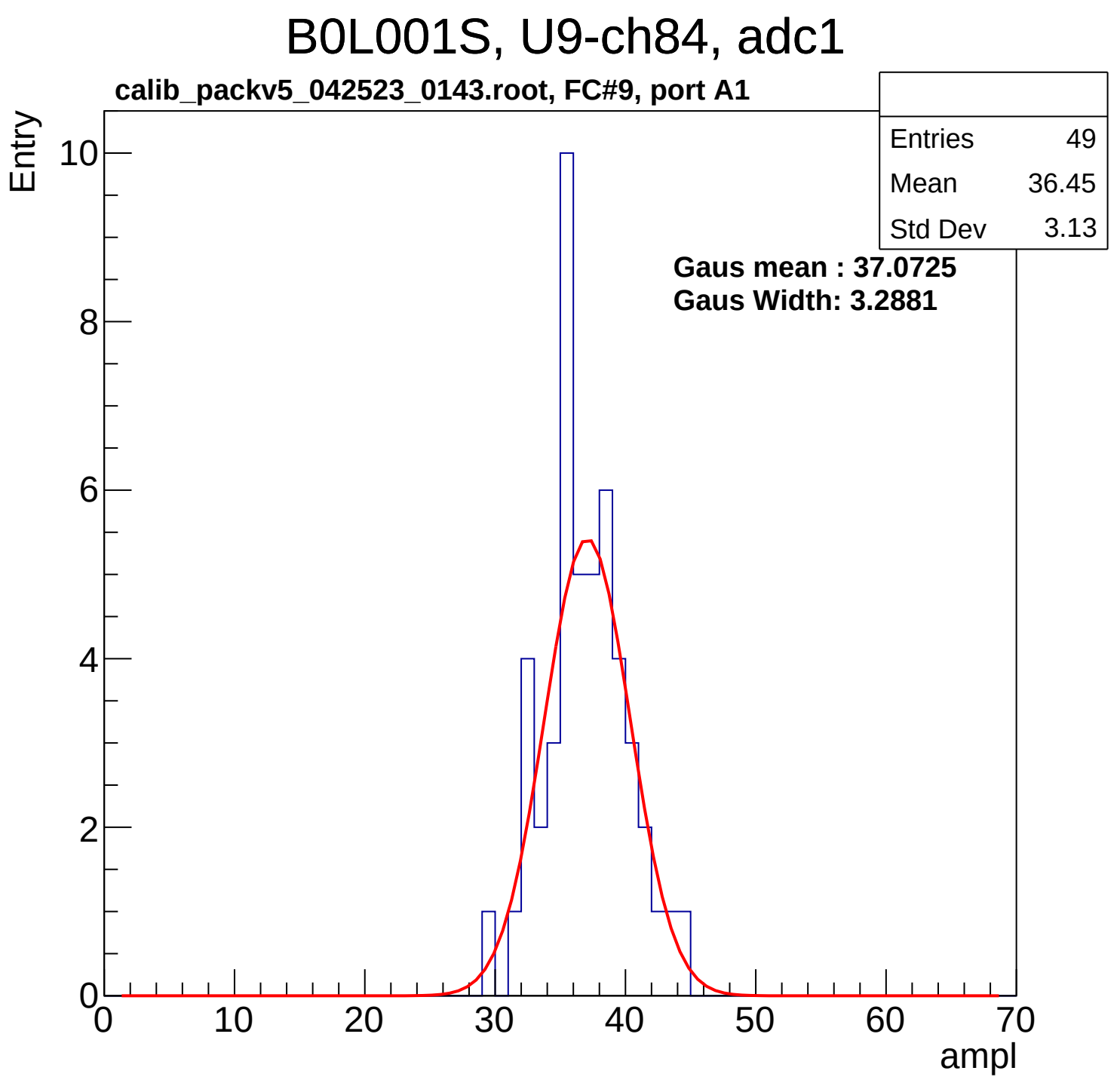
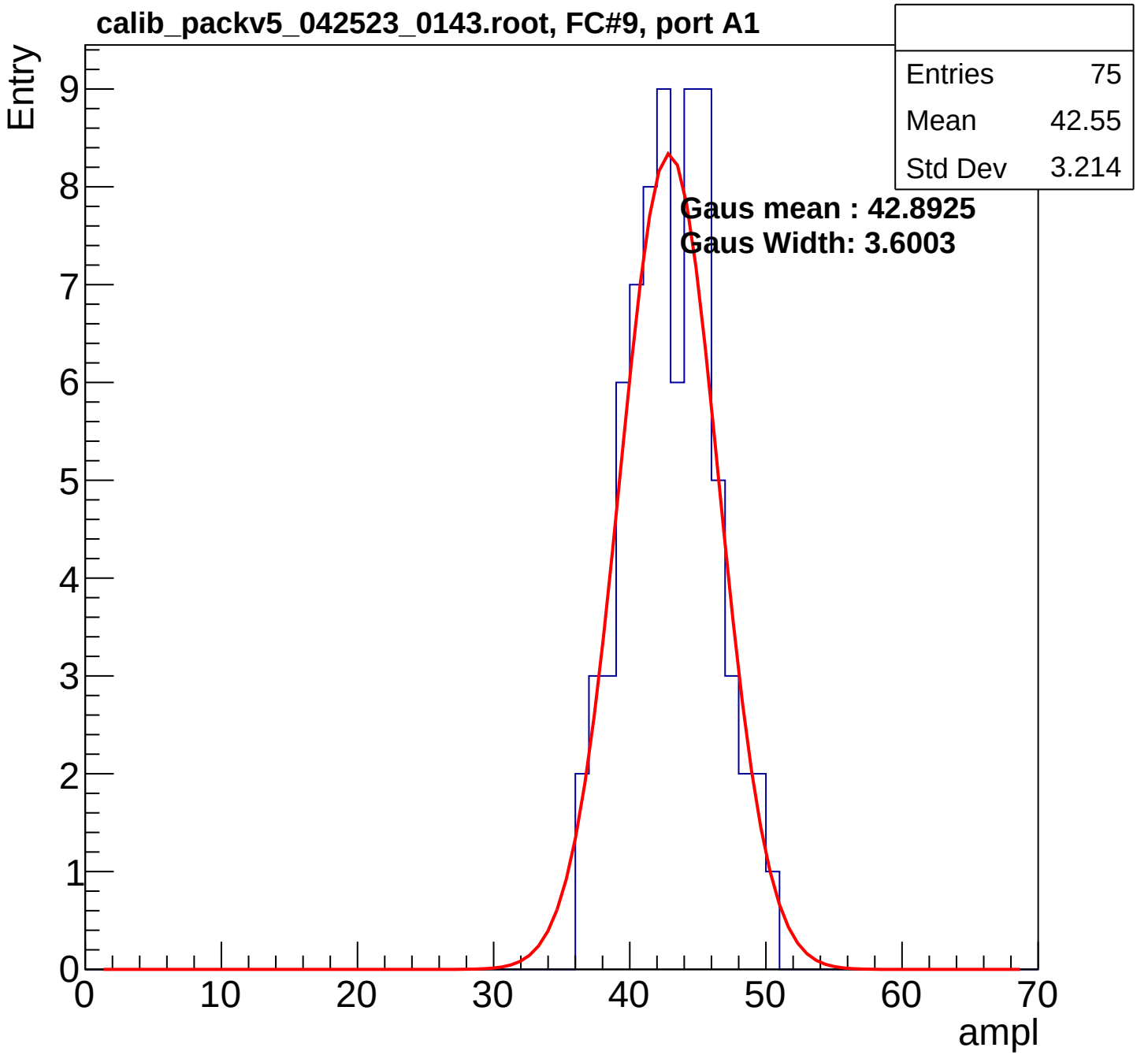
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch84, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.36
Std Dev 5.968
Gaus mean : 31.6487
Gaus Width: 3.4451
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch84, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 36.45
Std Dev 3.13
Gaus mean : 37.0725
Gaus Width: 3.2881
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch84, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 42.55
Std Dev 3.214
Gaus mean : 42.8925
Gaus Width: 3.6003
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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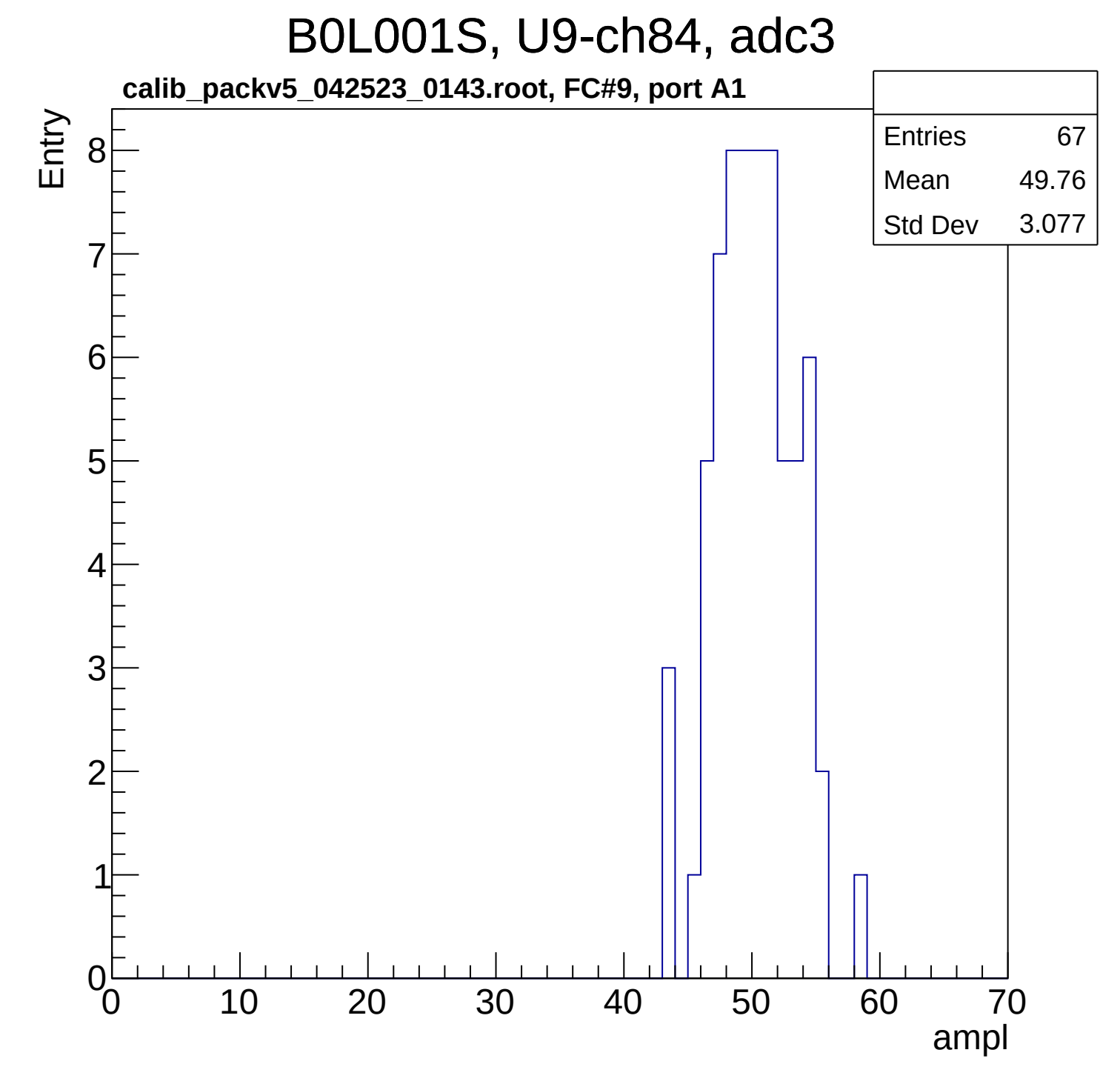
B0L001S, U9-ch84, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 49.76
Std Dev 3.077
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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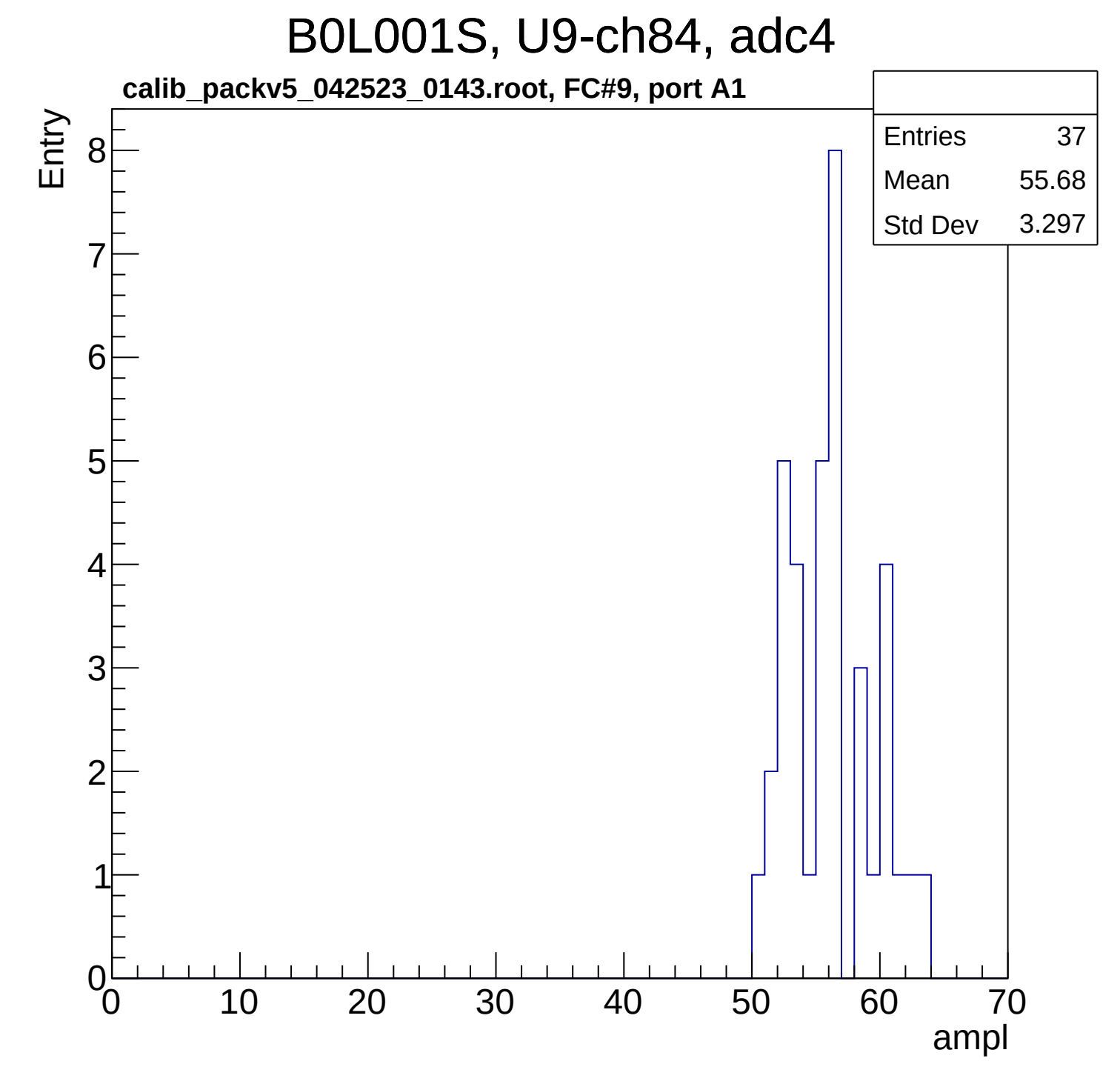
B0L001S, U9-ch84, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 55.68
Std Dev 3.297
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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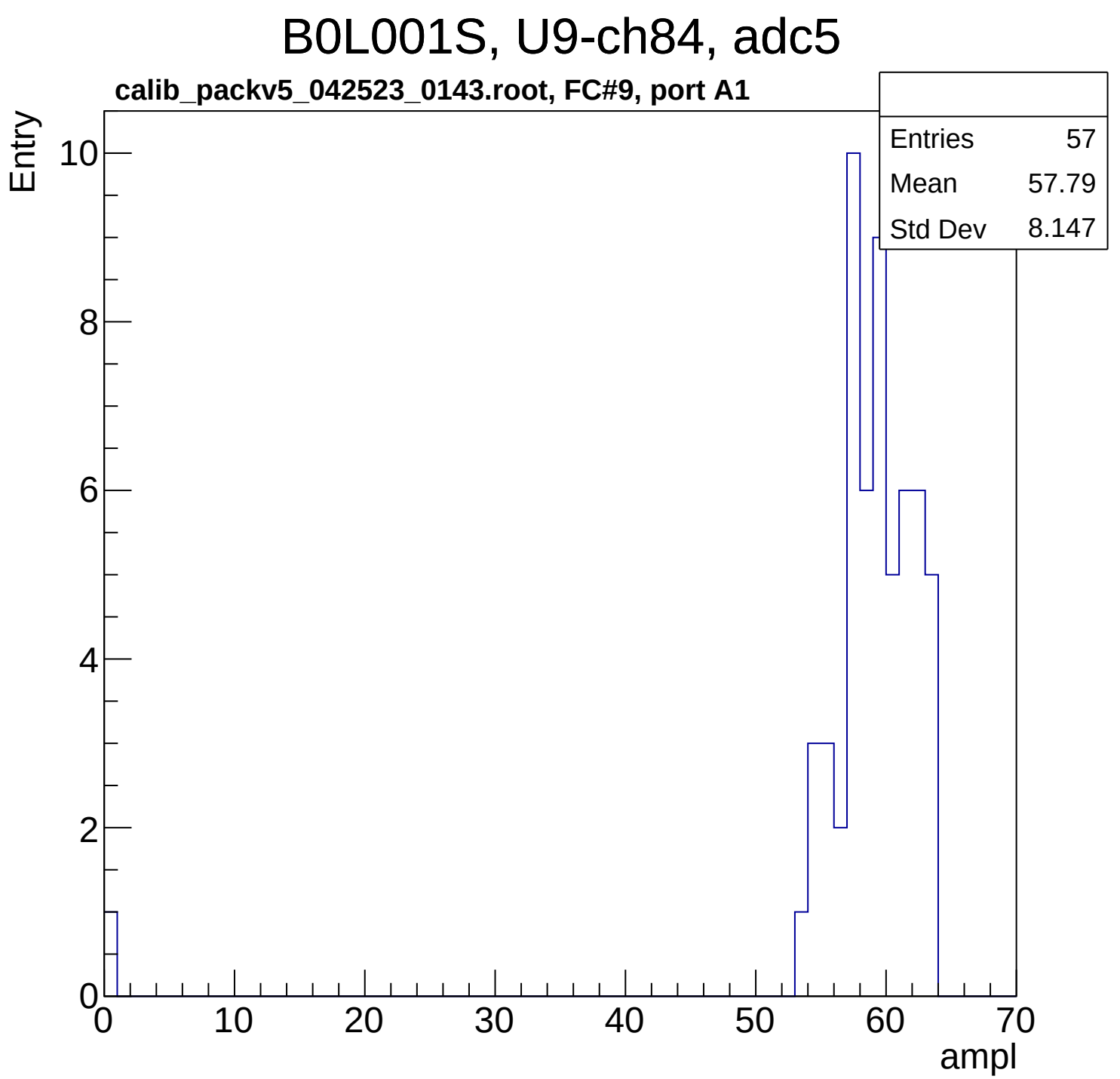
B0L001S, U9-ch84, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 57.79
Std Dev 8.147
Entry
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2
4
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10
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10
20
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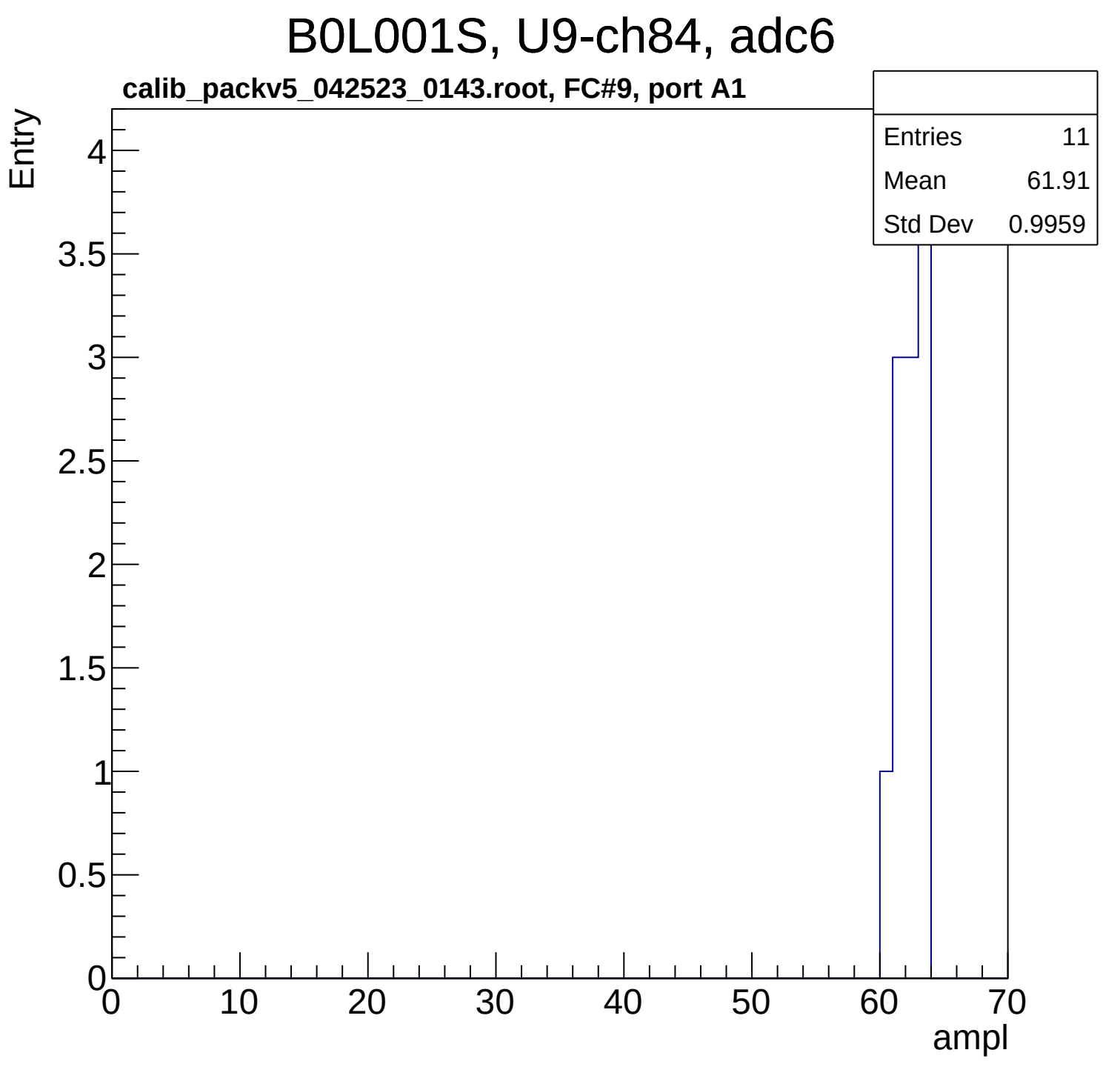
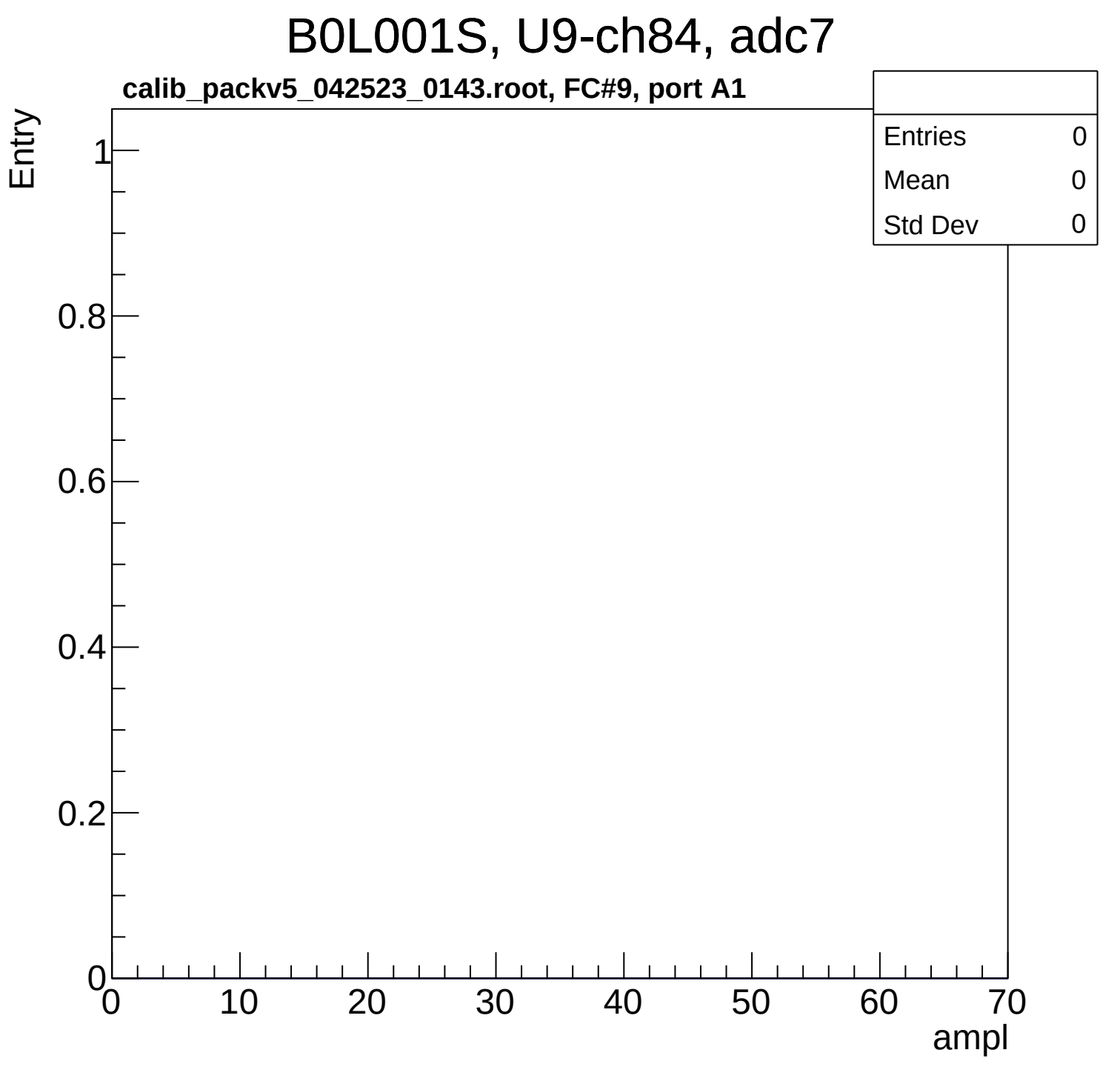
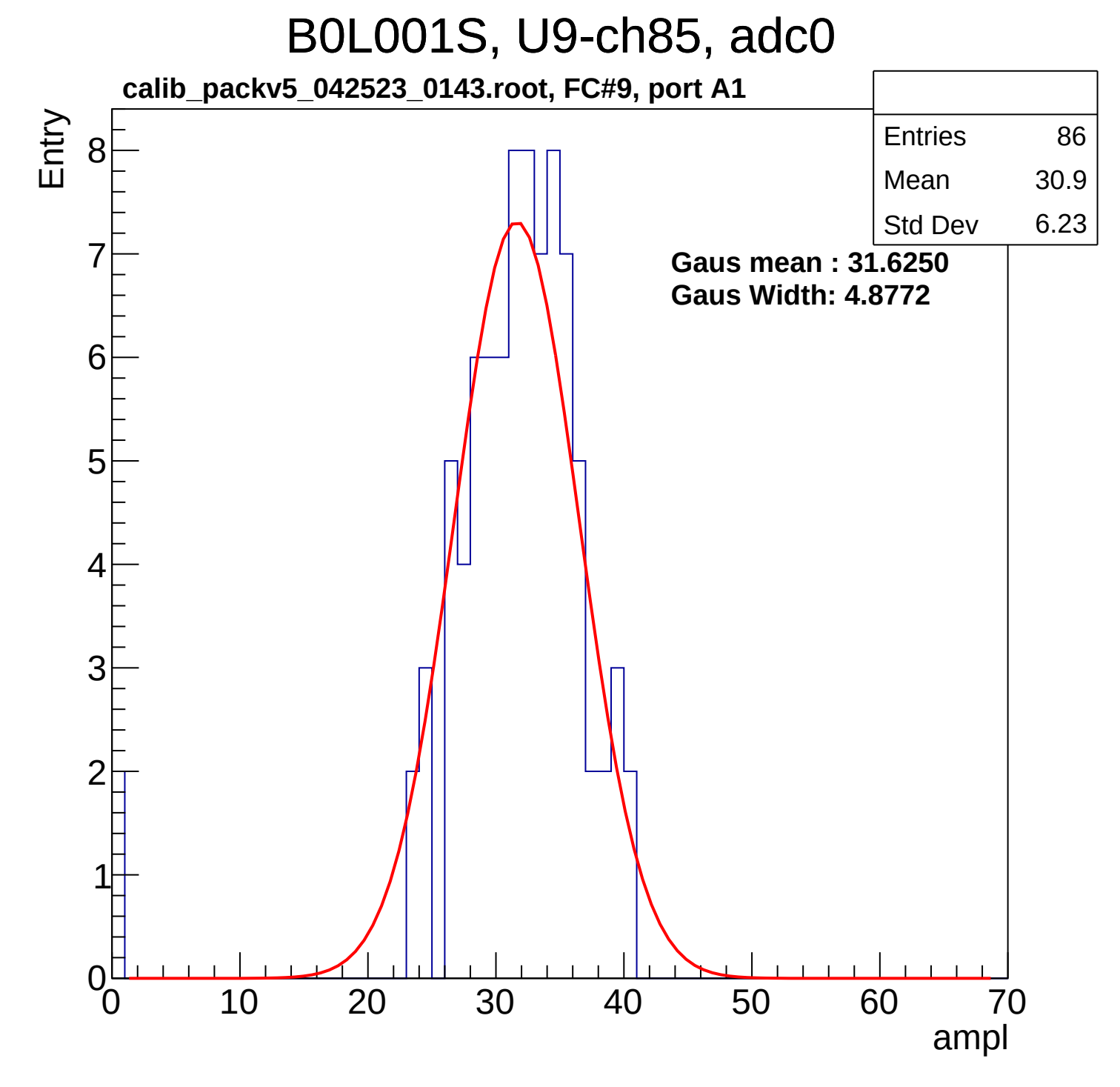
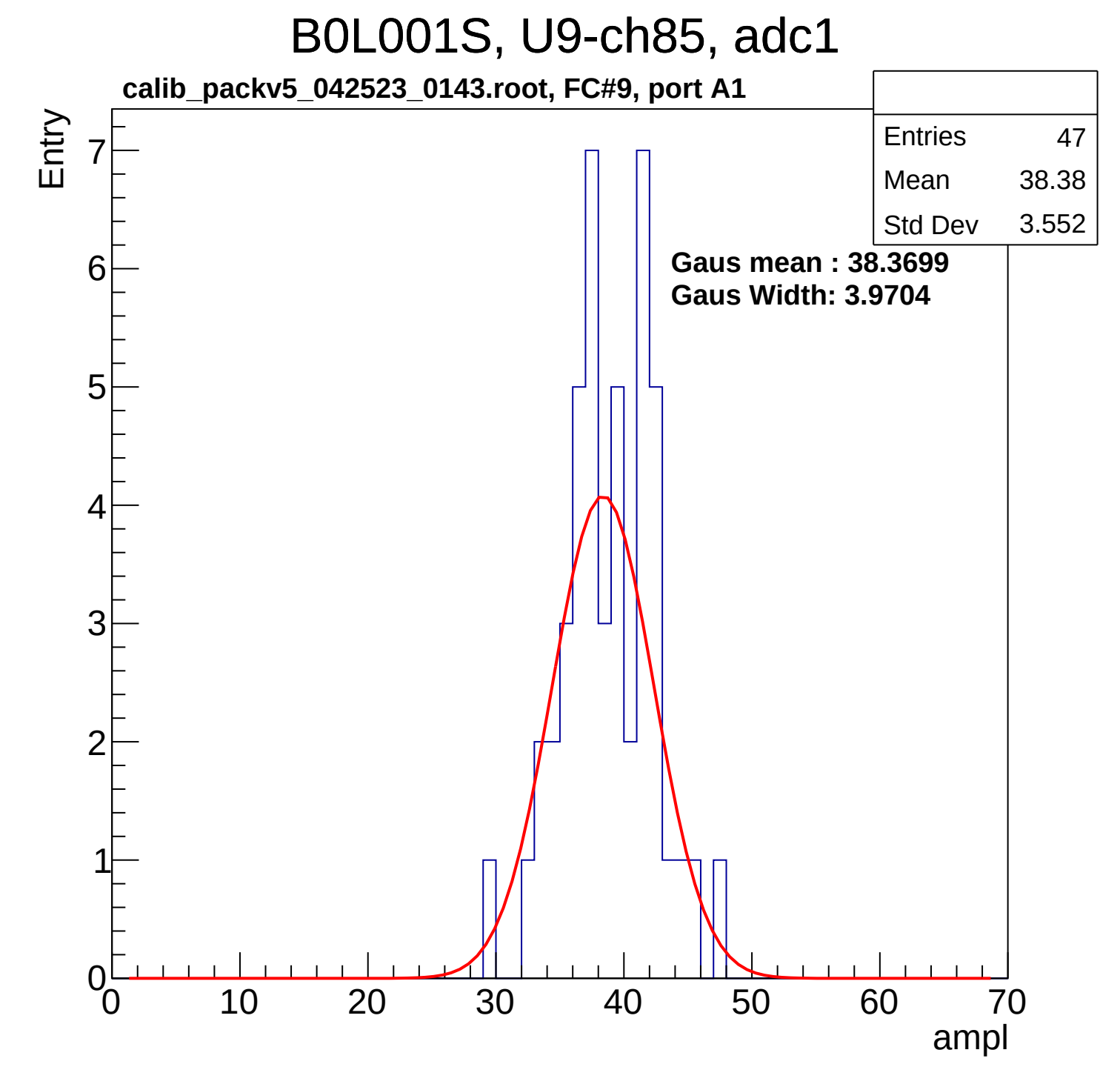
40
50
60
70
ampl


B0L001S, U9-ch84, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.91
Std Dev 0.9959
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U9-ch84, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch85, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 30.9
Std Dev 6.23
Gaus mean : 31.6250
Gaus Width: 4.8772
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch85, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 38.38
Std Dev 3.552
Gaus mean : 38.3699
Gaus Width: 3.9704
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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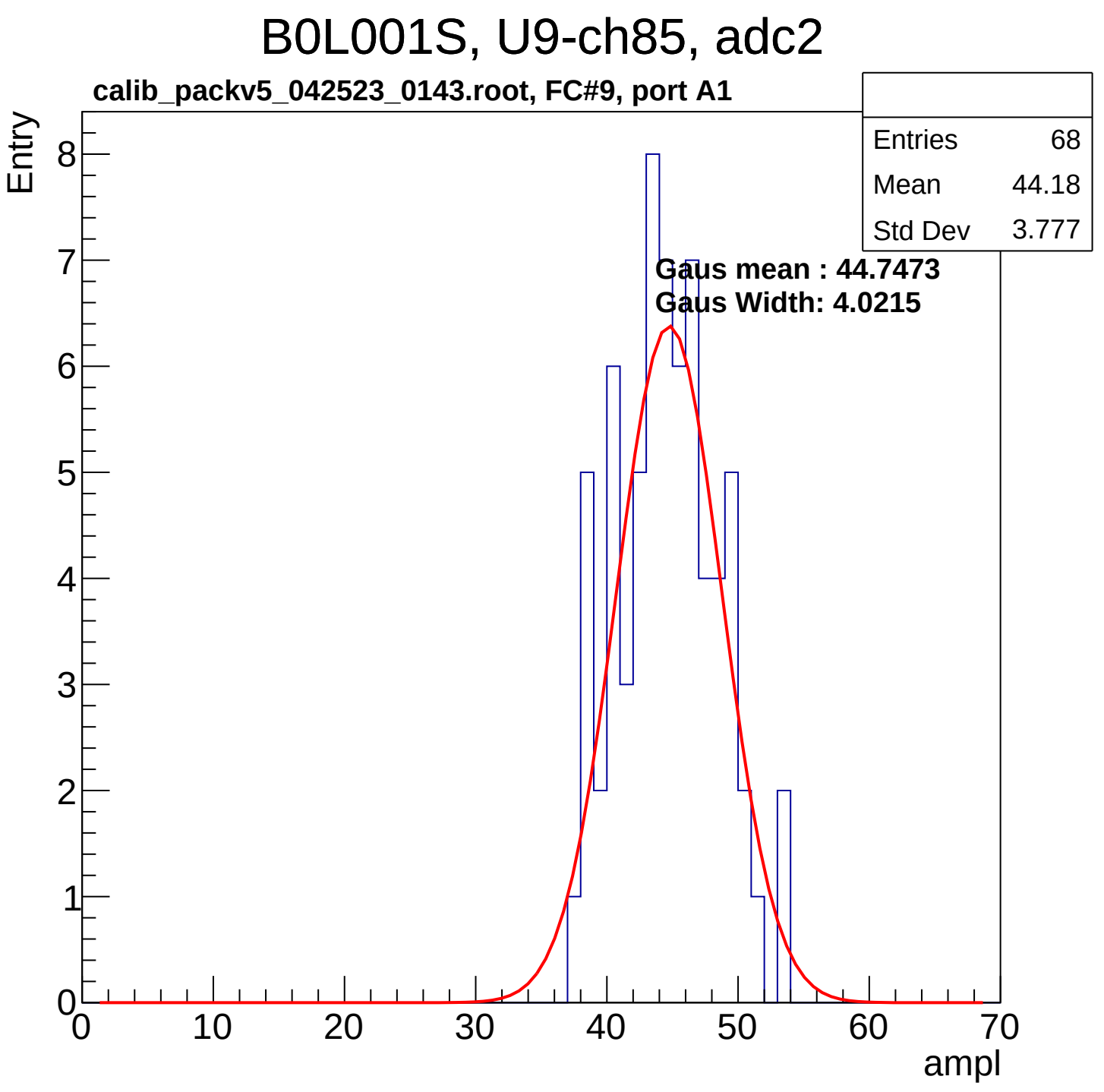
B0L001S, U9-ch85, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.18
Std Dev 3.777
Gaus mean : 44.7473
Gaus Width: 4.0215
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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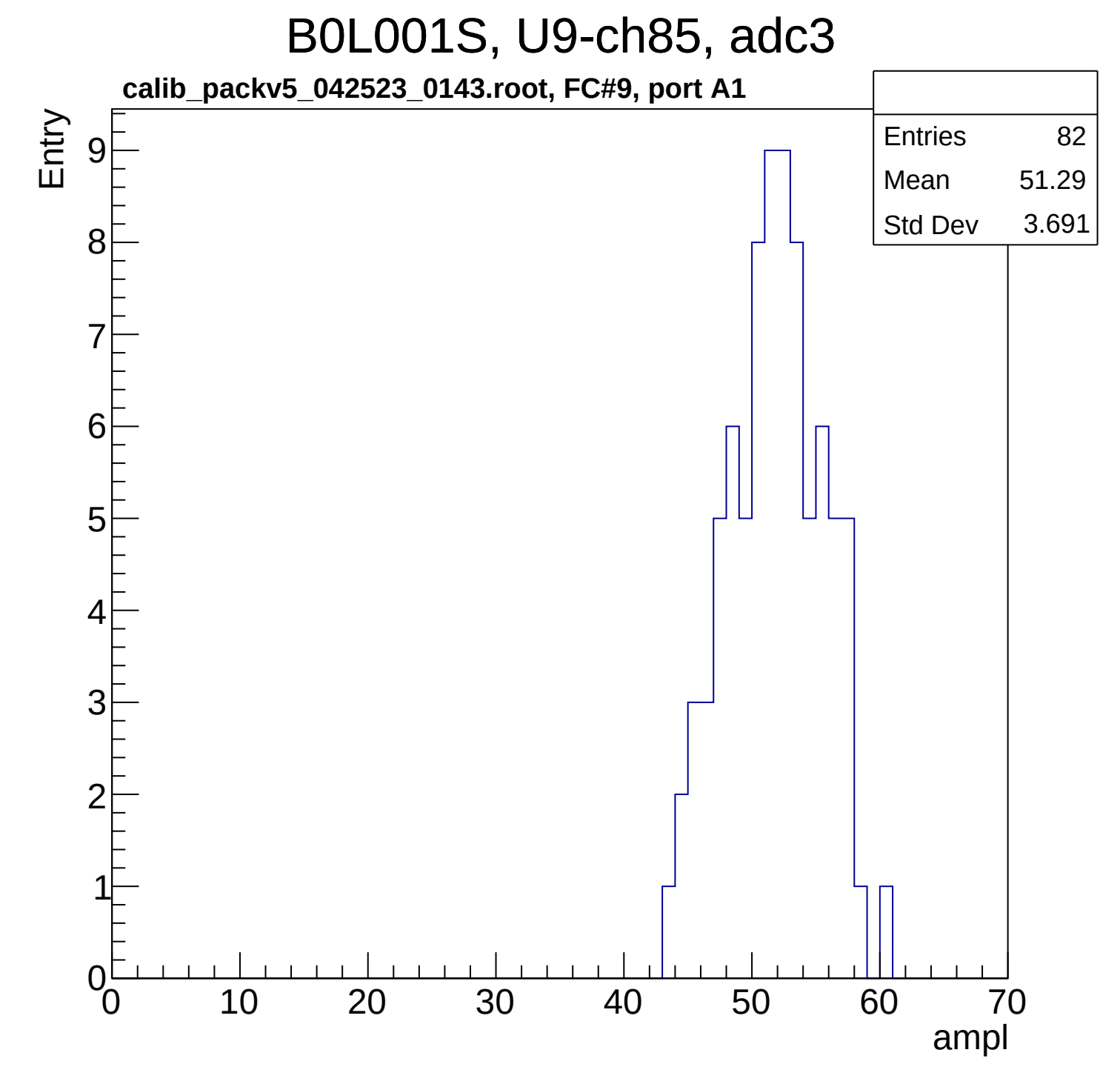
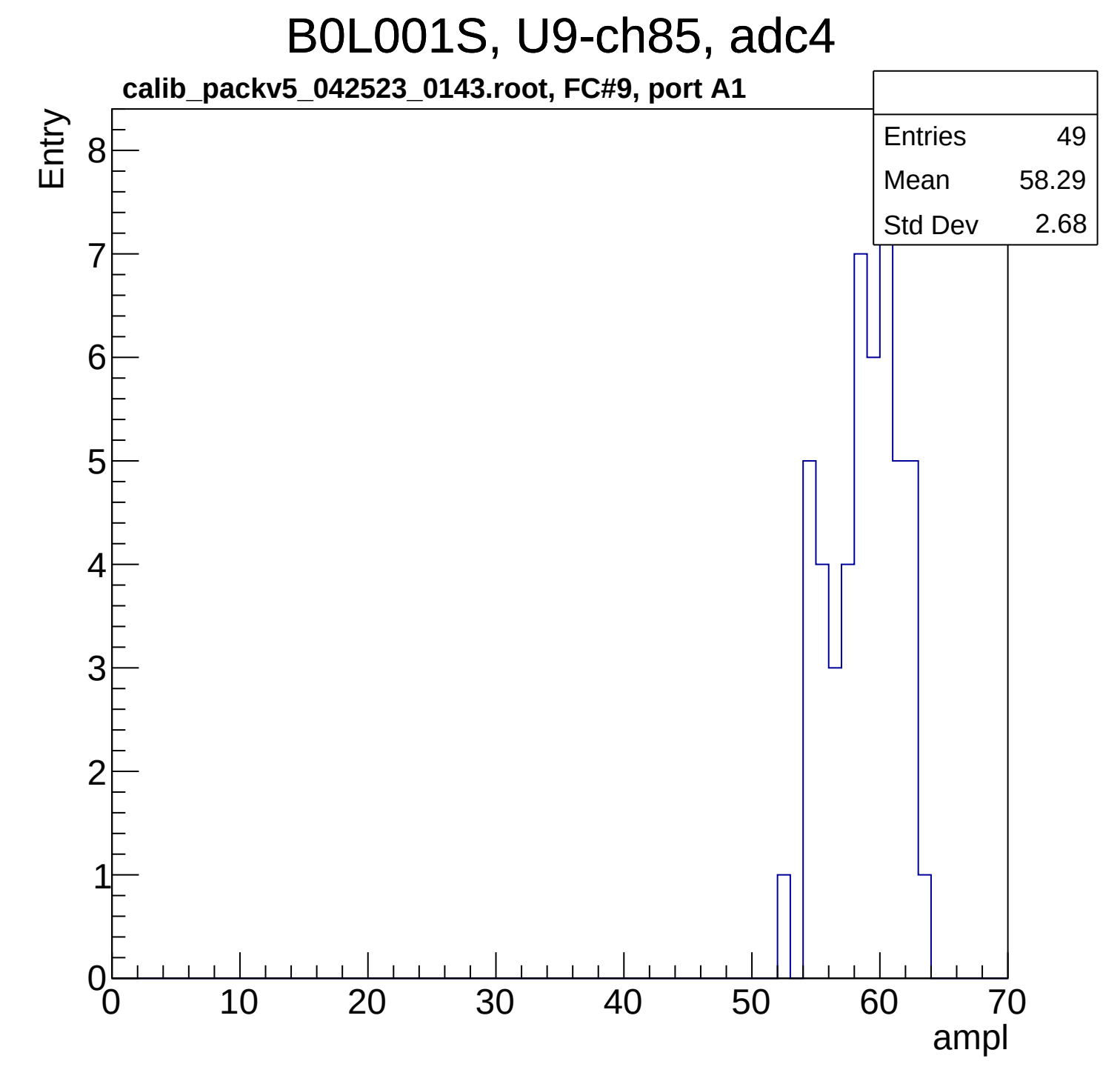

B0L001S, U9-ch85, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 51.29
Std Dev 3.691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch85, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.29
Std Dev 2.68
Entry
ampl
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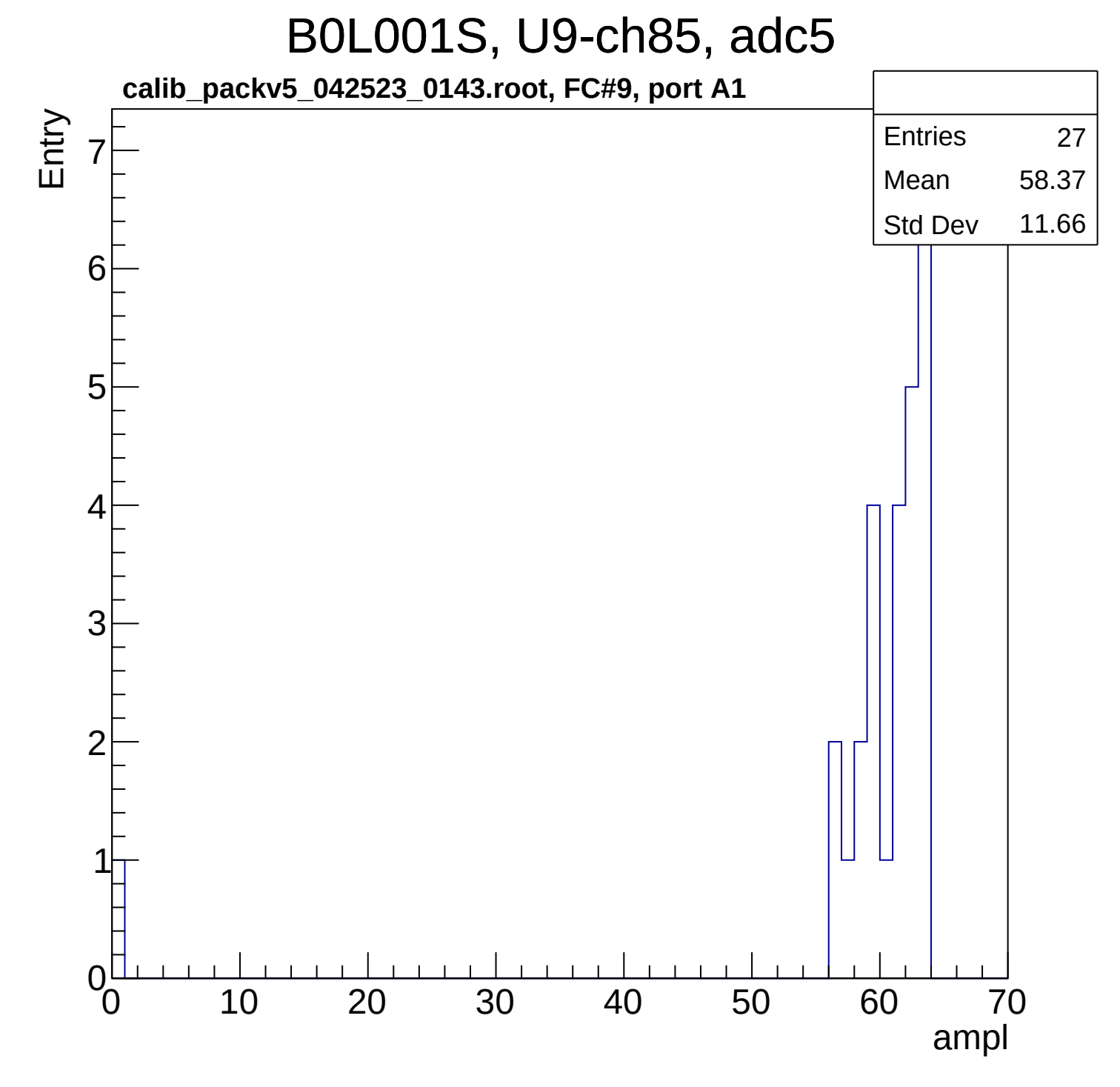
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch85, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.37
Std Dev 11.66
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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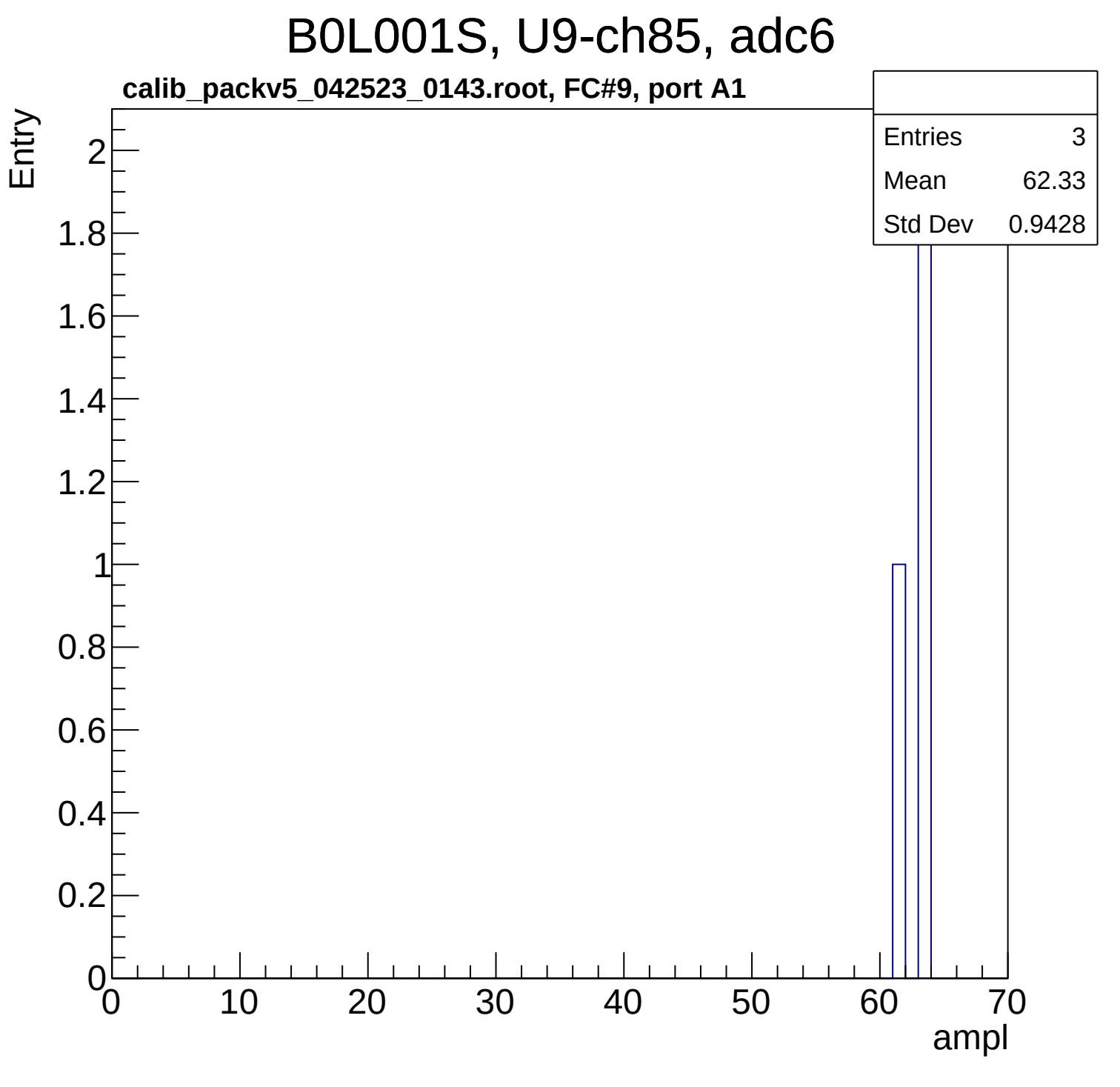
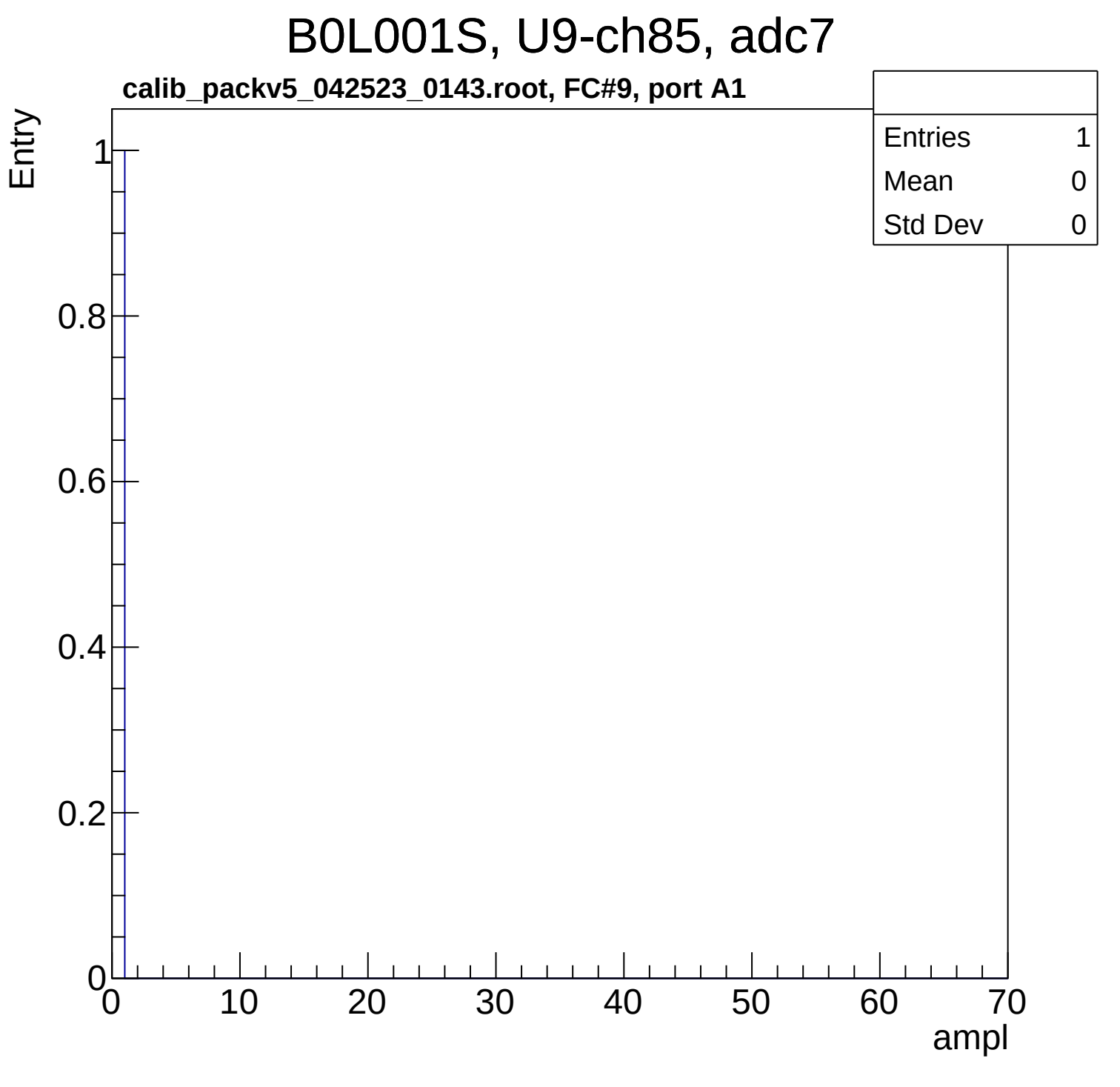
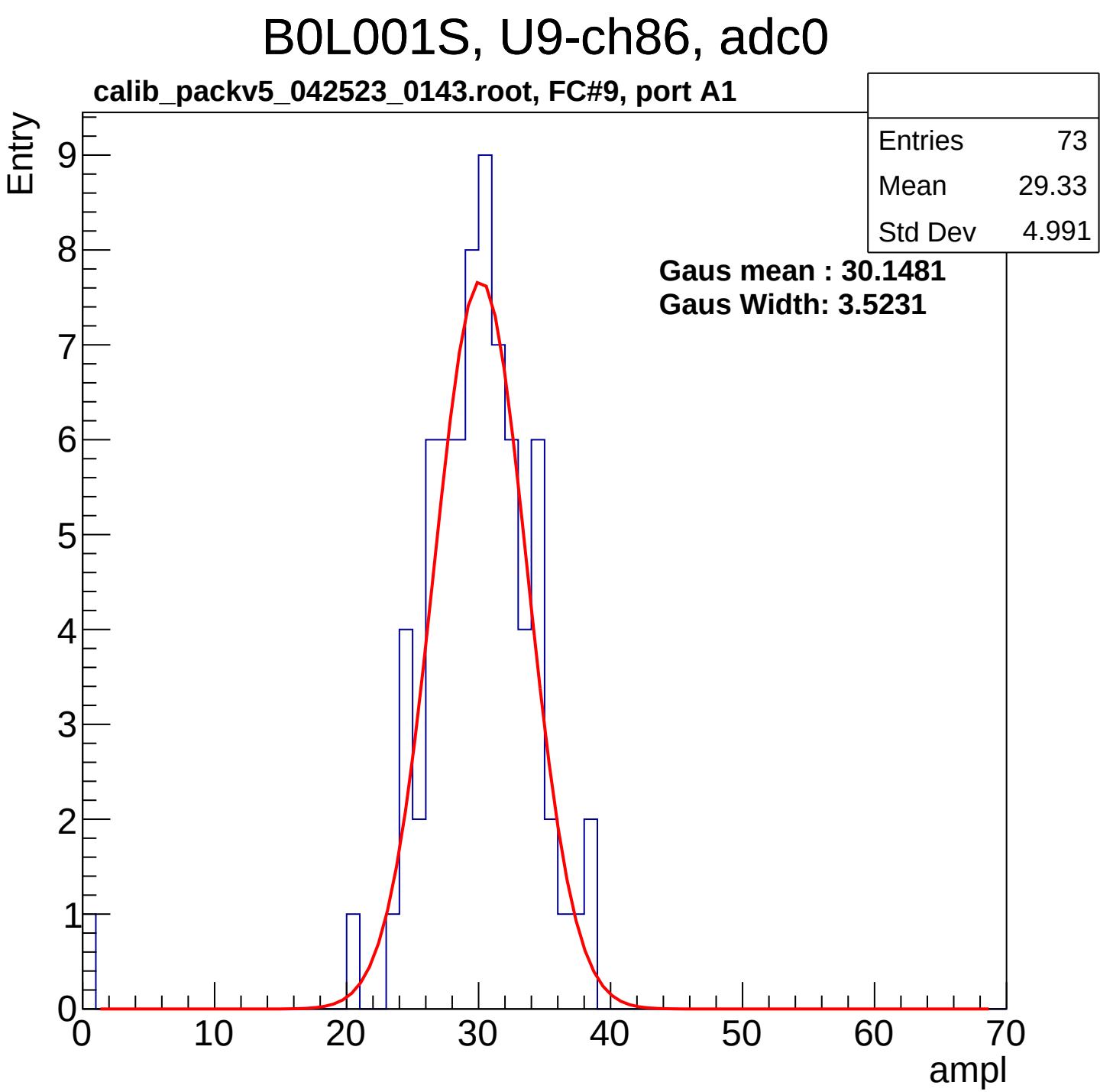
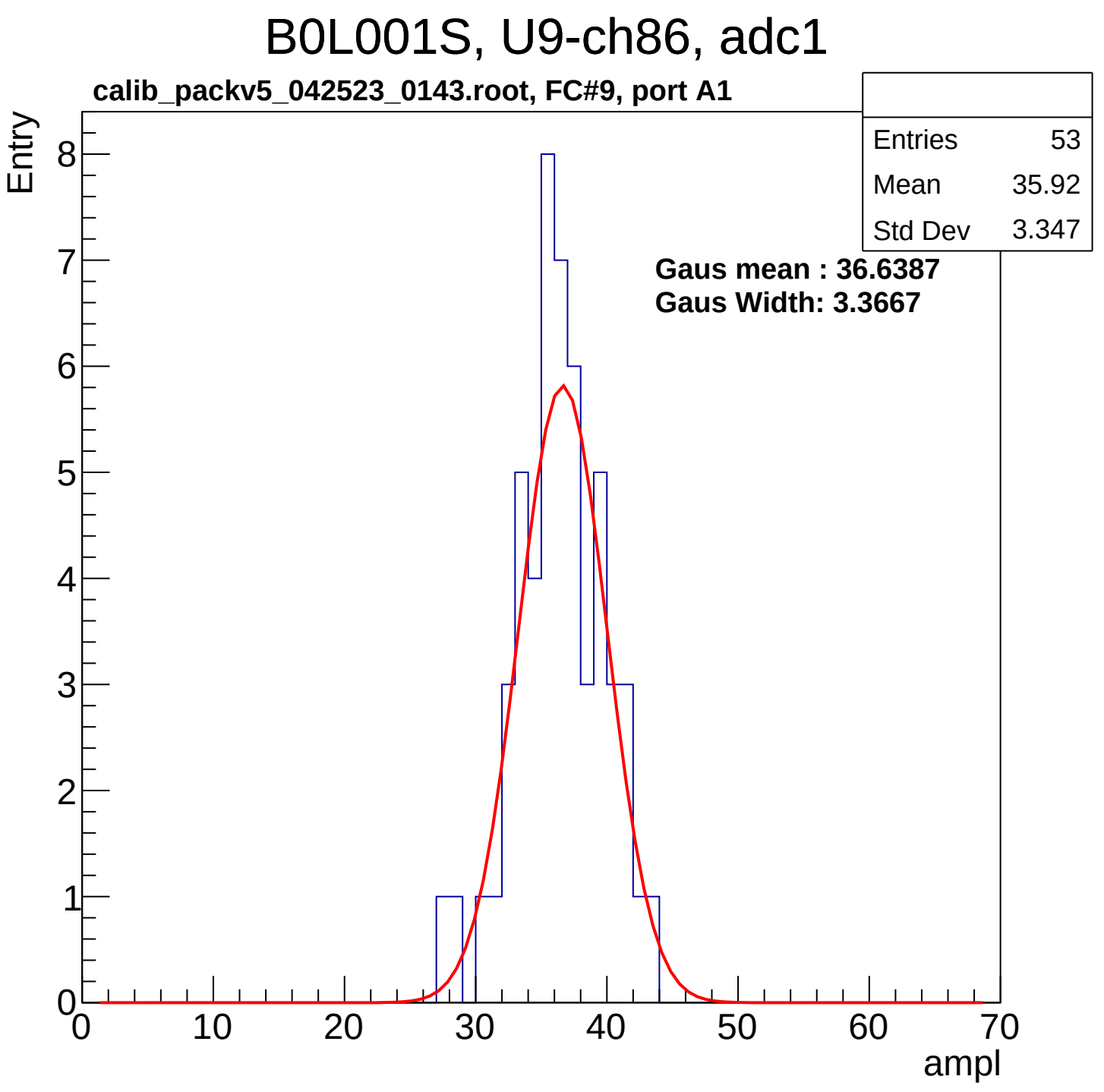
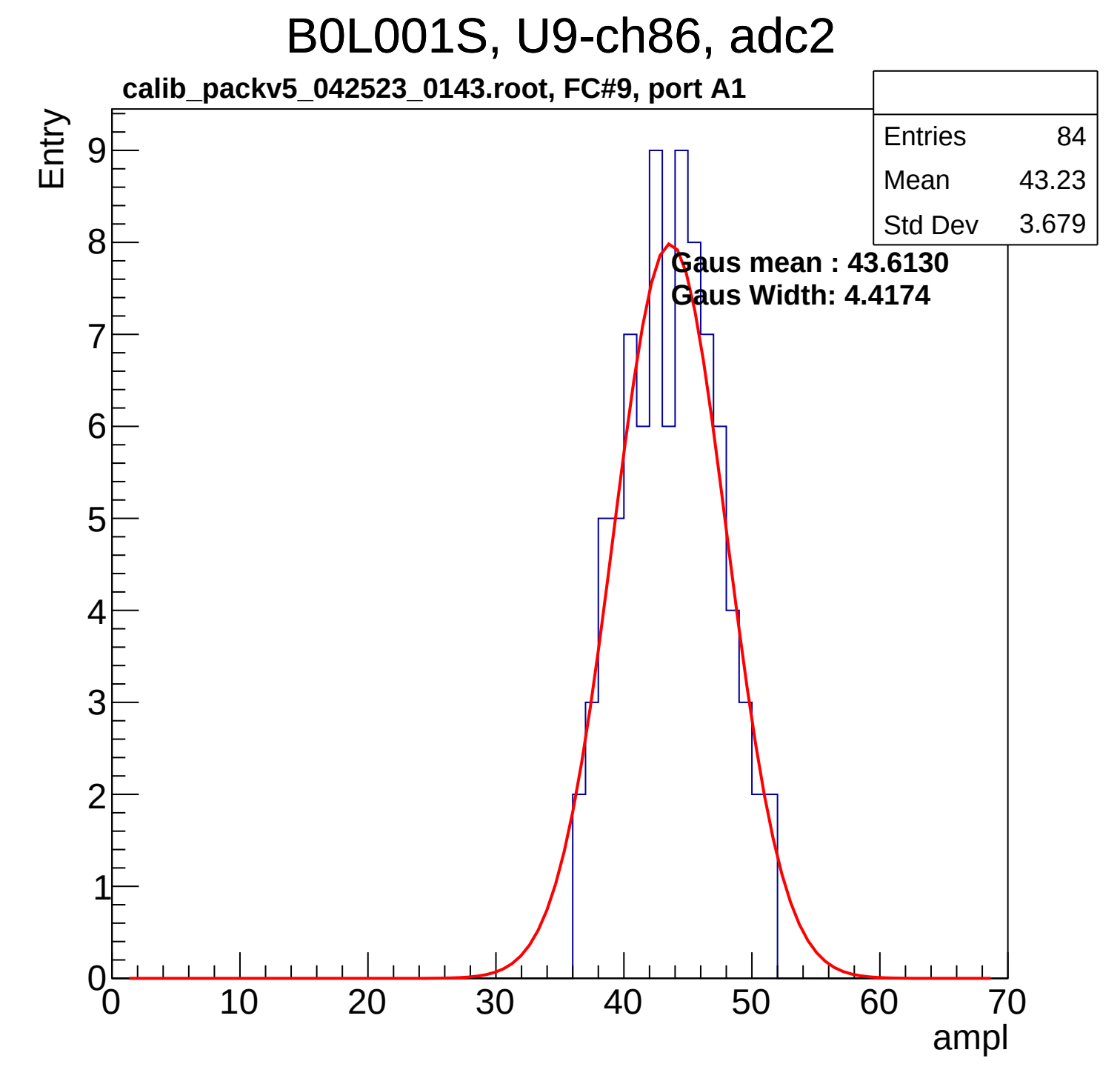

B0L001S, U9-ch85, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch85, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch86, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.33
Std Dev 4.991
Gaus mean : 30.1481
Gaus Width: 3.5231
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch86, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 35.92
Std Dev 3.347
Gaus mean : 36.6387
Gaus Width: 3.3667
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch86, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 84
Mean 43.23
Std Dev 3.679
Gaus mean : 43.6130
Gaus Width: 4.4174
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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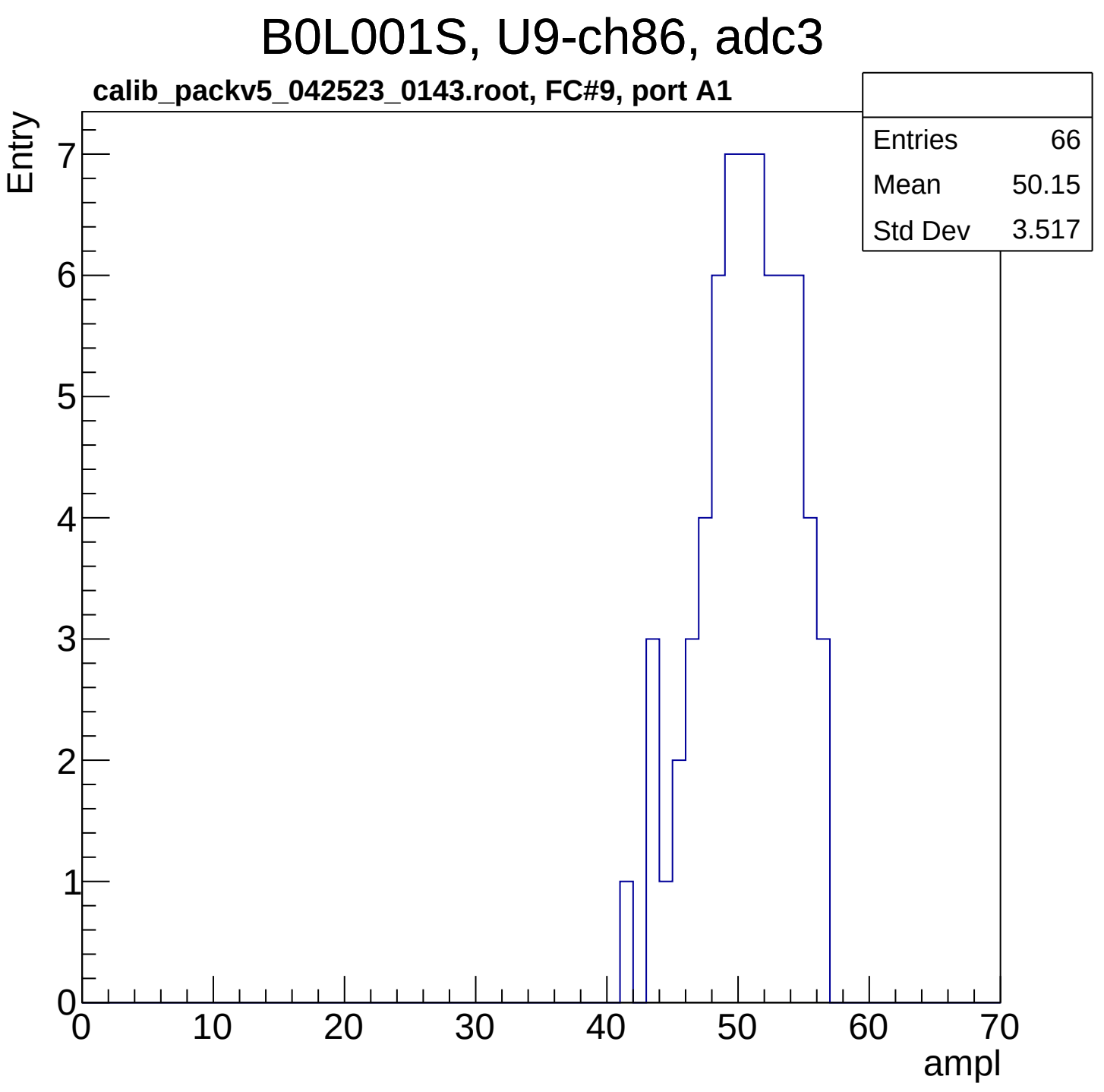
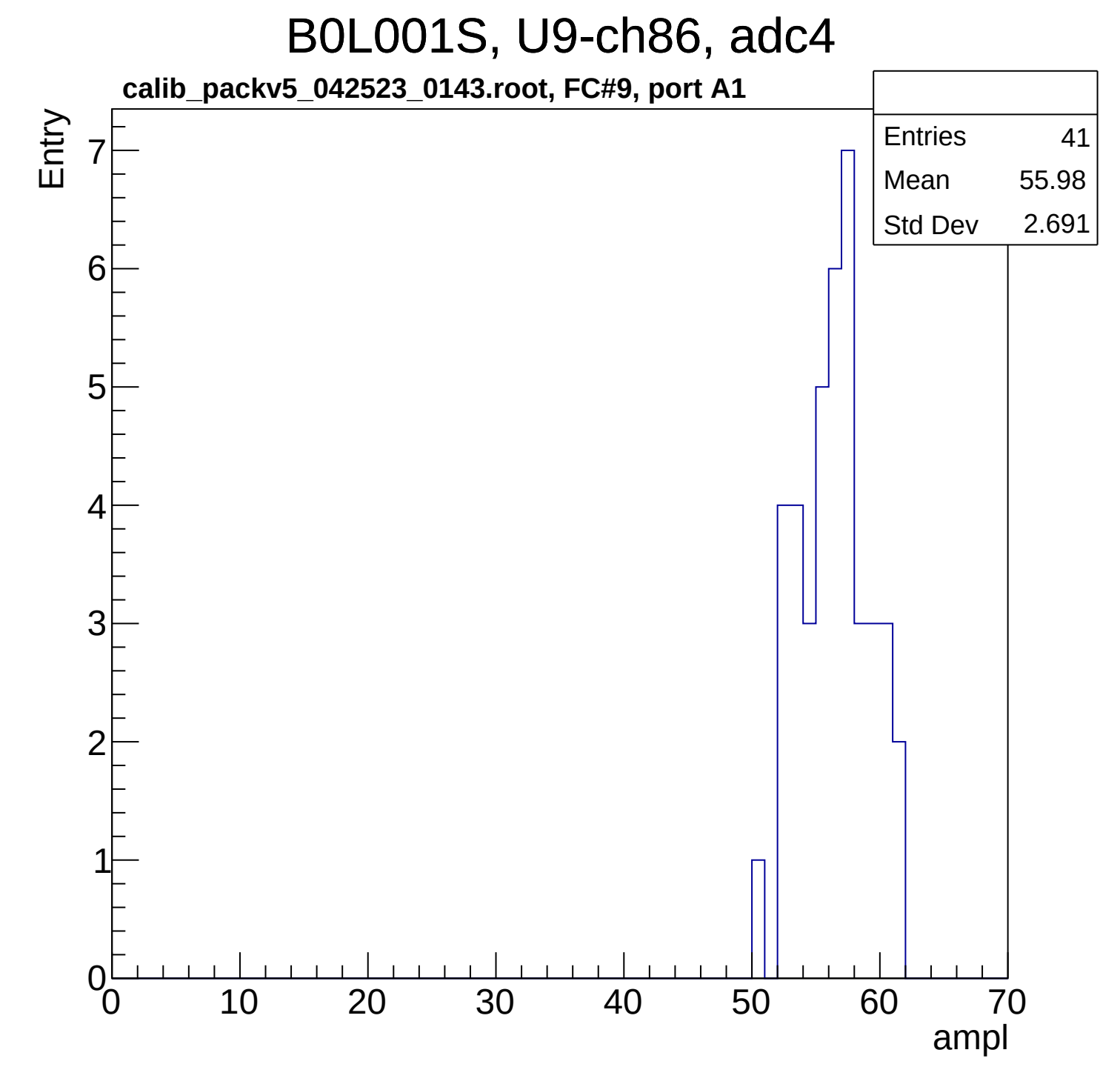
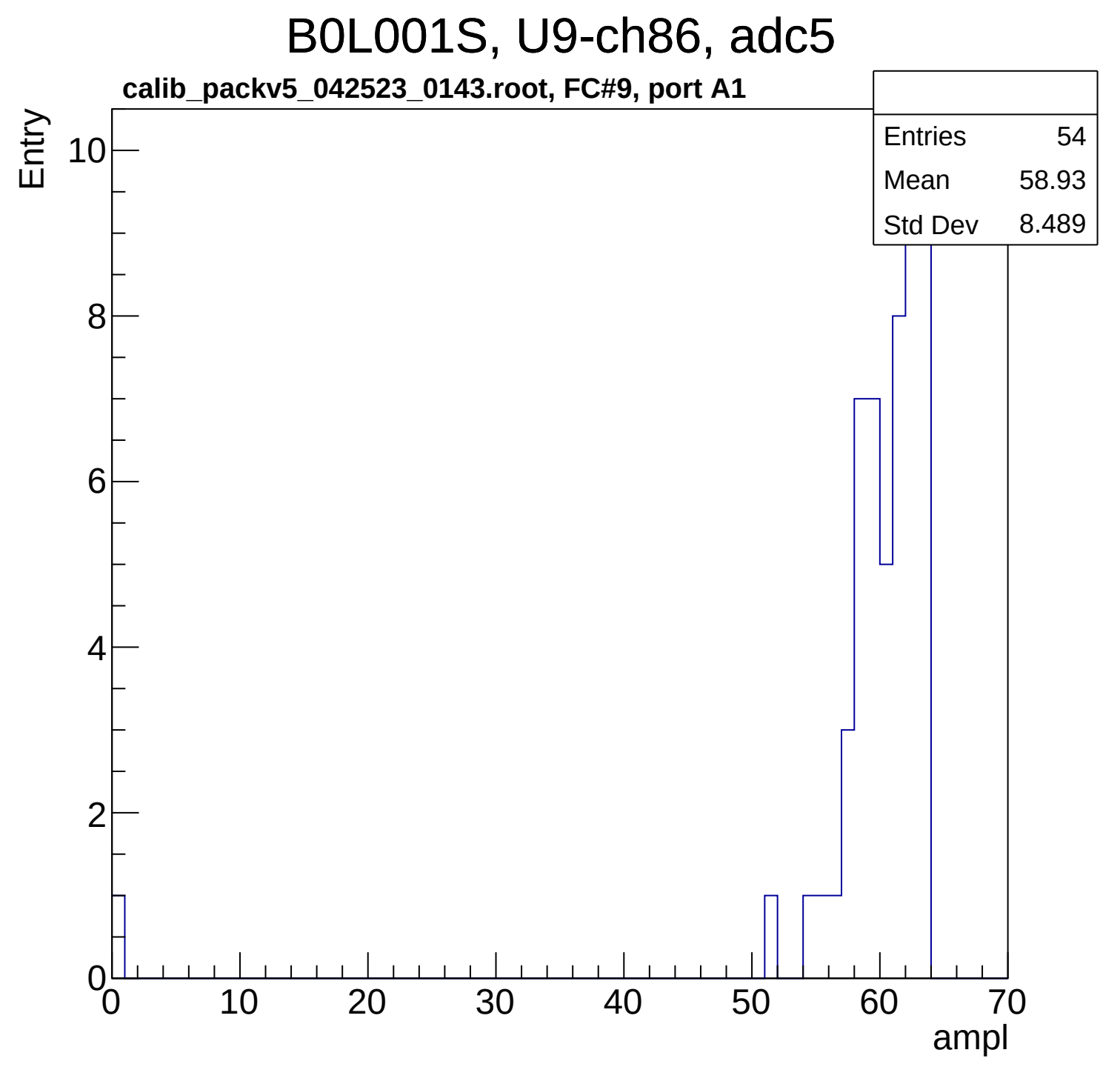
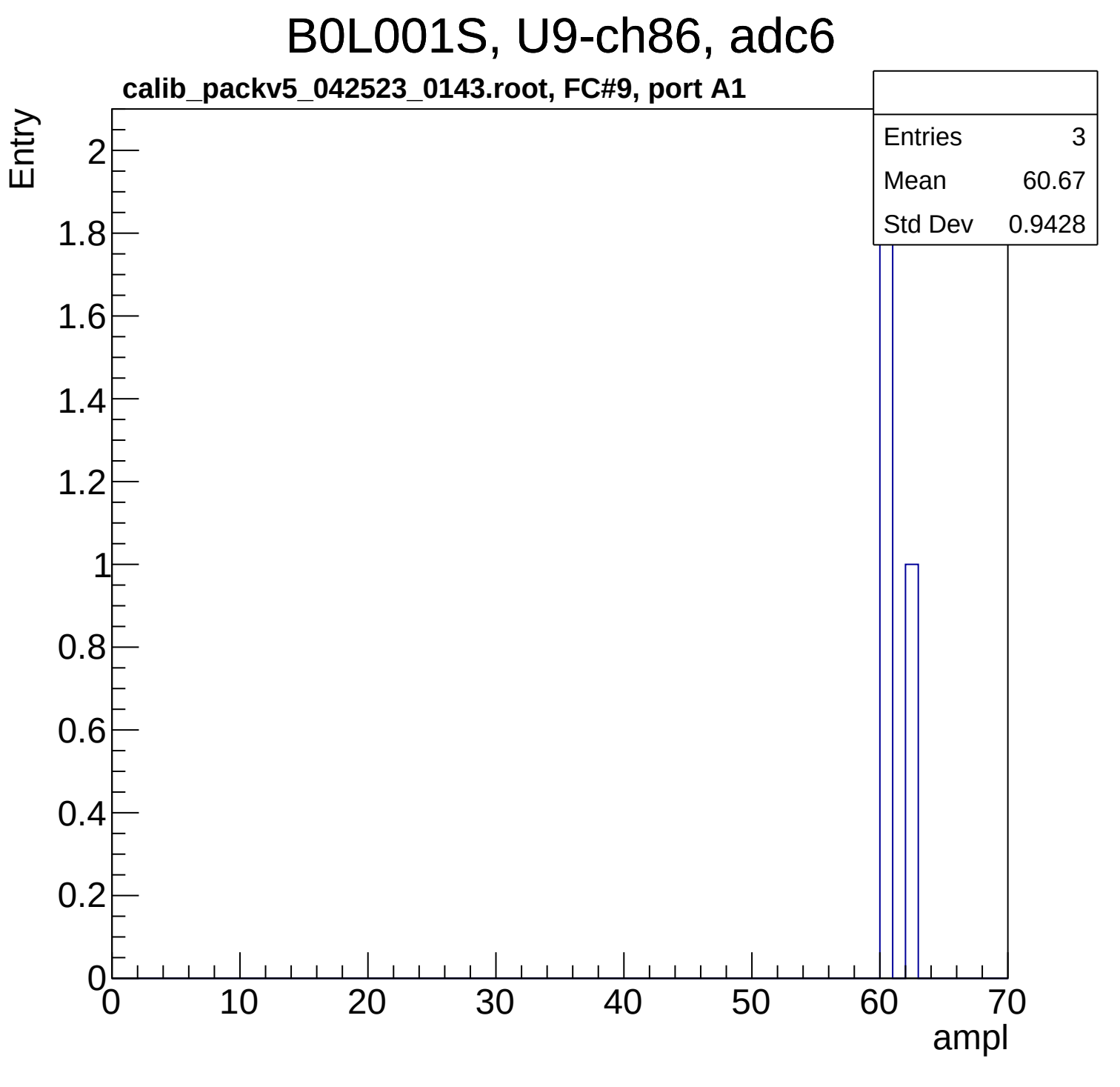
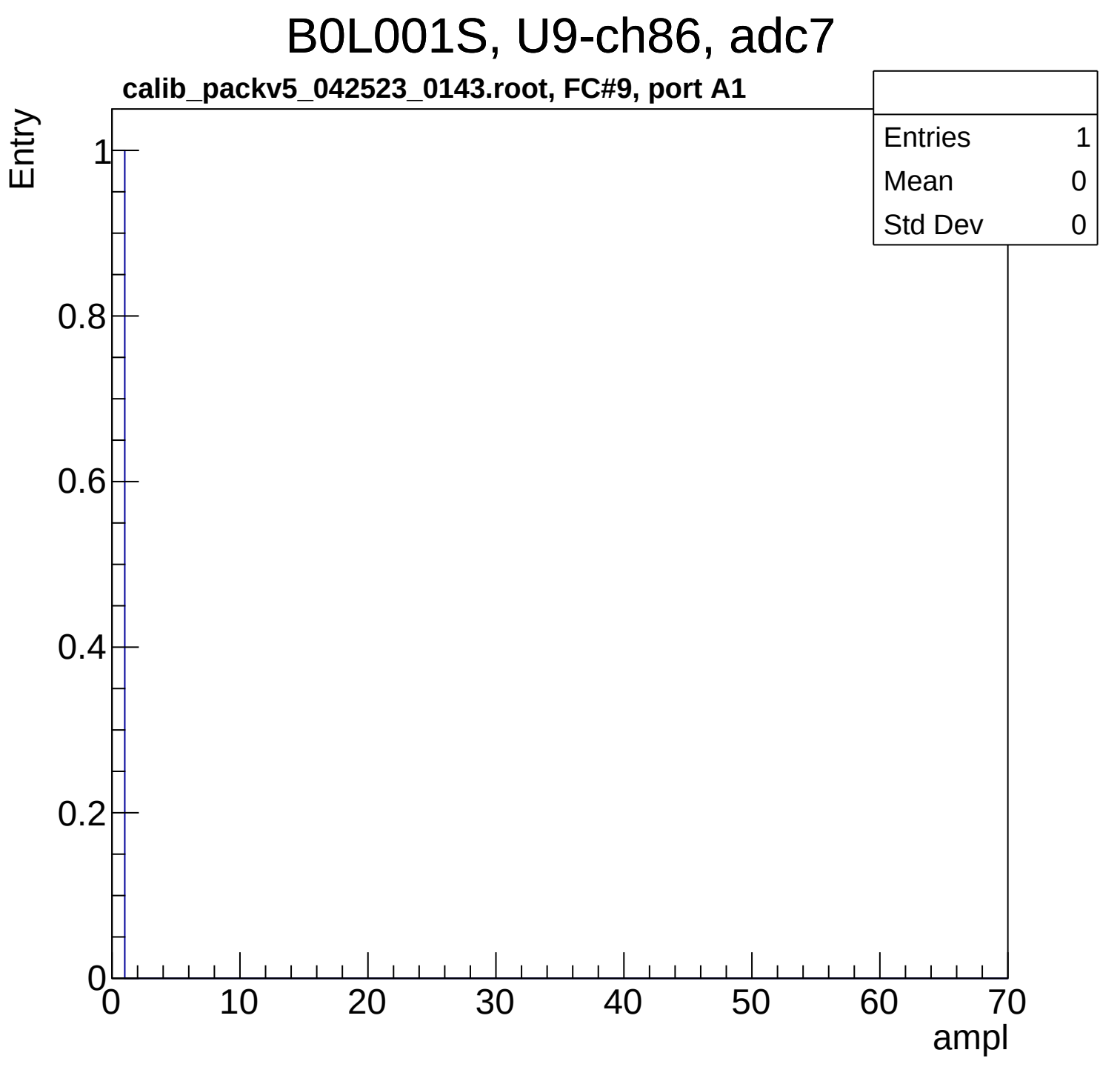
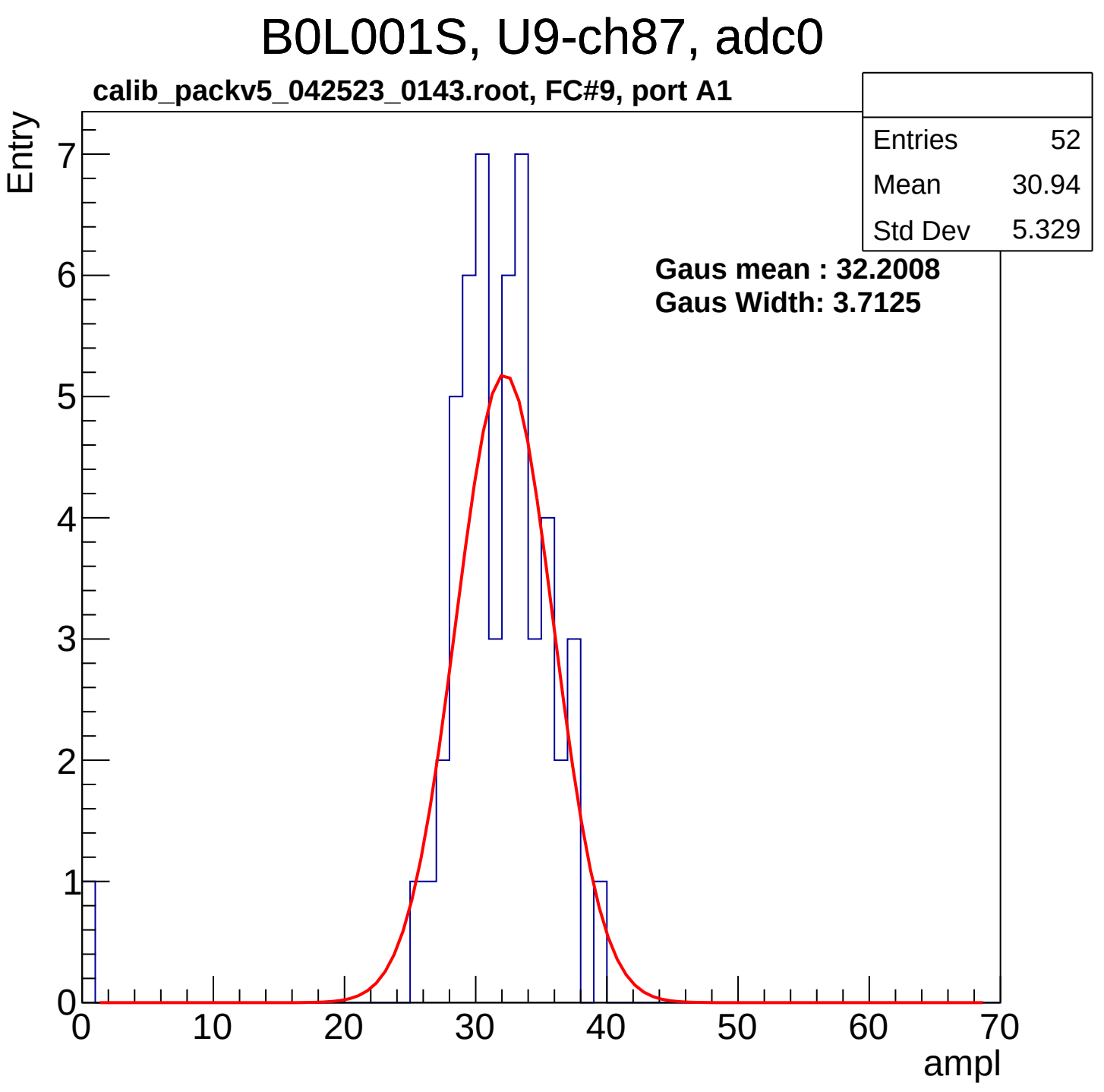

B0L001S, U9-ch86, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 50.15
Std Dev 3.517
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

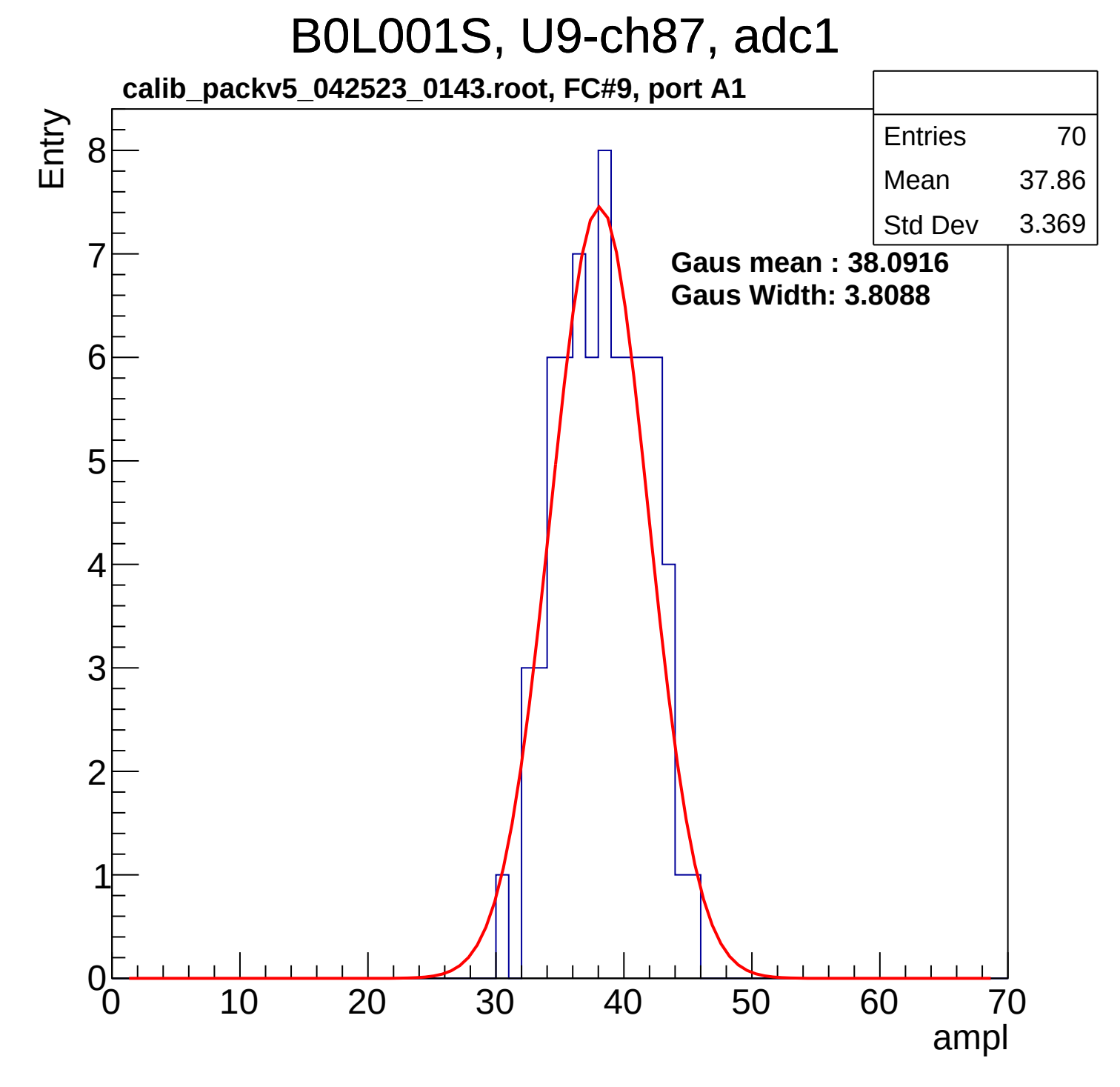

B0L001S, U9-ch86, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 55.98
Std Dev 2.691
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch86, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 58.93
Std Dev 8.489
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch86, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 60.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch86, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch87, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 30.94
Std Dev 5.329
Gaus mean : 32.2008
Gaus Width: 3.7125
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch87, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.86
Std Dev 3.369
Gaus mean : 38.0916
Gaus Width: 3.8088
Entry
ampl
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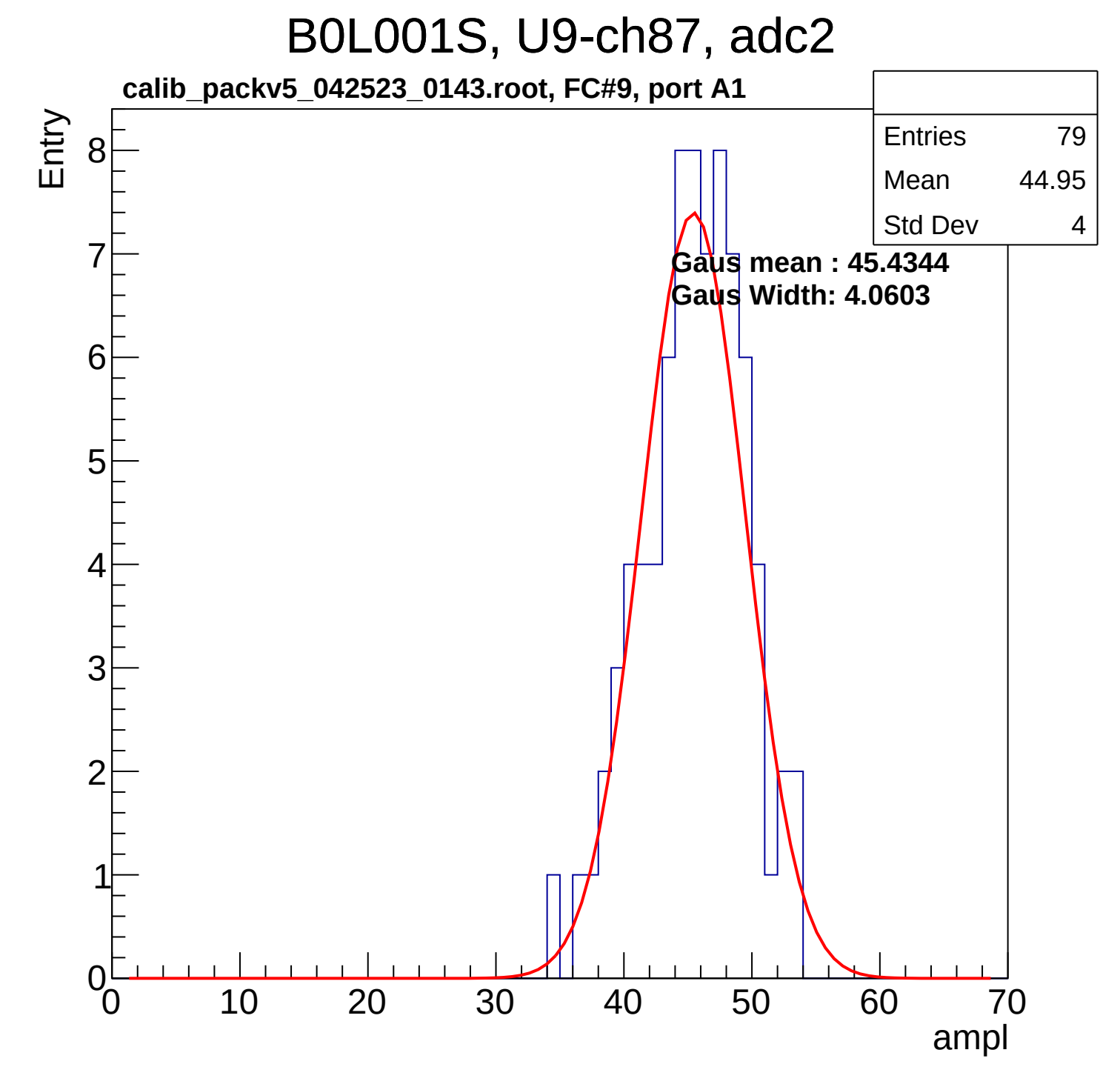
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch87, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 44.95
Std Dev 4
Gaus mean : 45.4344
Gaus Width: 4.0603
Entry
ampl
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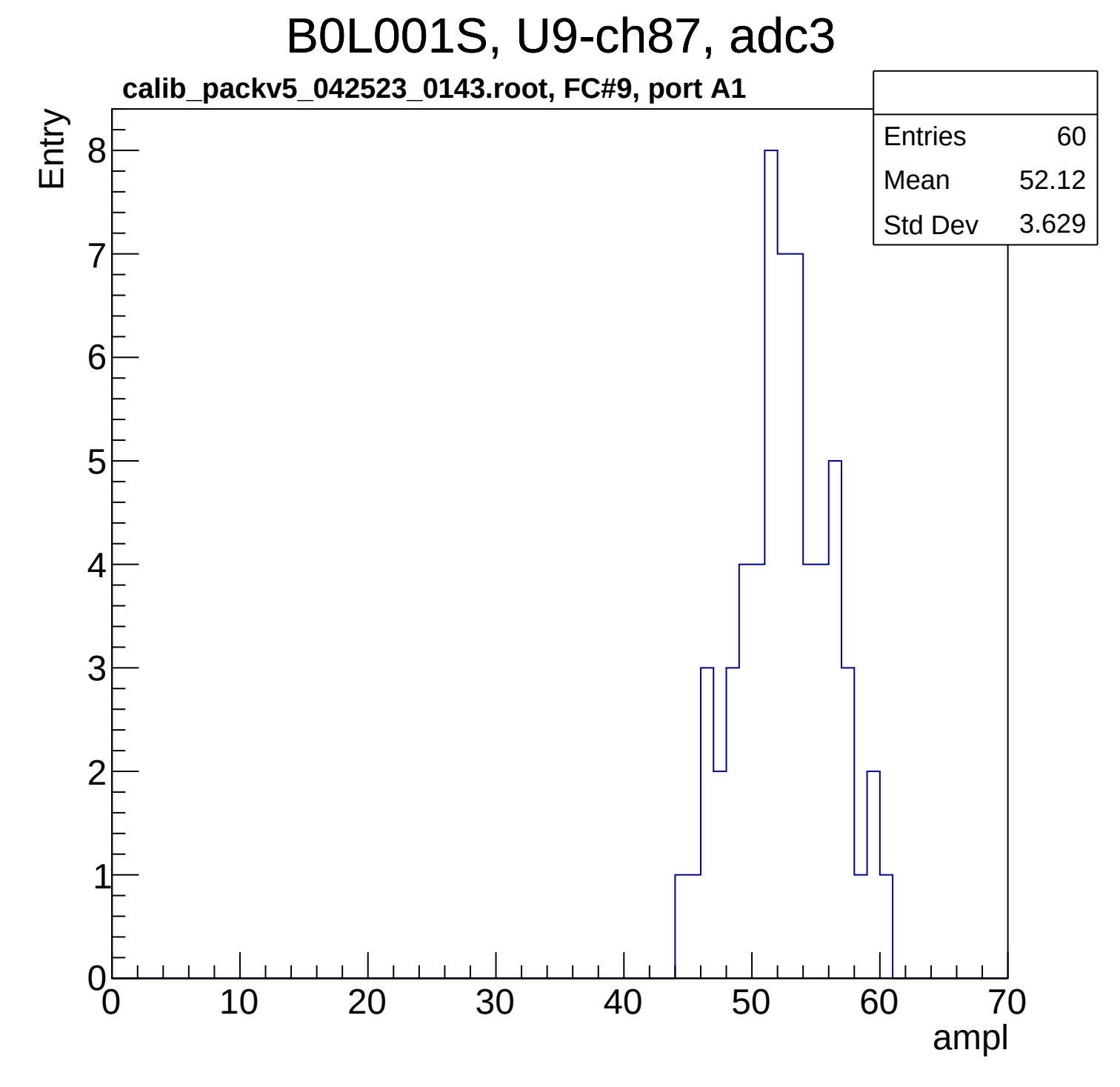
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch87, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 52.12
Std Dev 3.629
Entry
ampl
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3
4
5
6
7
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0
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20
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60
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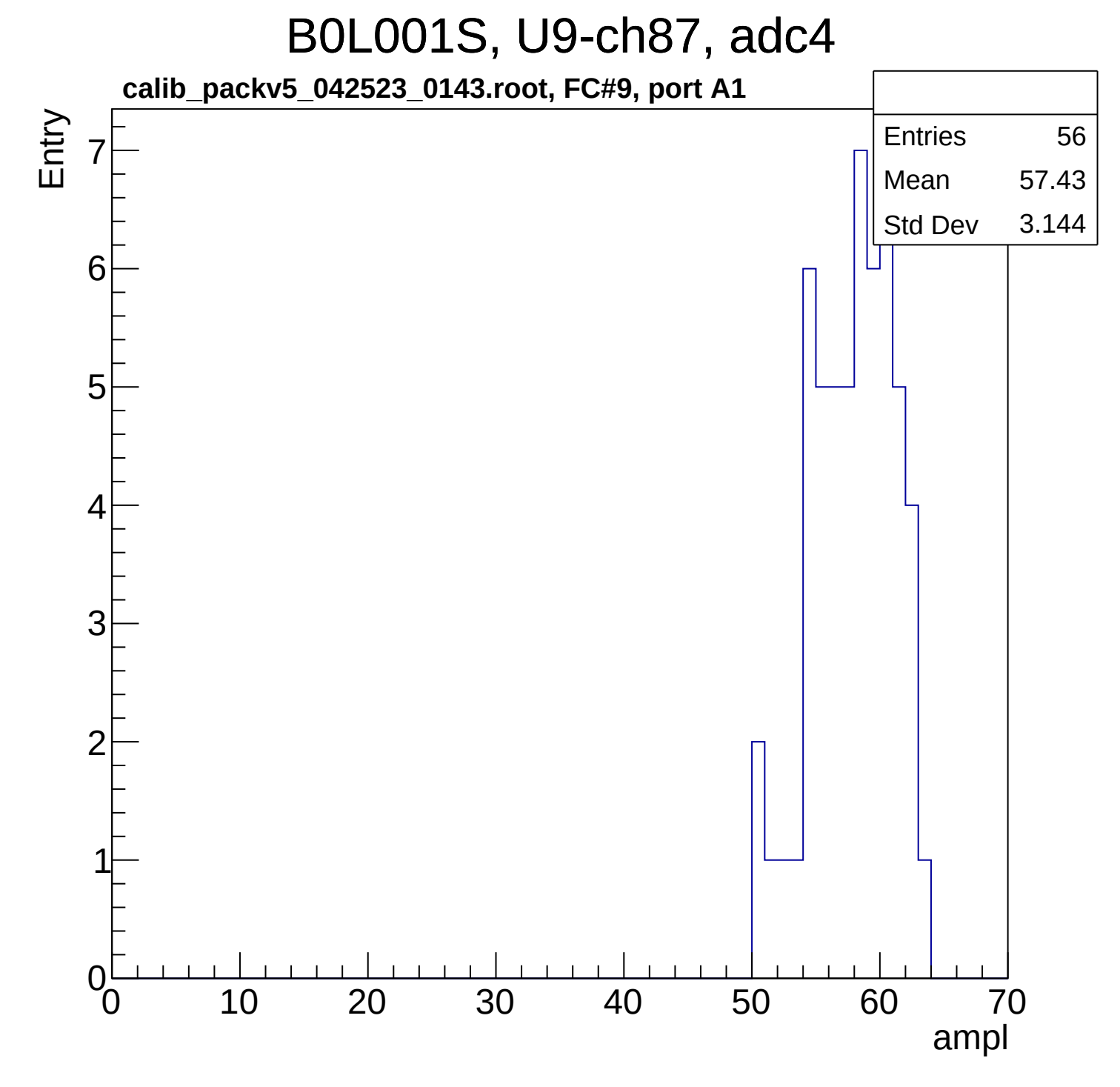
B0L001S, U9-ch87, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.43
Std Dev 3.144
Entry
ampl
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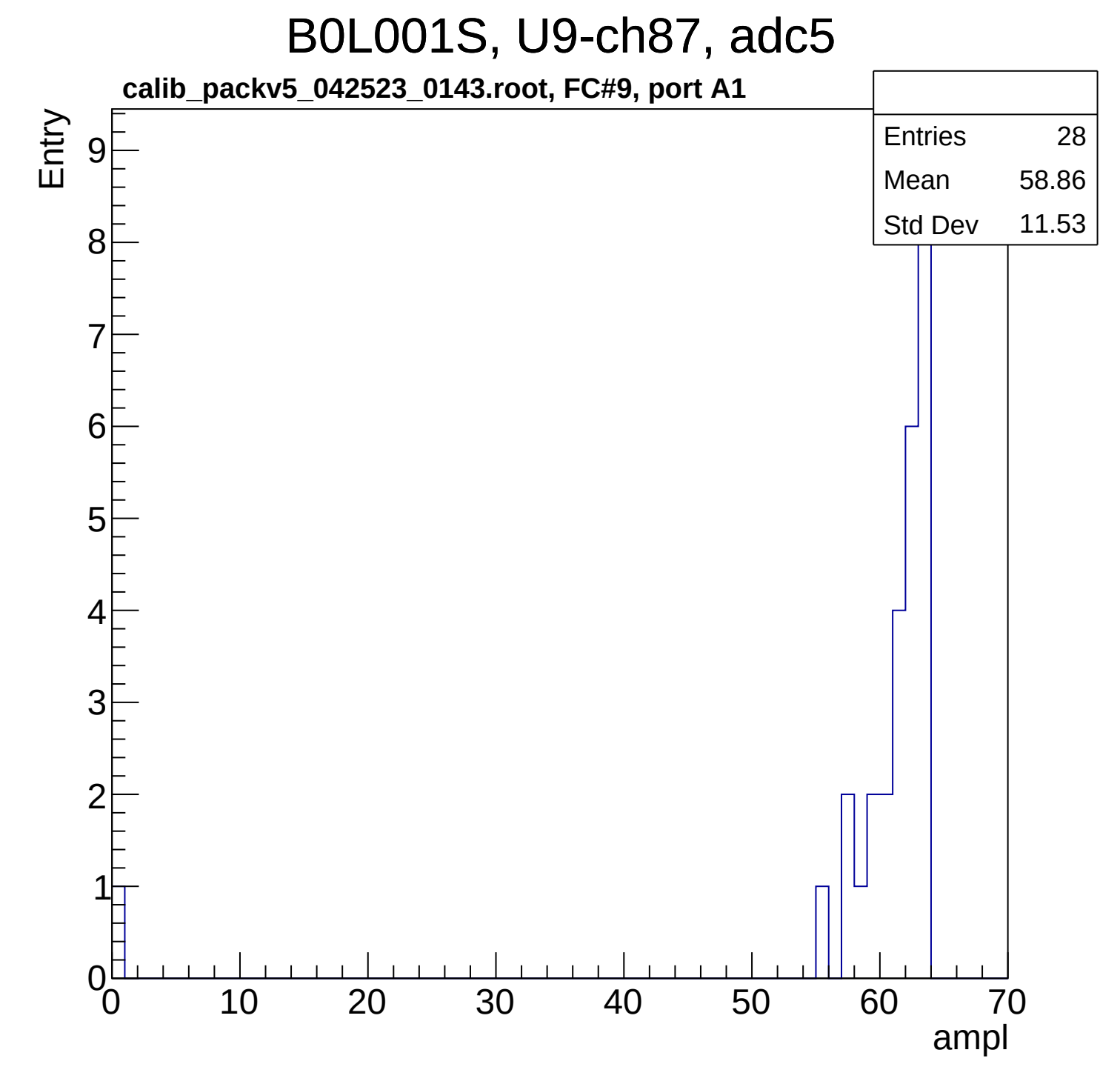
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch87, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.86
Std Dev 11.53
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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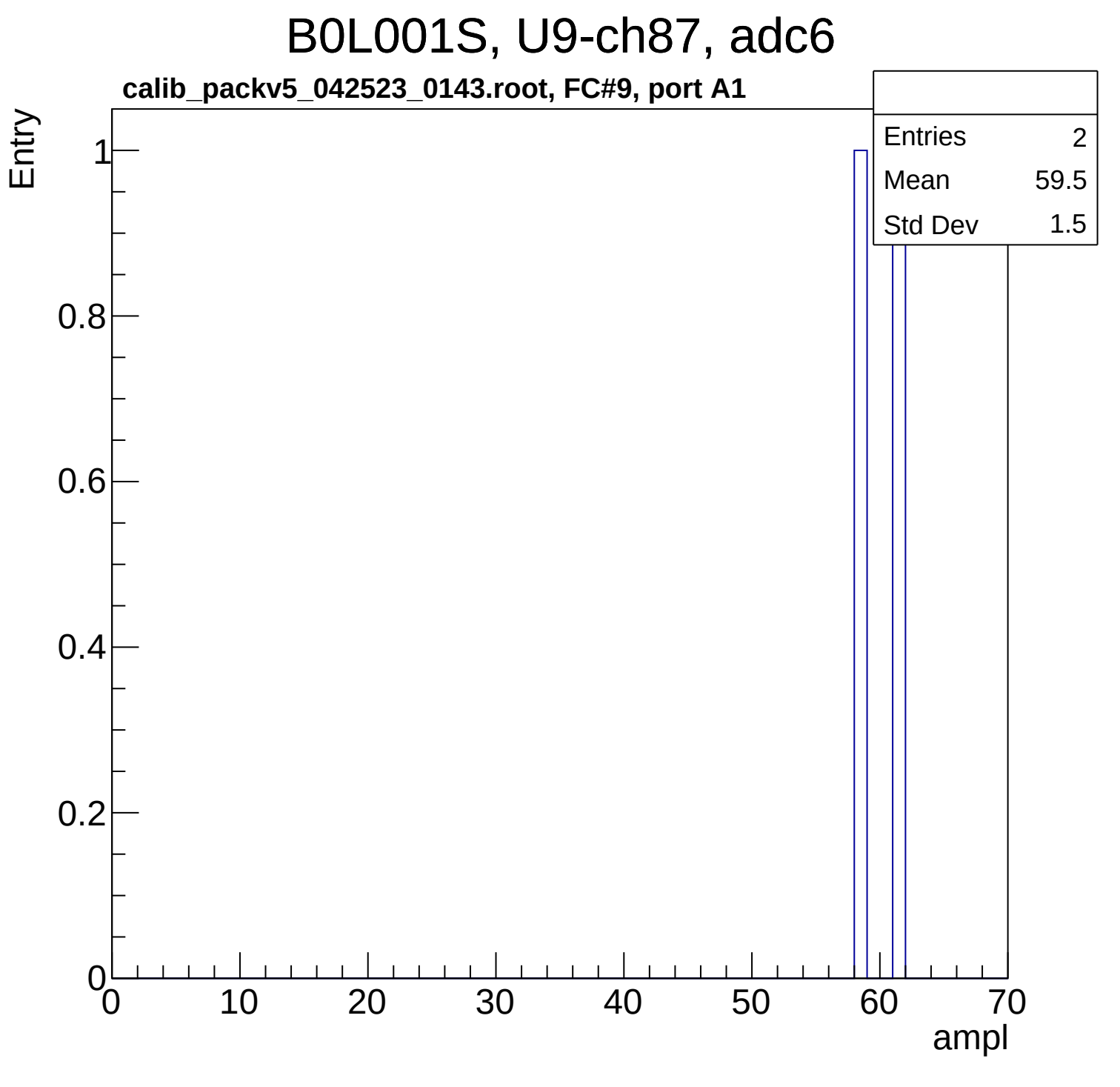
B0L001S, U9-ch87, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 59.5
Std Dev 1.5
Entry
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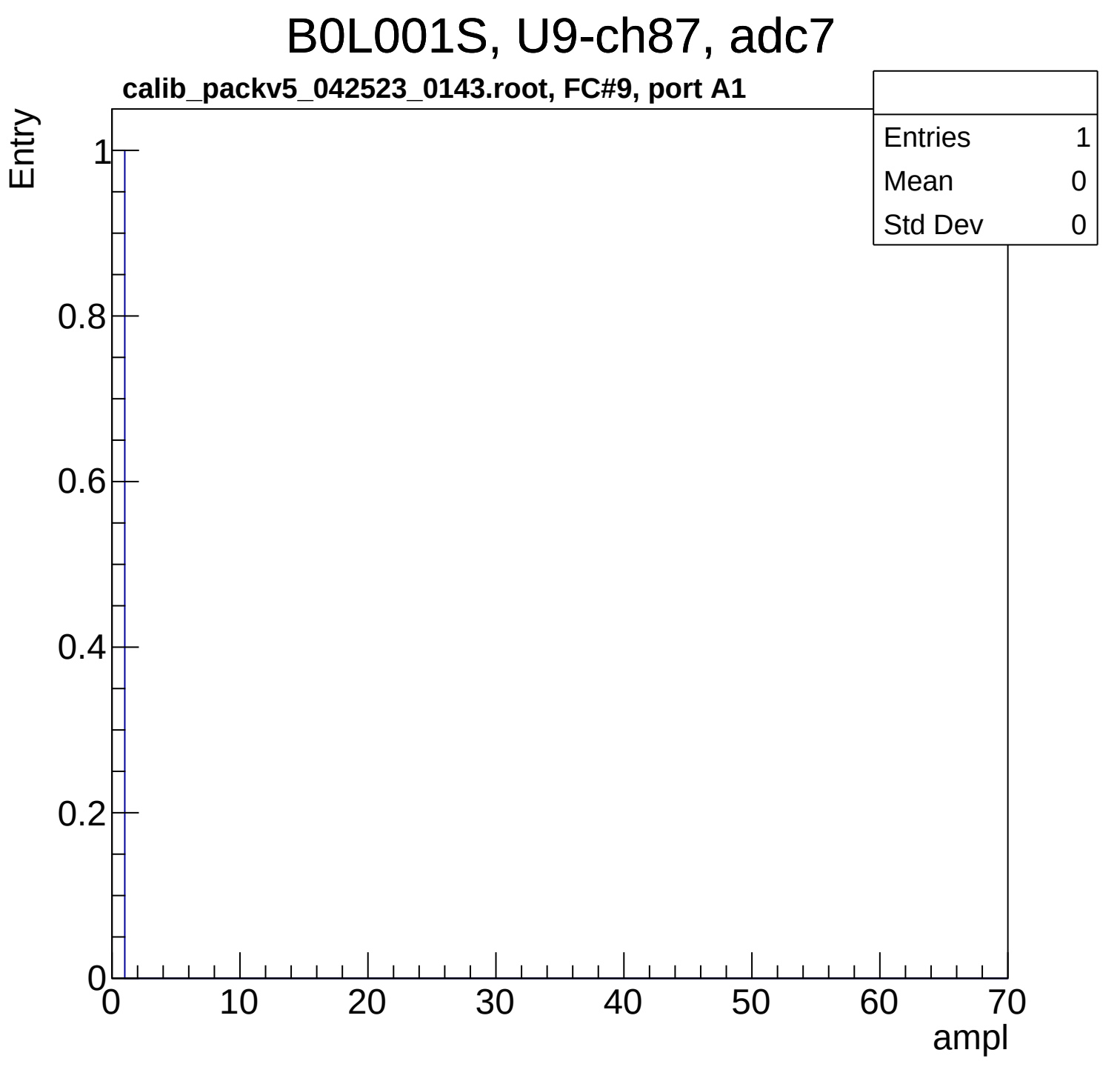
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch87, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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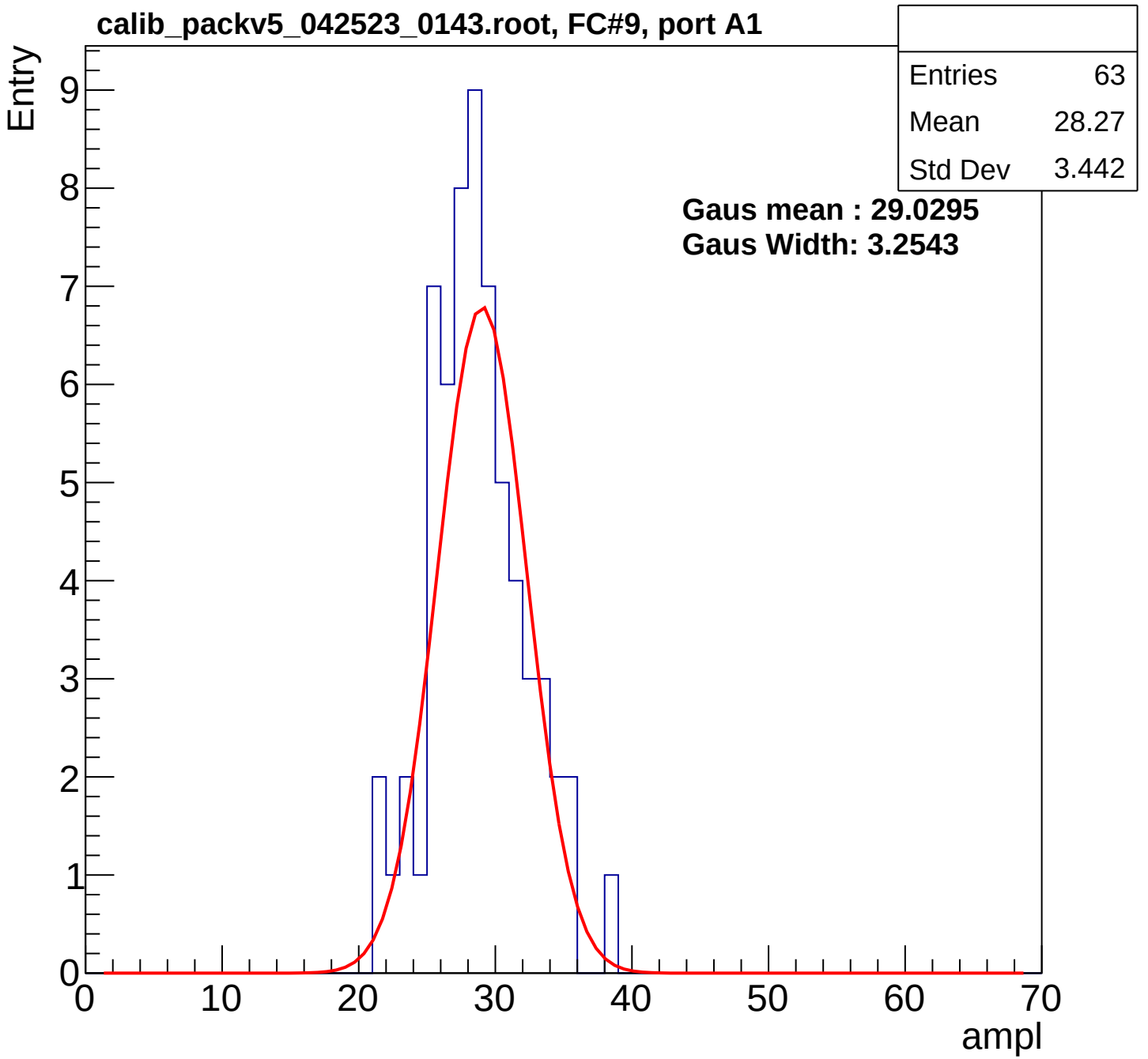
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch88, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 28.27
Std Dev 3.442
Gaus mean : 29.0295
Gaus Width: 3.2543
Entry
ampl
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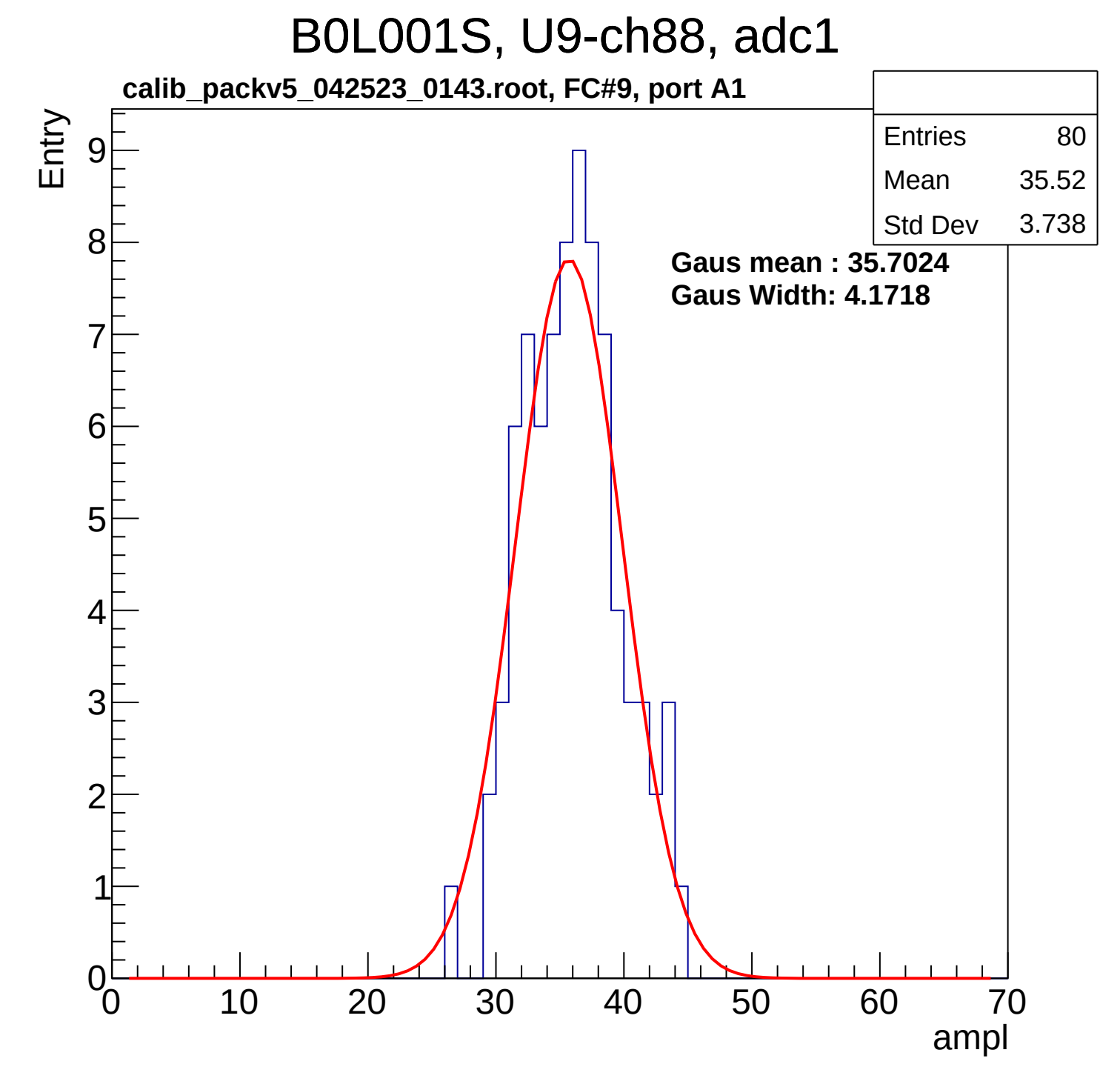
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch88, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 35.52
Std Dev 3.738
Gaus mean : 35.7024
Gaus Width: 4.1718
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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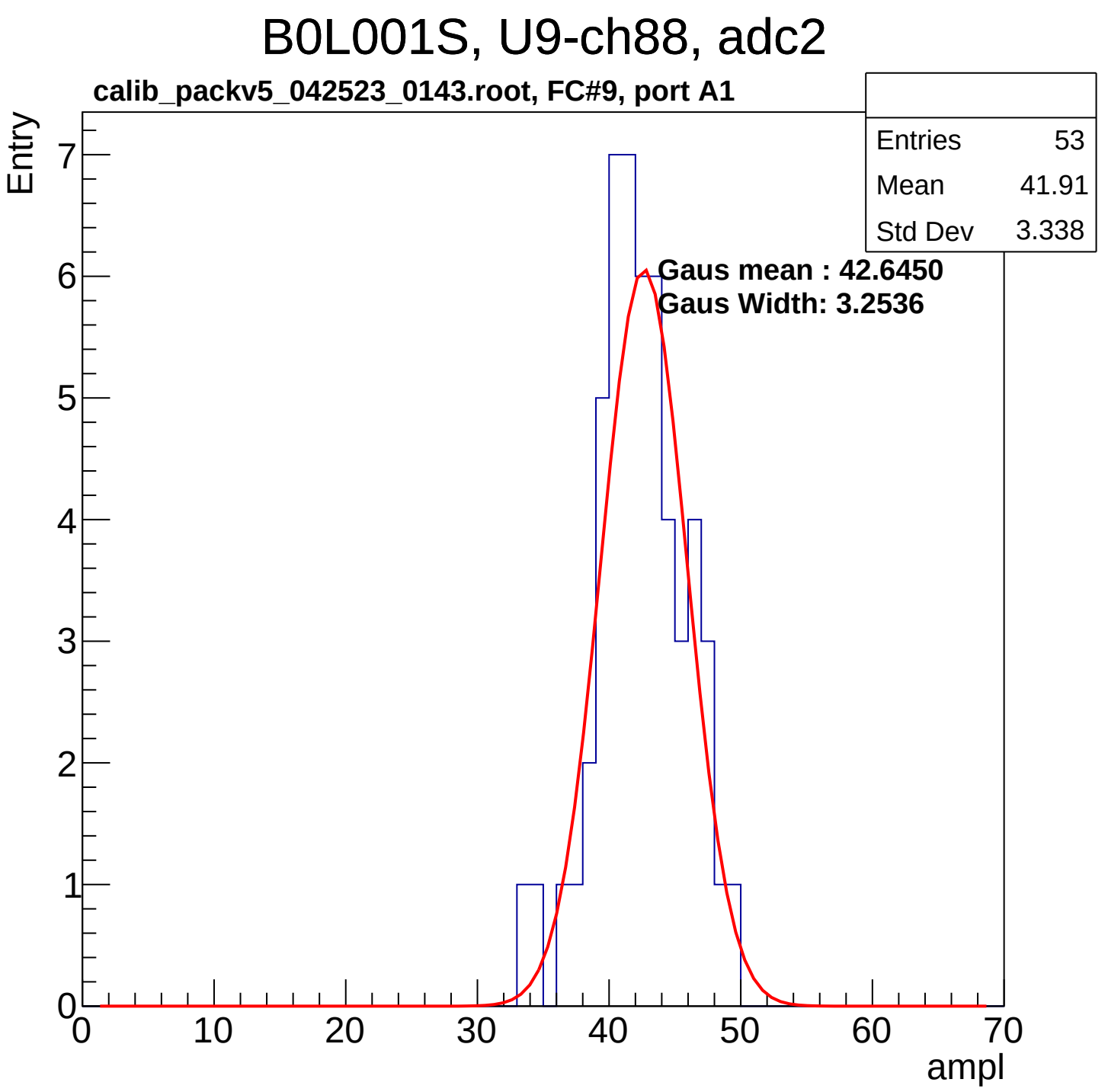
B0L001S, U9-ch88, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 41.91
Std Dev 3.338
Gaus mean : 42.6450
Gaus Width: 3.2536
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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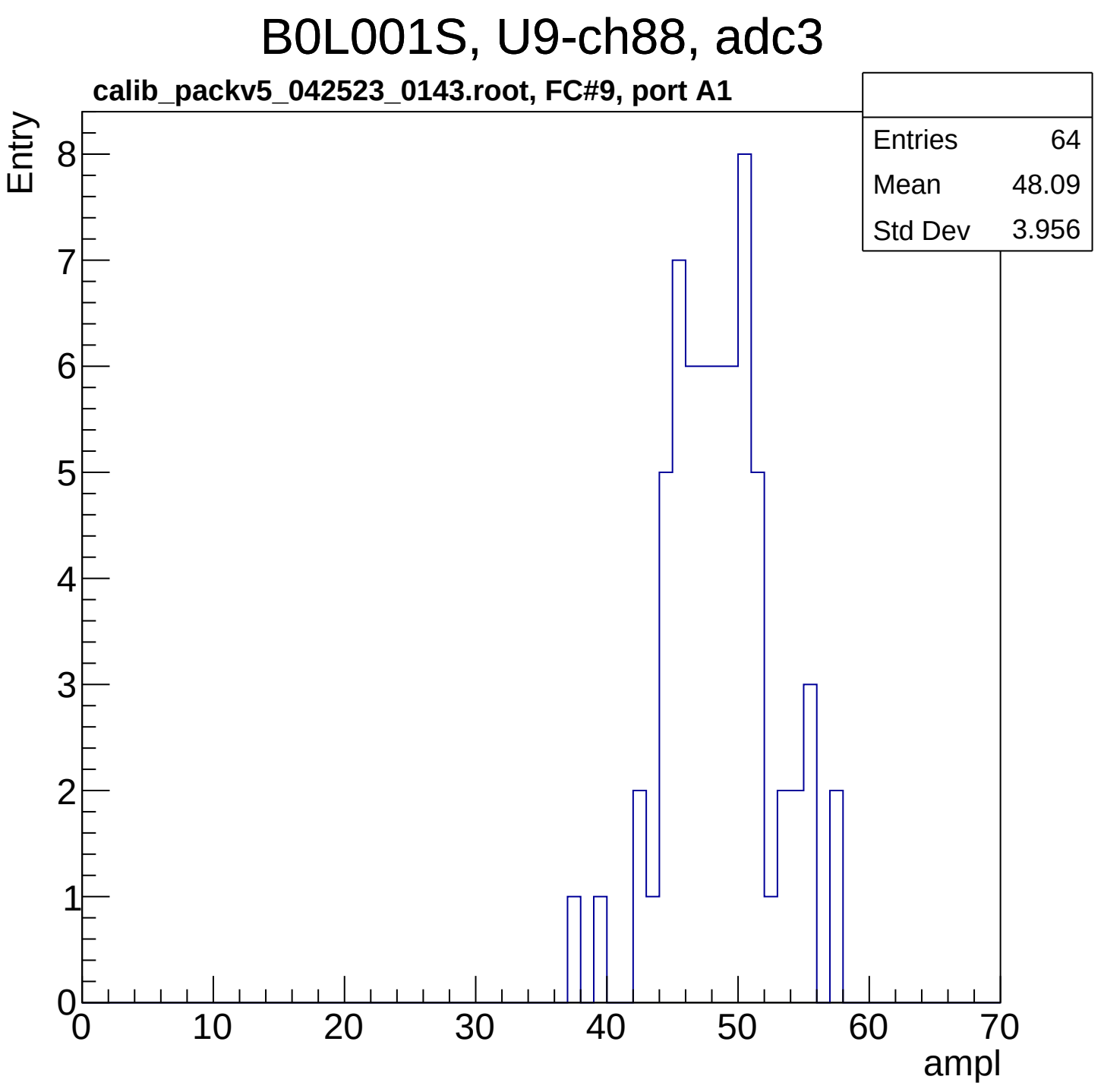
B0L001S, U9-ch88, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 48.09
Std Dev 3.956
Entry
ampl
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4
5
6
7
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60
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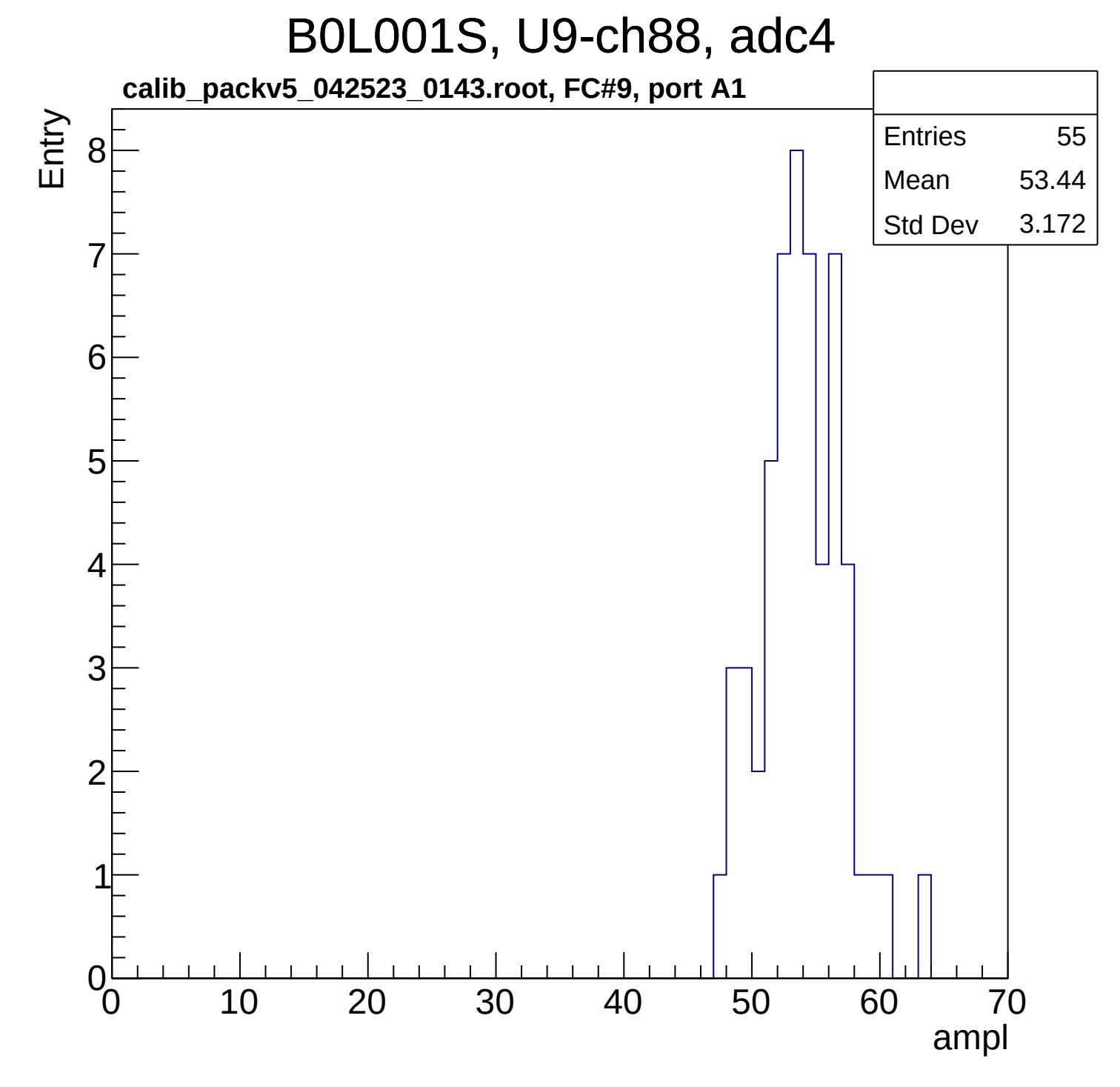
B0L001S, U9-ch88, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 53.44
Std Dev 3.172
Entry
ampl
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3
4
5
6
7
8
0
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20
30
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50
60
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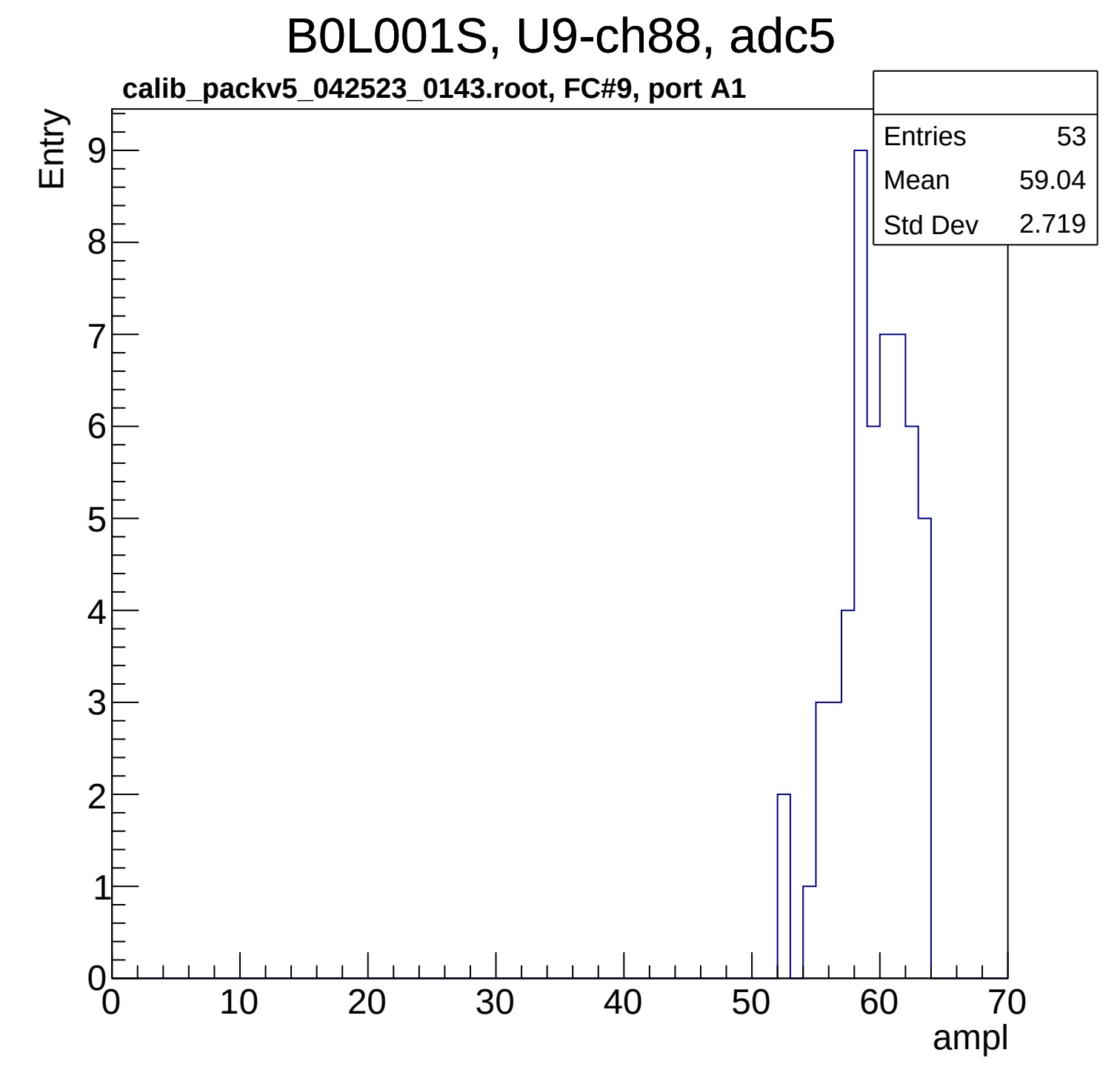
B0L001S, U9-ch88, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 59.04
Std Dev 2.719
Entry
ampl
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4
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6
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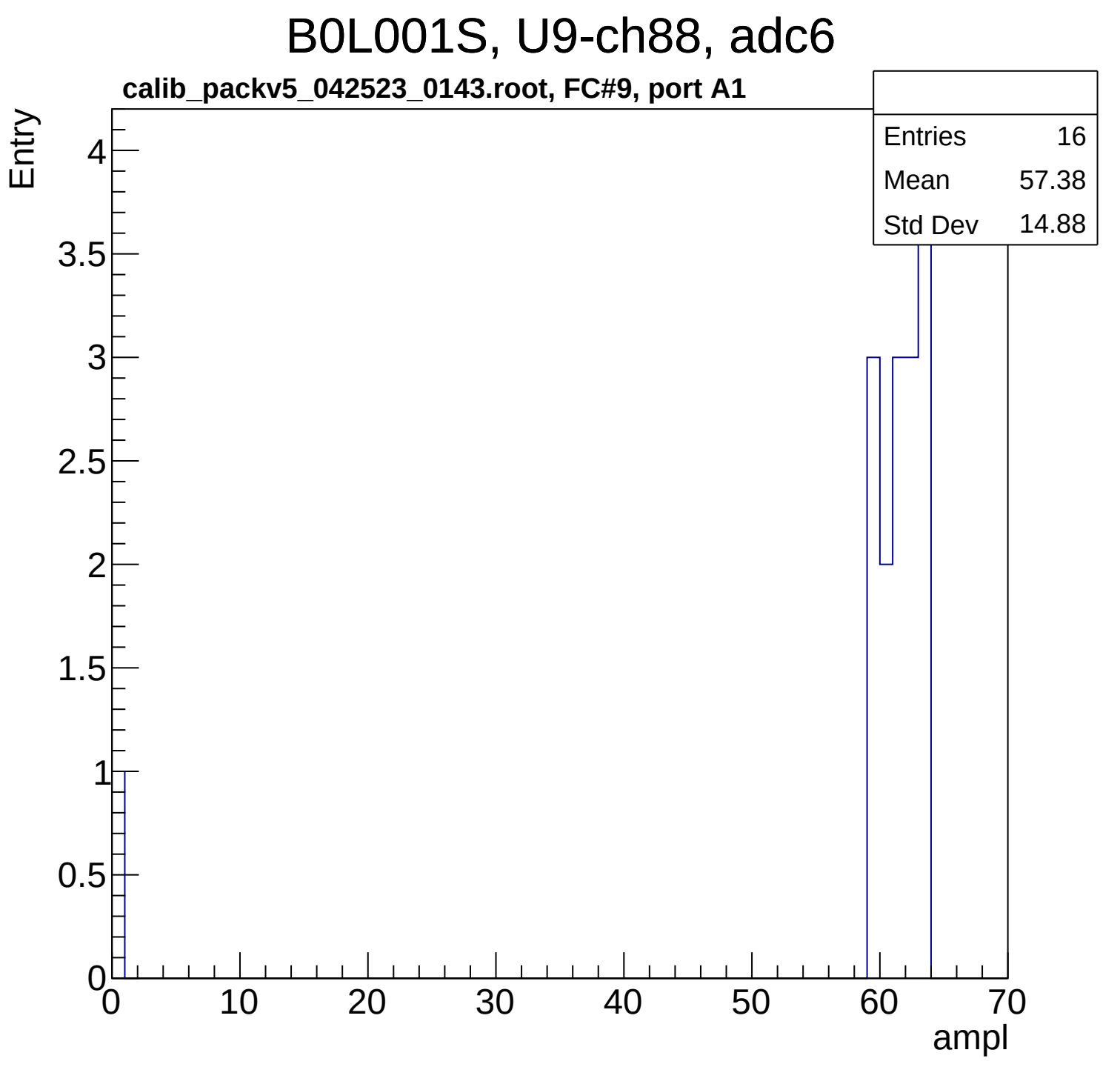
B0L001S, U9-ch88, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 57.38
Std Dev 14.88
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
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0
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40
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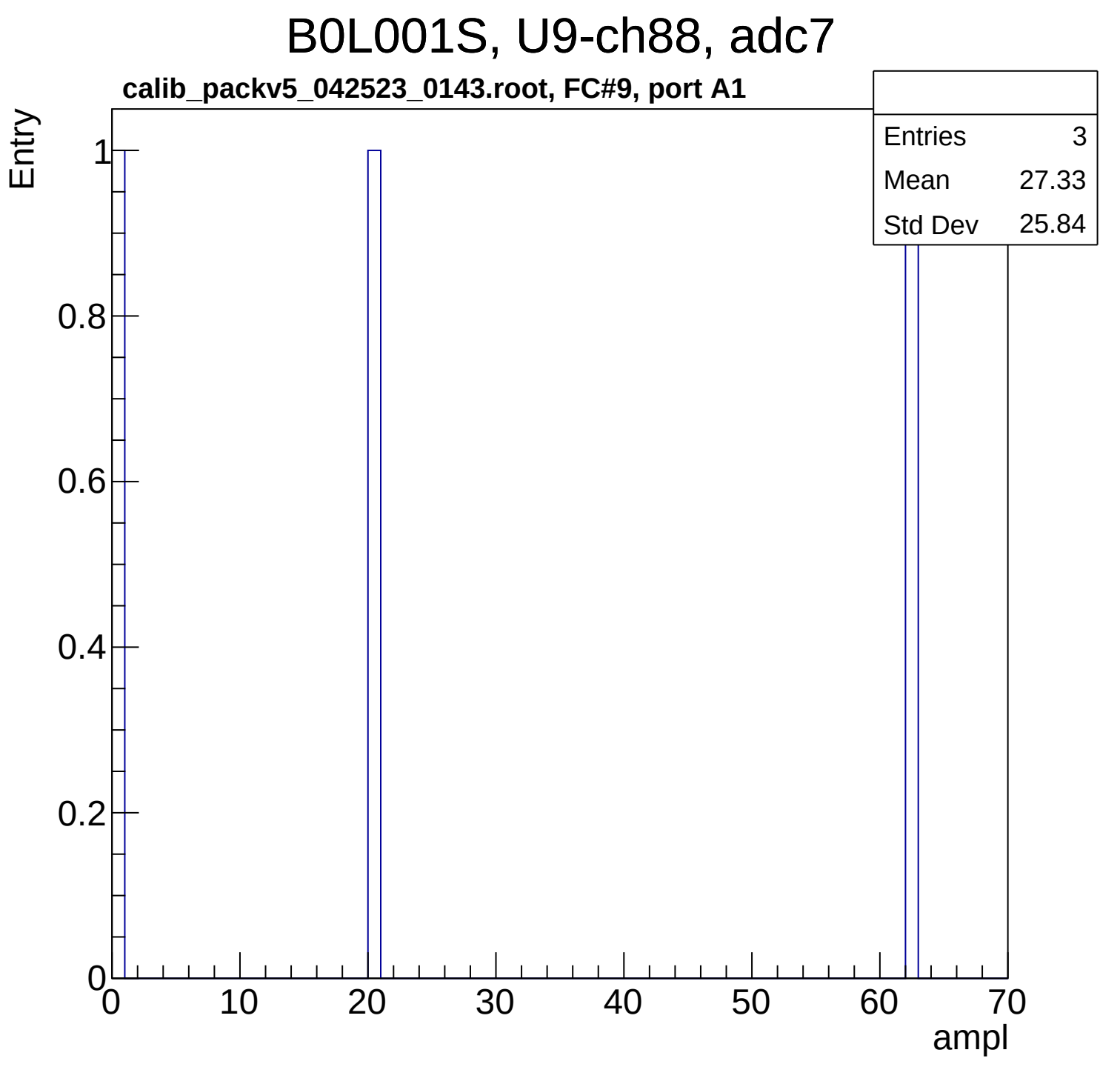
B0L001S, U9-ch88, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
0
0.2
0.4
0.6
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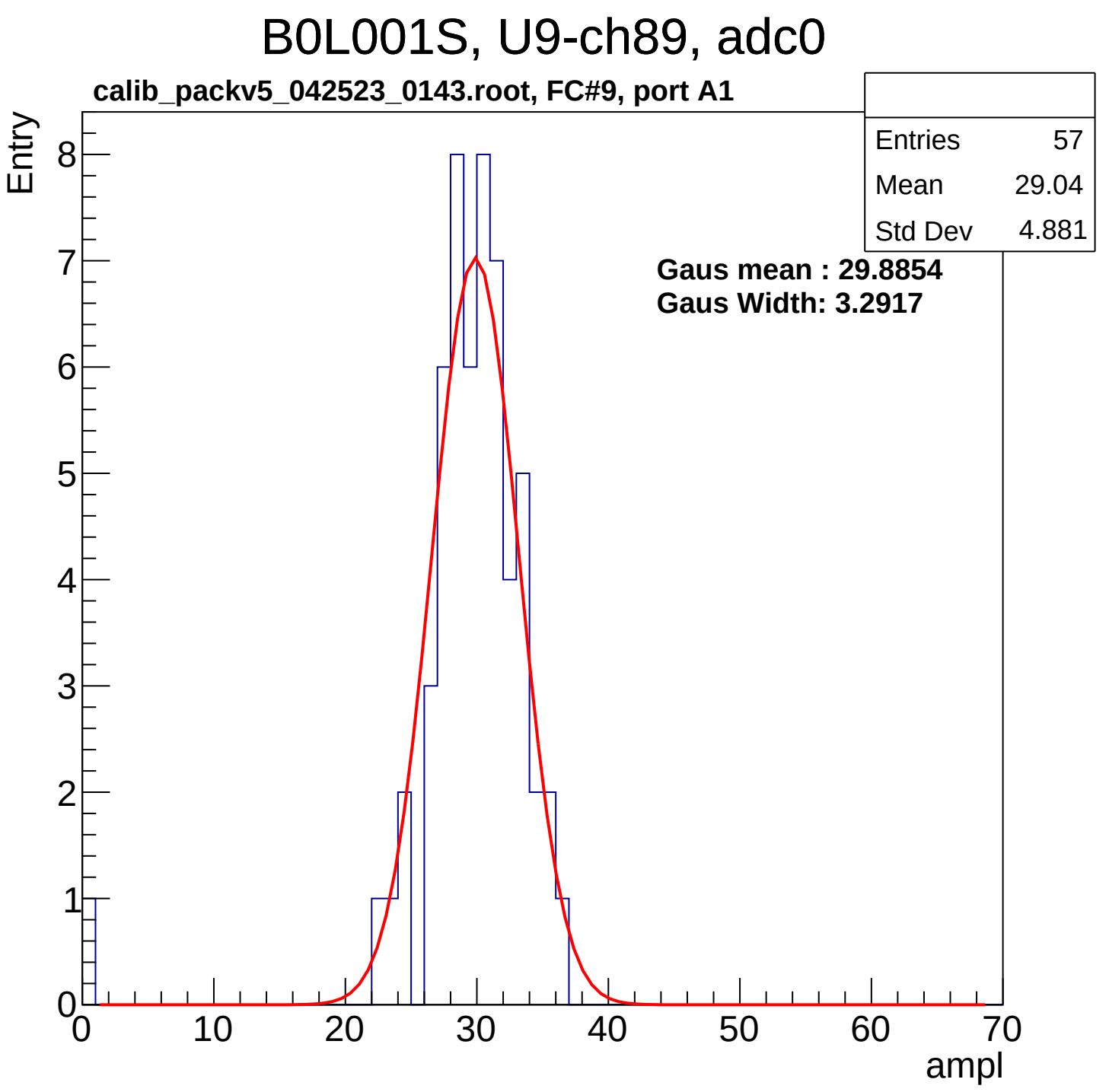
B0L001S, U9-ch89, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 29.04
Std Dev 4.881
Gaus mean : 29.8854
Gaus Width: 3.2917
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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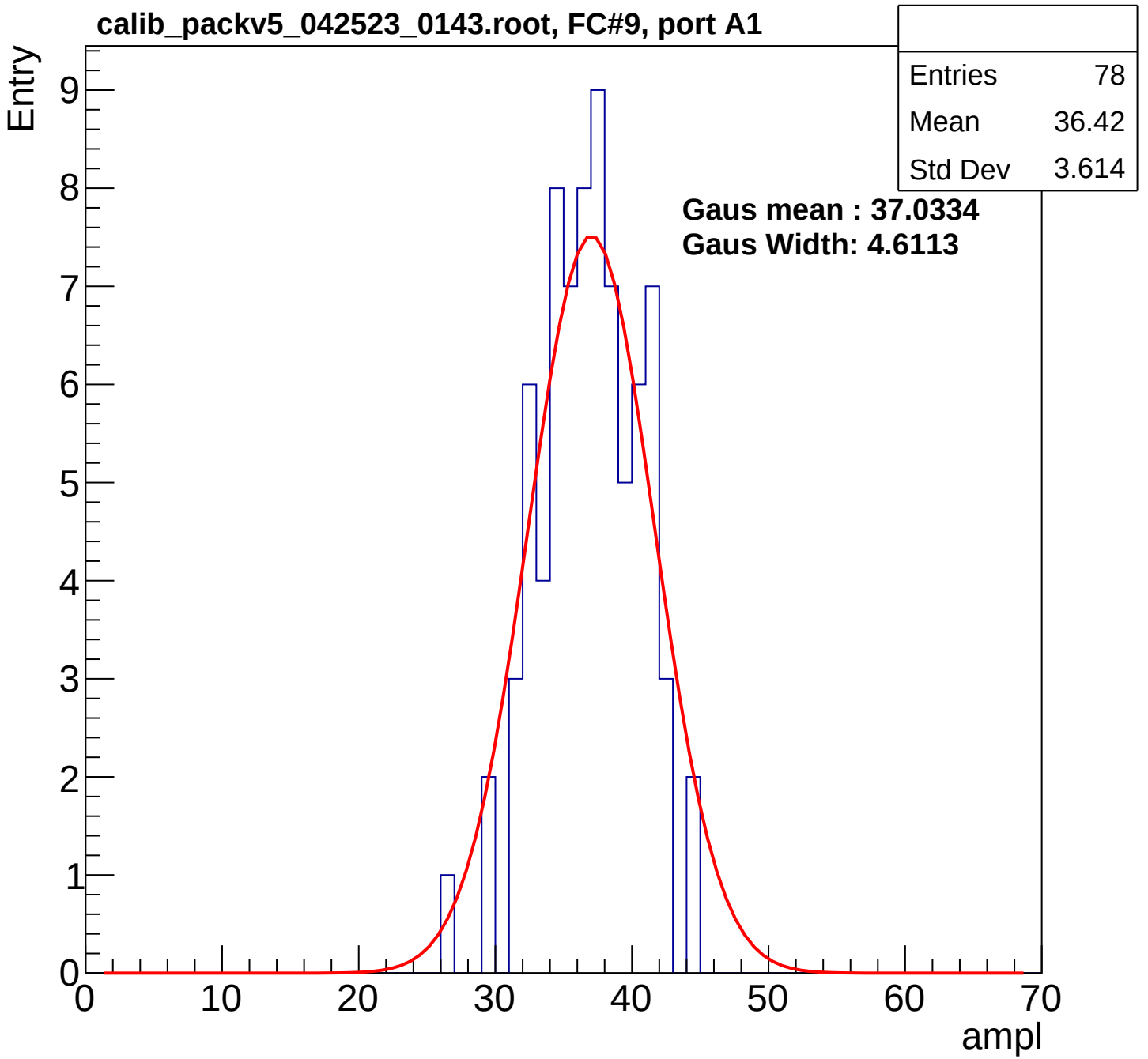
B0L001S, U9-ch89, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 36.42
Std Dev 3.614
Gaus mean : 37.0334
Gaus Width: 4.6113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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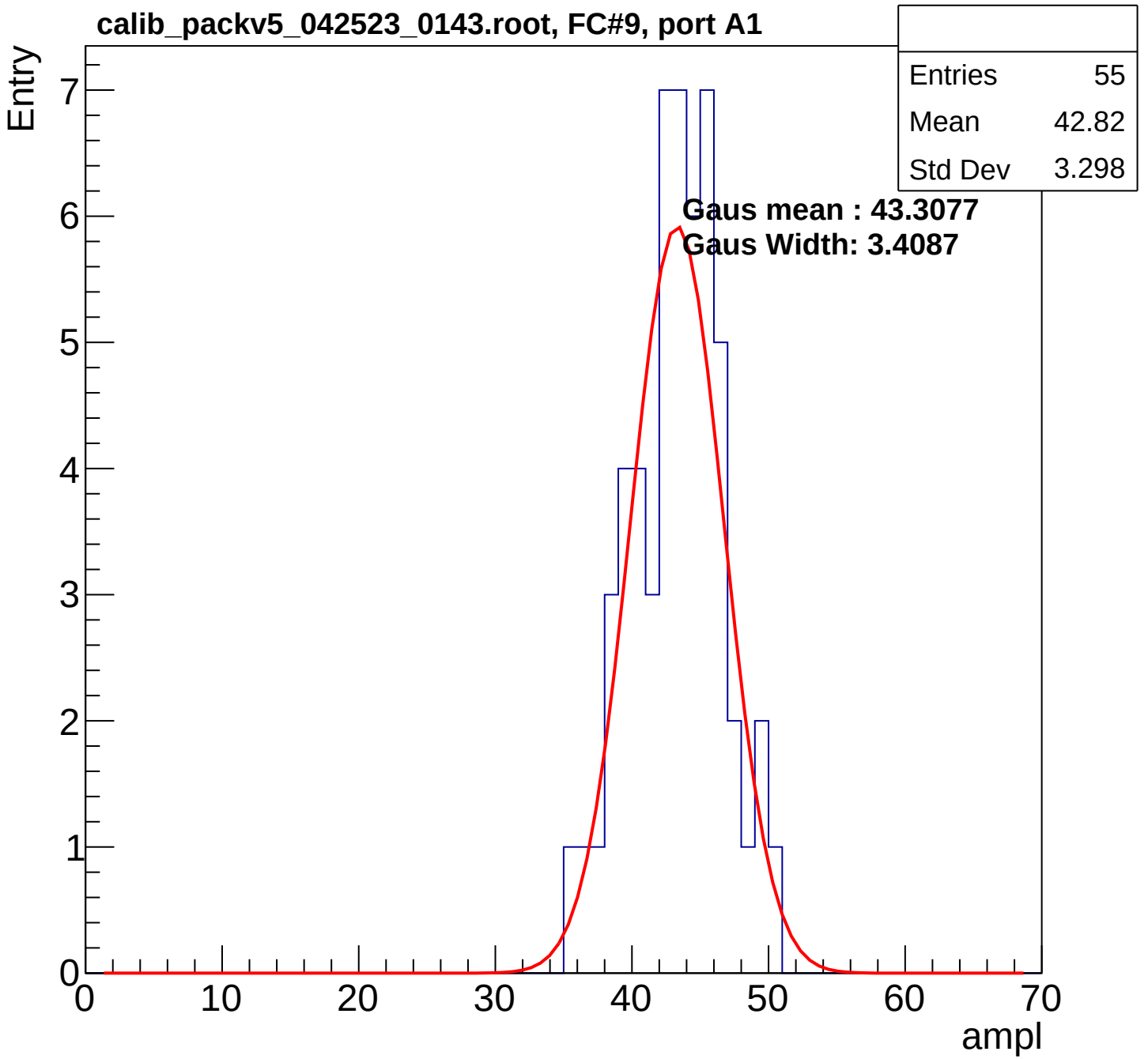
B0L001S, U9-ch89, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.82
Std Dev 3.298
Gaus mean : 43.3077
Gaus Width: 3.4087
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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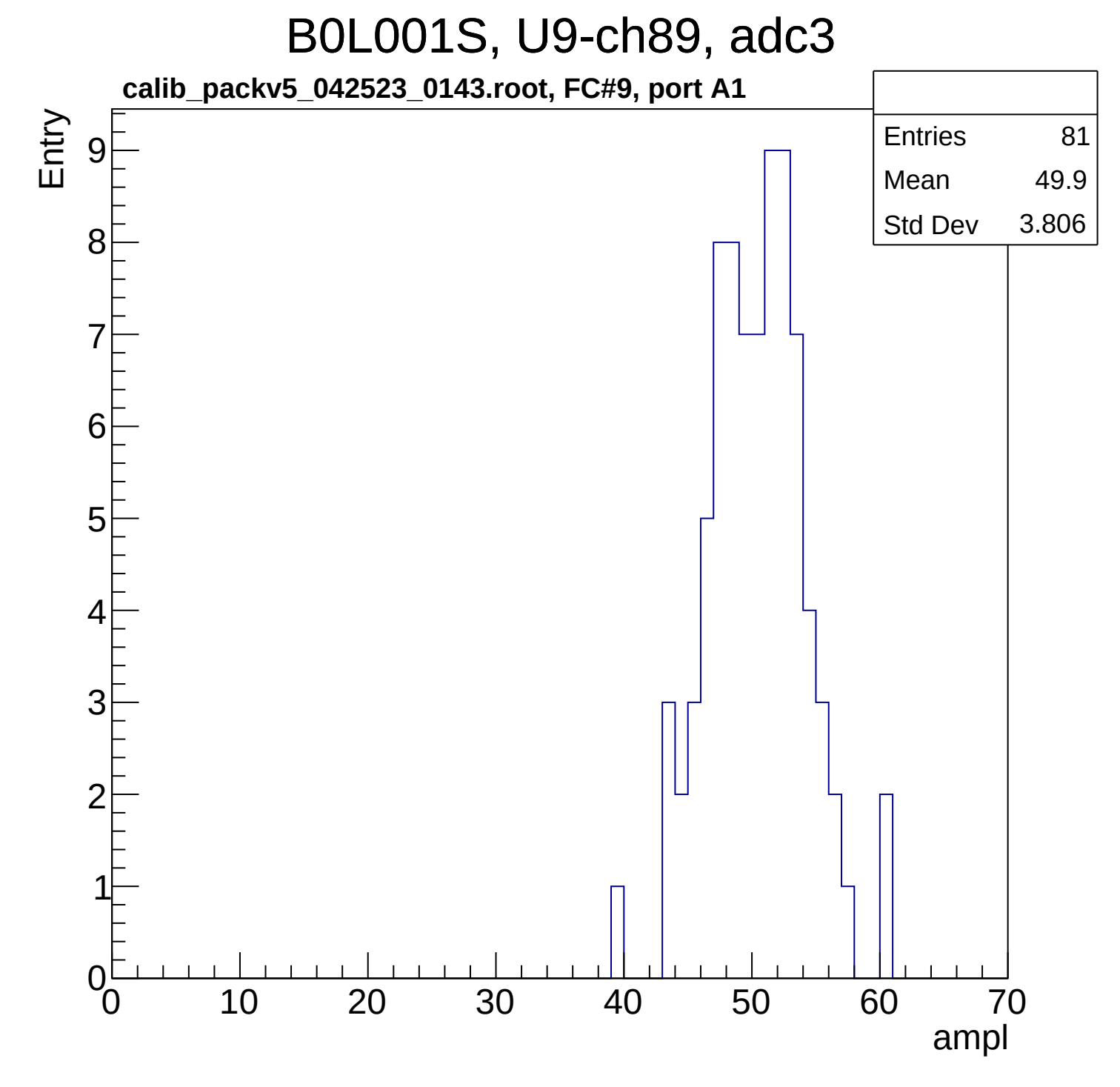
B0L001S, U9-ch89, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 49.9
Std Dev 3.806
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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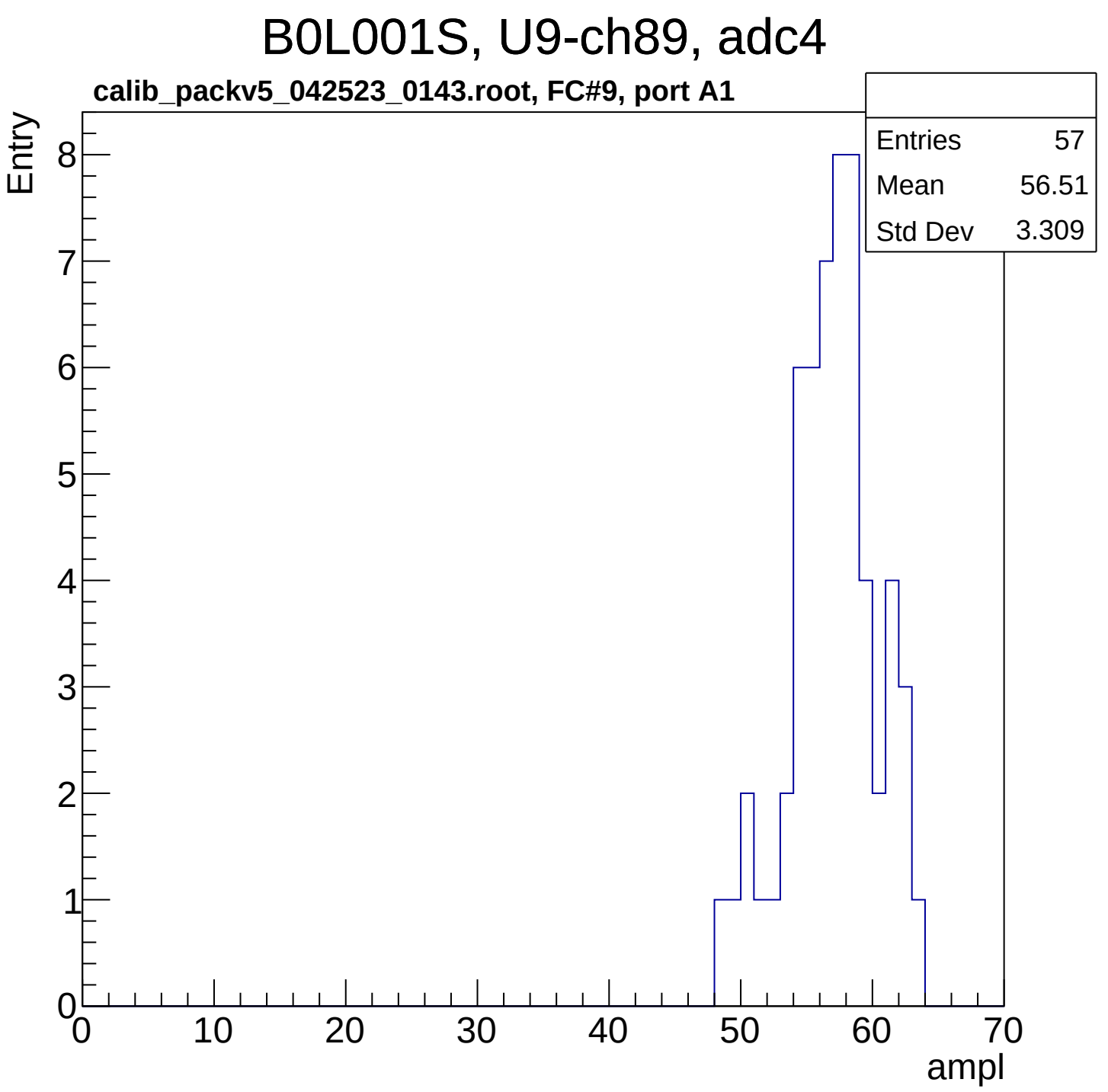
B0L001S, U9-ch89, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.51
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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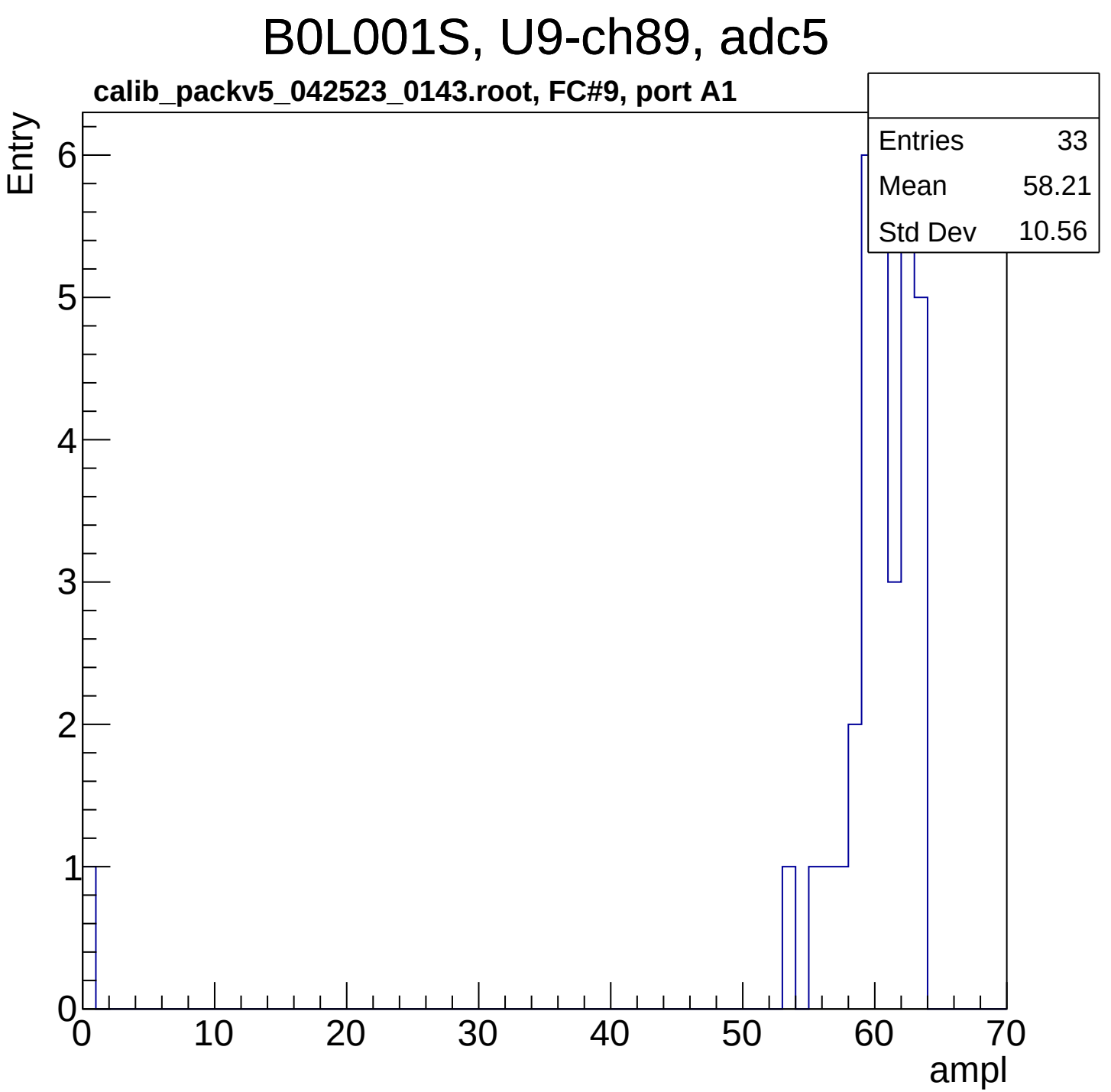
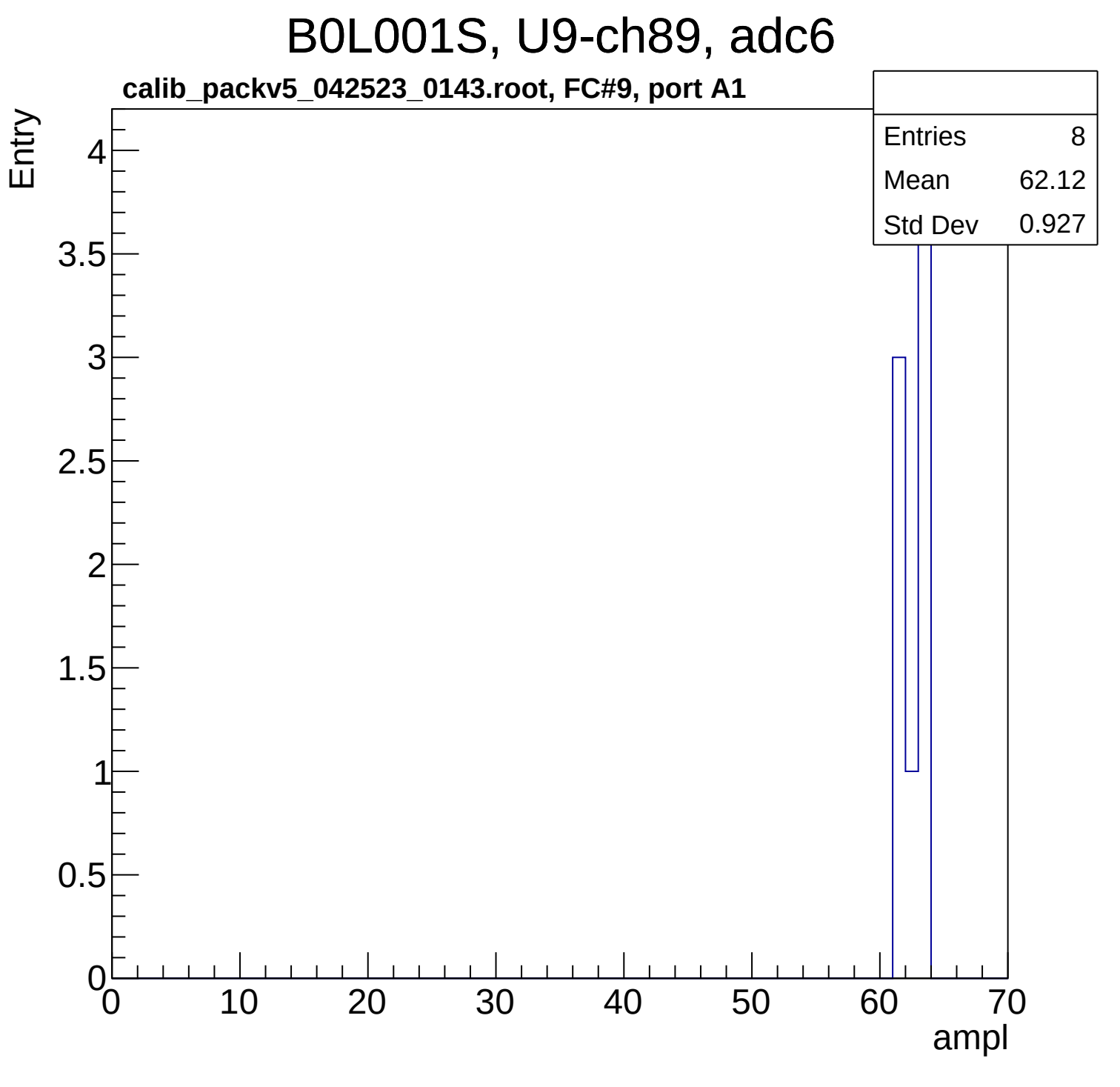
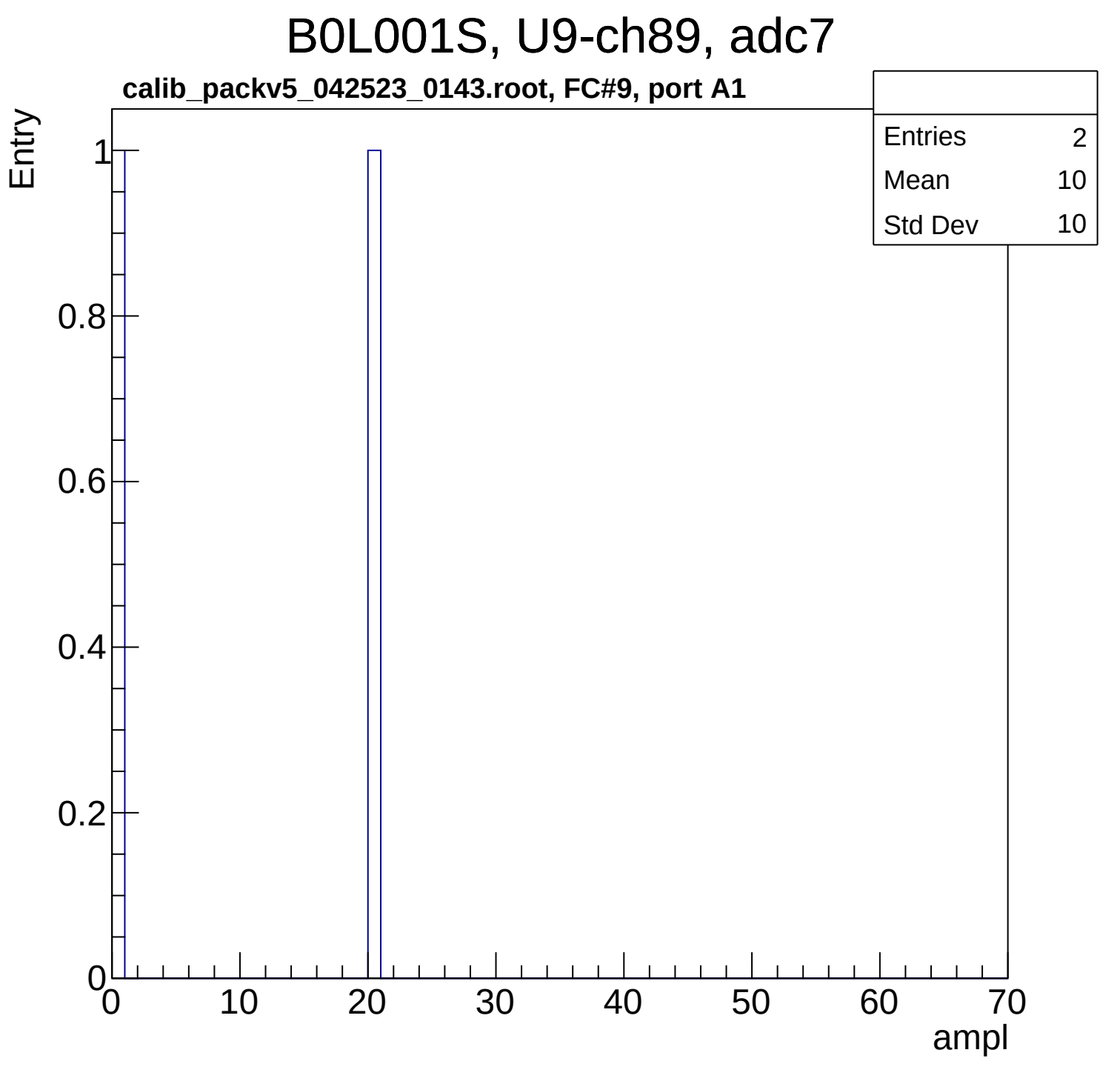
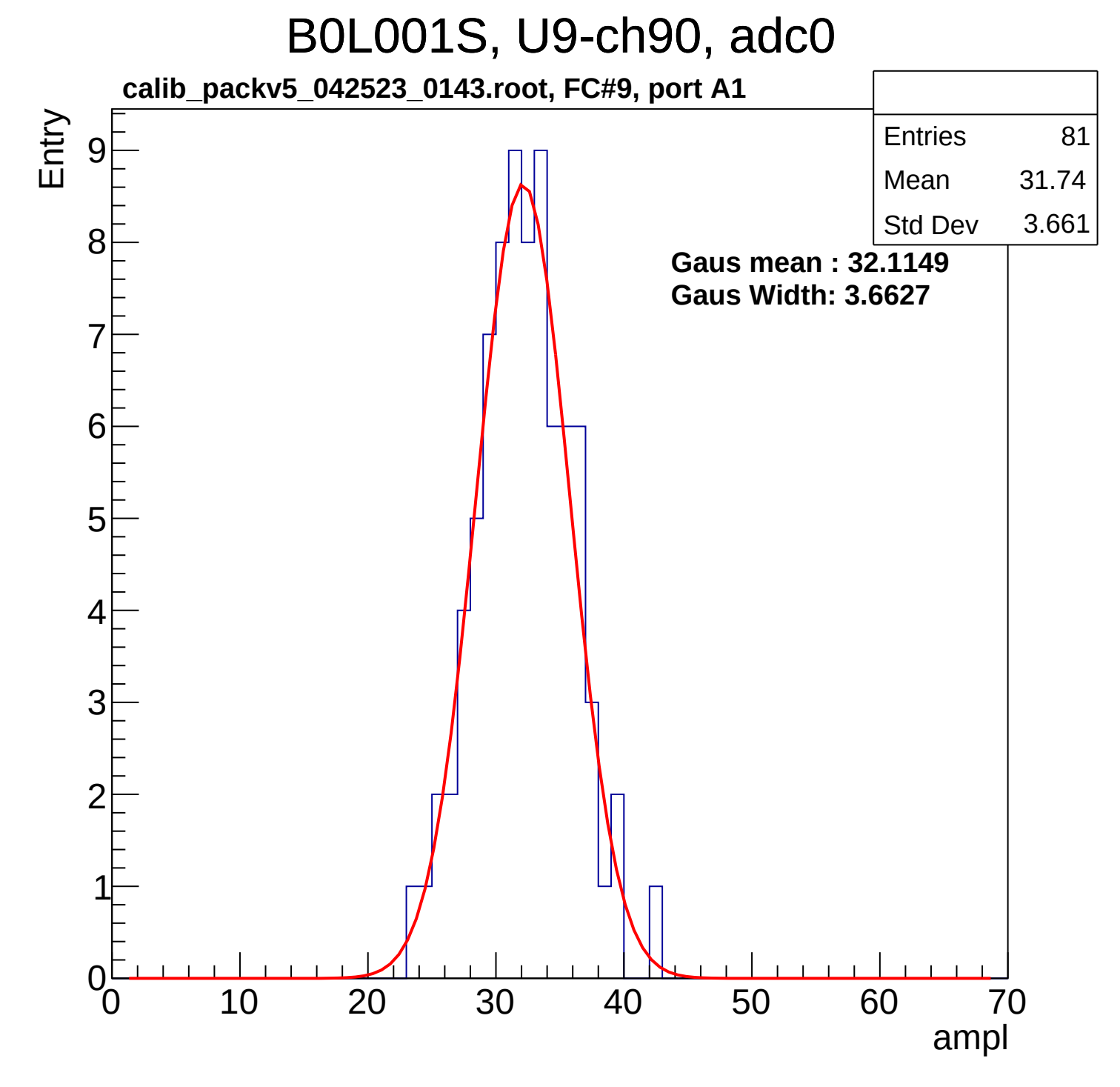
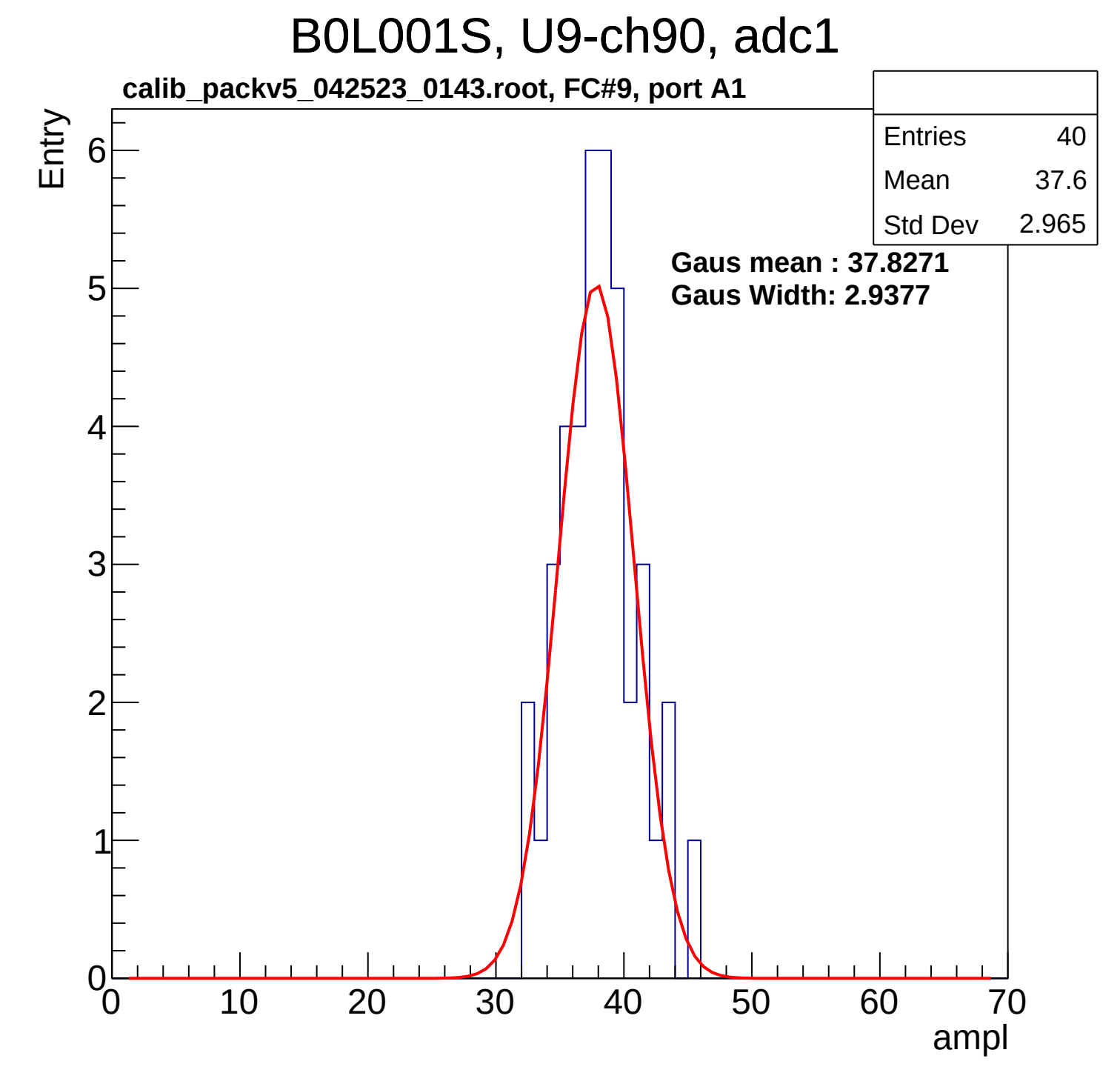
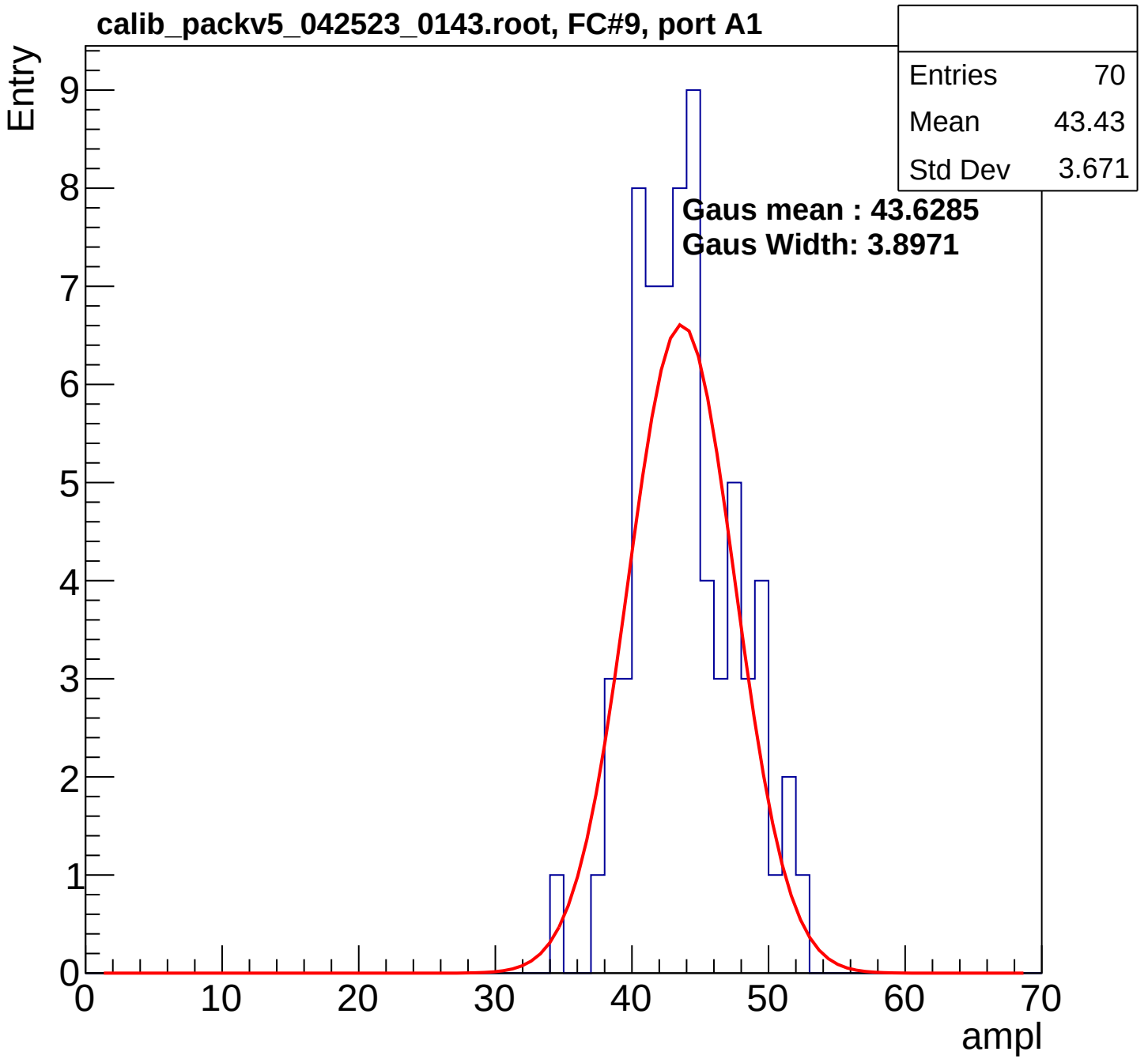

B0L001S, U9-ch89, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.21
Std Dev 10.56
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

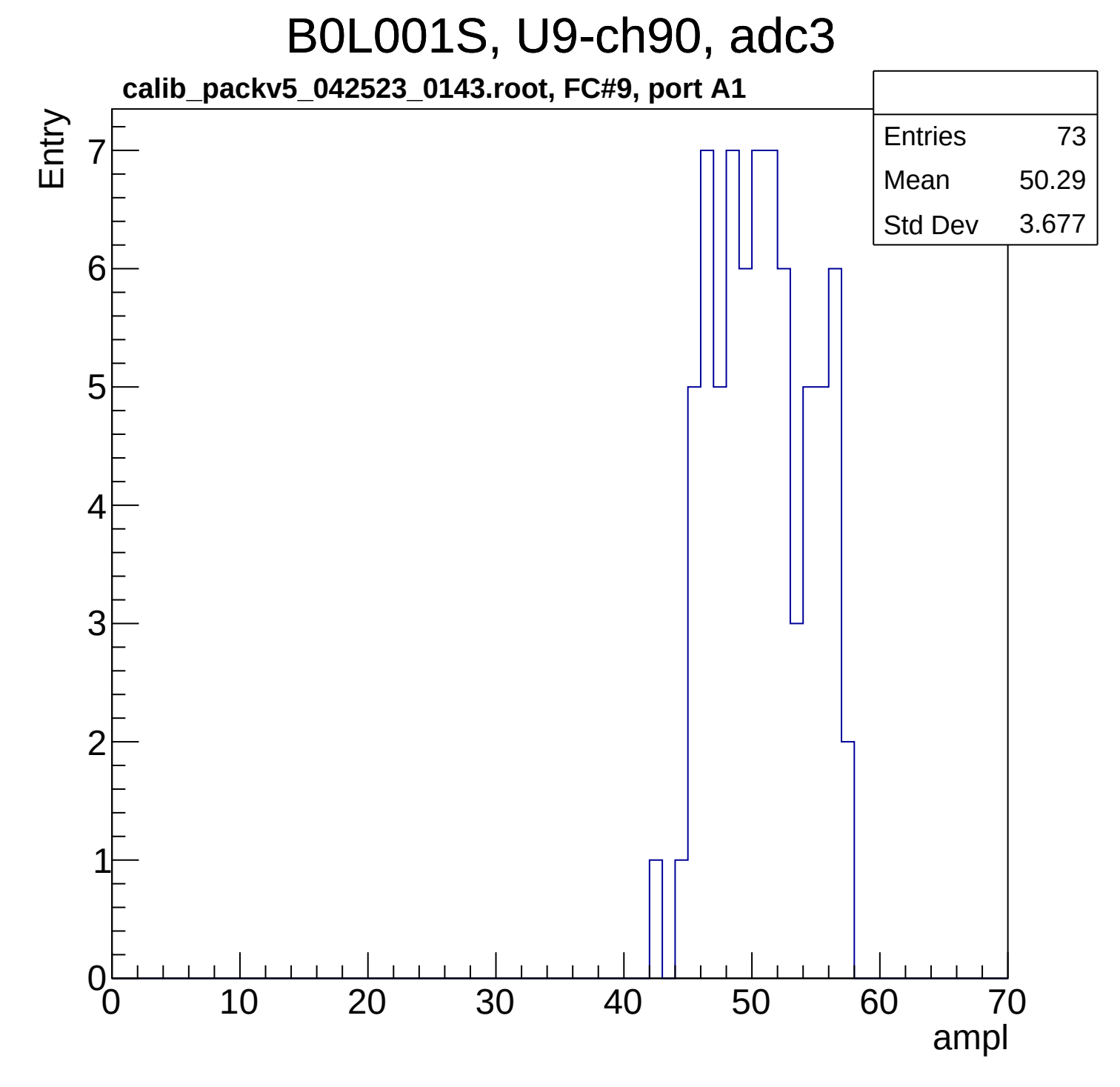

B0L001S, U9-ch89, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62.12
Std Dev 0.927
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U9-ch89, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch90, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 31.74
Std Dev 3.661
Gaus mean : 32.1149
Gaus Width: 3.6627
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch90, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 37.6
Std Dev 2.965
Gaus mean : 37.8271
Gaus Width: 2.9377
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U9-ch90, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.43
Std Dev 3.671
Gaus mean : 43.6285
Gaus Width: 3.8971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch90, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 50.29
Std Dev 3.677
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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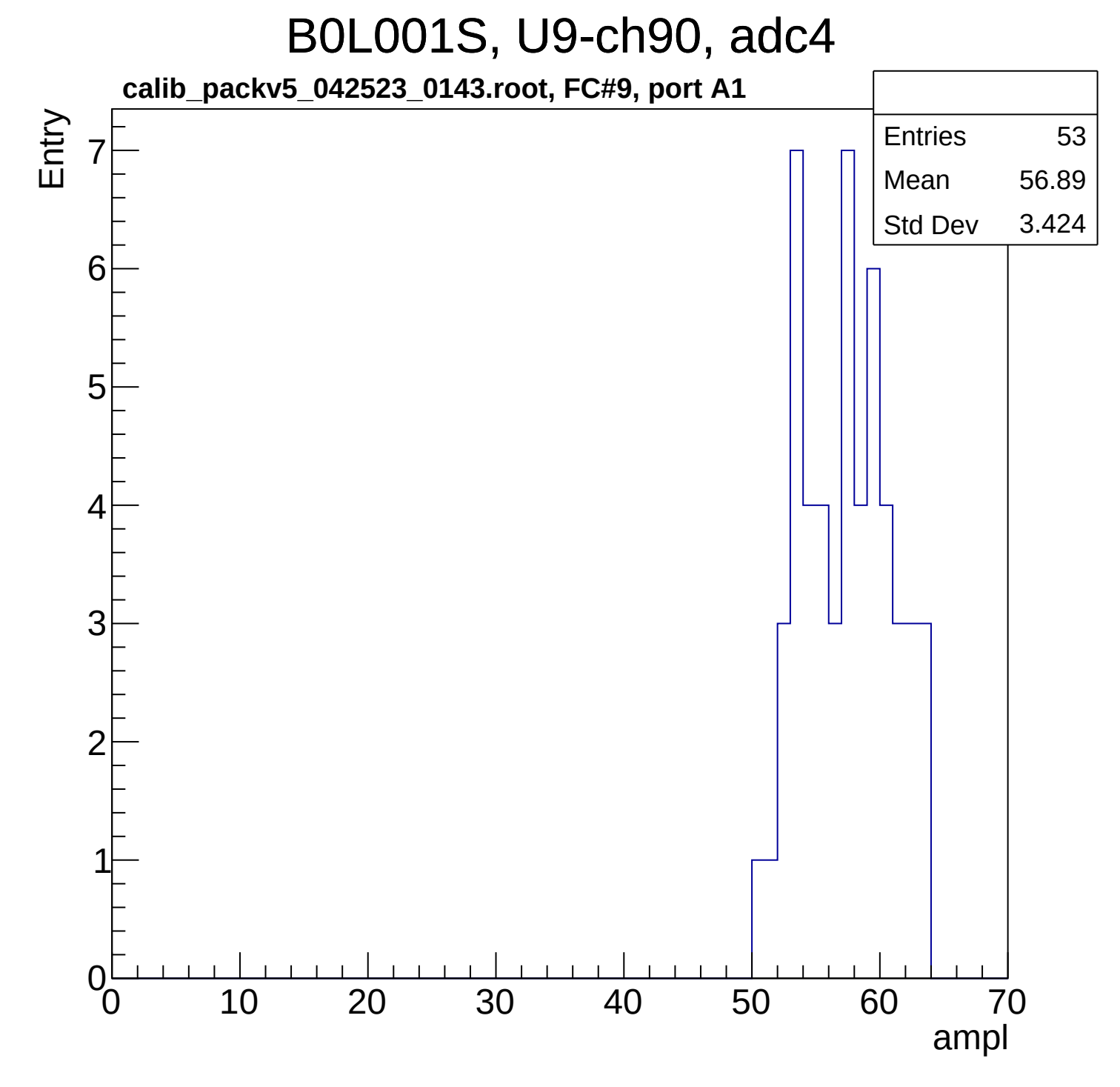
B0L001S, U9-ch90, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.89
Std Dev 3.424
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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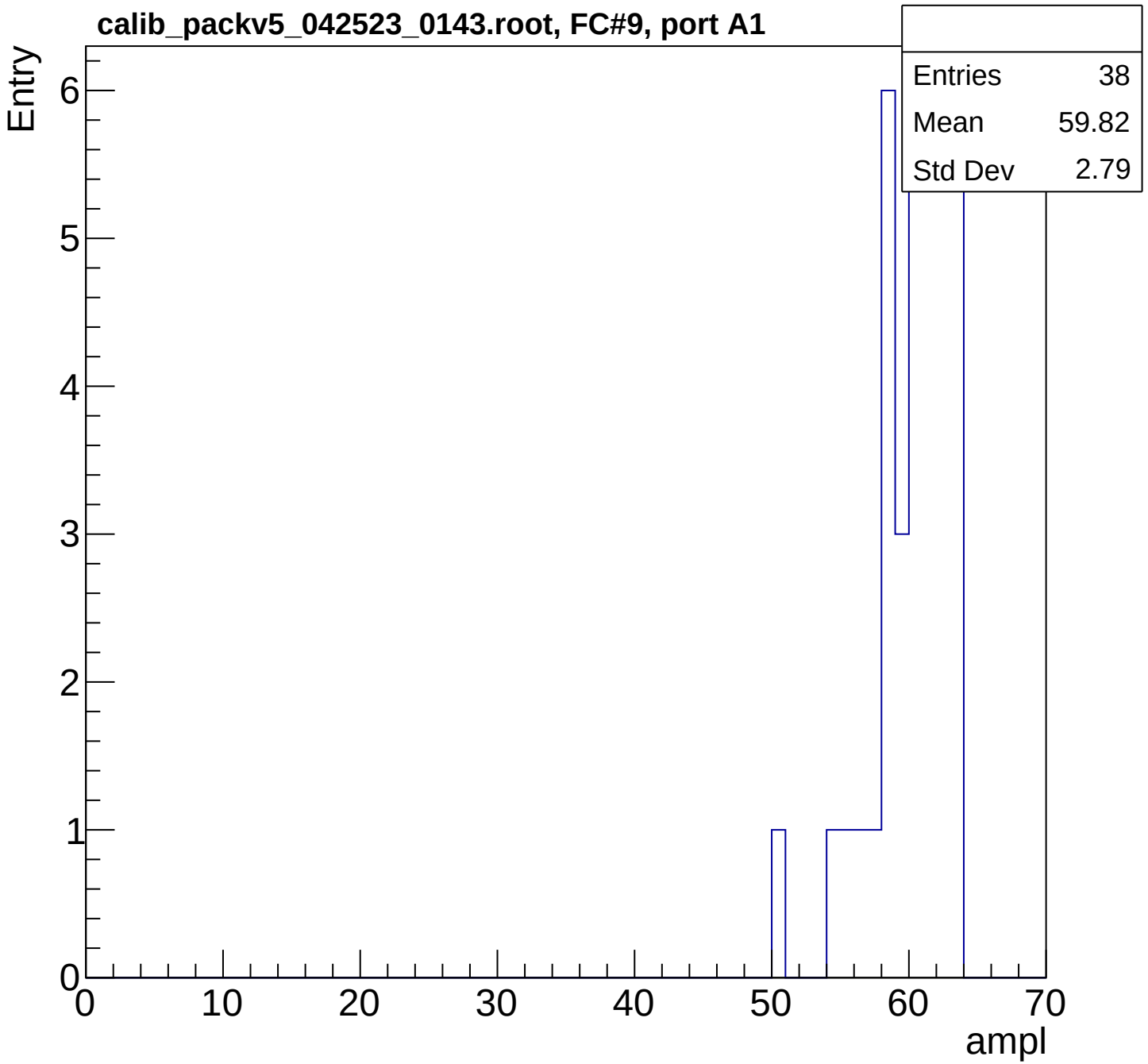
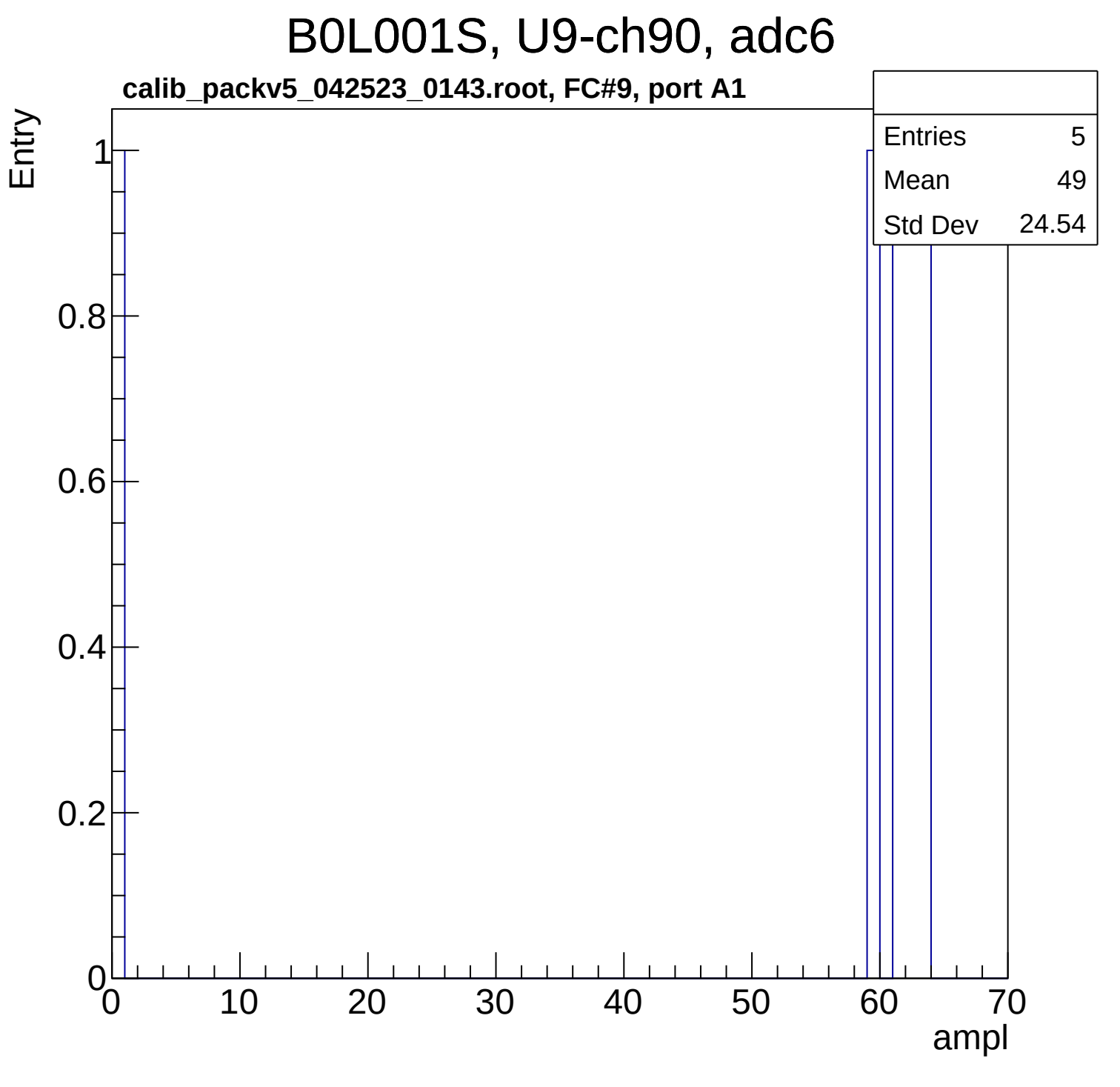
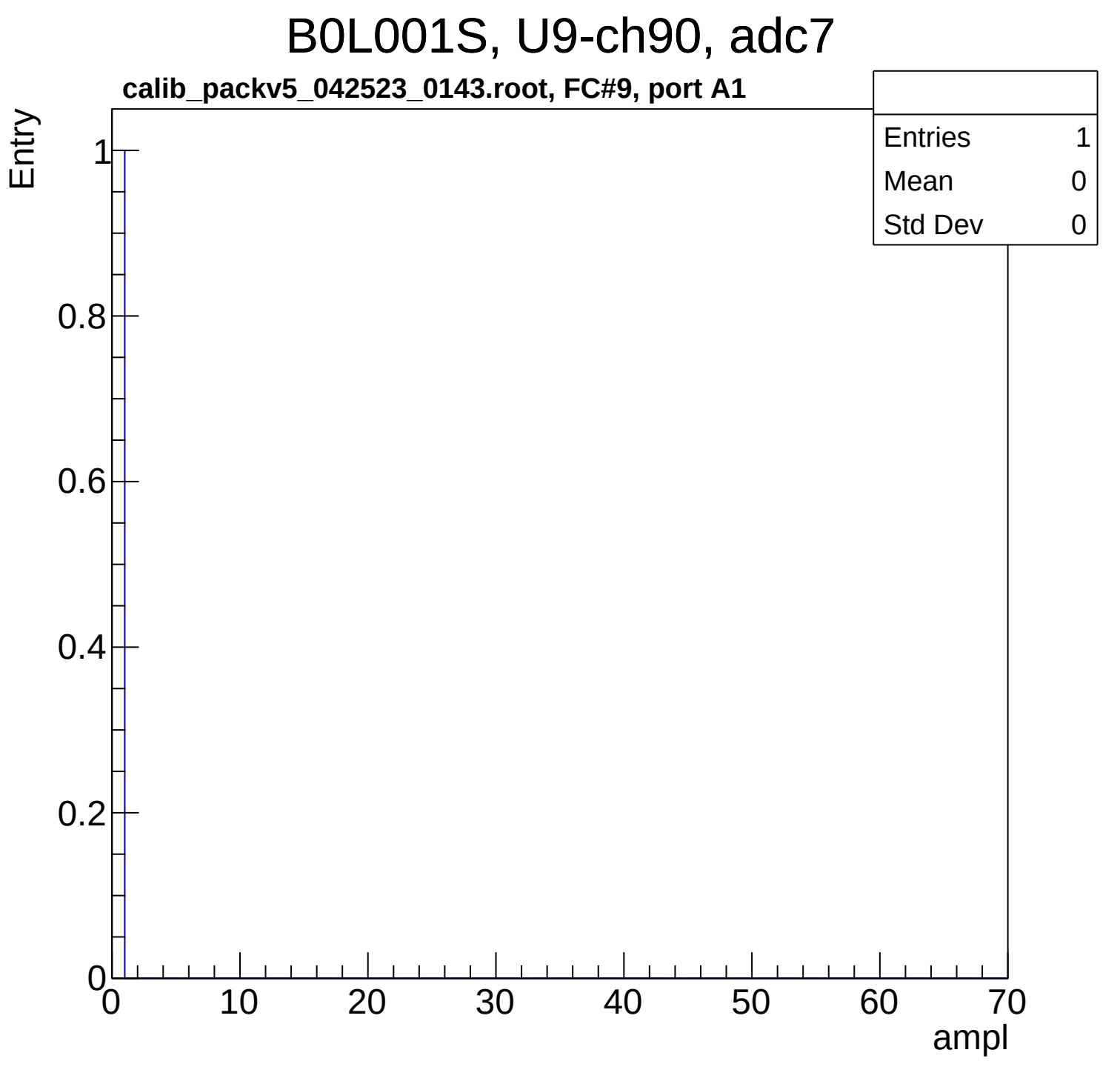
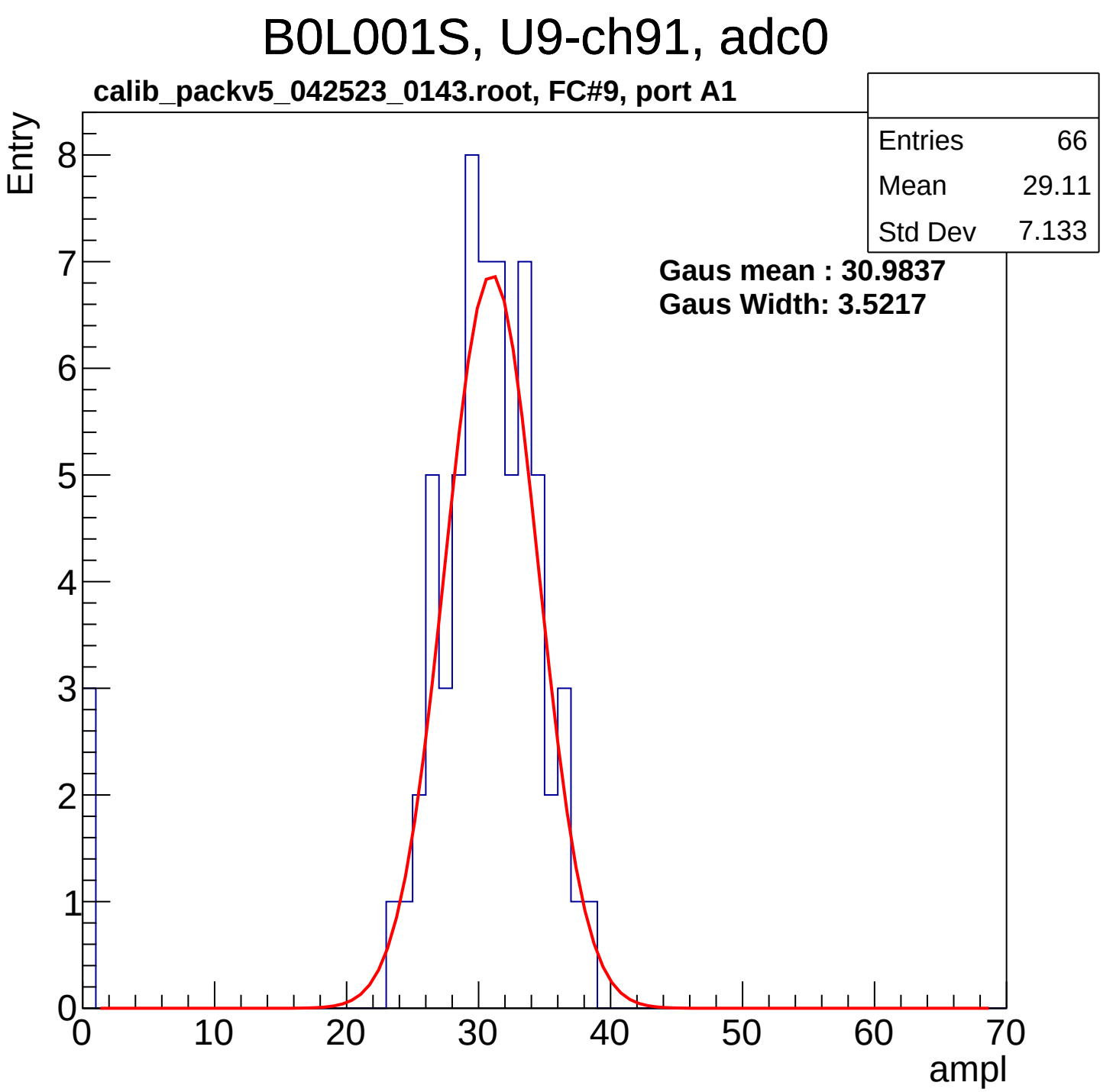
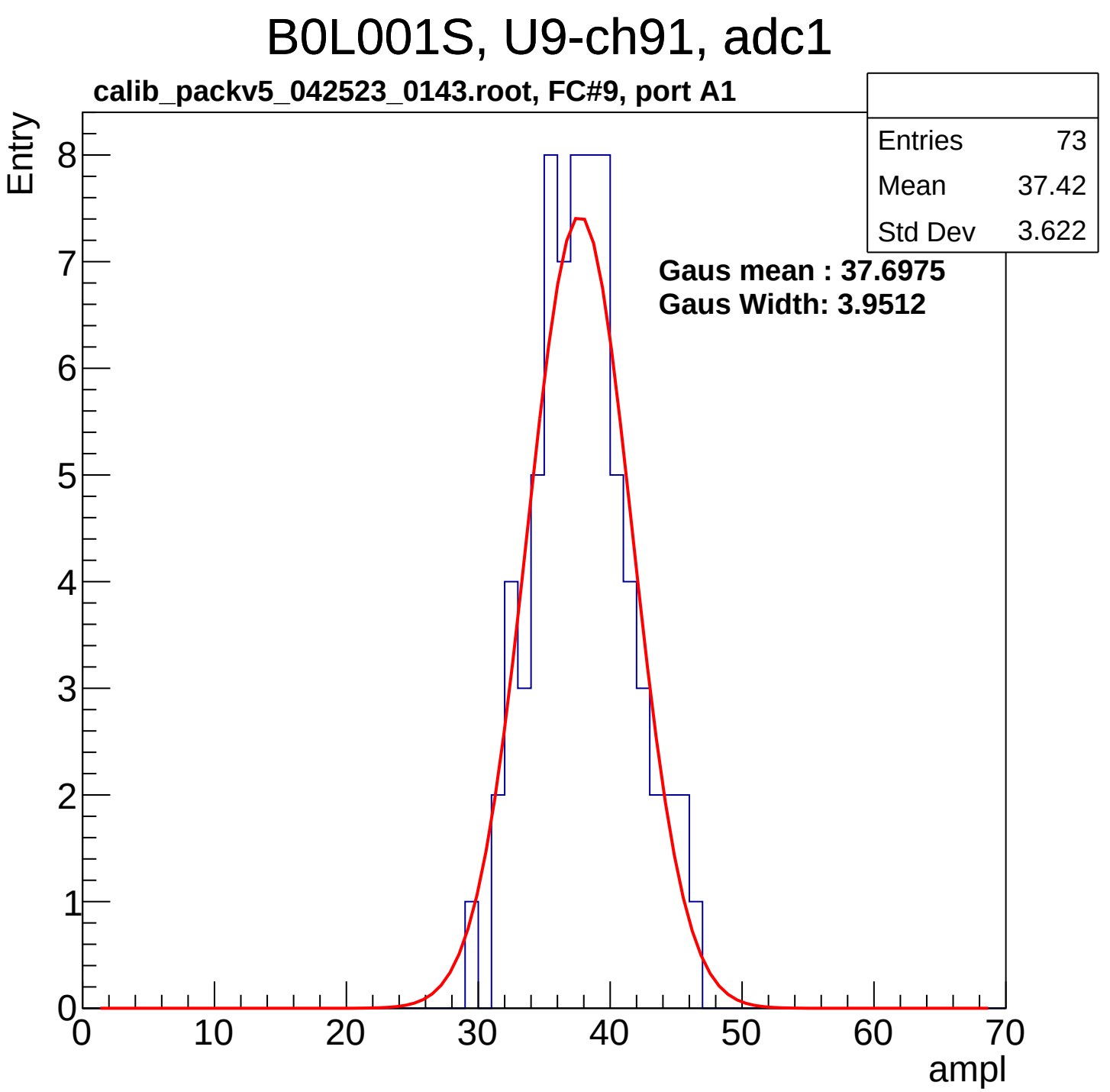

B0L001S, U9-ch90, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 59.82
Std Dev 2.79
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U9-ch90, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49
Std Dev 24.54
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch90, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch91, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 29.11
Std Dev 7.133
Gaus mean : 30.9837
Gaus Width: 3.5217
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch91, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.42
Std Dev 3.622
Gaus mean : 37.6975
Gaus Width: 3.9512
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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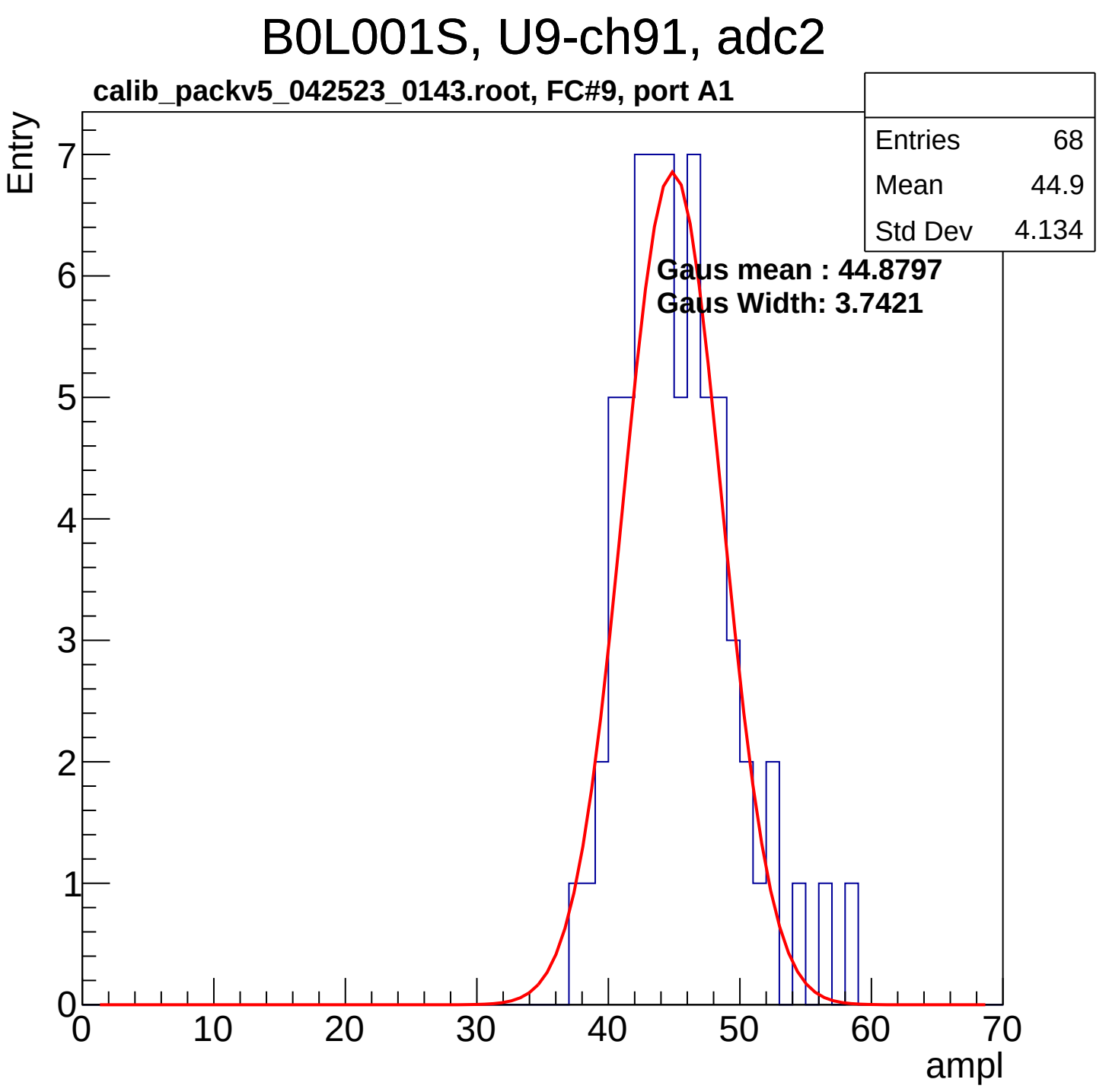
B0L001S, U9-ch91, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 44.9
Std Dev 4.134
Gaus mean : 44.8797
Gaus Width: 3.7421
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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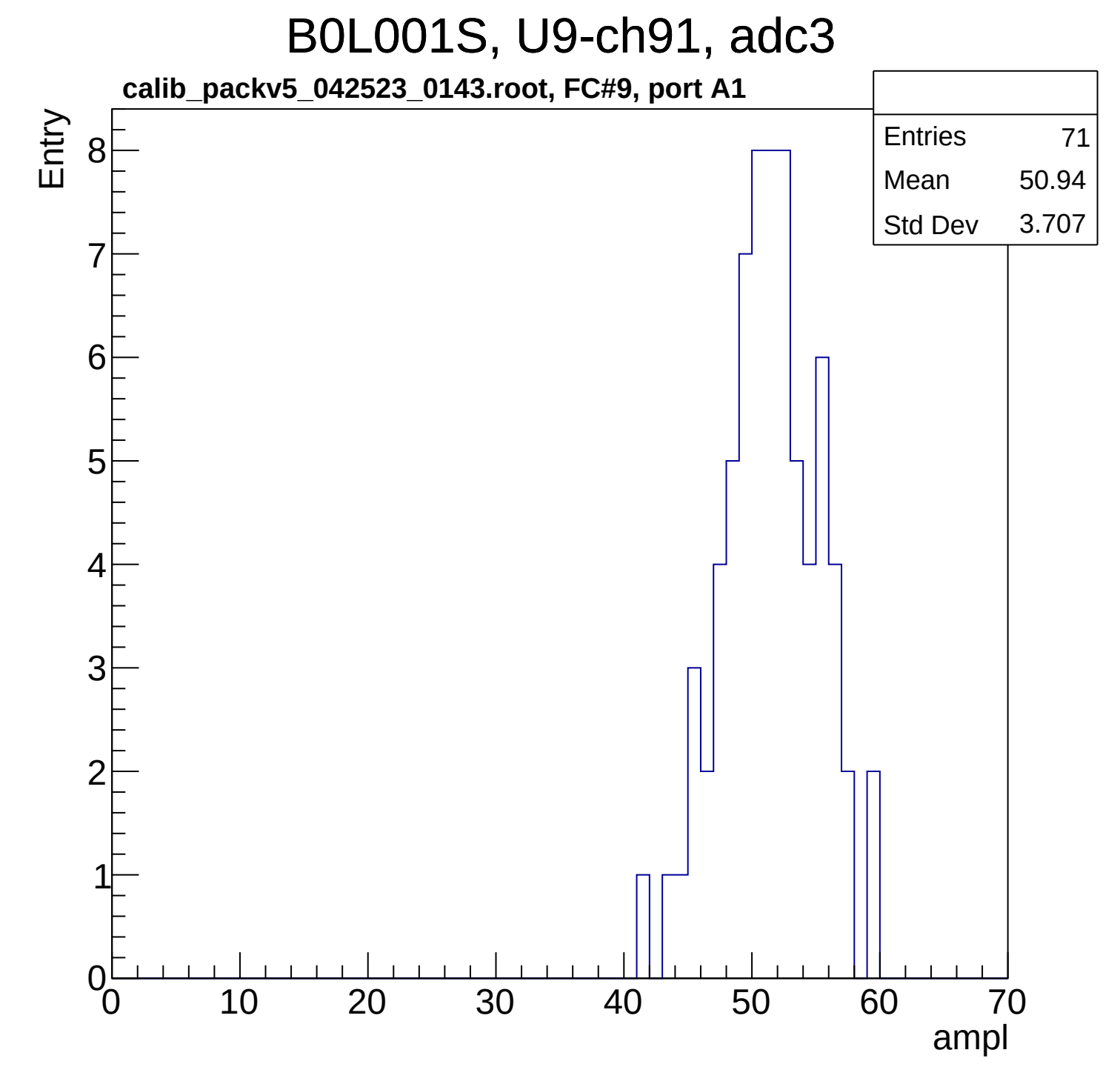
B0L001S, U9-ch91, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 50.94
Std Dev 3.707
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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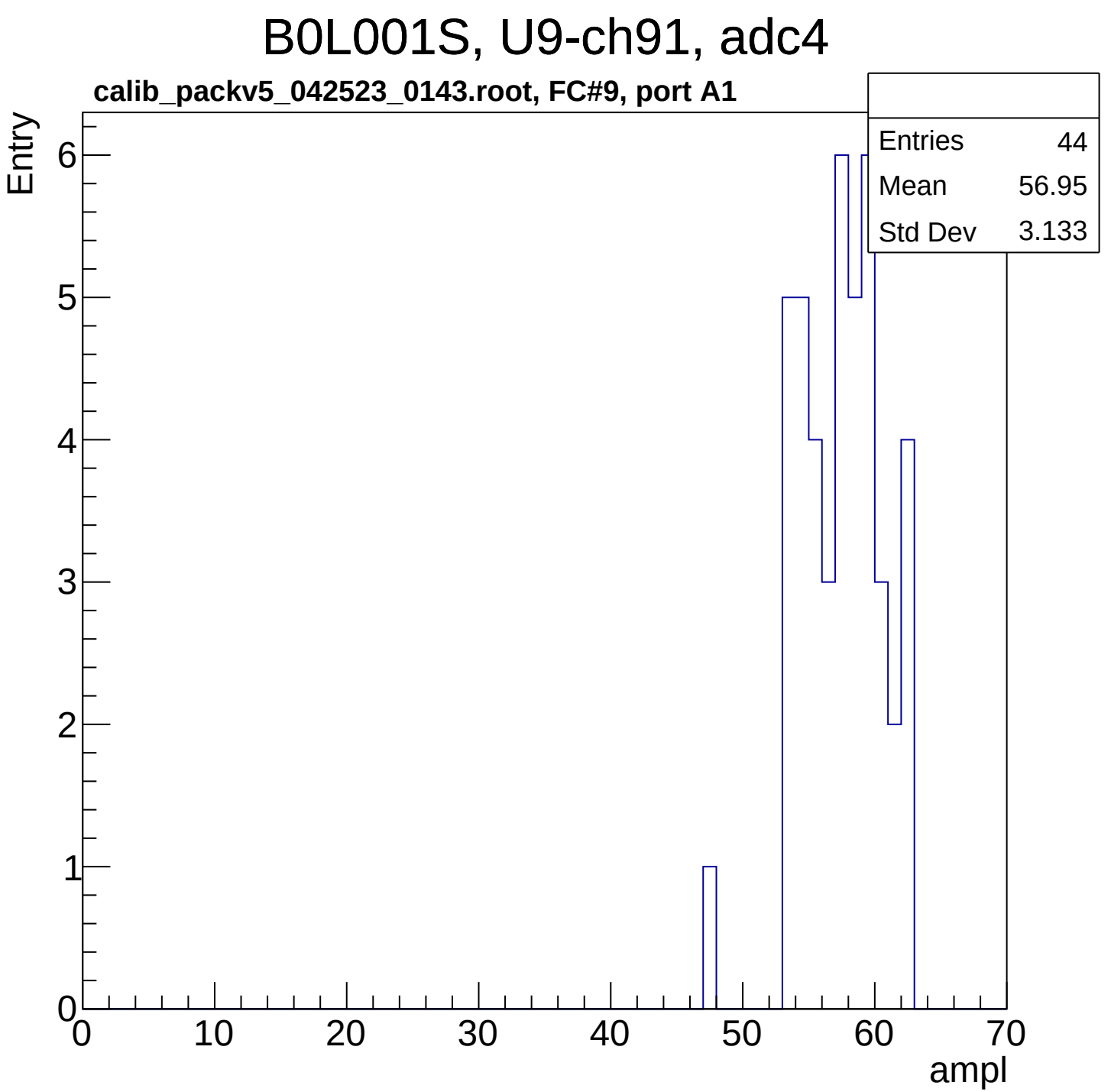
B0L001S, U9-ch91, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 56.95
Std Dev 3.133
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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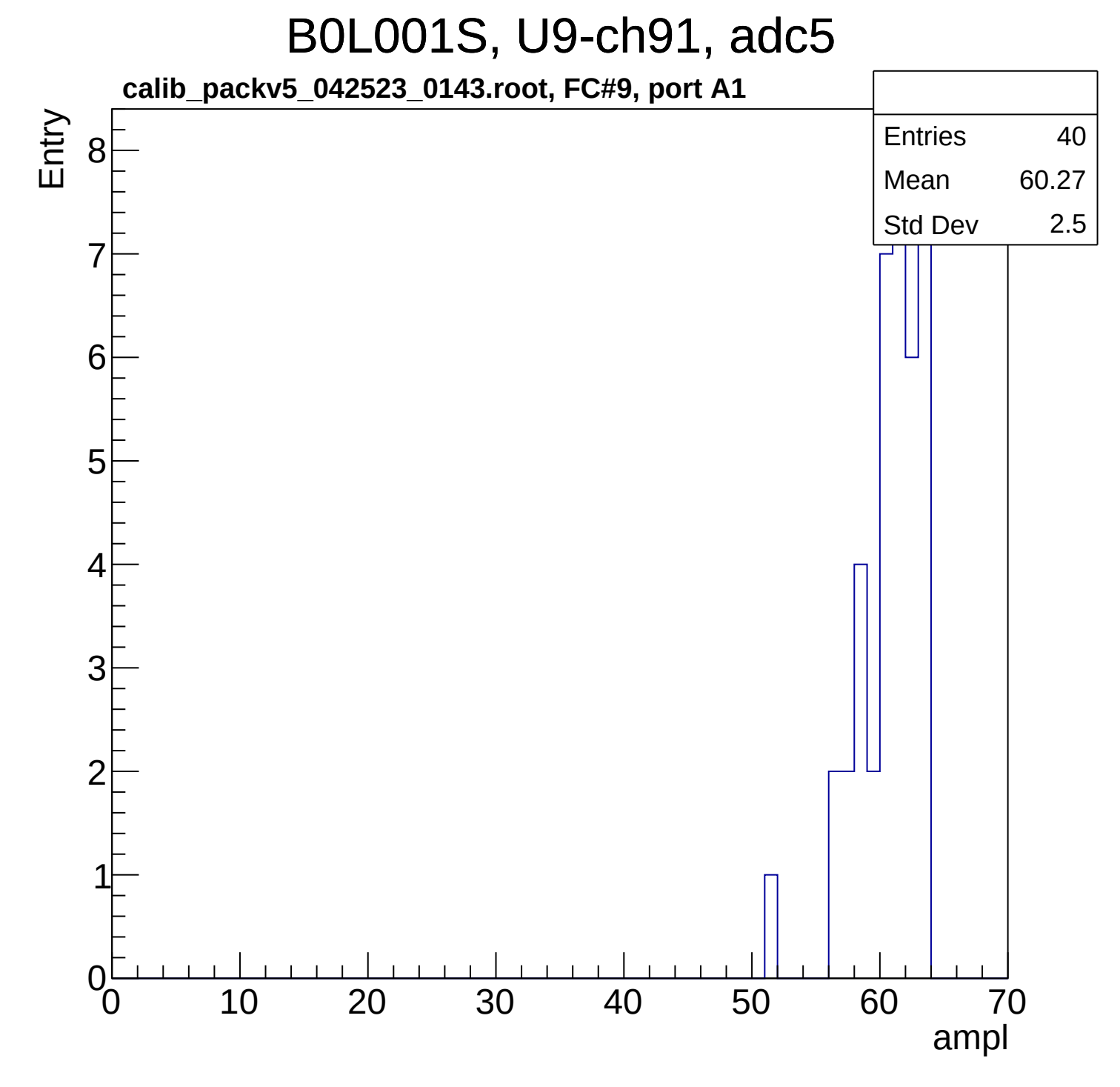
B0L001S, U9-ch91, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 60.27
Std Dev 2.5
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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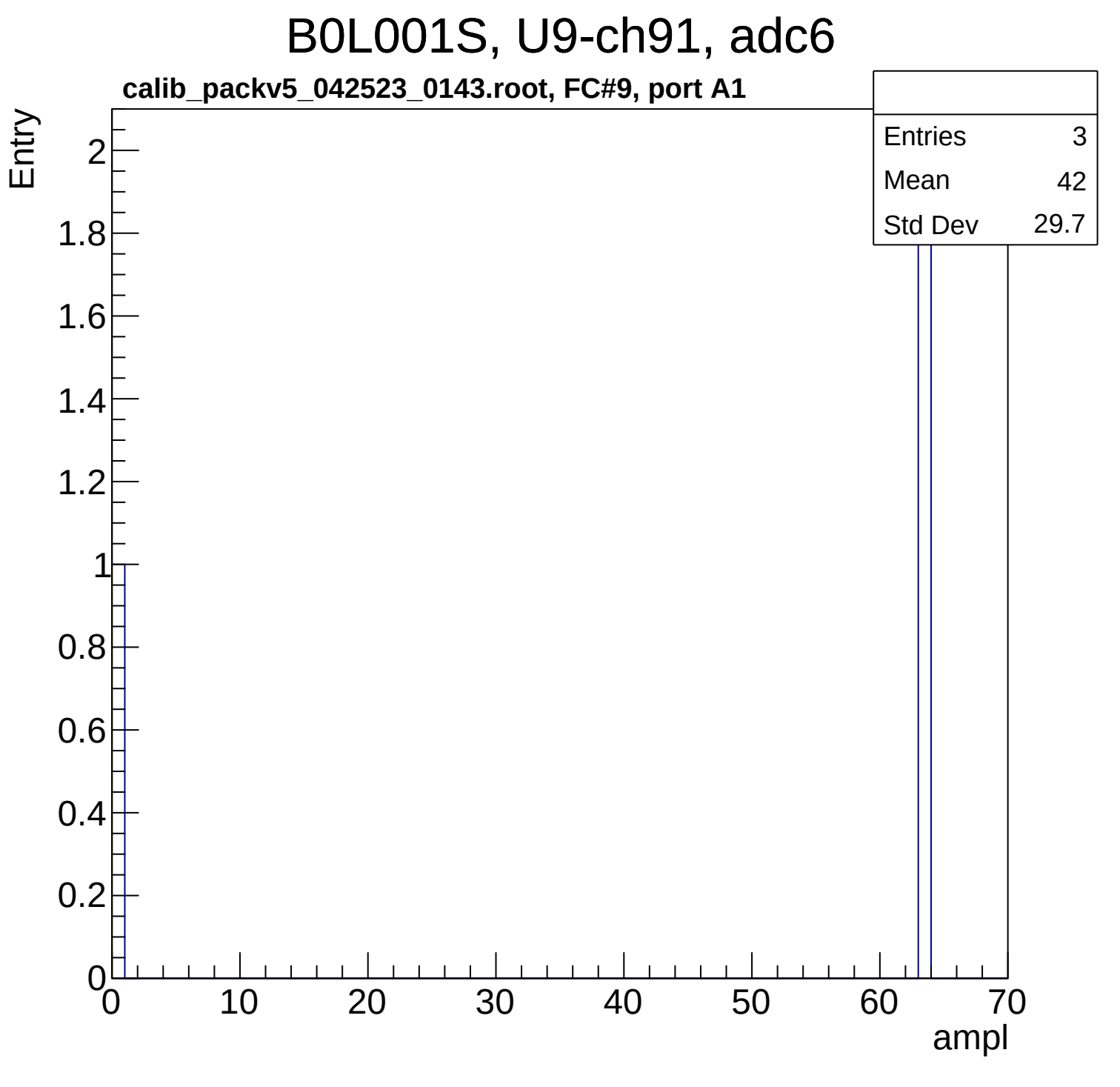
B0L001S, U9-ch91, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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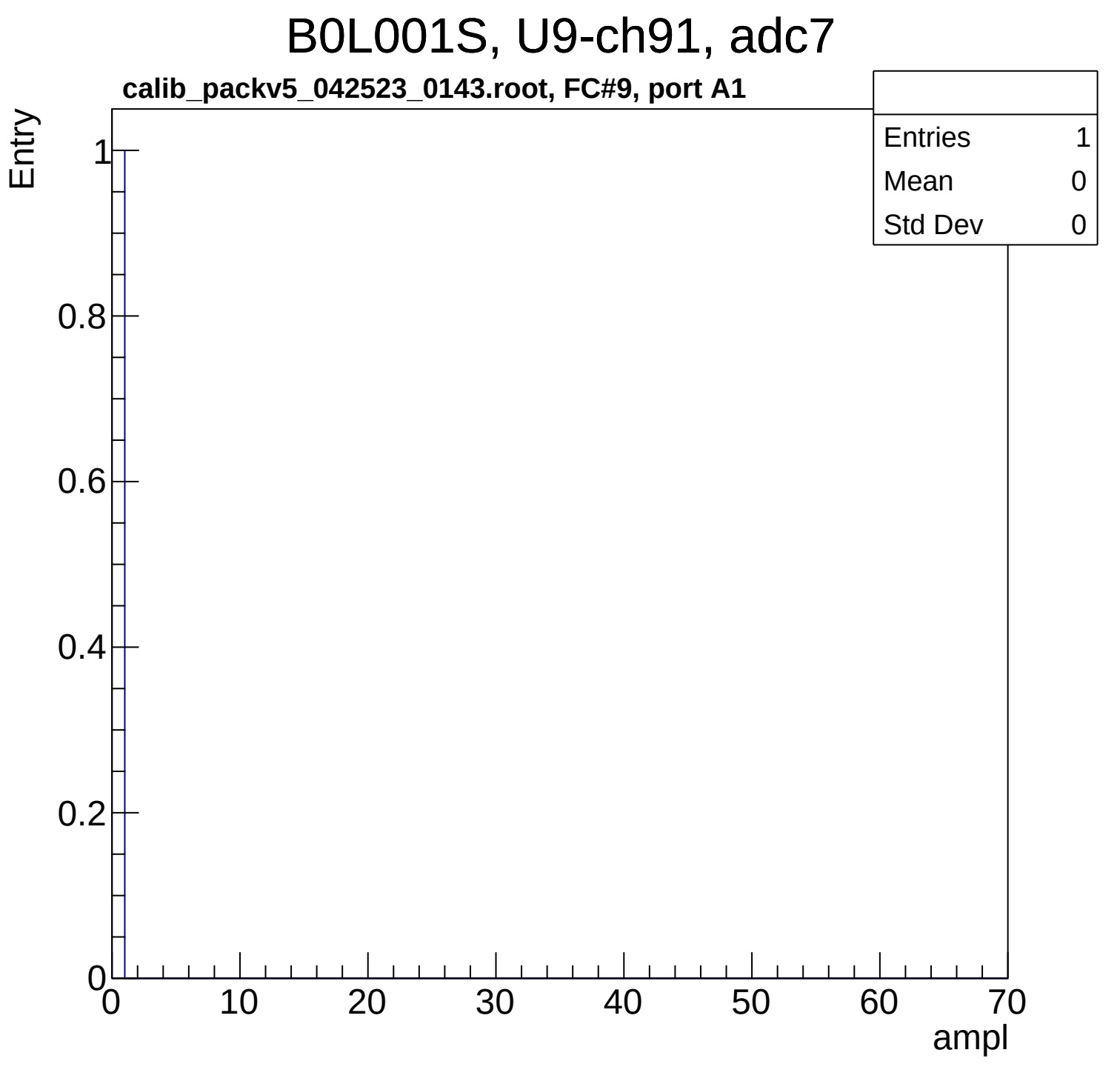
B0L001S, U9-ch91, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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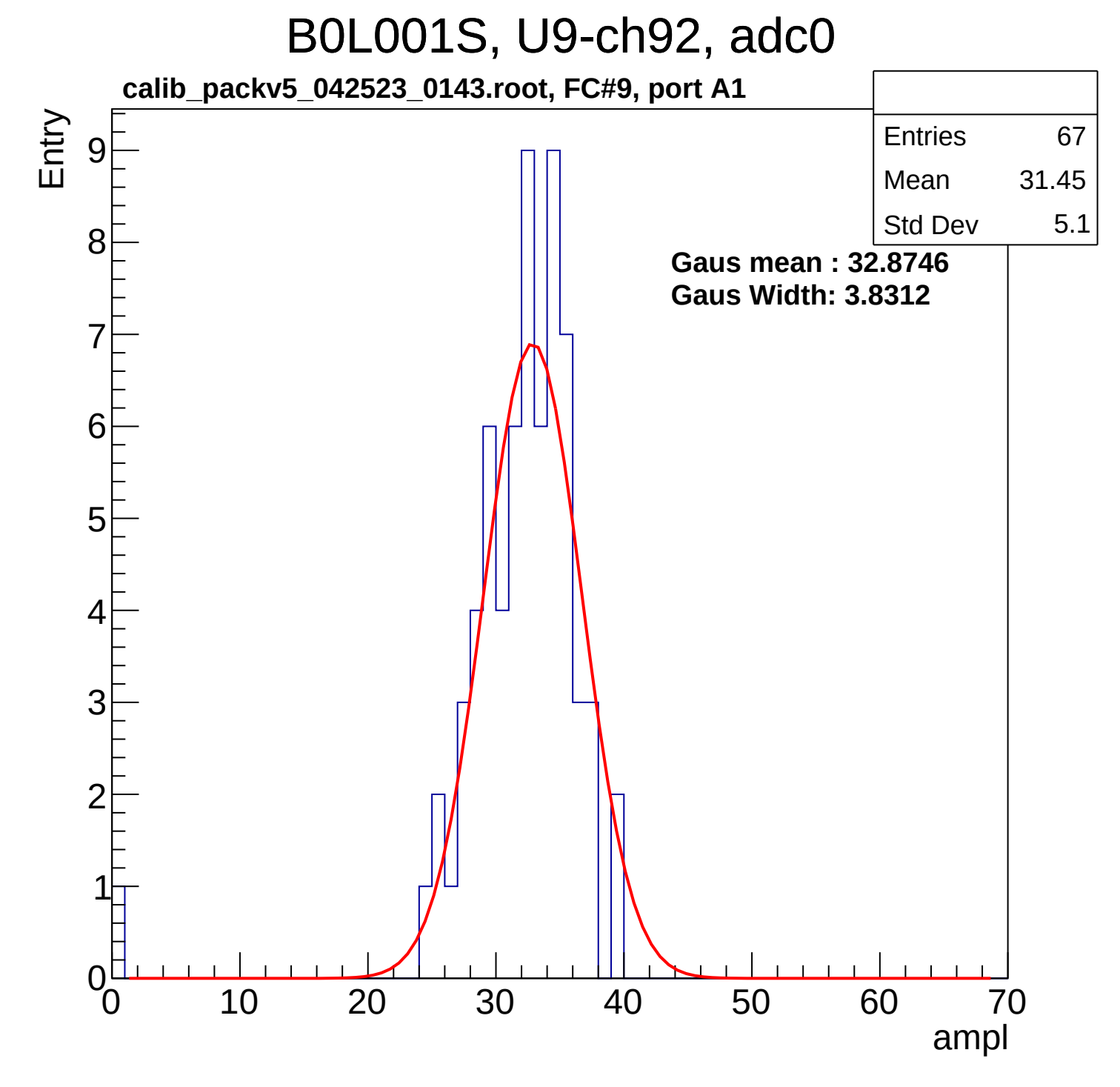
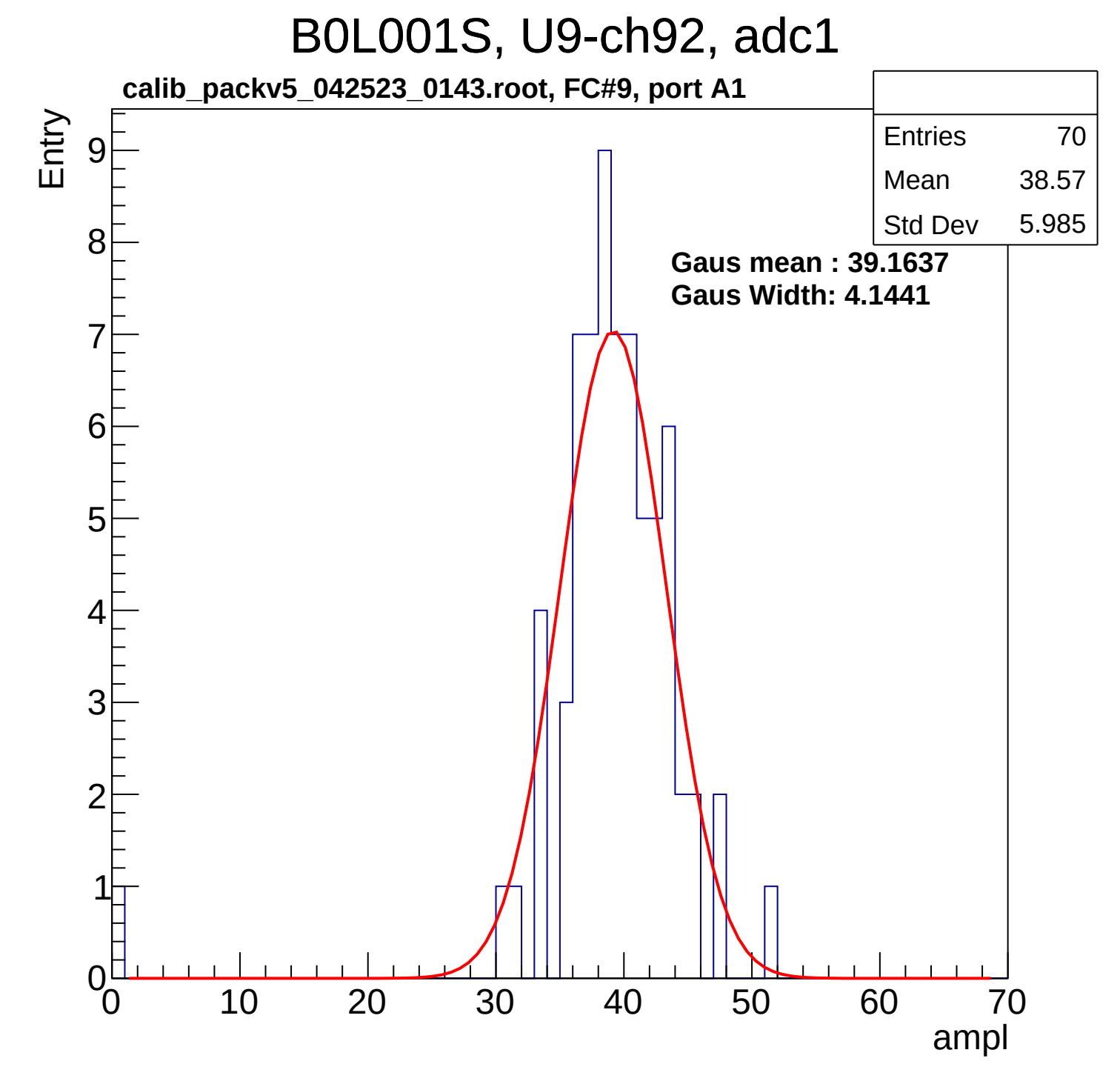
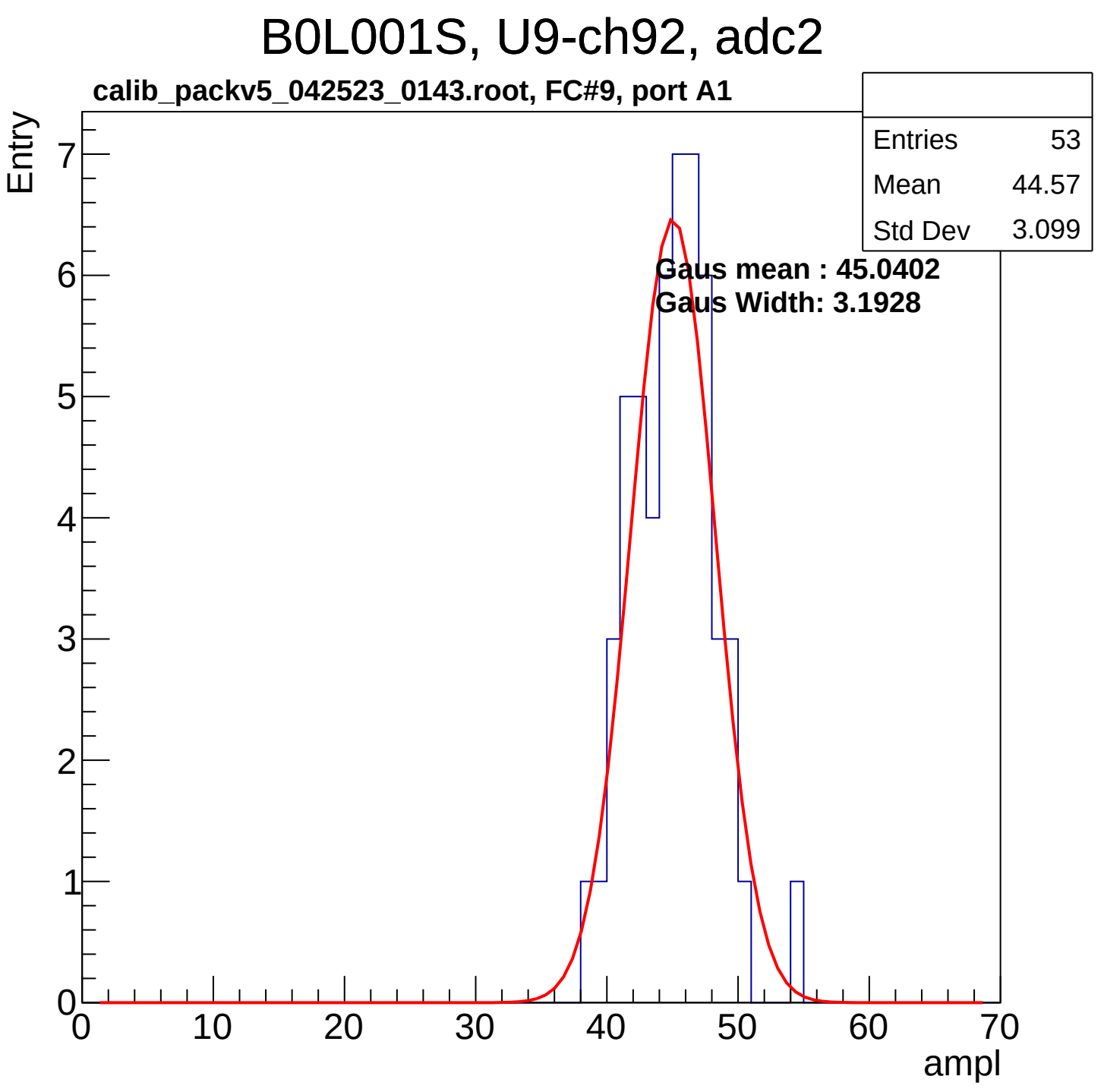
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch92, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.45
Std Dev 5.1
Gaus mean : 32.8746
Gaus Width: 3.8312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch92, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 38.57
Std Dev 5.985
Gaus mean : 39.1637
Gaus Width: 4.1441
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch92, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 44.57
Std Dev 3.099
Gaus mean : 45.0402
Gaus Width: 3.1928
Entry
ampl
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1
2
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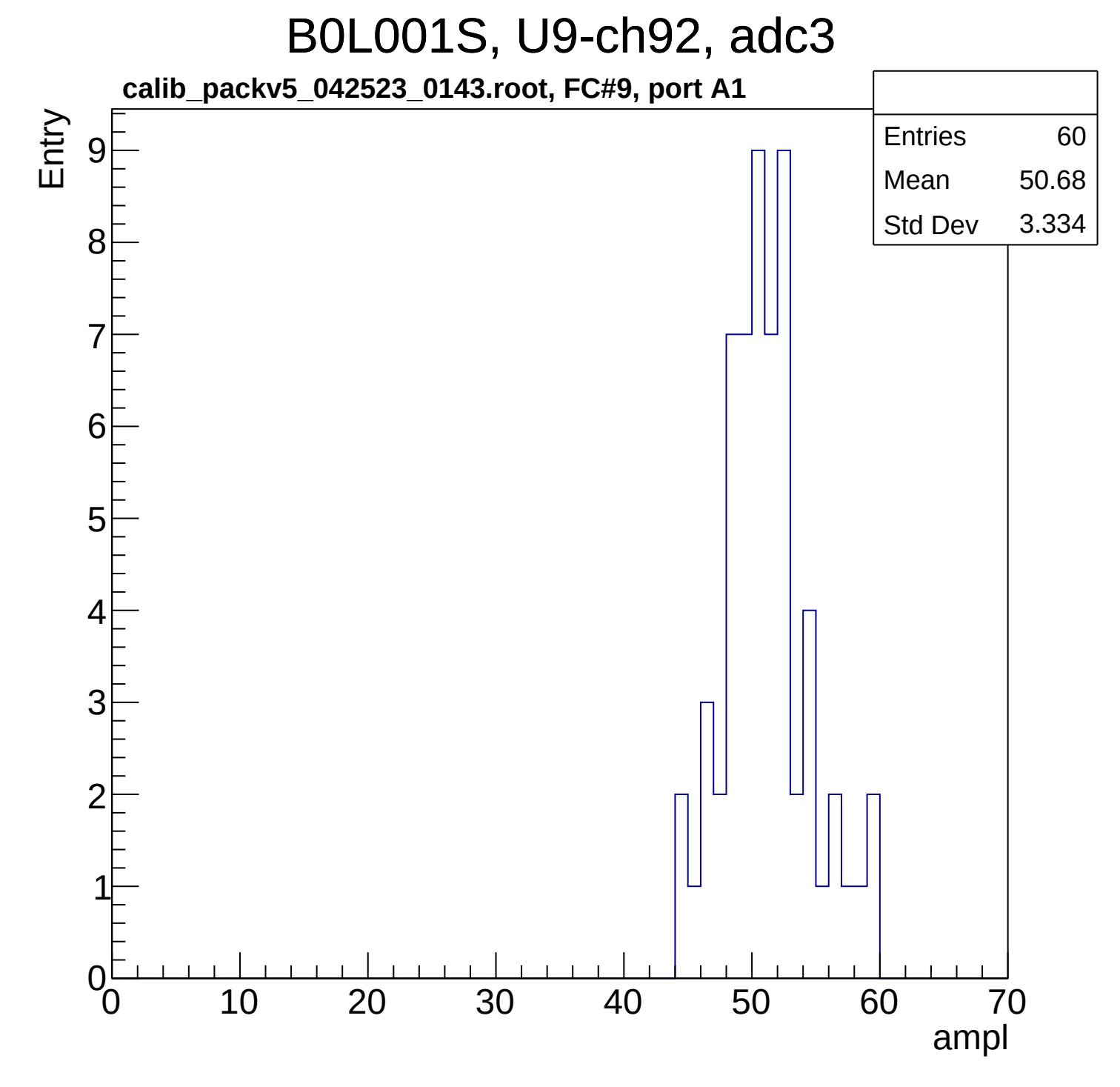
B0L001S, U9-ch92, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.68
Std Dev 3.334
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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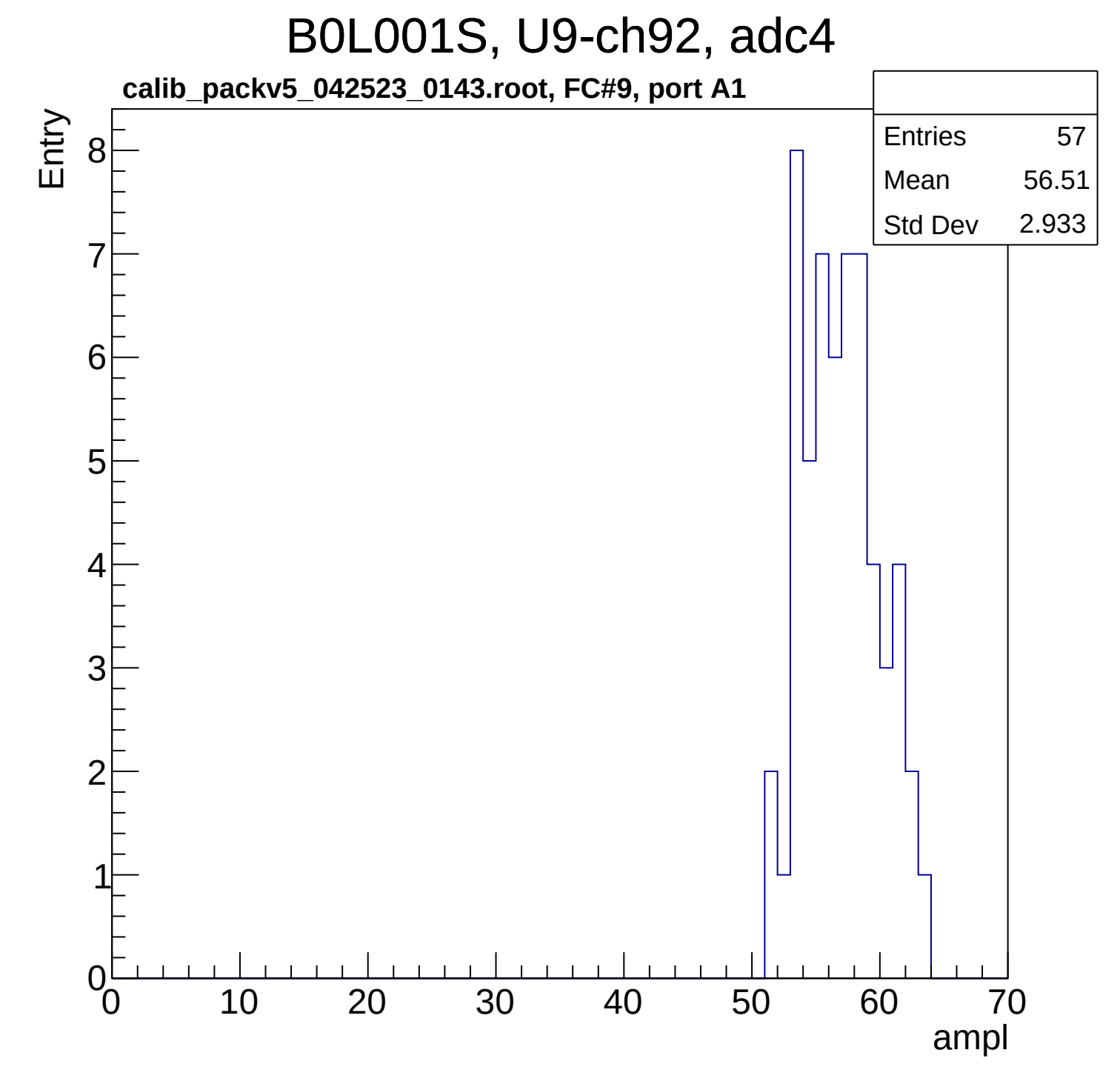
B0L001S, U9-ch92, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.51
Std Dev 2.933
Entry
ampl
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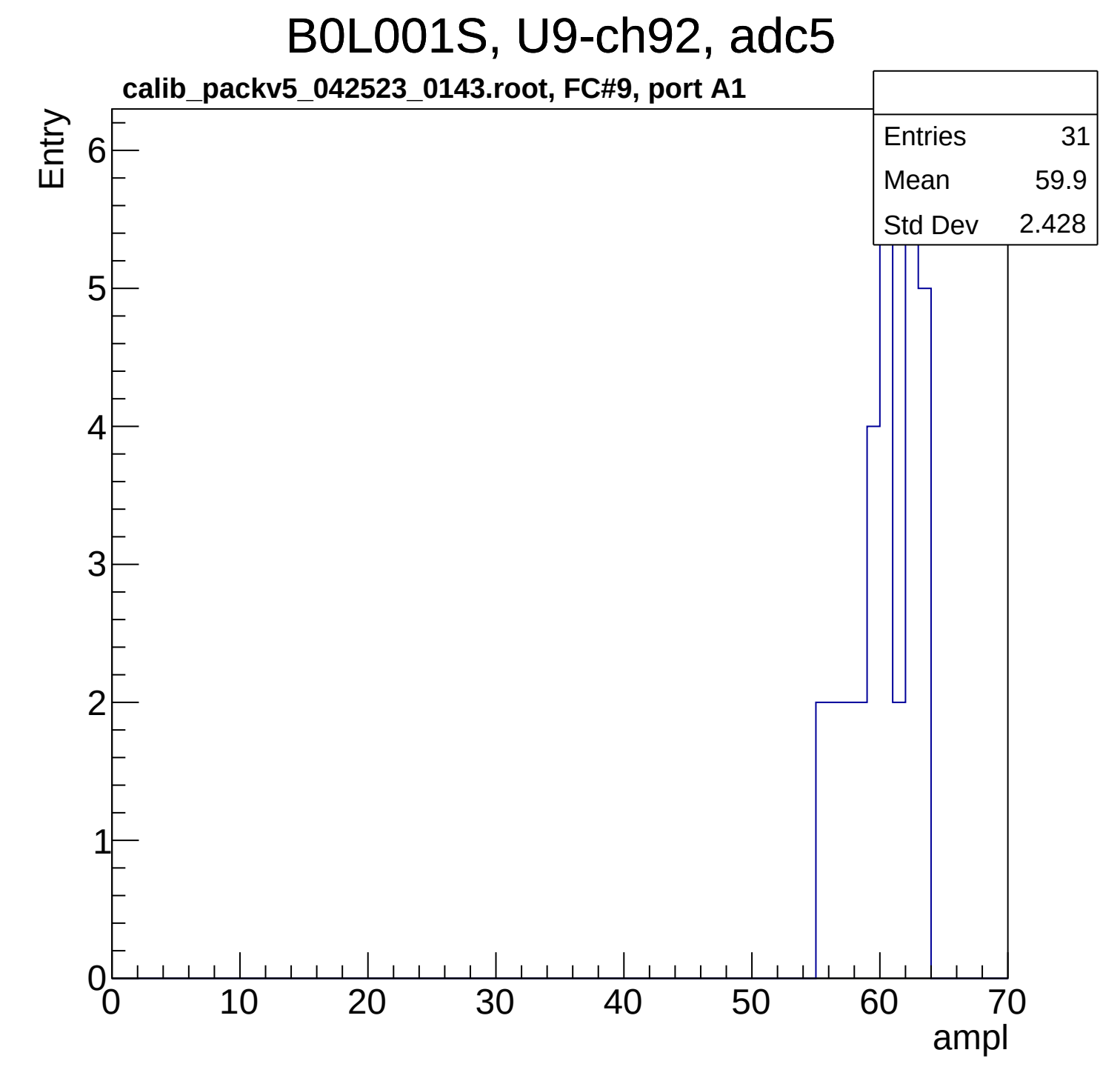
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch92, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59.9
Std Dev 2.428
Entry
0
1
2
3
4
5
6
0
10
20
30
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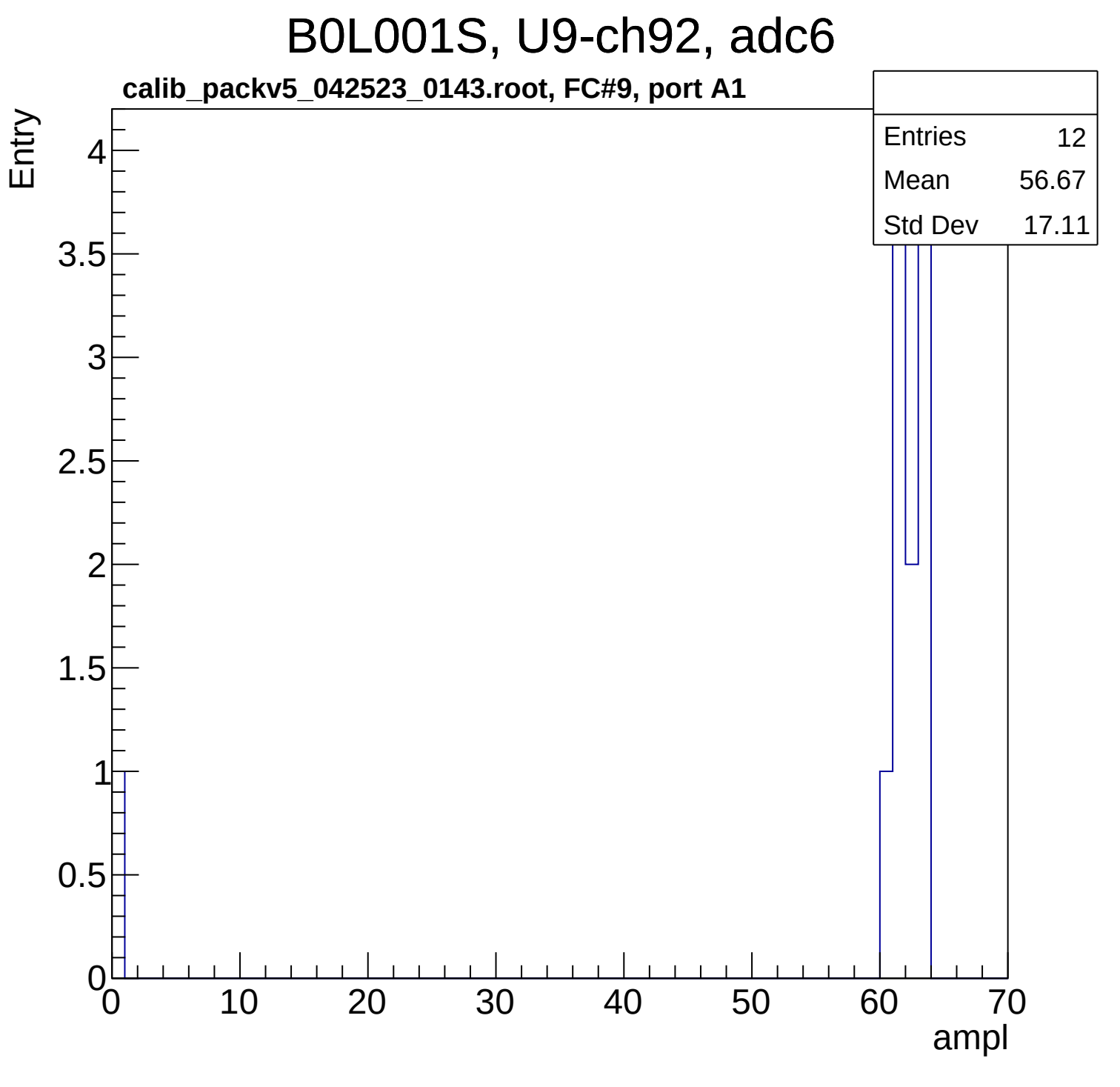
50
60
70
ampl


B0L001S, U9-ch92, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 56.67
Std Dev 17.11
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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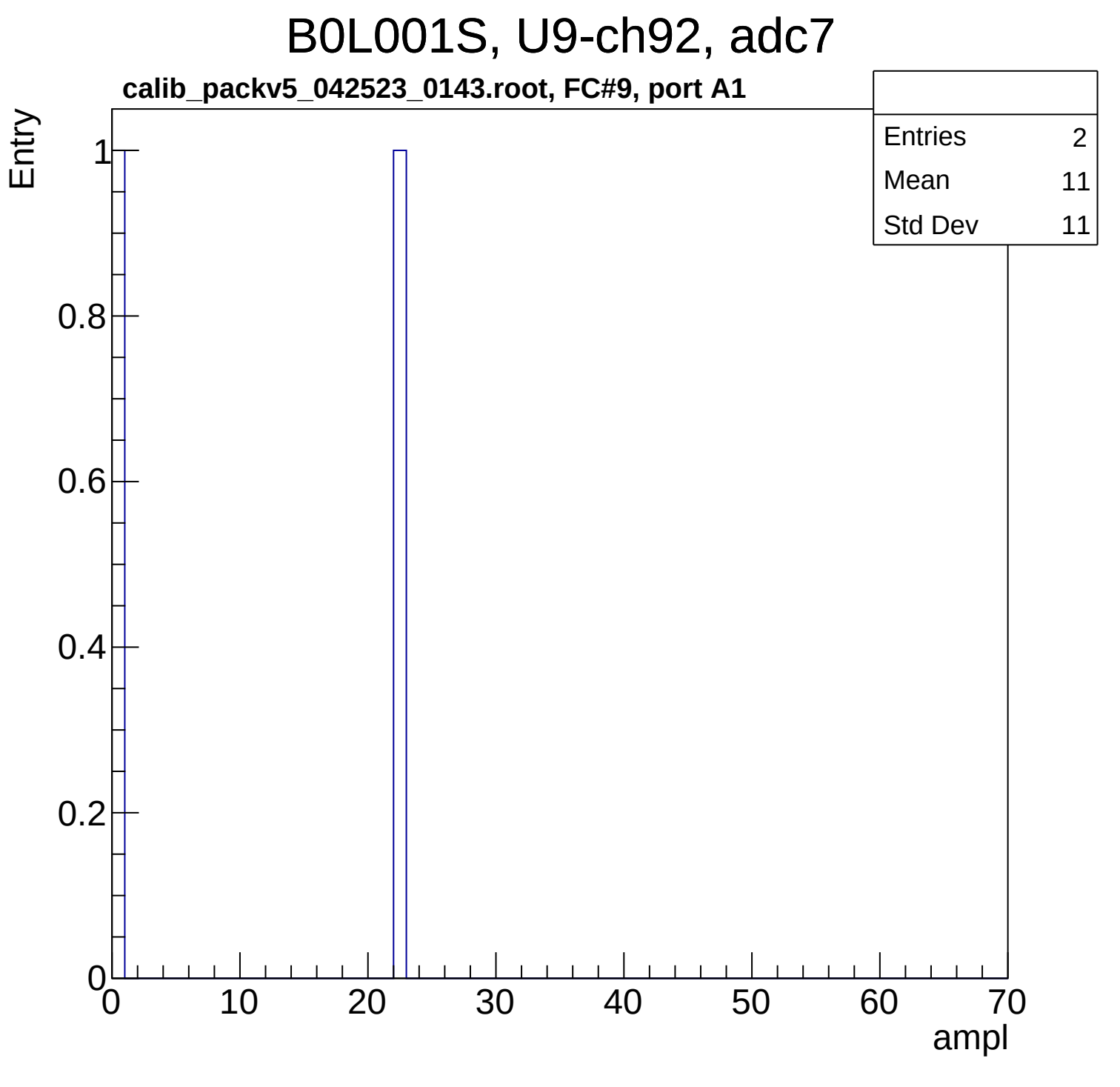
B0L001S, U9-ch92, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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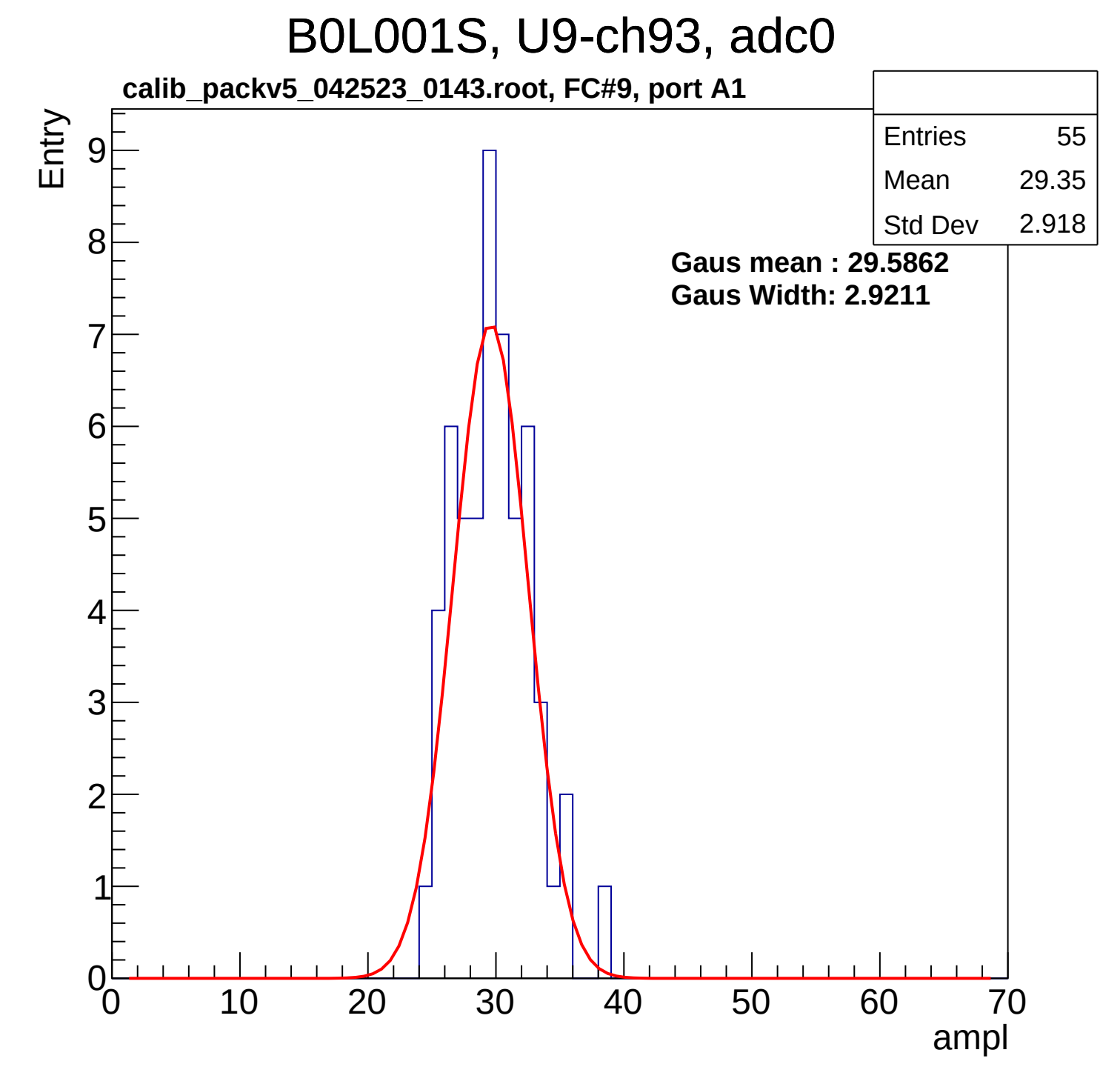
30
40
50
60
70
ampl


B0L001S, U9-ch93, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 29.35
Std Dev 2.918
Gaus mean : 29.5862
Gaus Width: 2.9211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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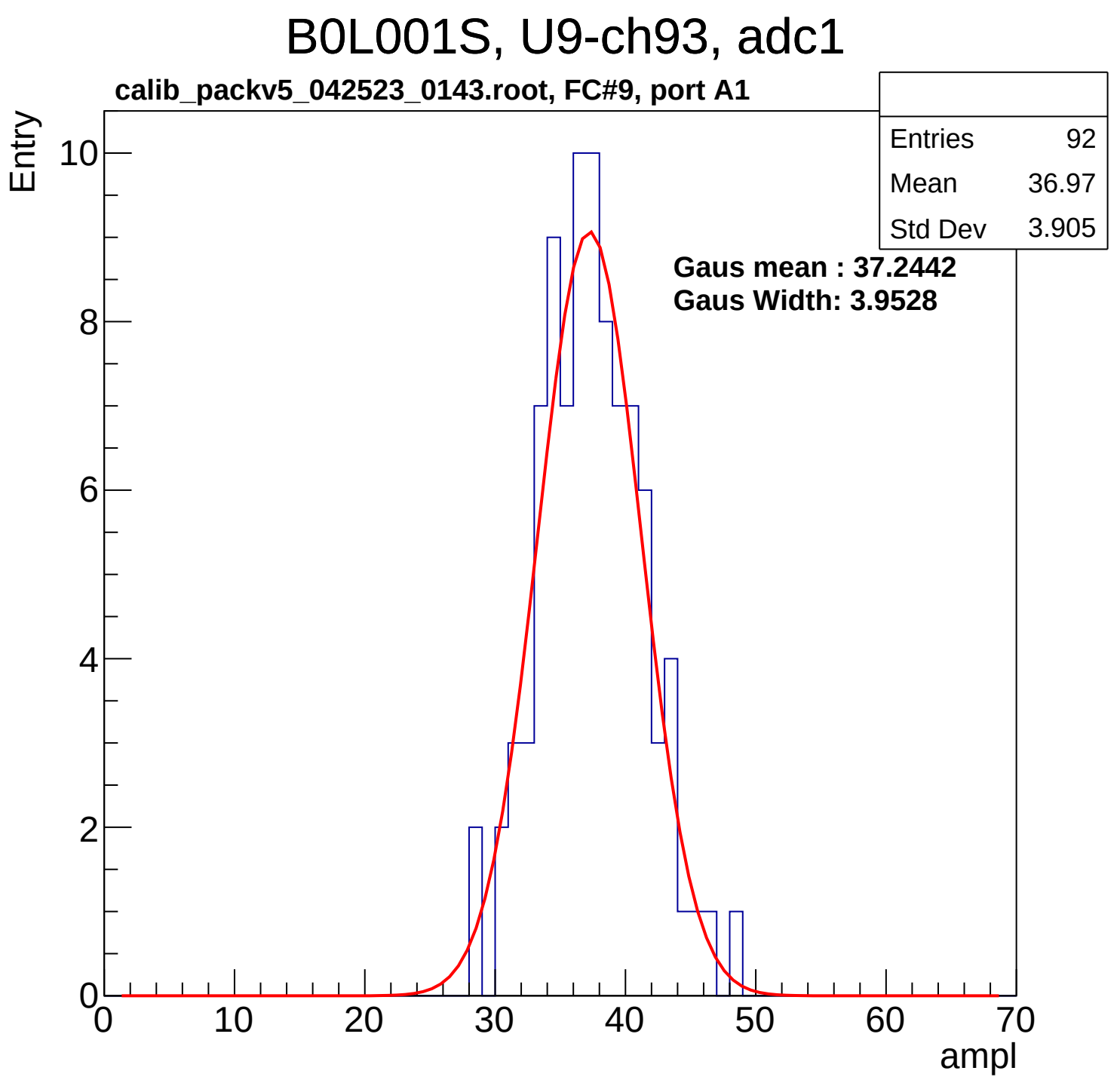
B0L001S, U9-ch93, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 92
Mean 36.97
Std Dev 3.905
Gaus mean : 37.2442
Gaus Width: 3.9528
Entry
ampl
0
2
4
6
8
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0
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40
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60
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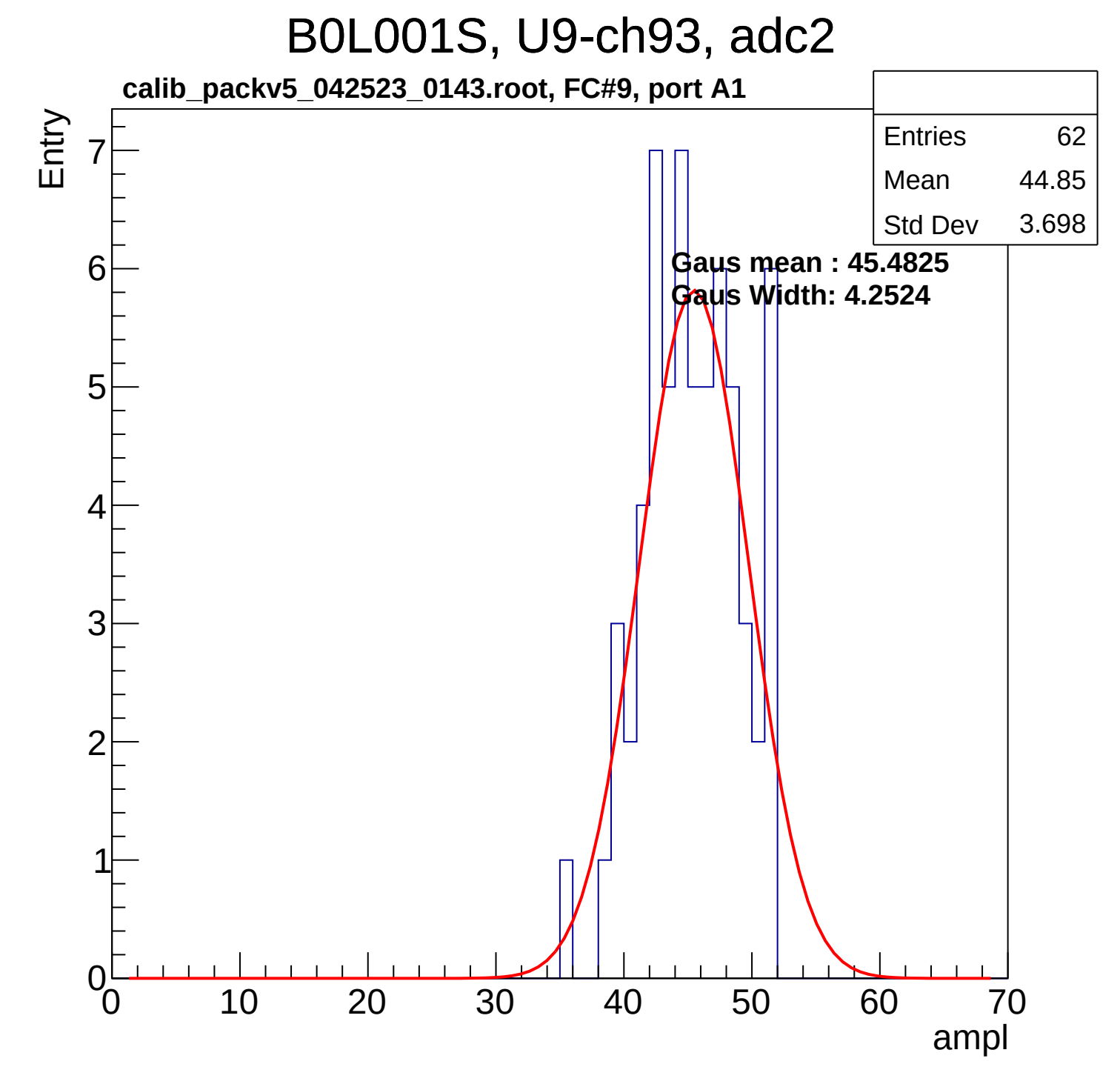
B0L001S, U9-ch93, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.85
Std Dev 3.698
Gaus mean : 45.4825
Gaus Width: 4.2524
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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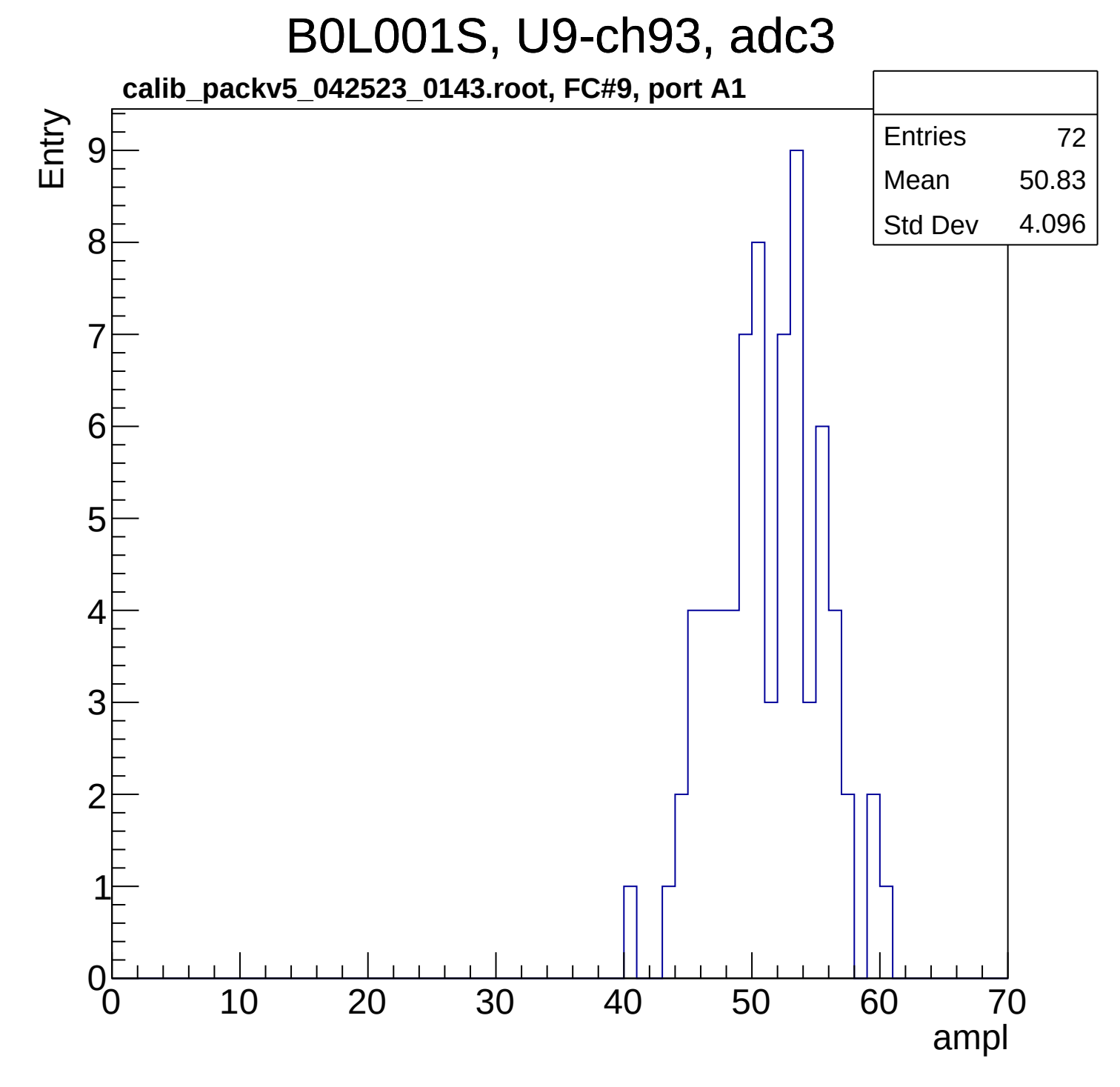
B0L001S, U9-ch93, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 50.83
Std Dev 4.096
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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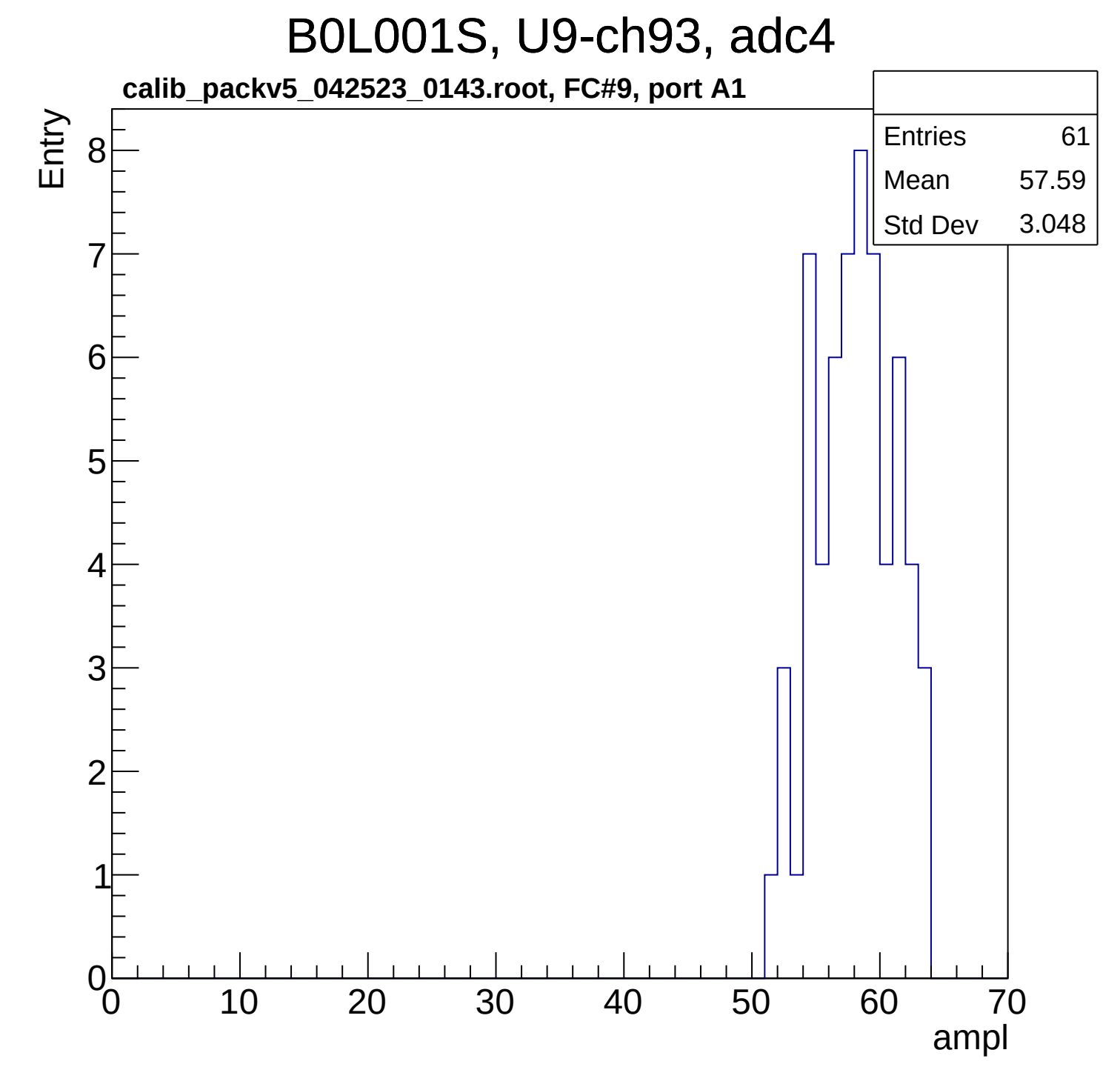
B0L001S, U9-ch93, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 57.59
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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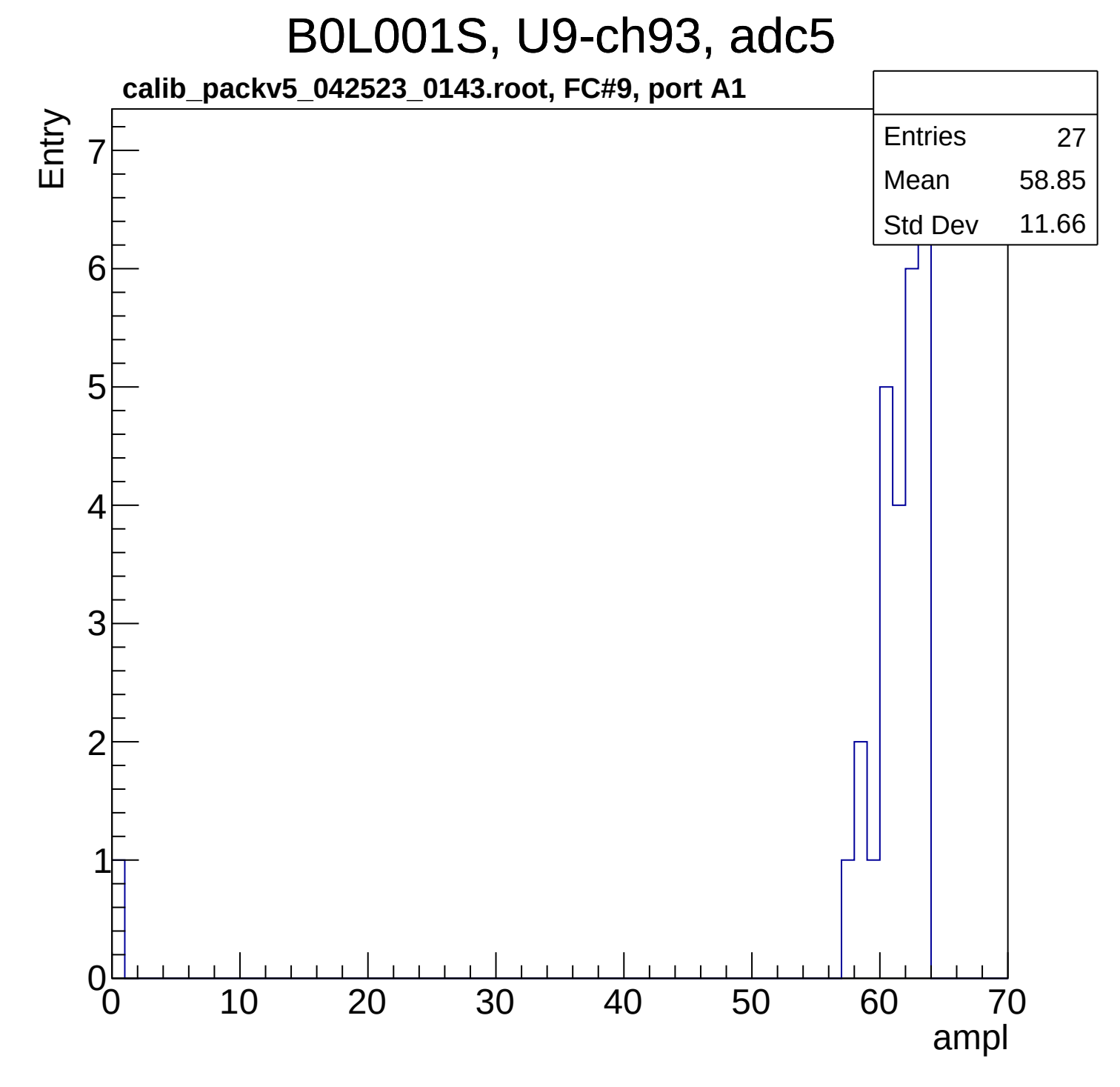
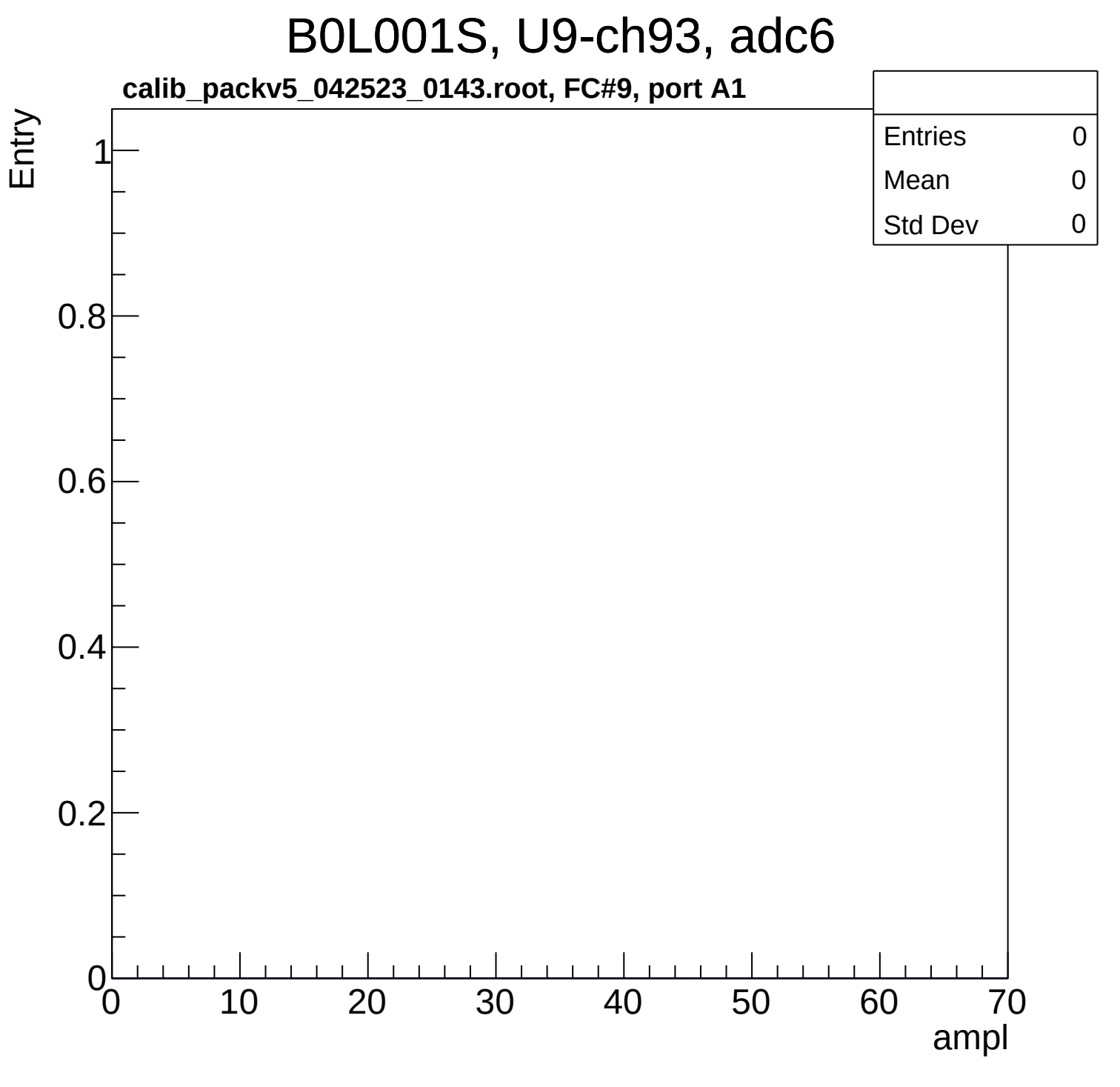
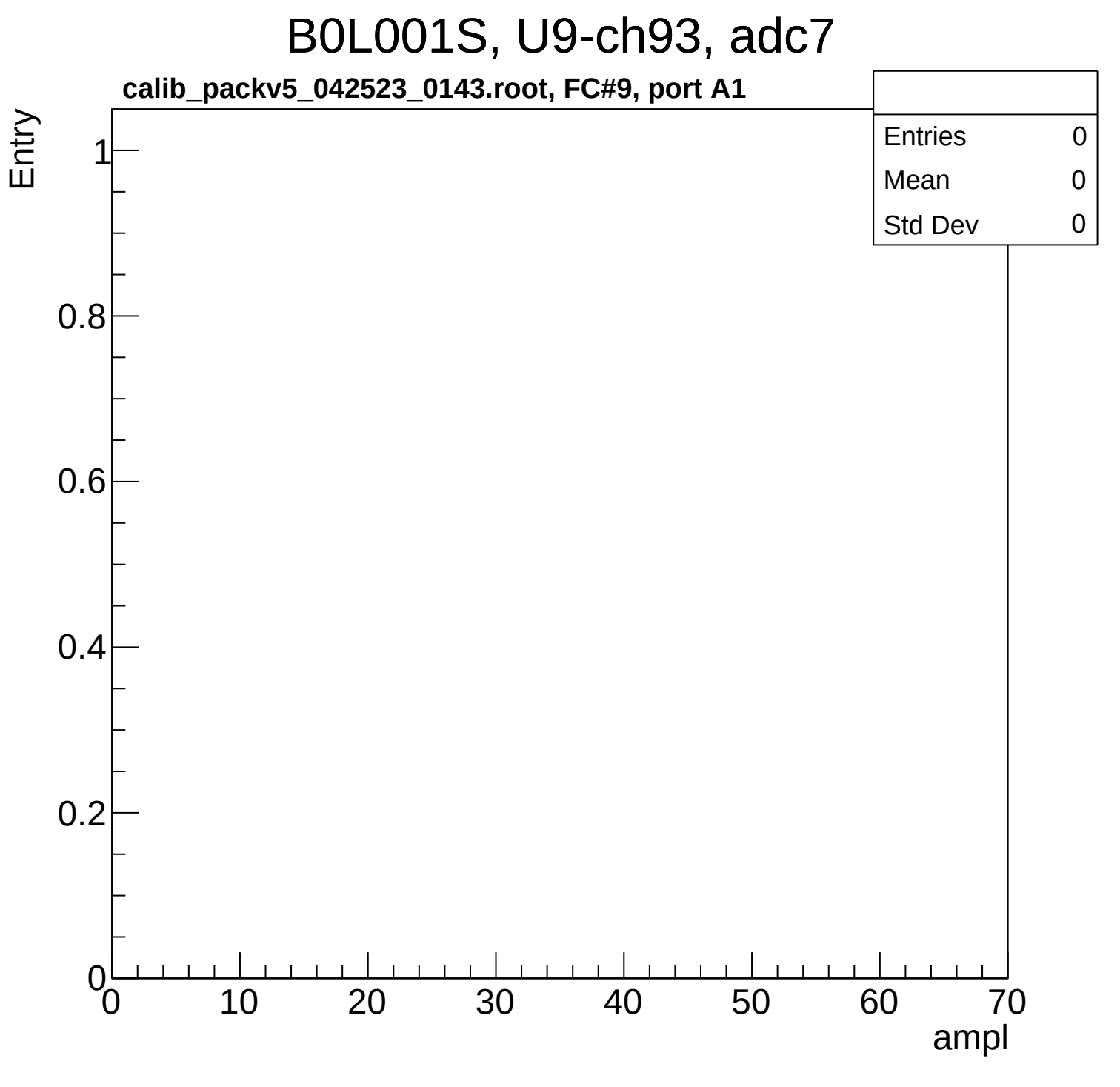
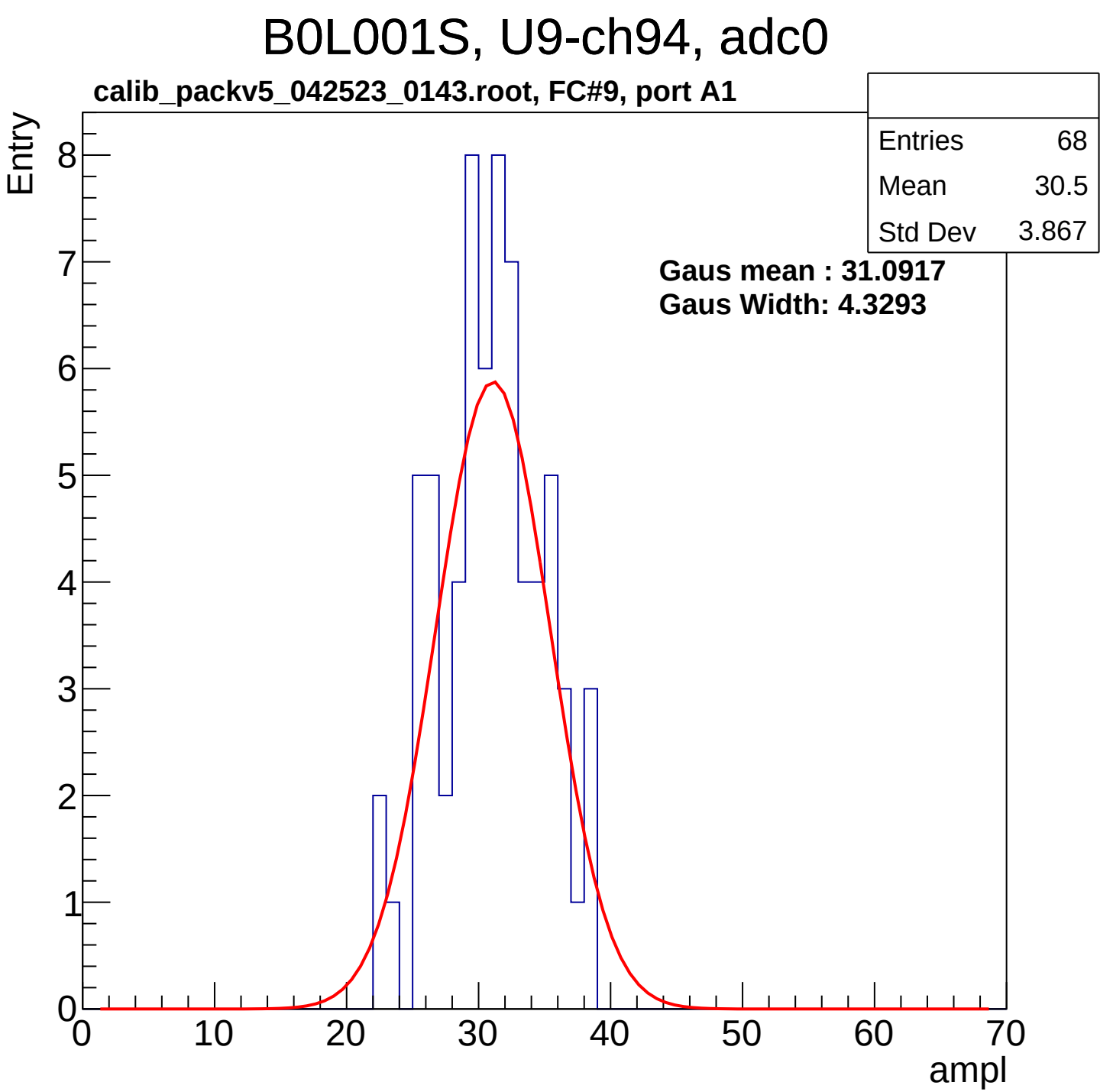

B0L001S, U9-ch93, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.85
Std Dev 11.66
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch93, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch93, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch94, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.5
Std Dev 3.867
Gaus mean : 31.0917
Gaus Width: 4.3293
Entry
ampl
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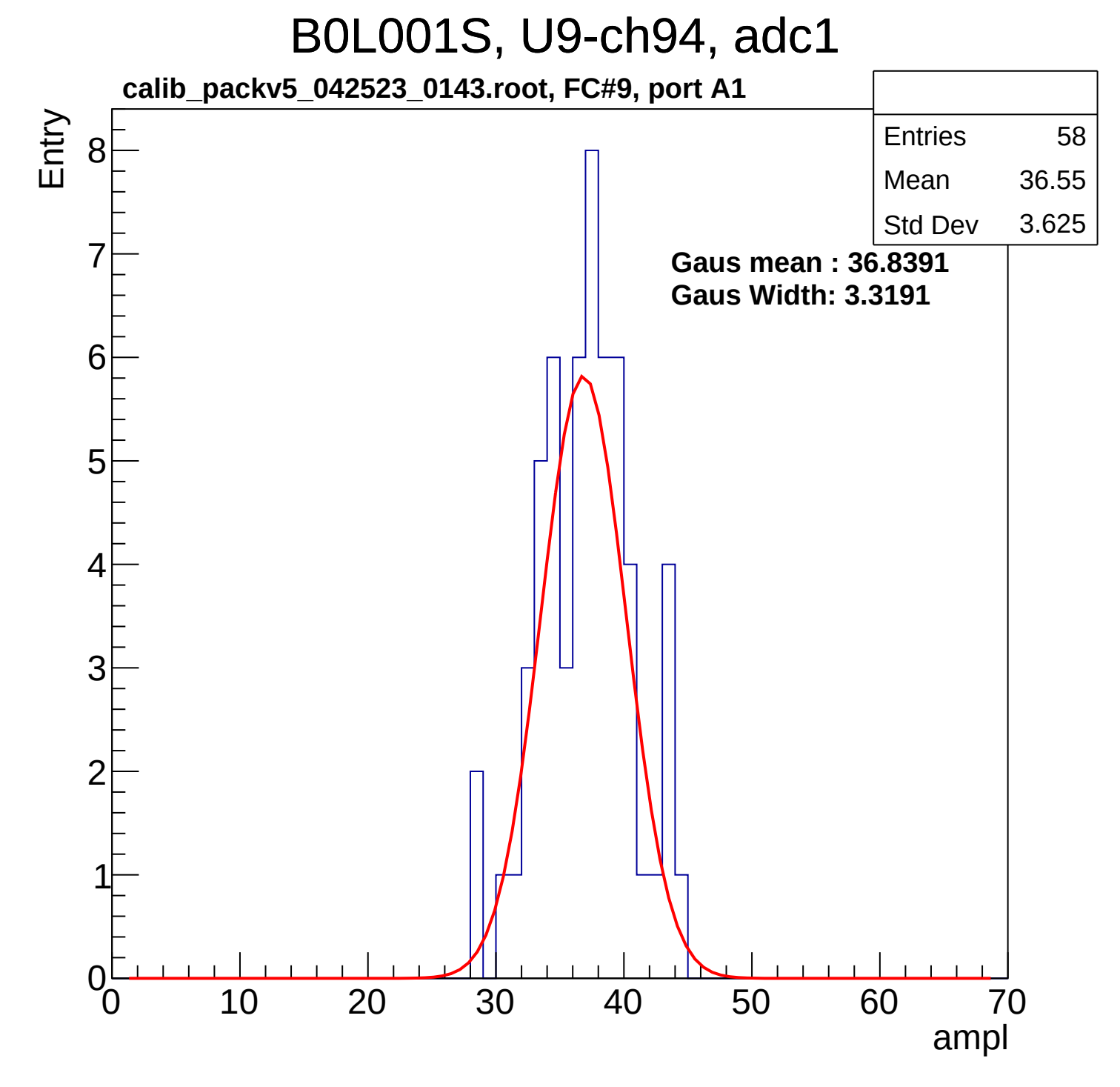
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch94, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 36.55
Std Dev 3.625
Gaus mean : 36.8391
Gaus Width: 3.3191
Entry
ampl
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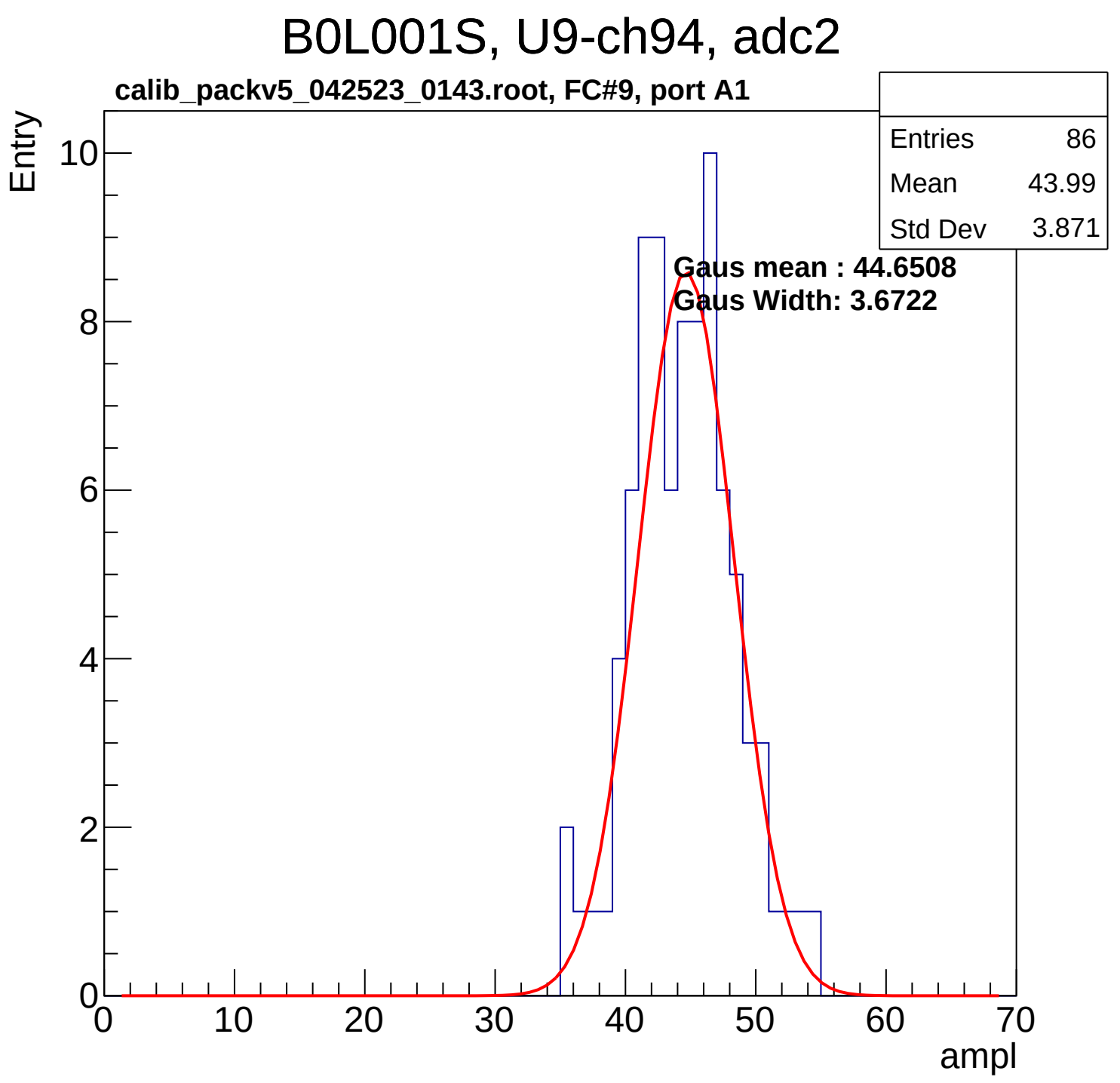
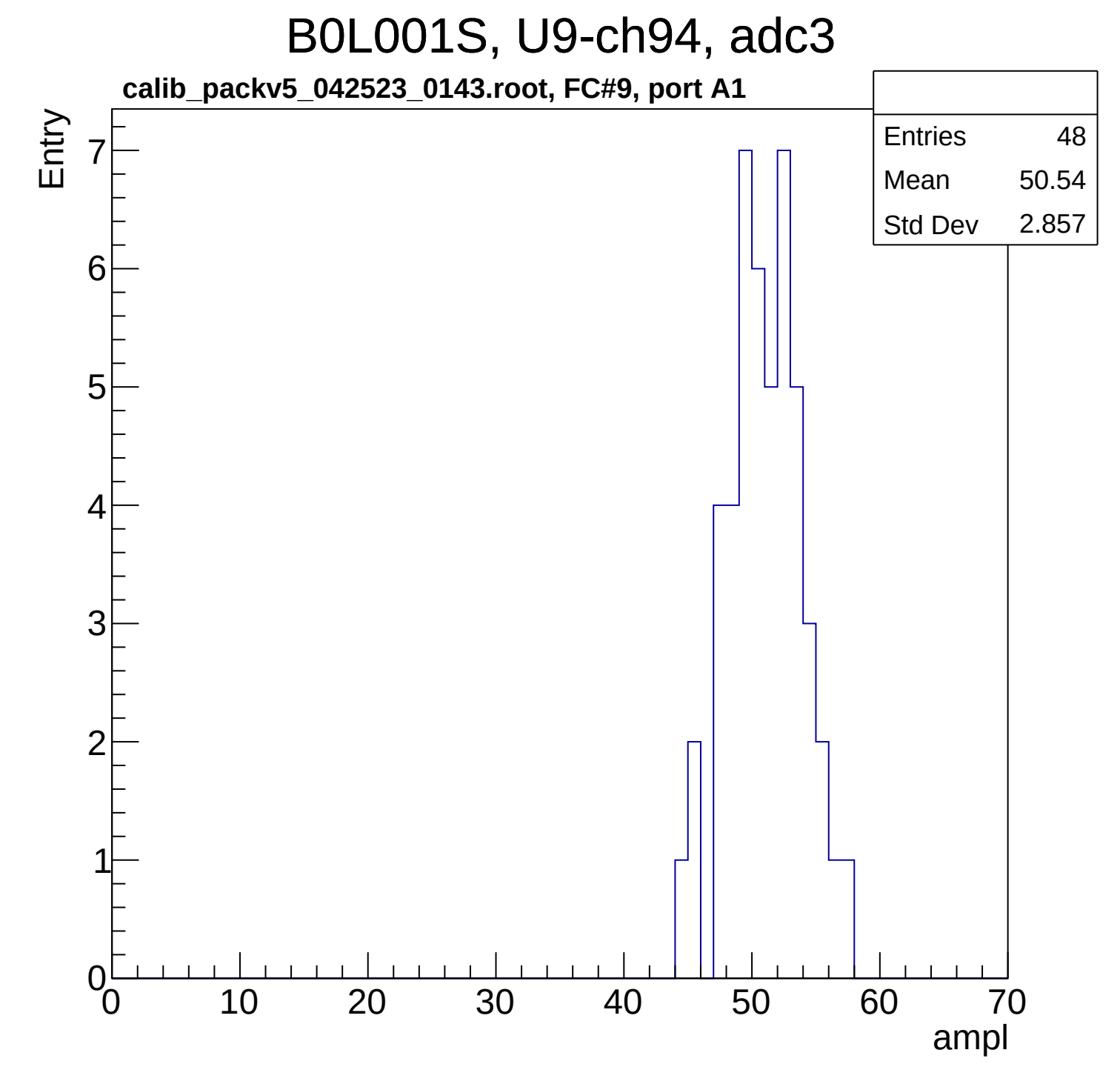
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch94, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 43.99
Std Dev 3.871
Gaus mean : 44.6508
Gaus Width: 3.6722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch94, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 50.54
Std Dev 2.857
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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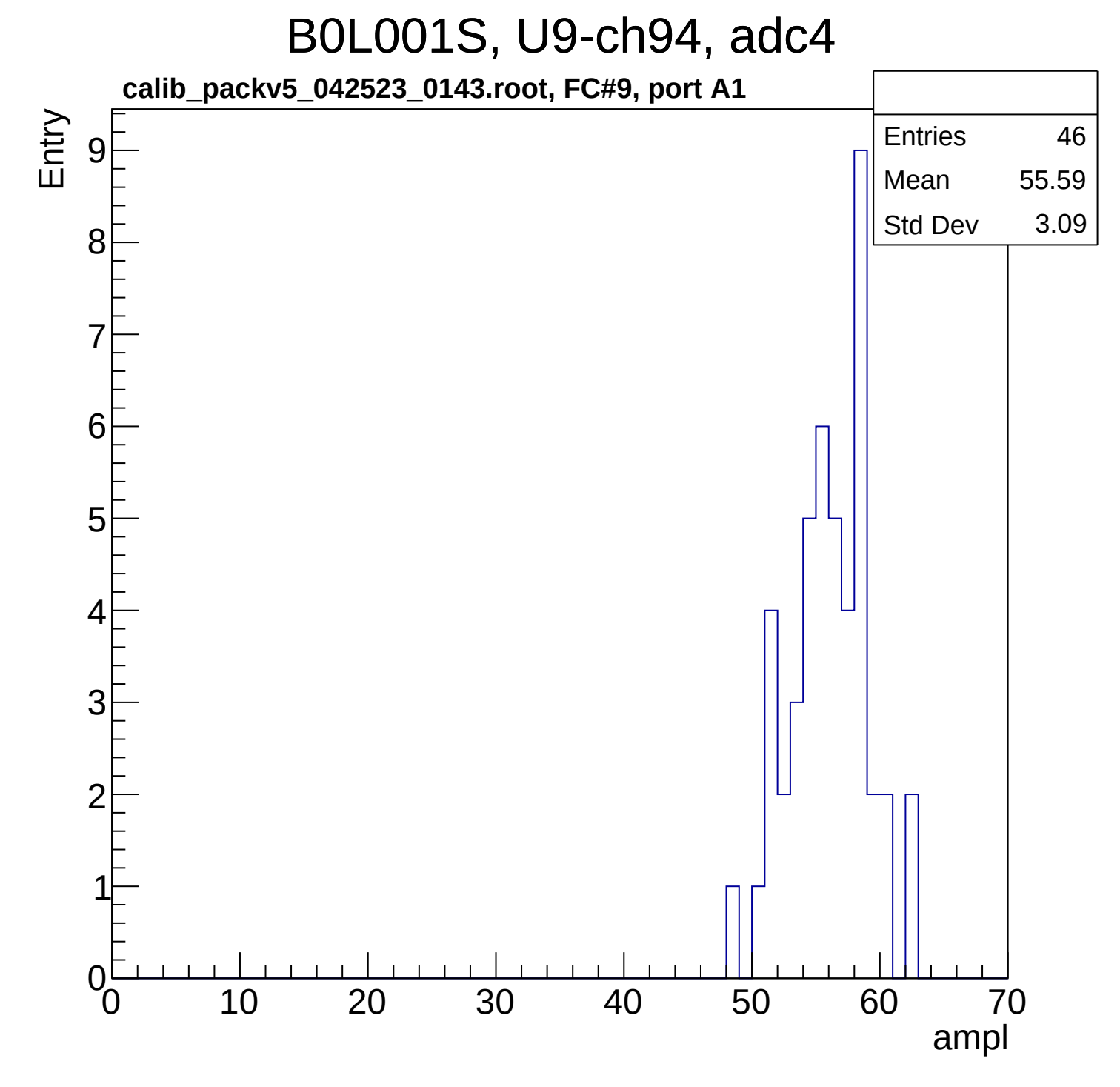
B0L001S, U9-ch94, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 55.59
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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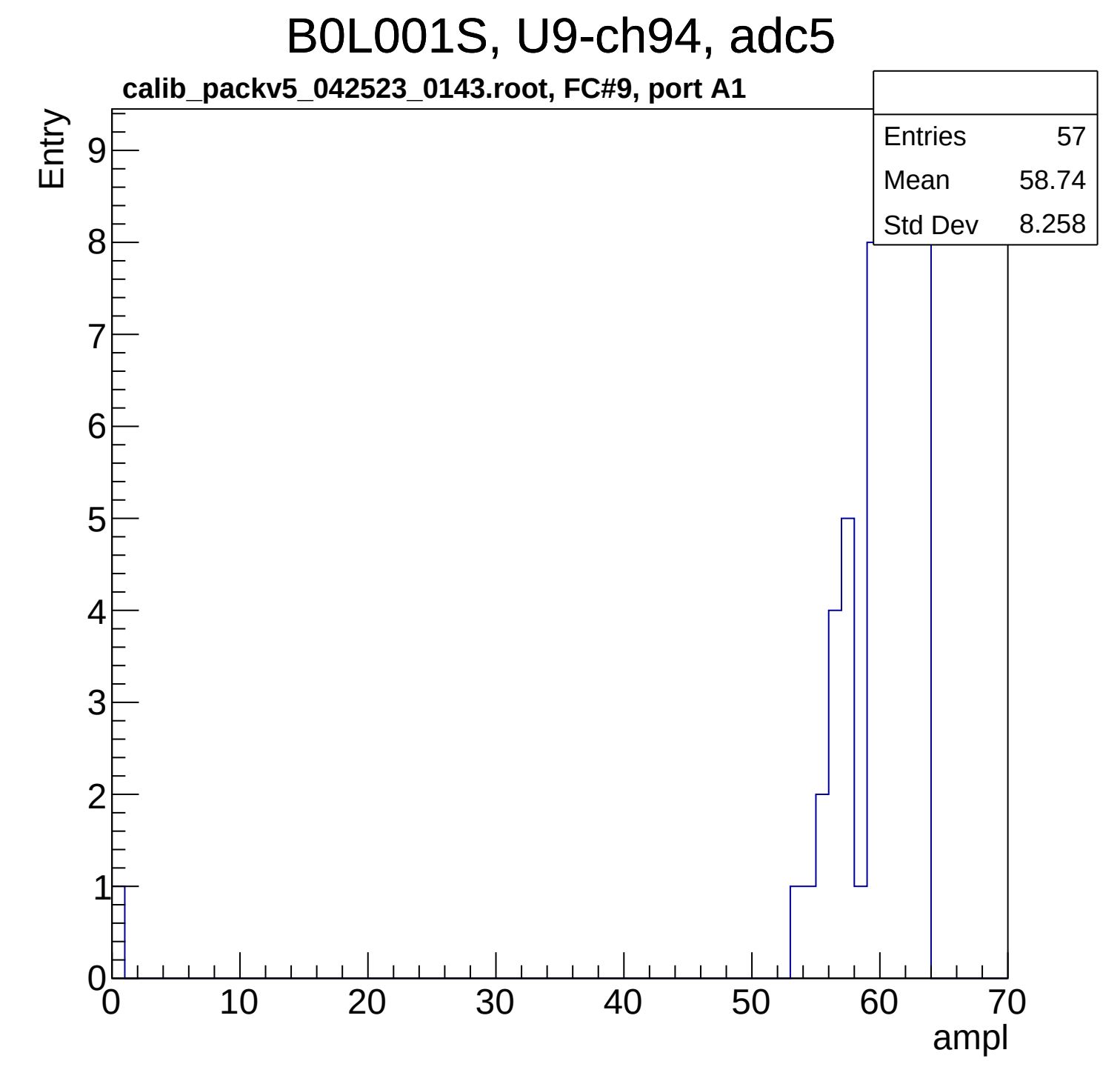
40
50
60
70


B0L001S, U9-ch94, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 58.74
Std Dev 8.258
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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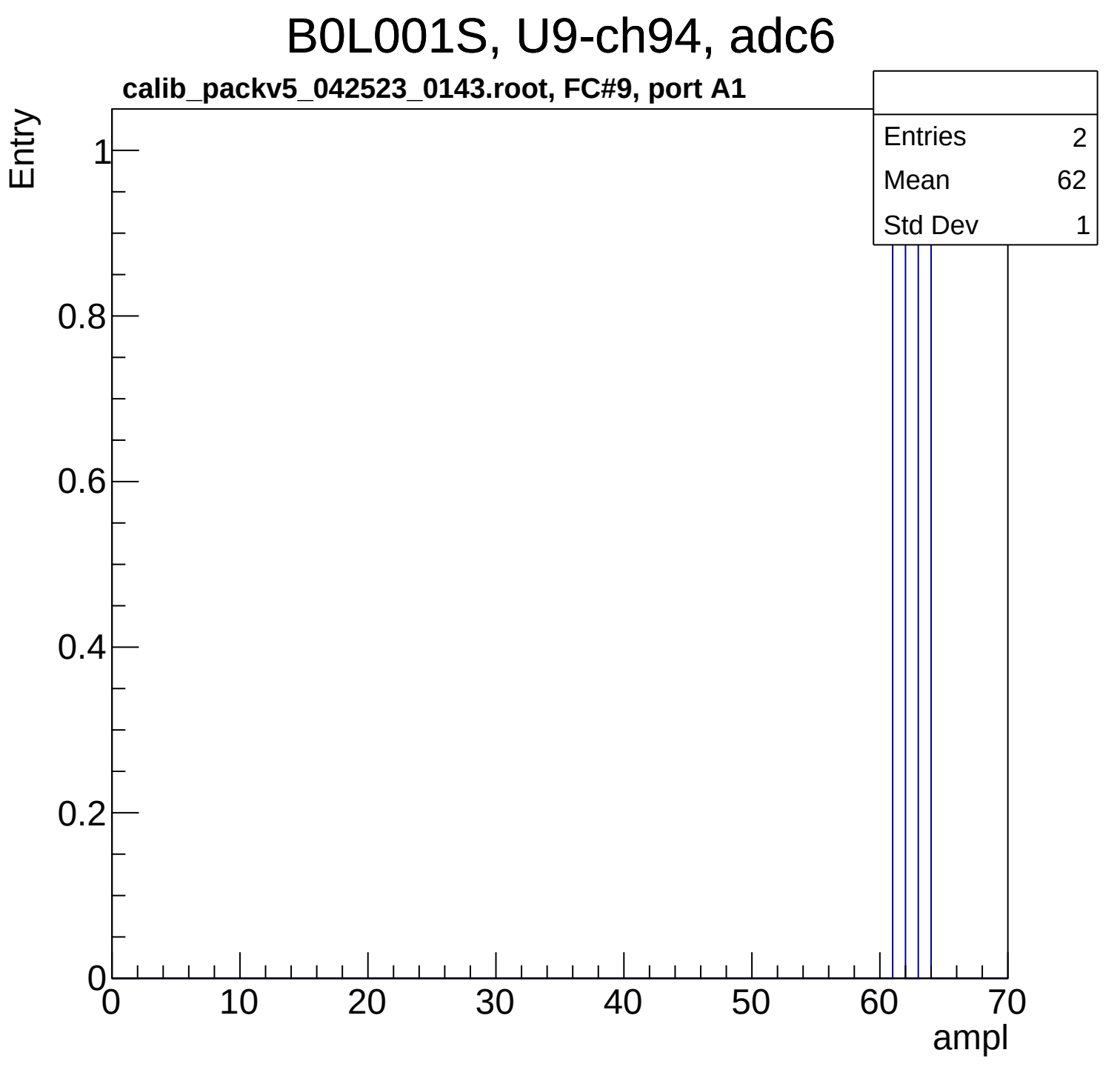
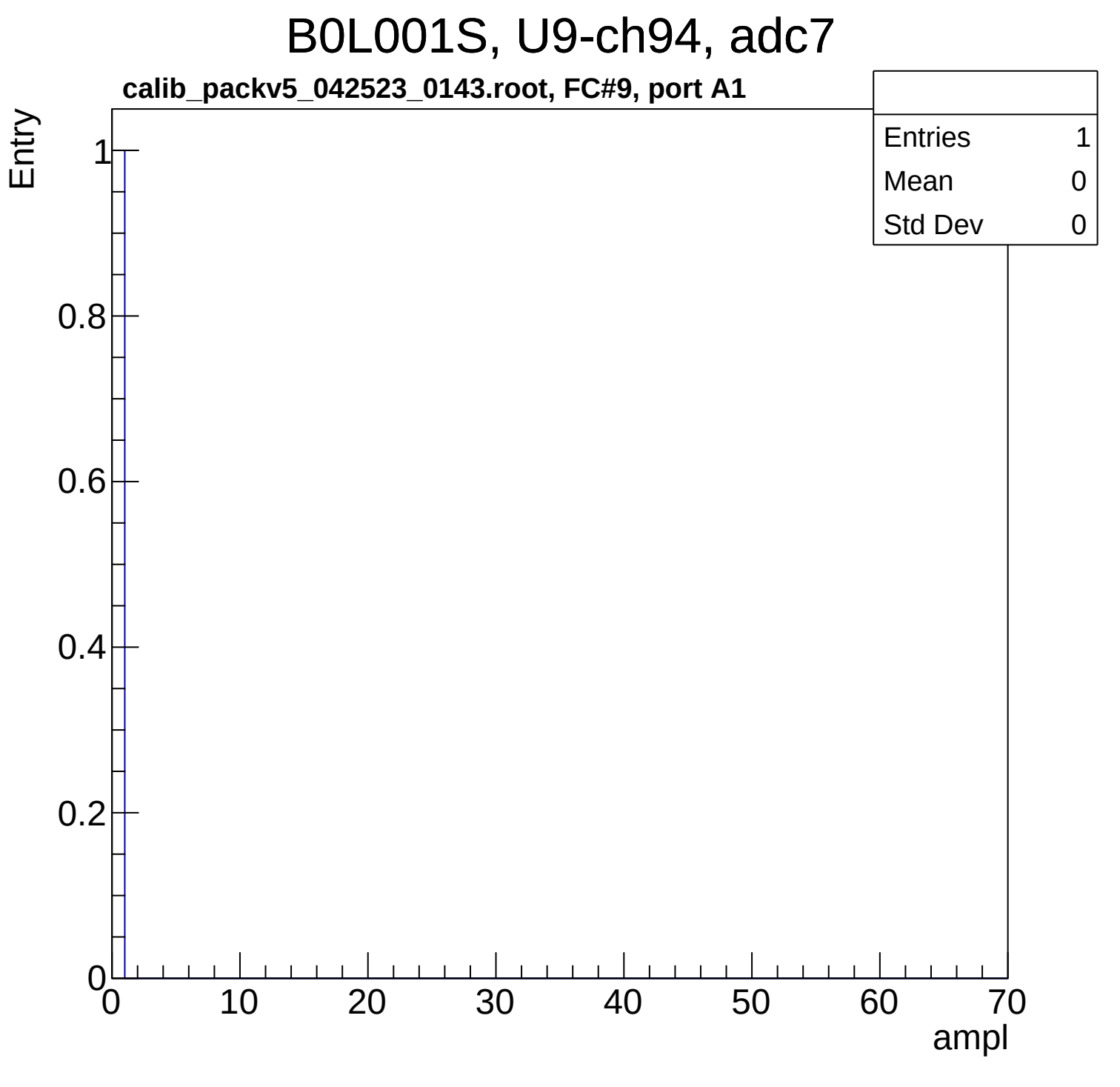

B0L001S, U9-ch94, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch94, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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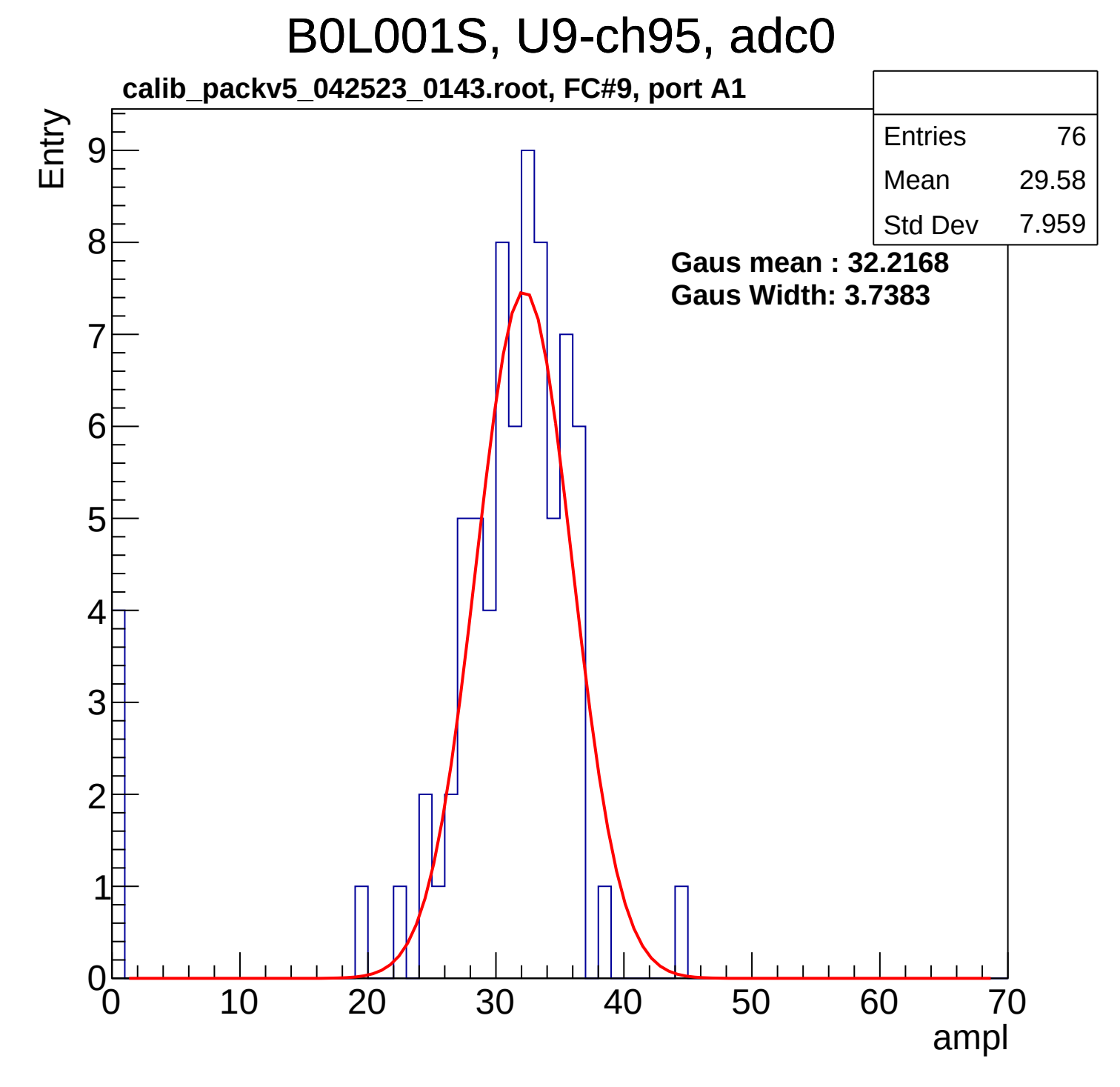
50
60
70
ampl


B0L001S, U9-ch95, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 29.58
Std Dev 7.959
Gaus mean : 32.2168
Gaus Width: 3.7383
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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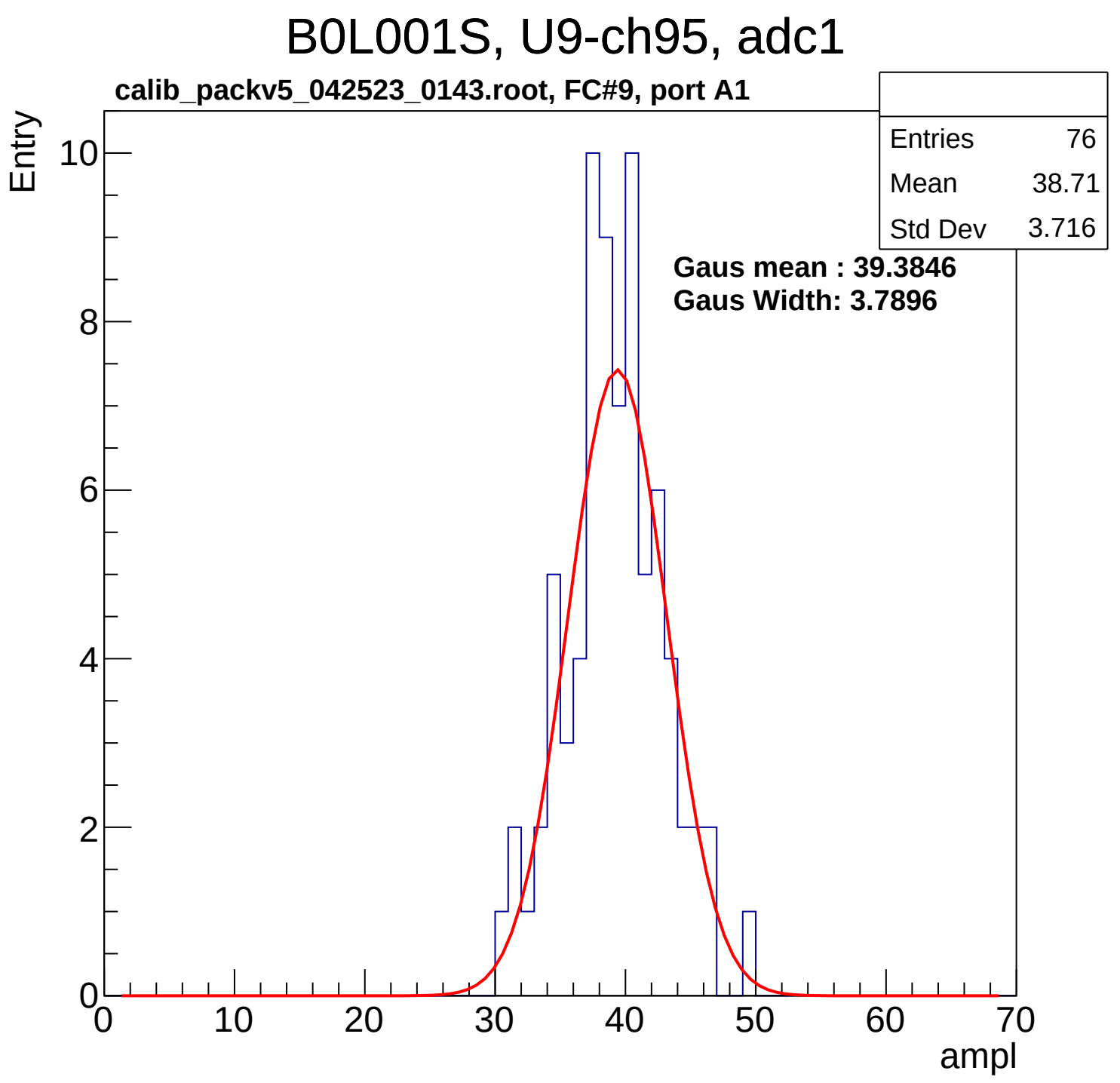
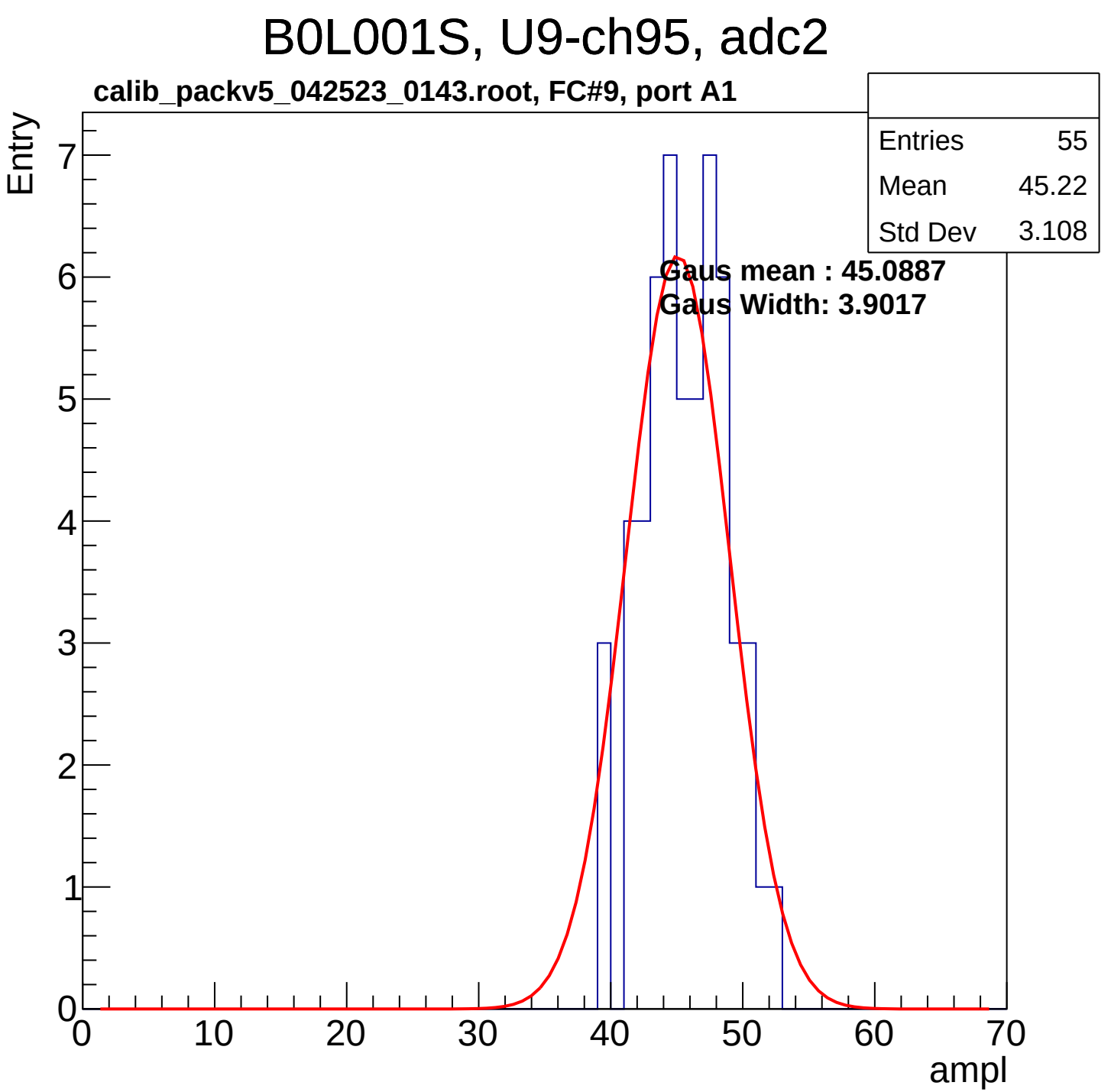

B0L001S, U9-ch95, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 38.71
Std Dev 3.716
Gaus mean : 39.3846
Gaus Width: 3.7896
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch95, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 45.22
Std Dev 3.108
Gaus mean : 45.0887
Gaus Width: 3.9017
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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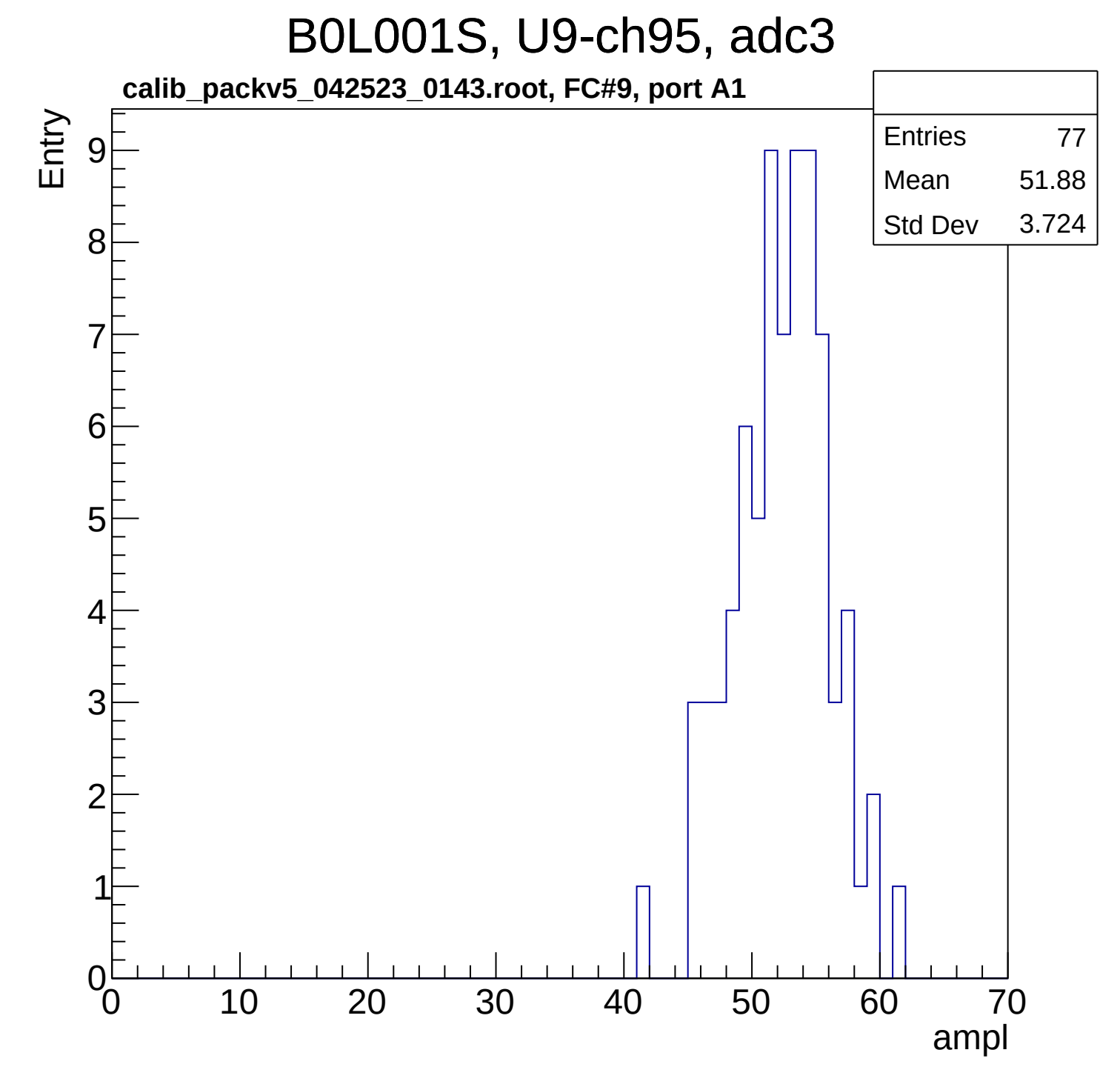
B0L001S, U9-ch95, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.88
Std Dev 3.724
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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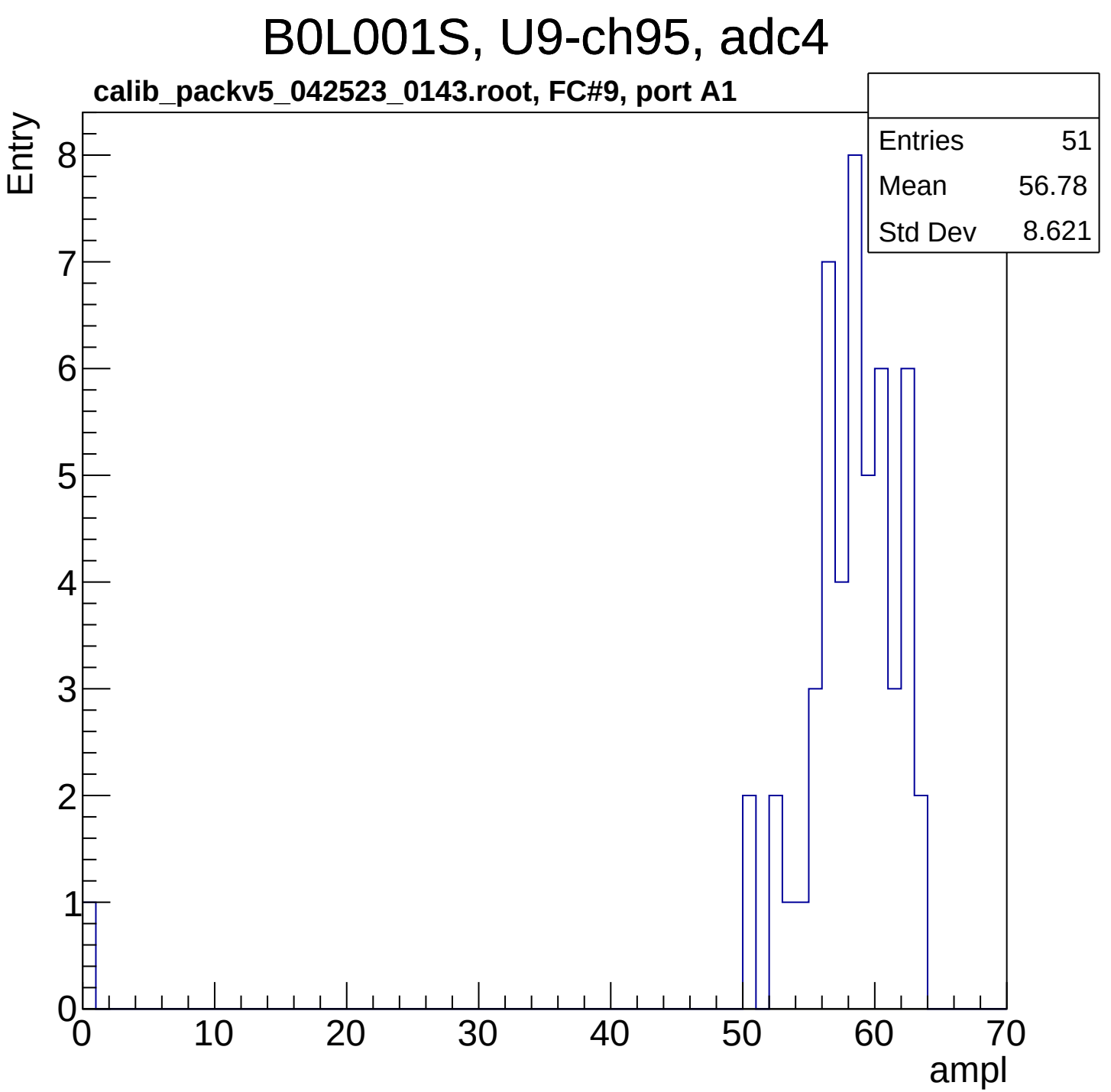
40
50
60
70


B0L001S, U9-ch95, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.78
Std Dev 8.621
Entry
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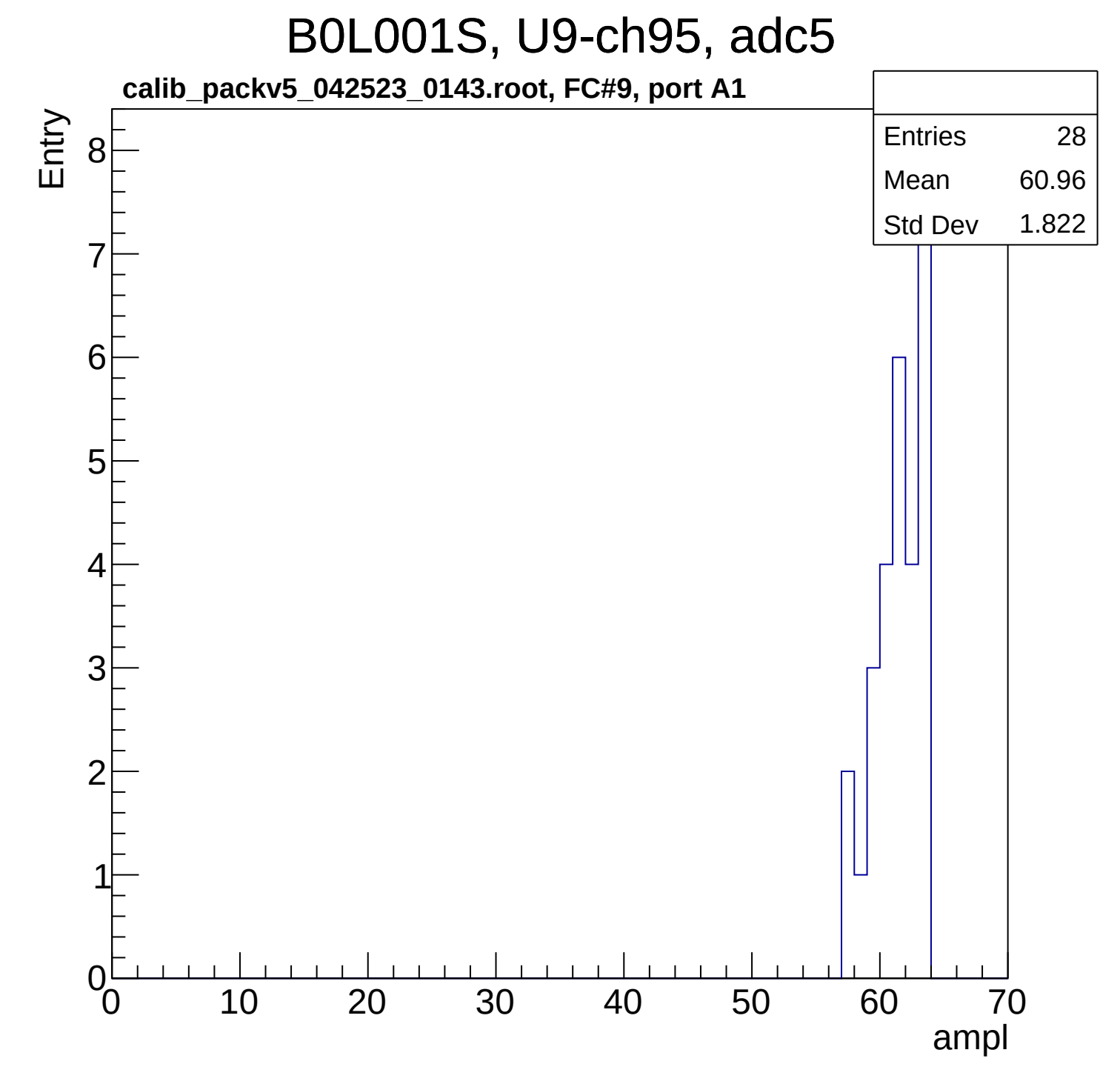
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch95, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 60.96
Std Dev 1.822
Entry
ampl
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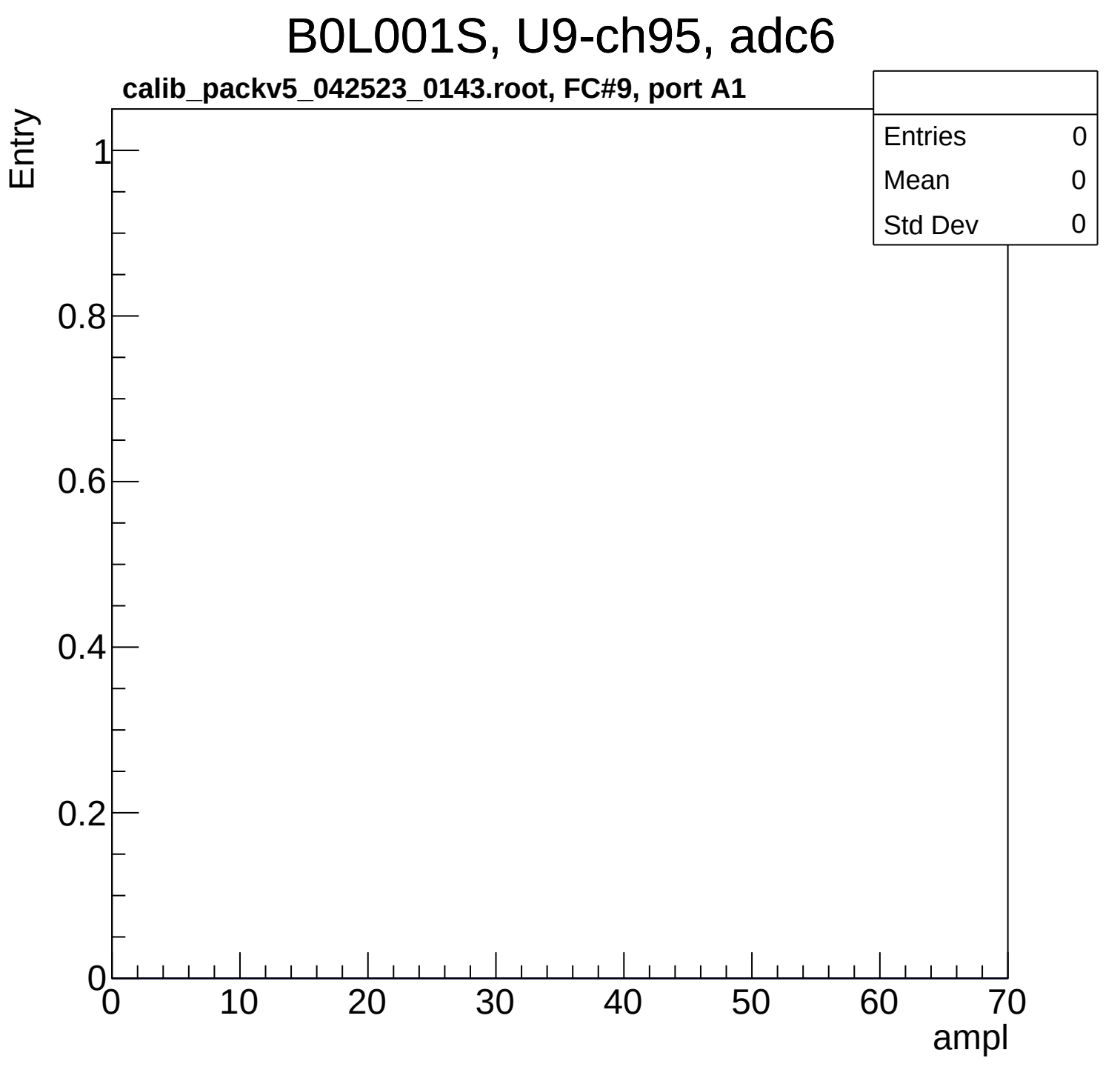
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch95, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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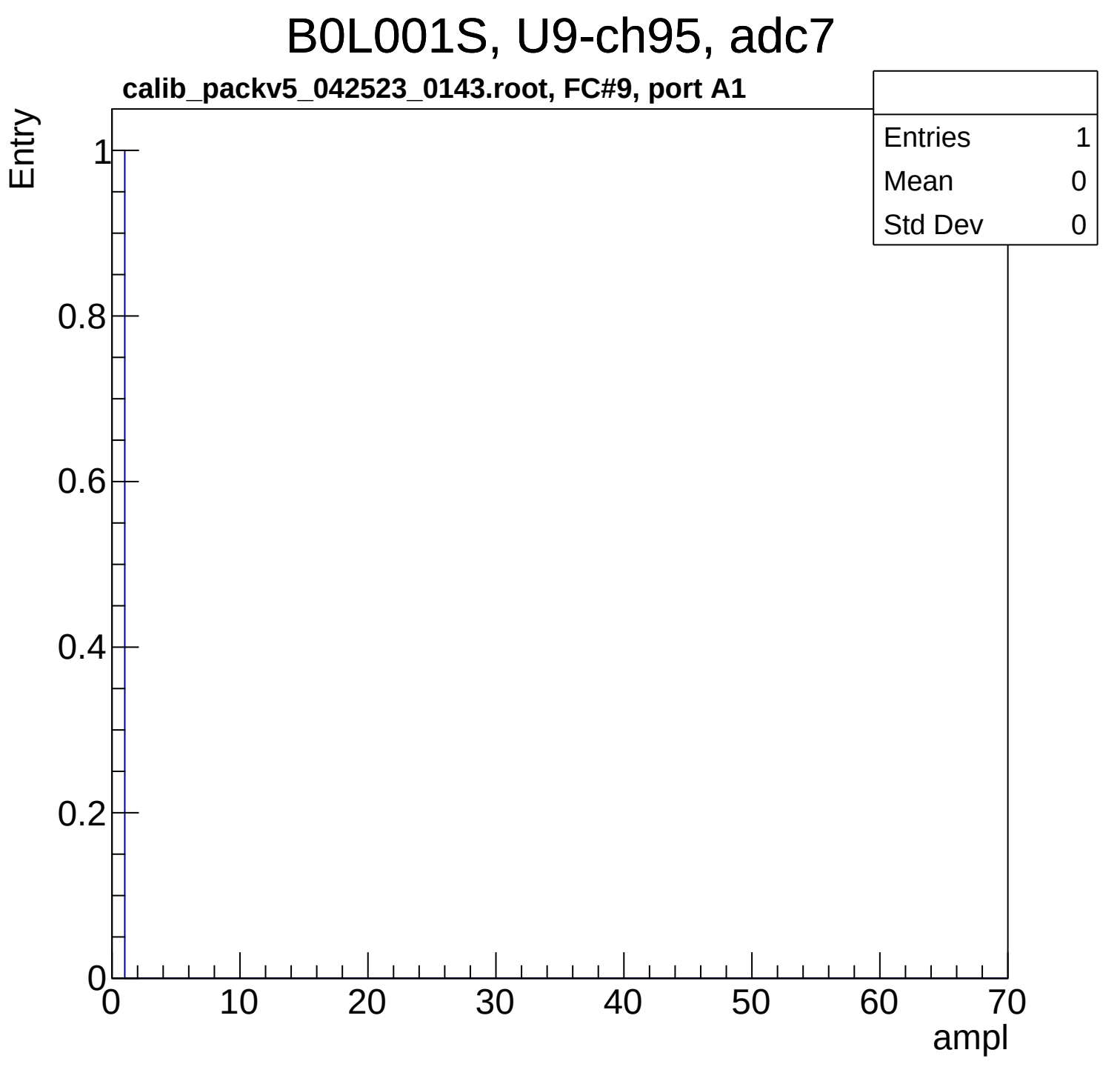
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch95, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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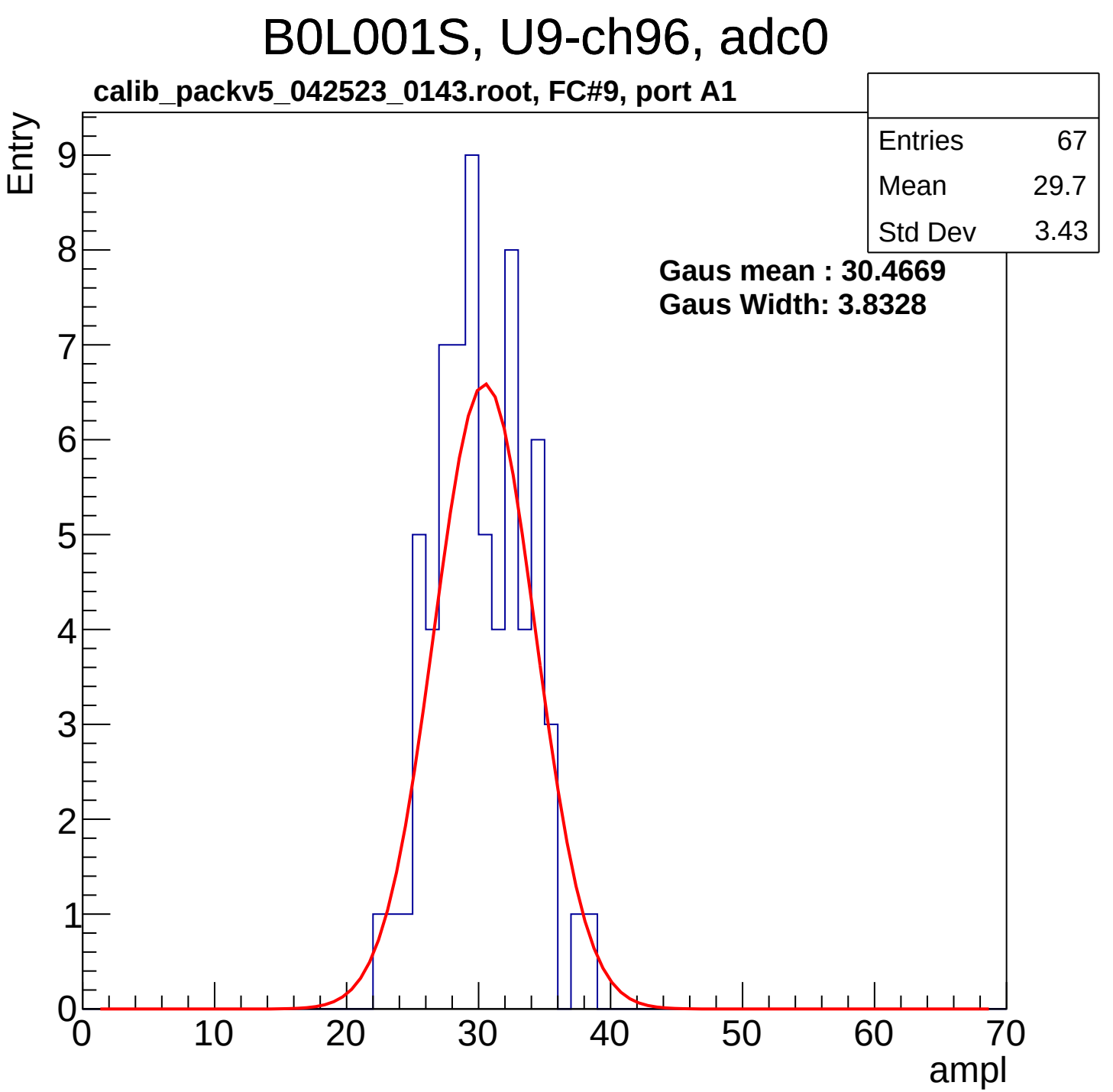
50
60
70
ampl


B0L001S, U9-ch96, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 29.7
Std Dev 3.43
Gaus mean : 30.4669
Gaus Width: 3.8328
Entry
ampl
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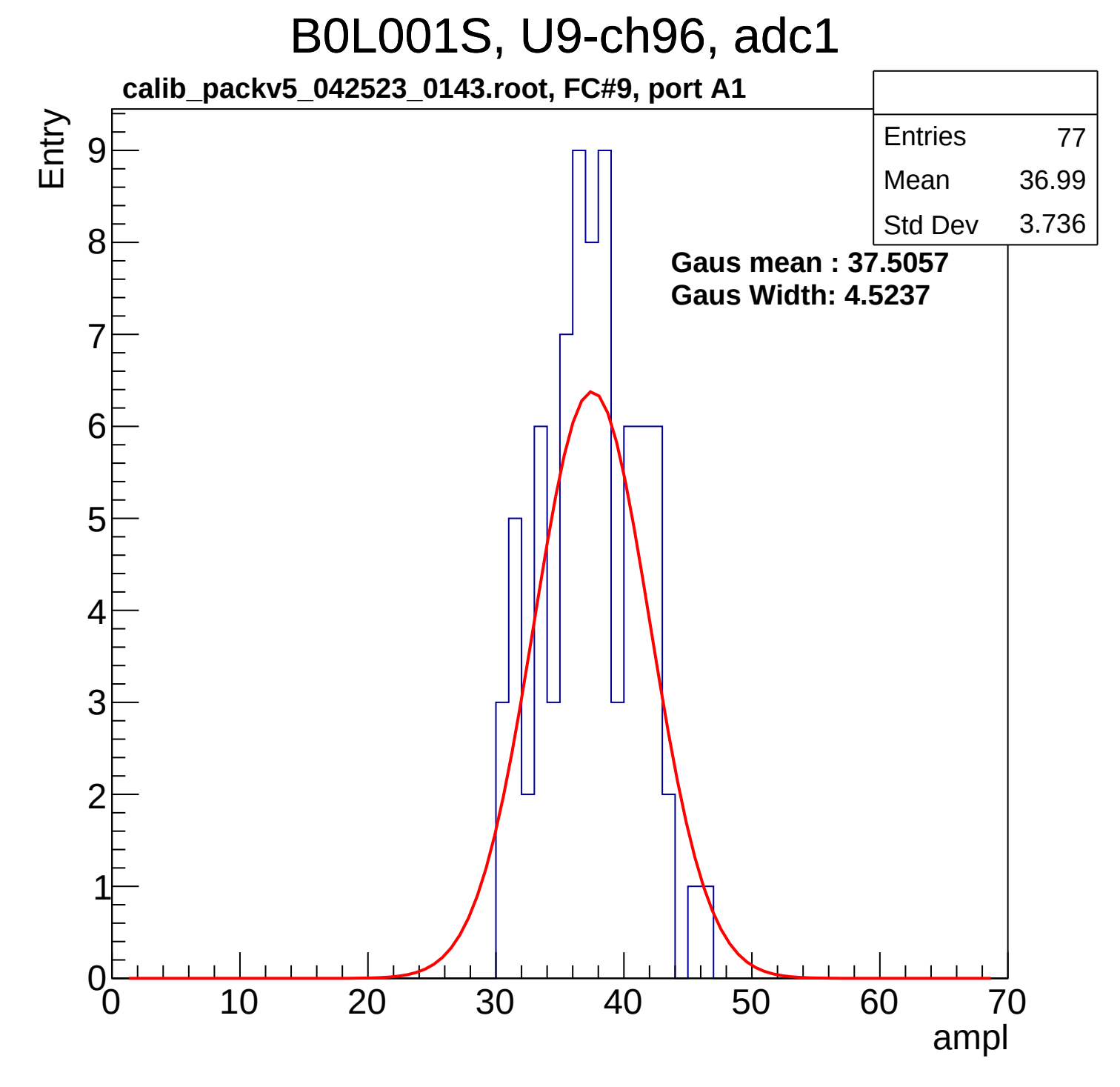
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch96, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 36.99
Std Dev 3.736
Gaus mean : 37.5057
Gaus Width: 4.5237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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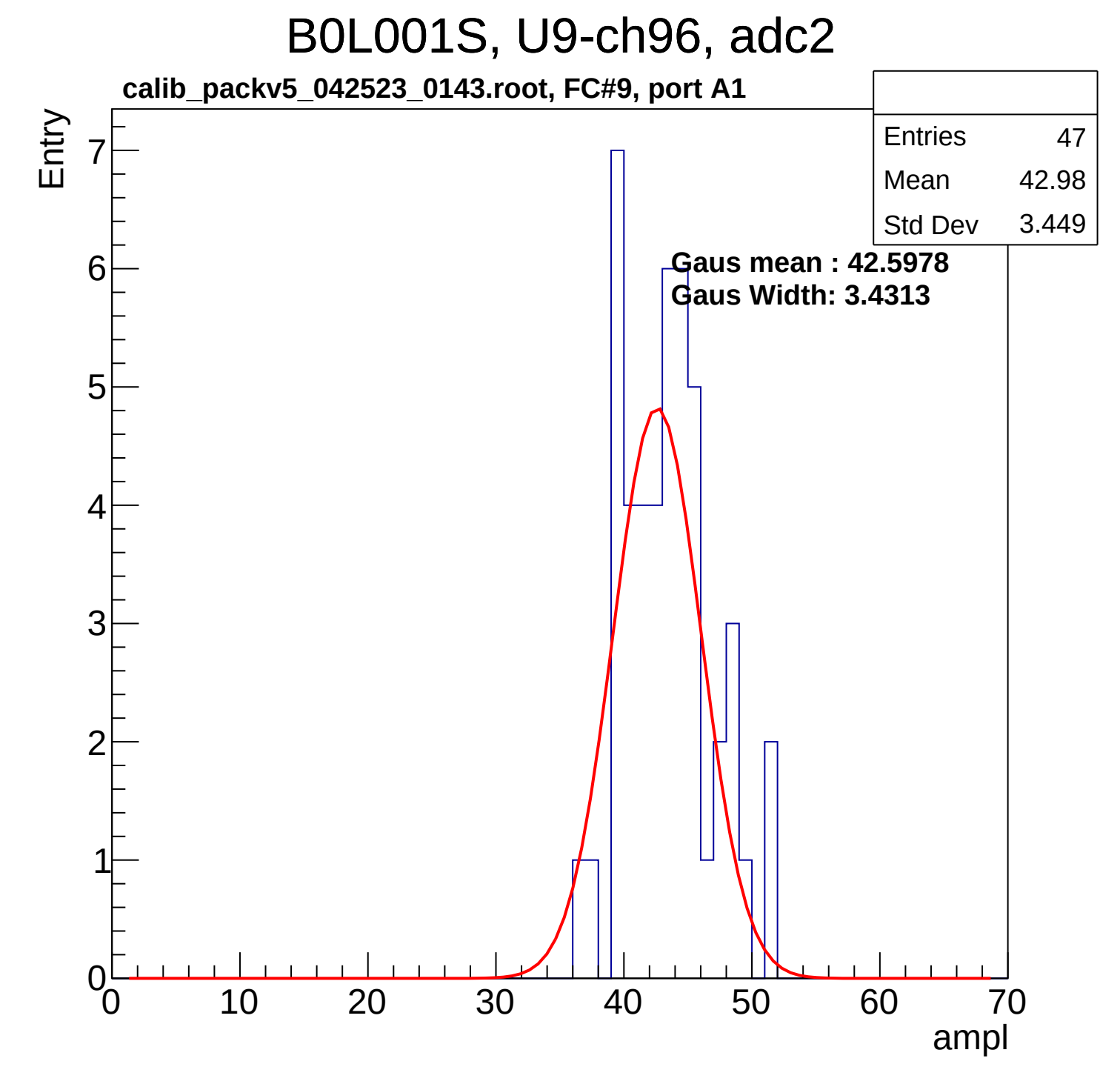
B0L001S, U9-ch96, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 42.98
Std Dev 3.449
Gaus mean : 42.5978
Gaus Width: 3.4313
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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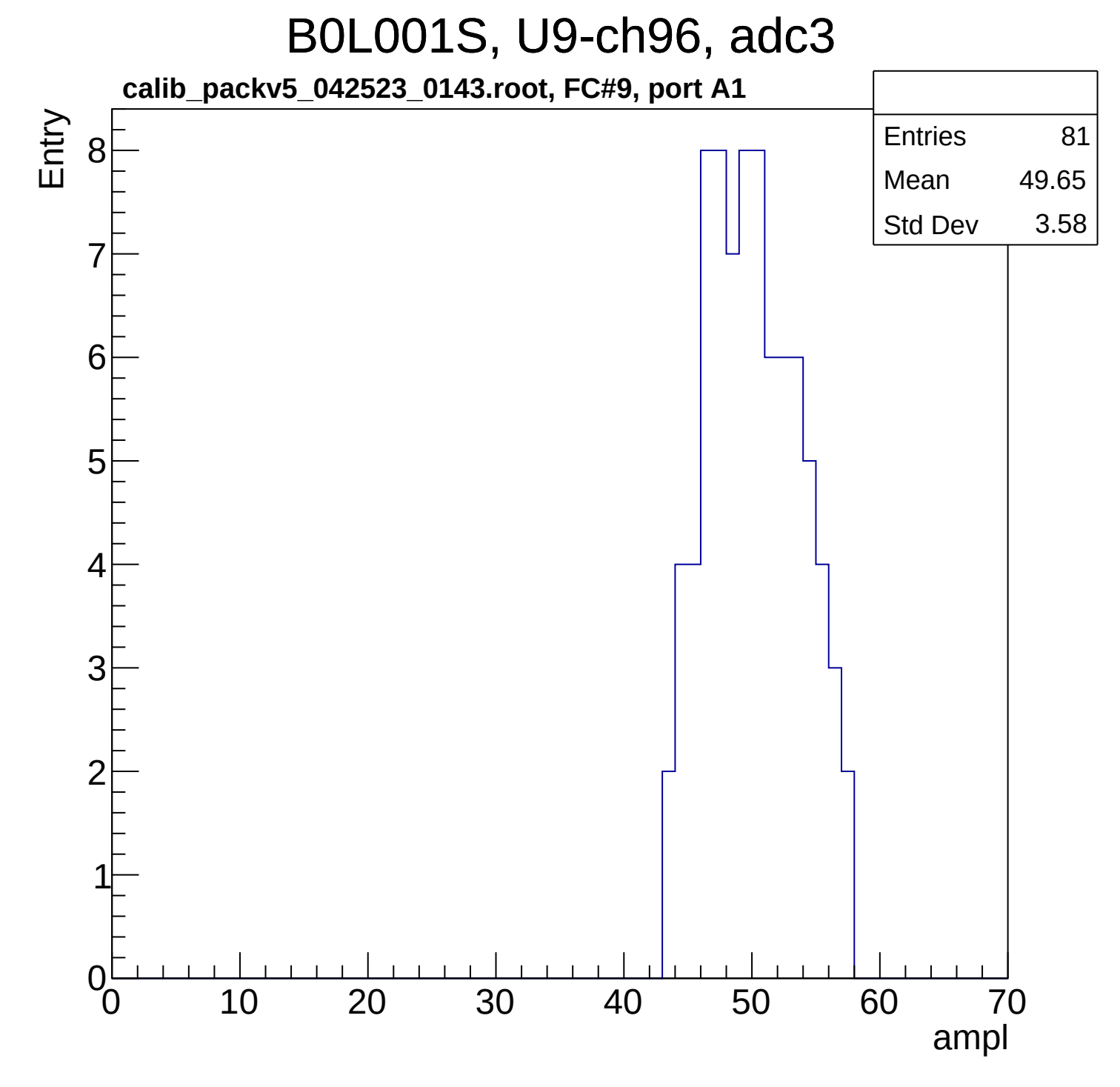
B0L001S, U9-ch96, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 49.65
Std Dev 3.58
Entry
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2
3
4
5
6
7
8
0
10
20
30
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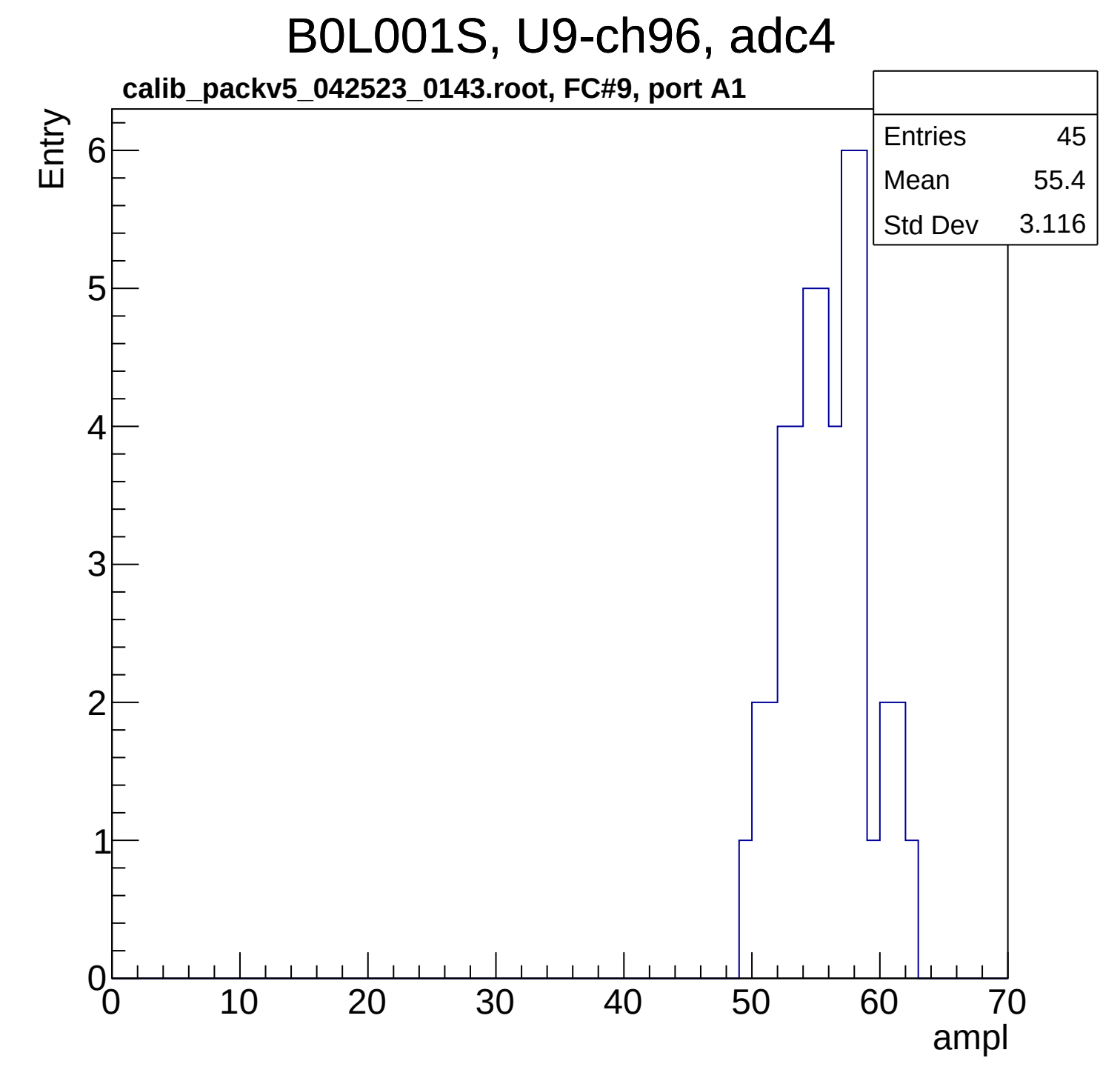
50
60
70
ampl


B0L001S, U9-ch96, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 55.4
Std Dev 3.116
Entry
0
1
2
3
4
5
6
0
10
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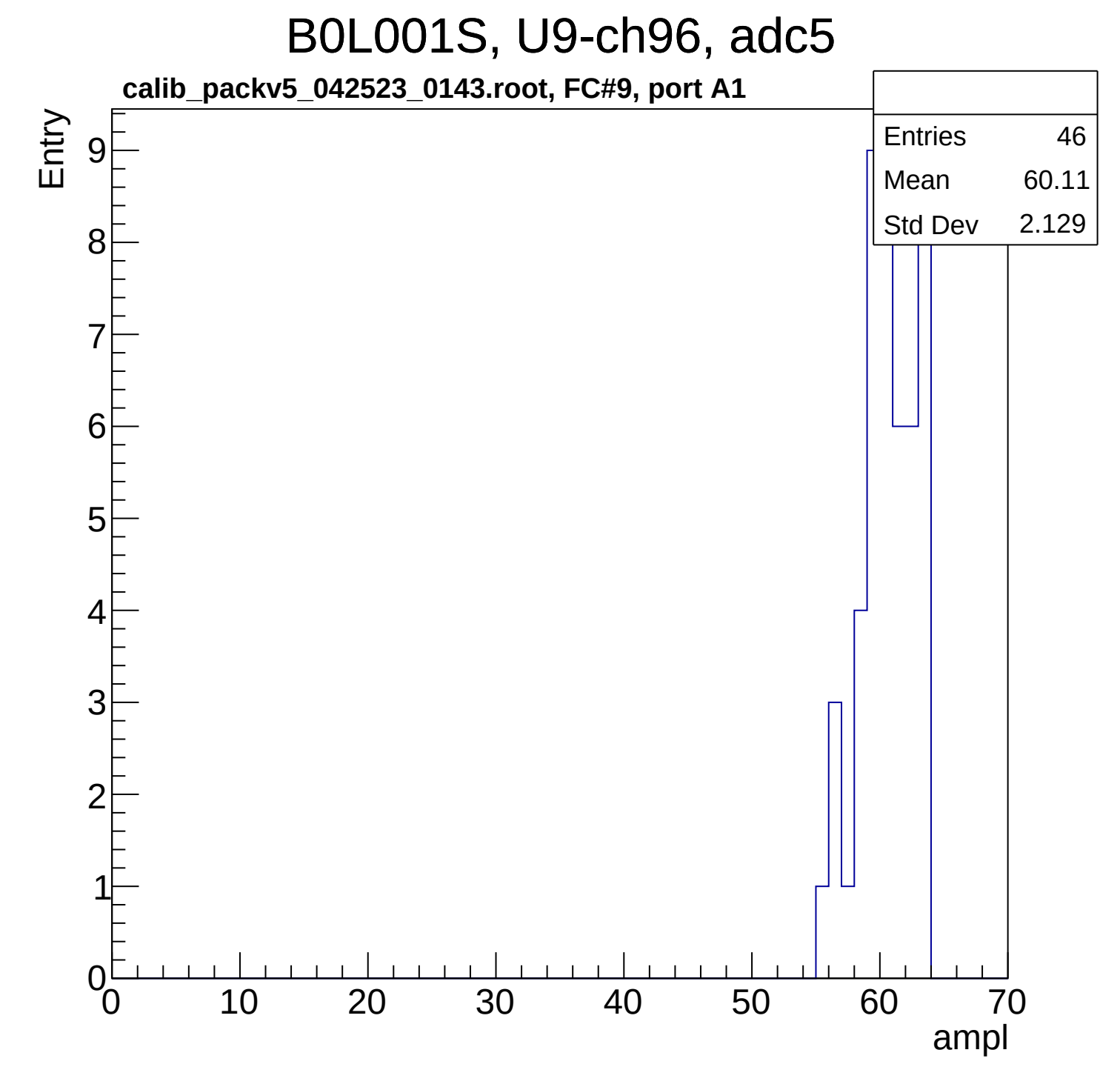
40
50
60
70
ampl


B0L001S, U9-ch96, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 60.11
Std Dev 2.129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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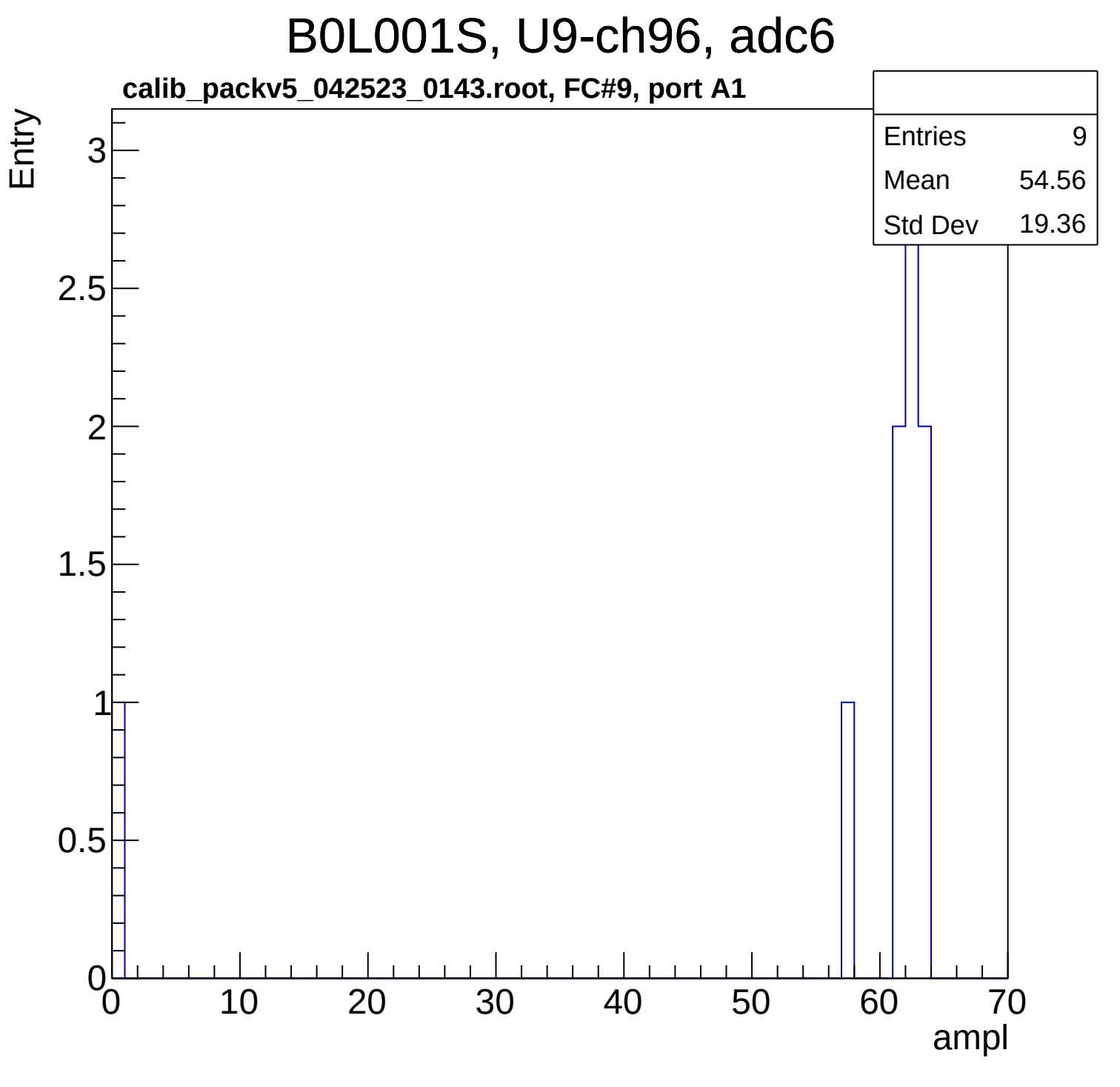
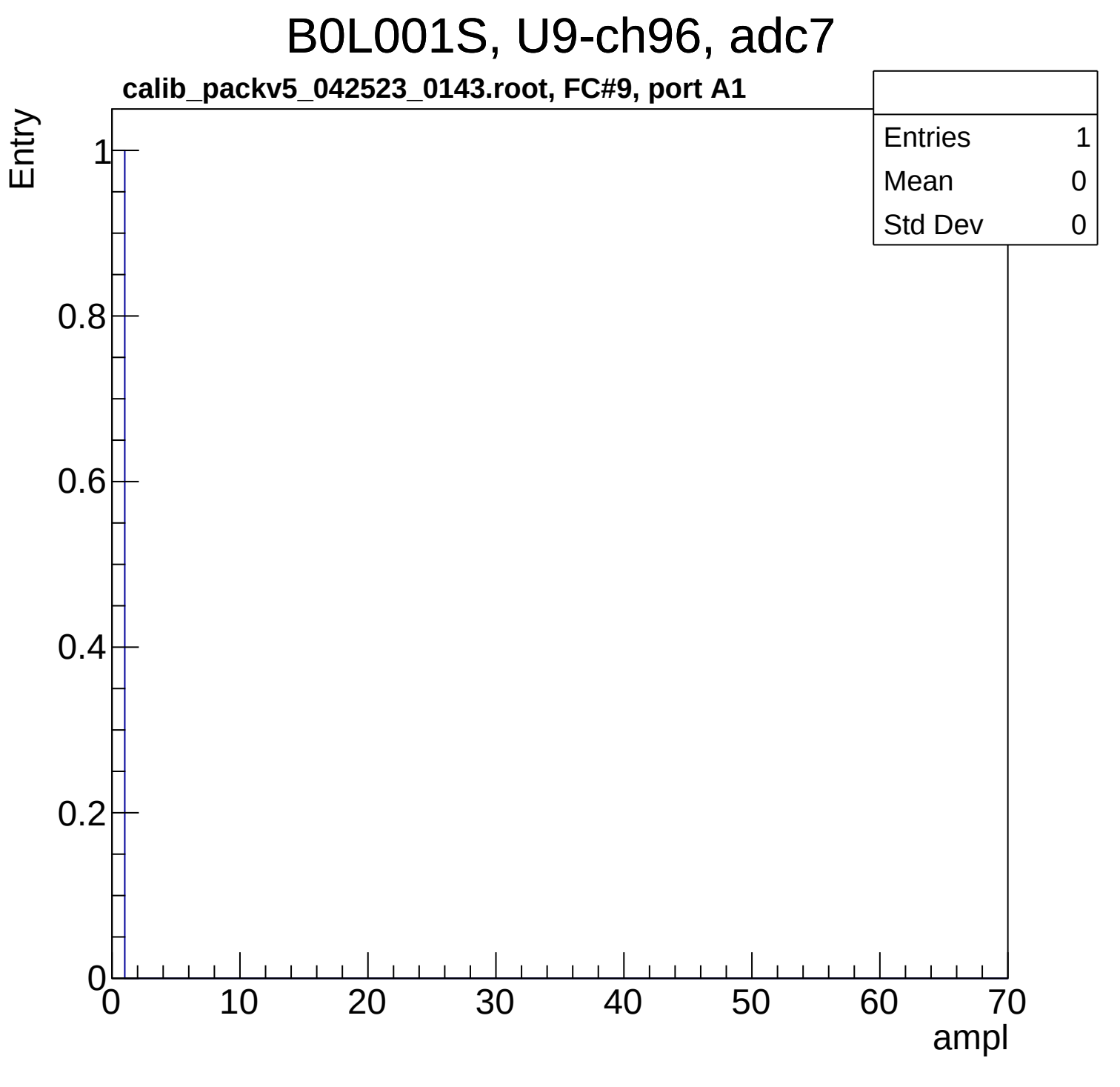
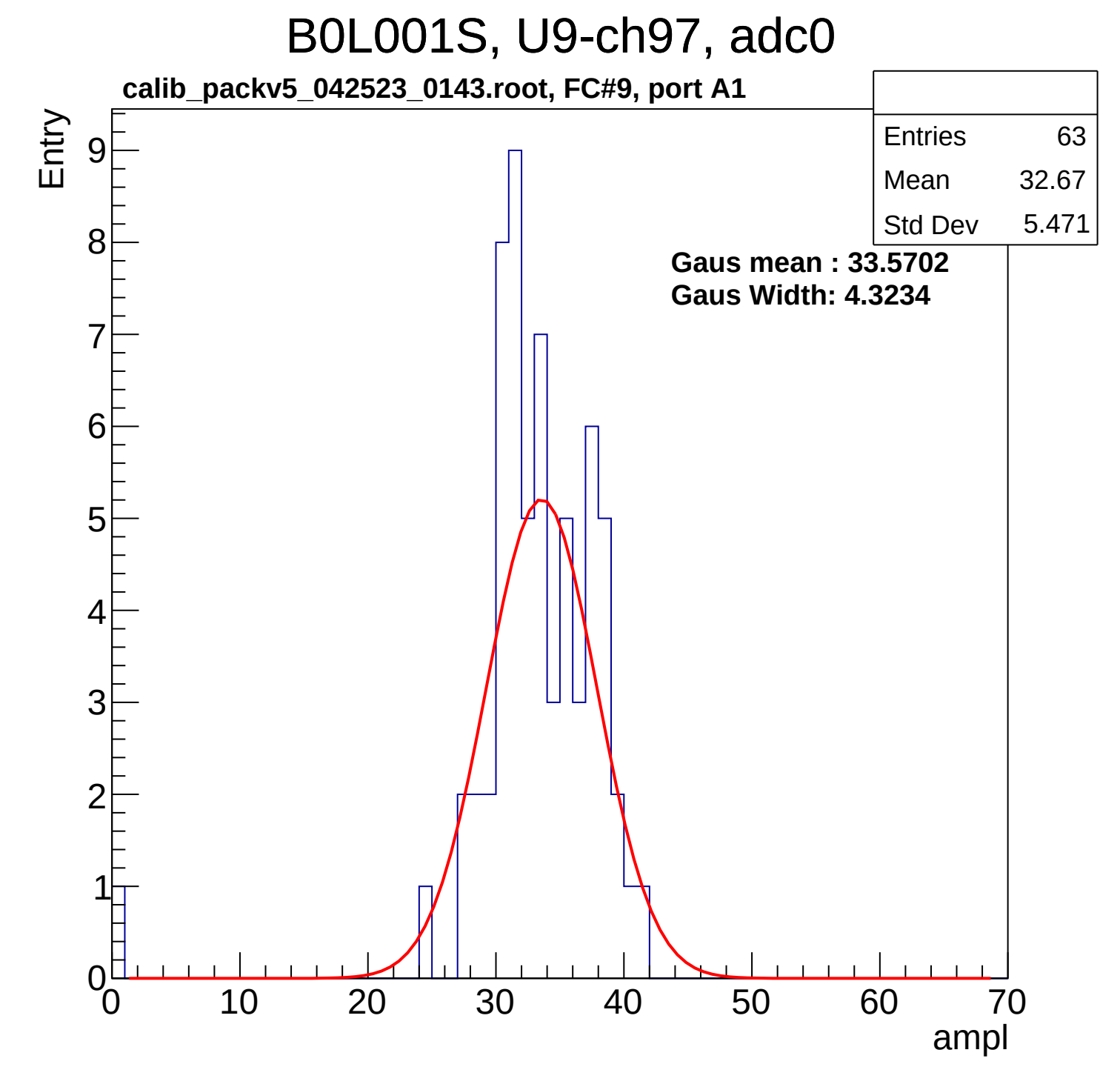

B0L001S, U9-ch96, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 54.56
Std Dev 19.36
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch96, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch97, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 32.67
Std Dev 5.471
Gaus mean : 33.5702
Gaus Width: 4.3234
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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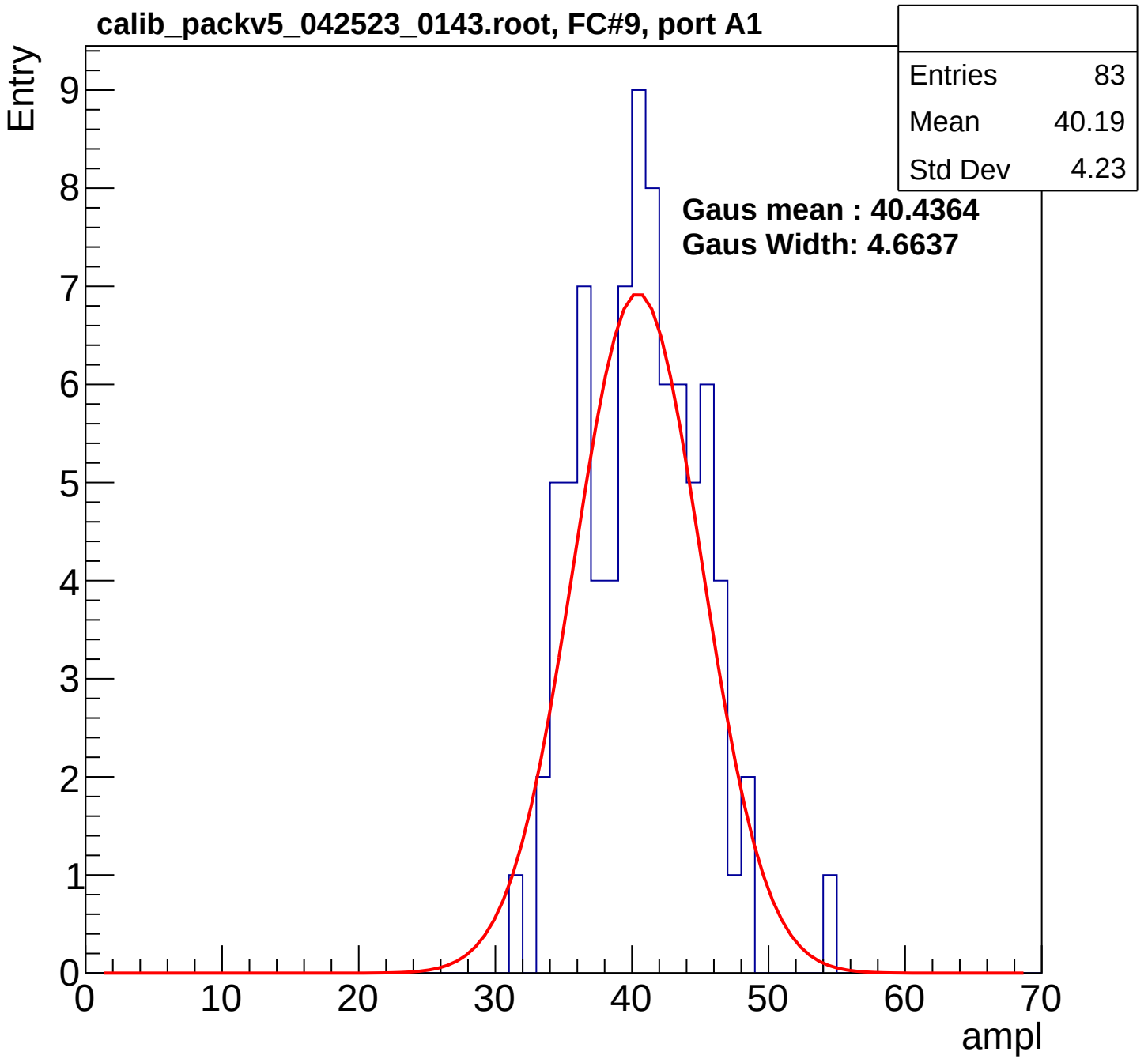
B0L001S, U9-ch97, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 40.19
Std Dev 4.23
Gaus mean : 40.4364
Gaus Width: 4.6637
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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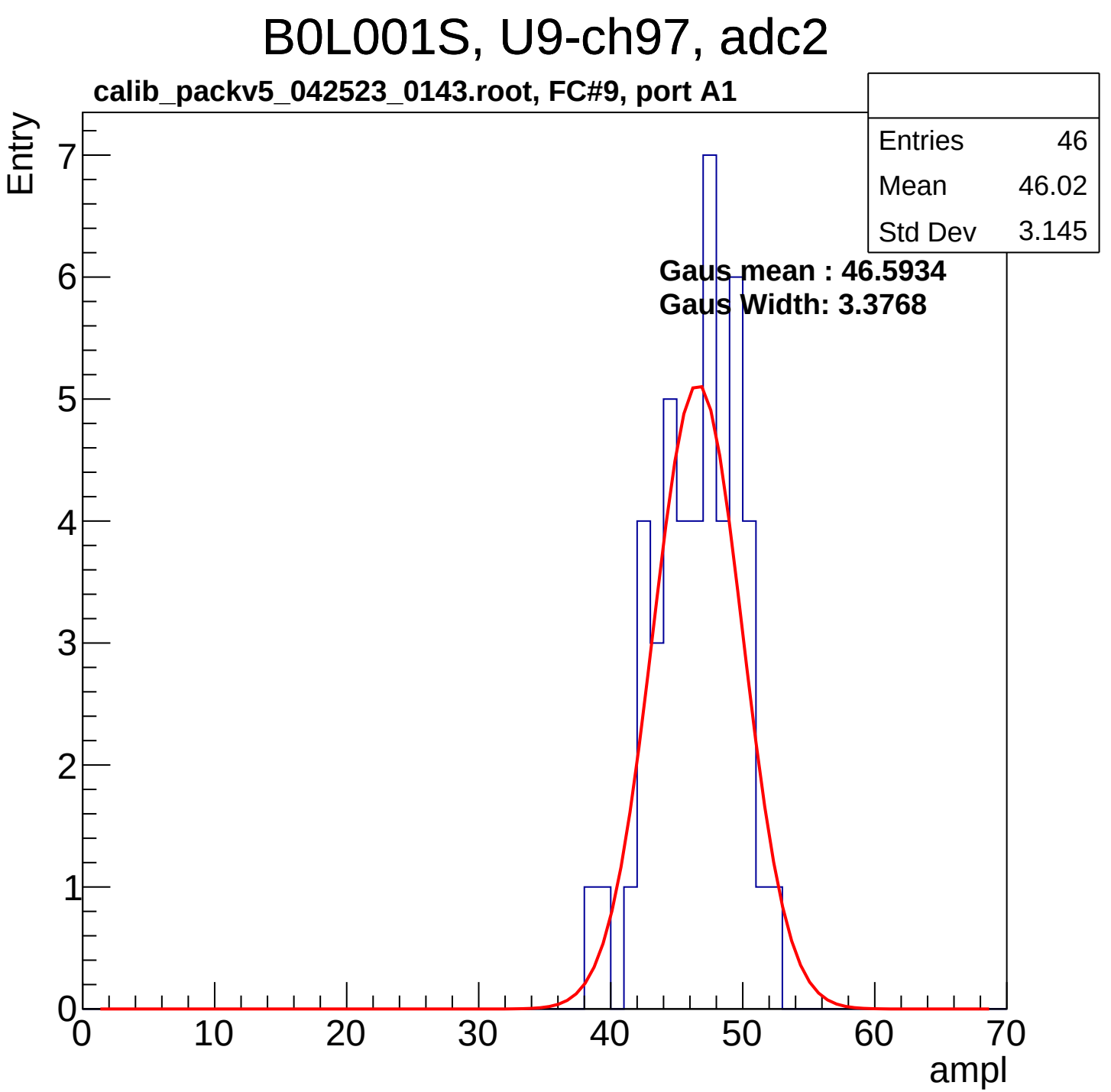
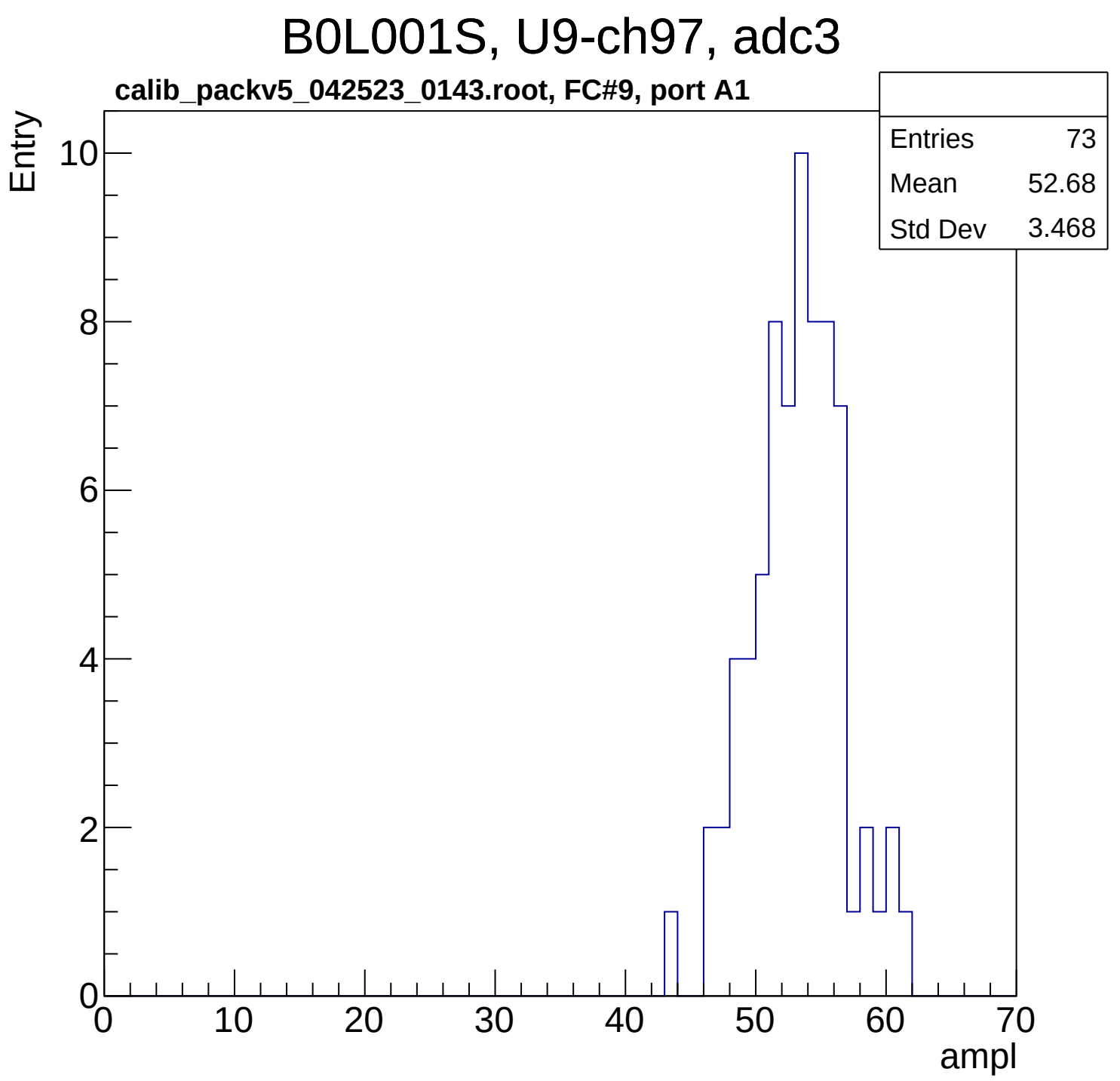
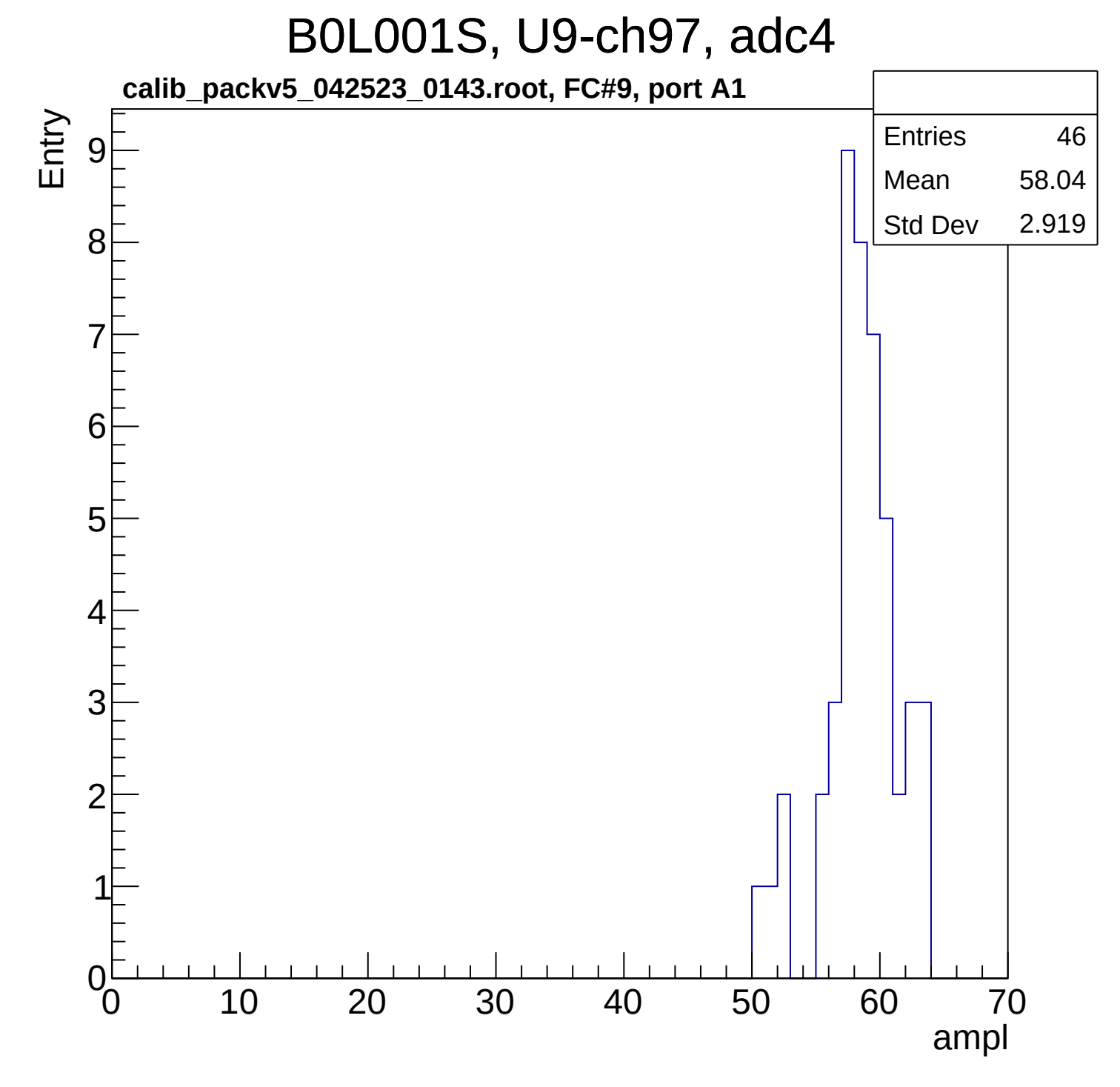
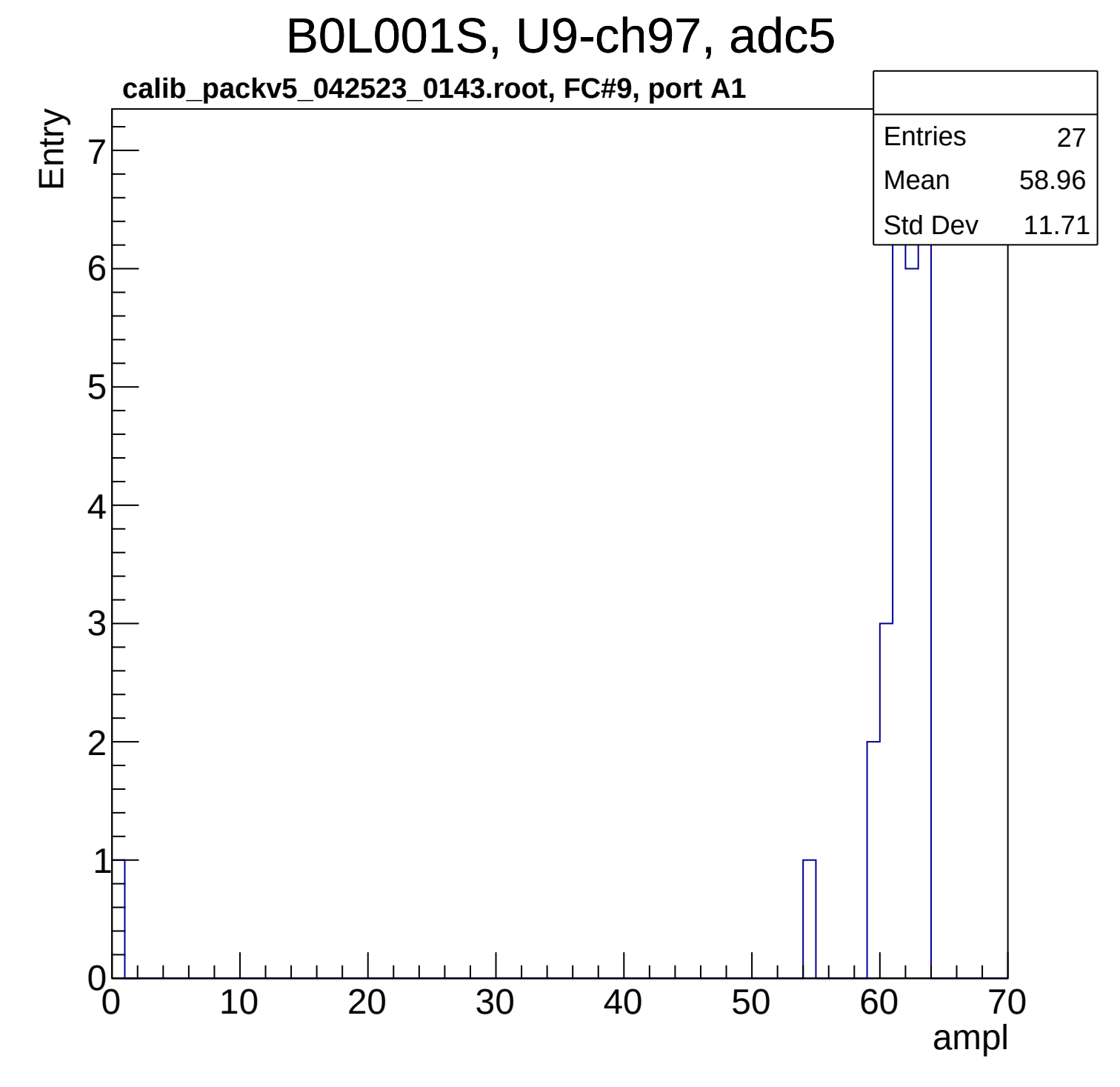
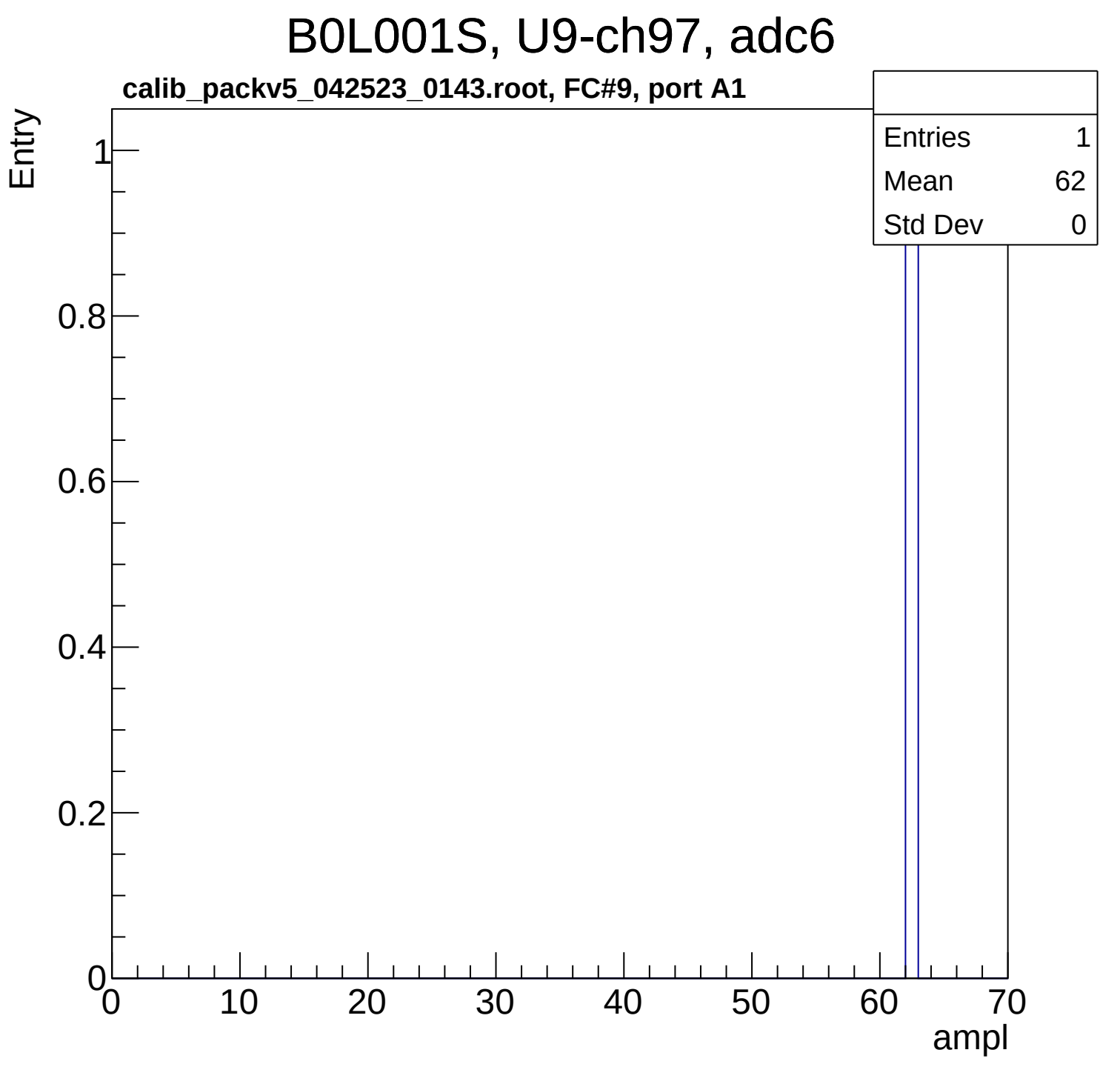
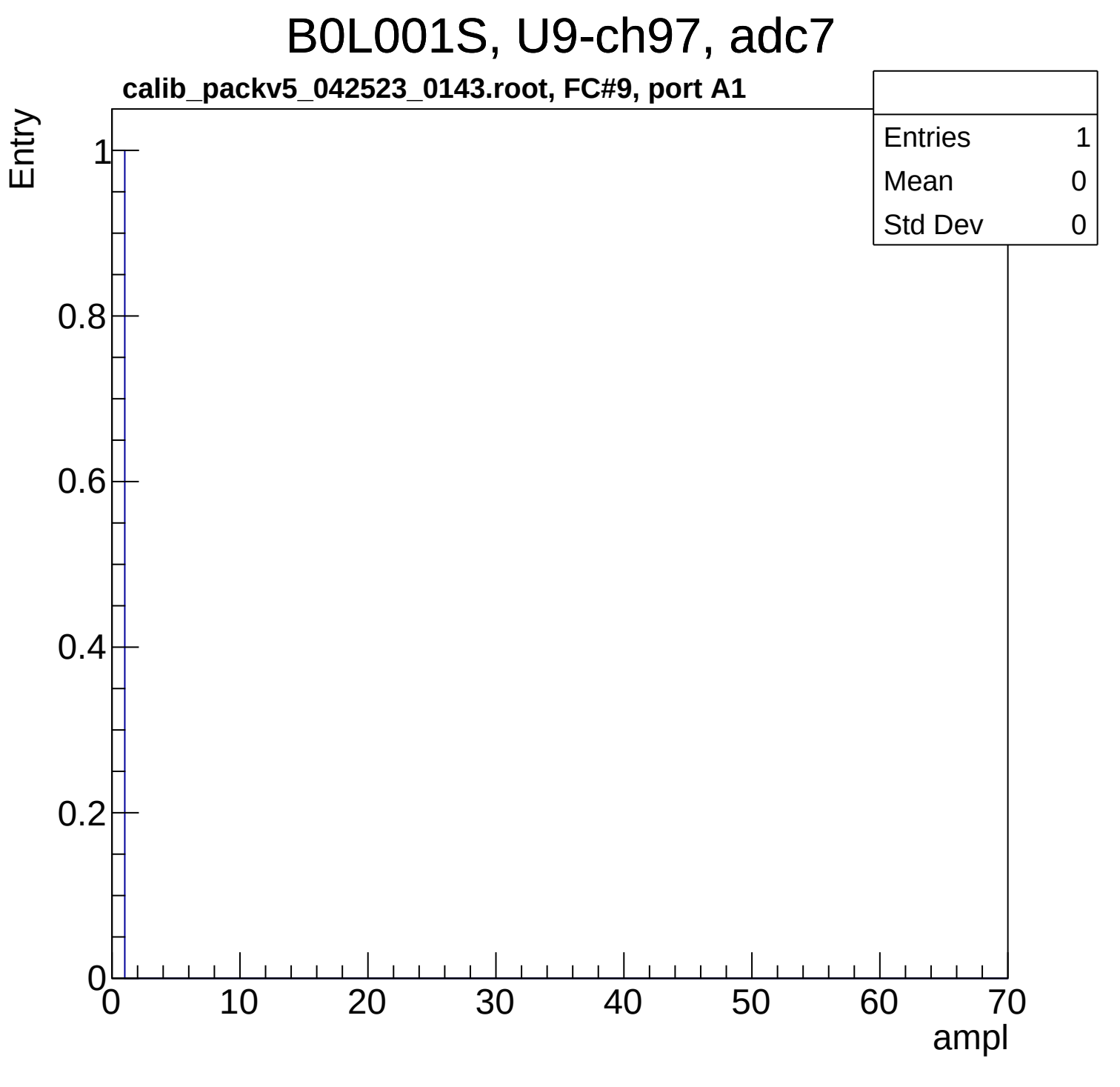

B0L001S, U9-ch97, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 46.02
Std Dev 3.145
Gaus mean : 46.5934
Gaus Width: 3.3768
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

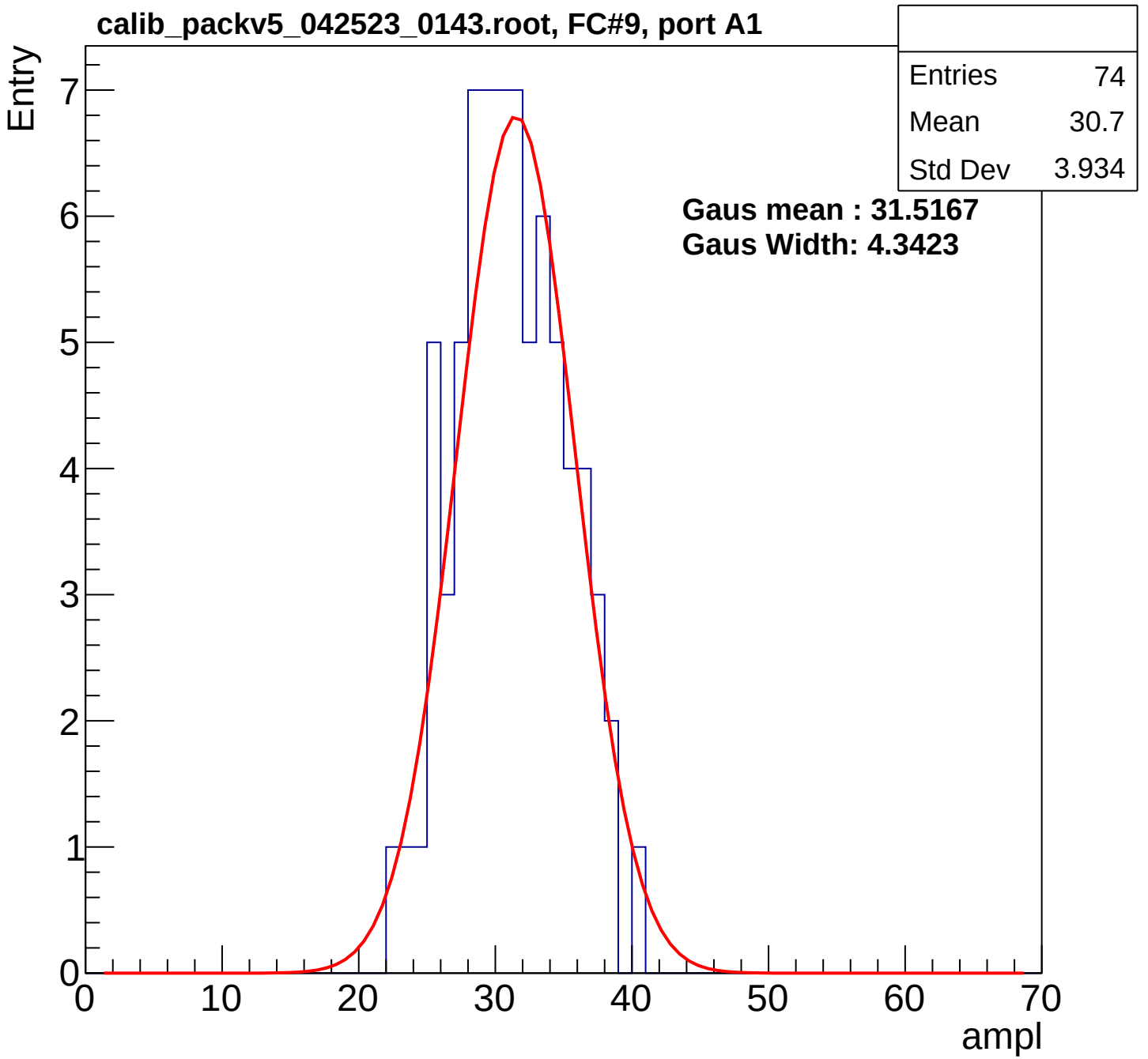

B0L001S, U9-ch97, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 52.68
Std Dev 3.468
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

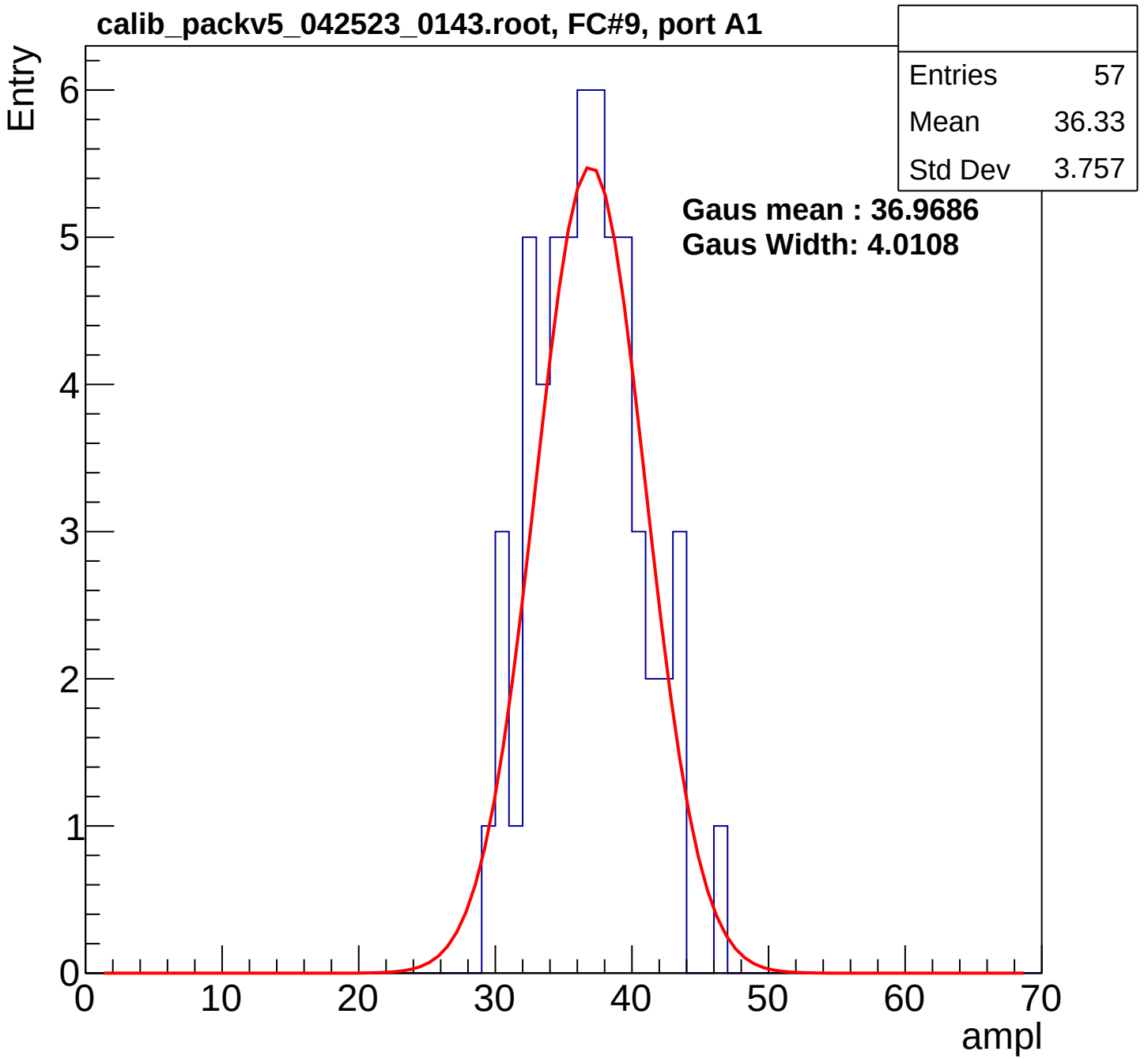

B0L001S, U9-ch97, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.04
Std Dev 2.919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

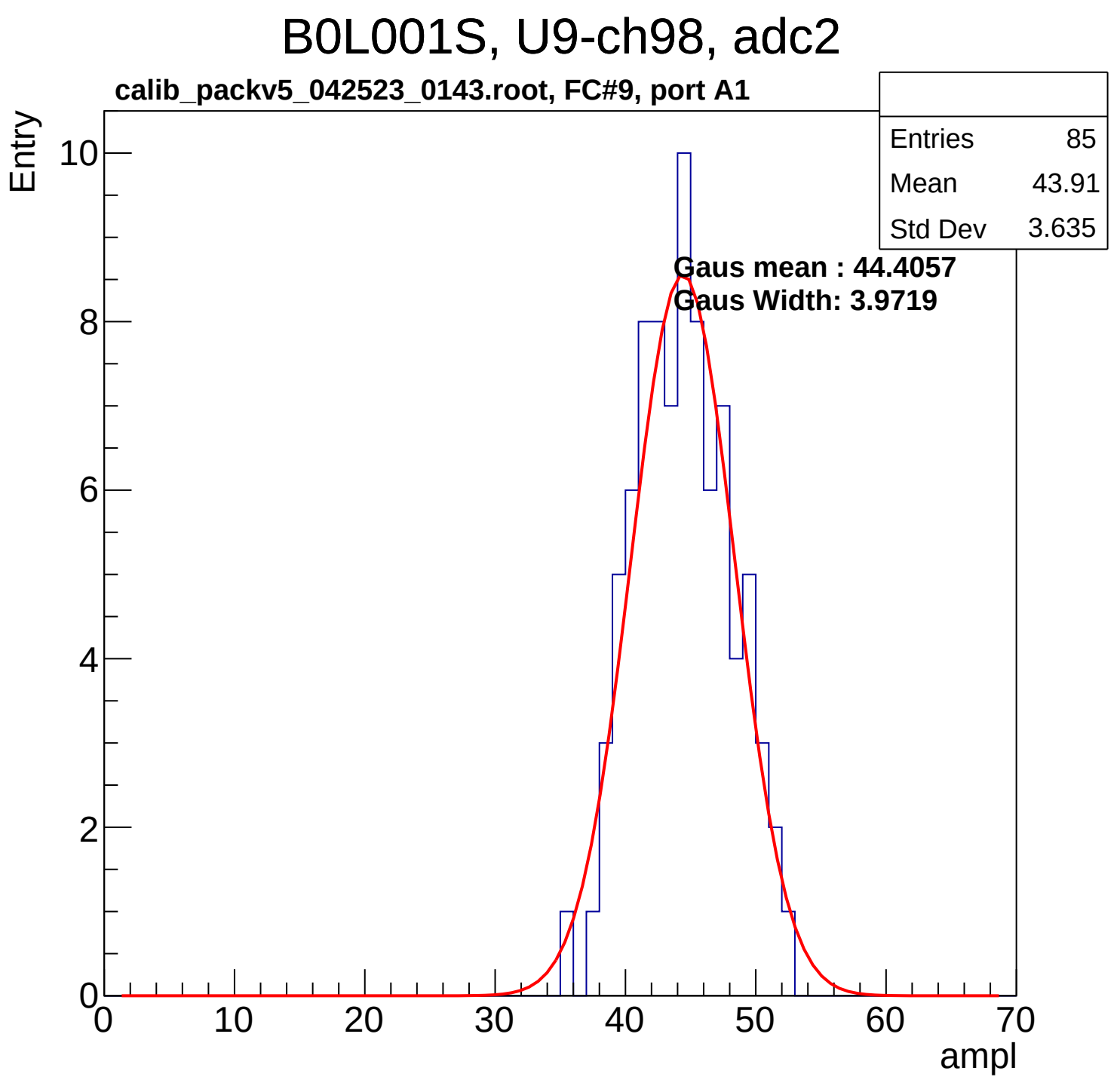

B0L001S, U9-ch97, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.96
Std Dev 11.71
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

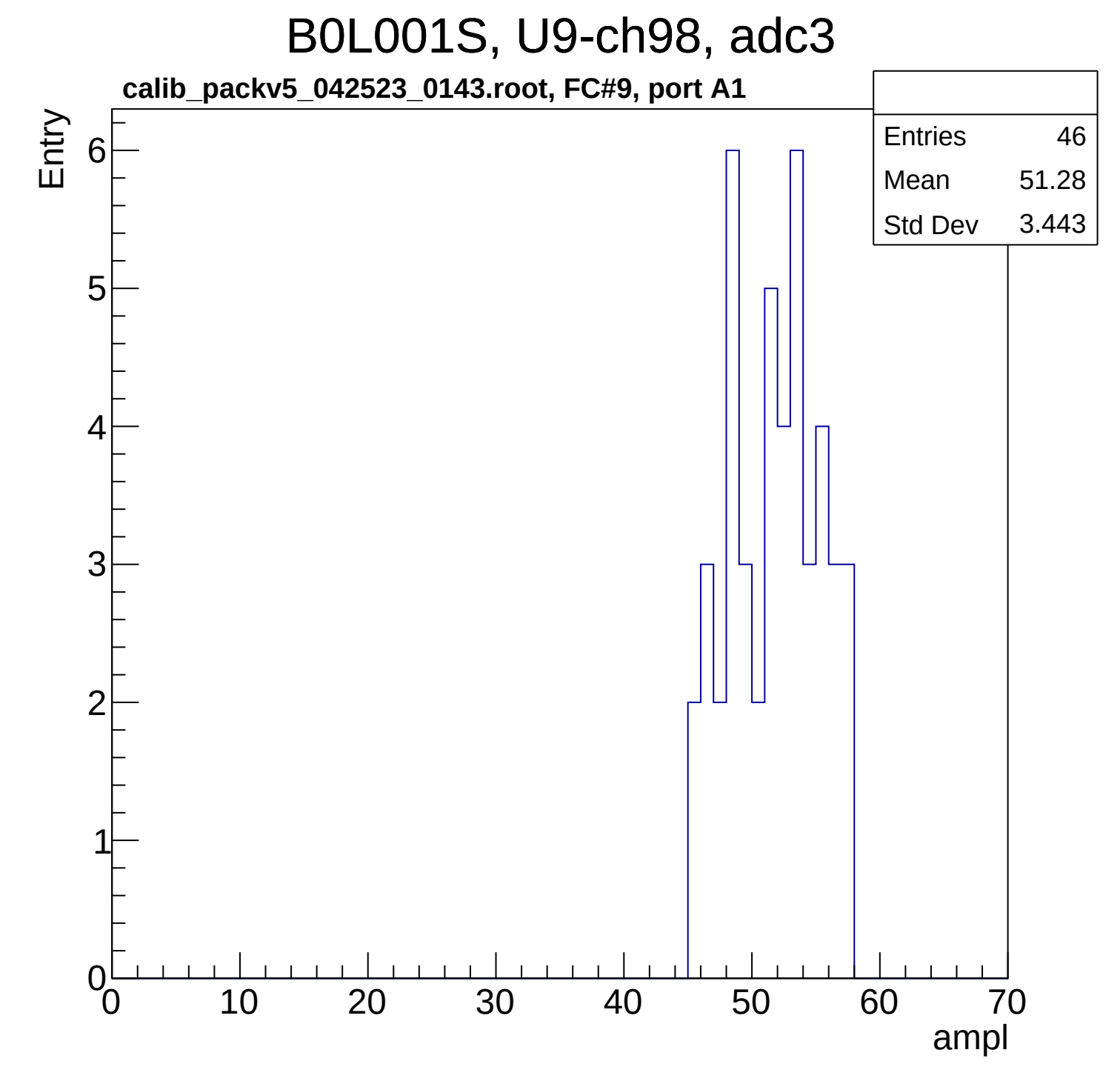

B0L001S, U9-ch97, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

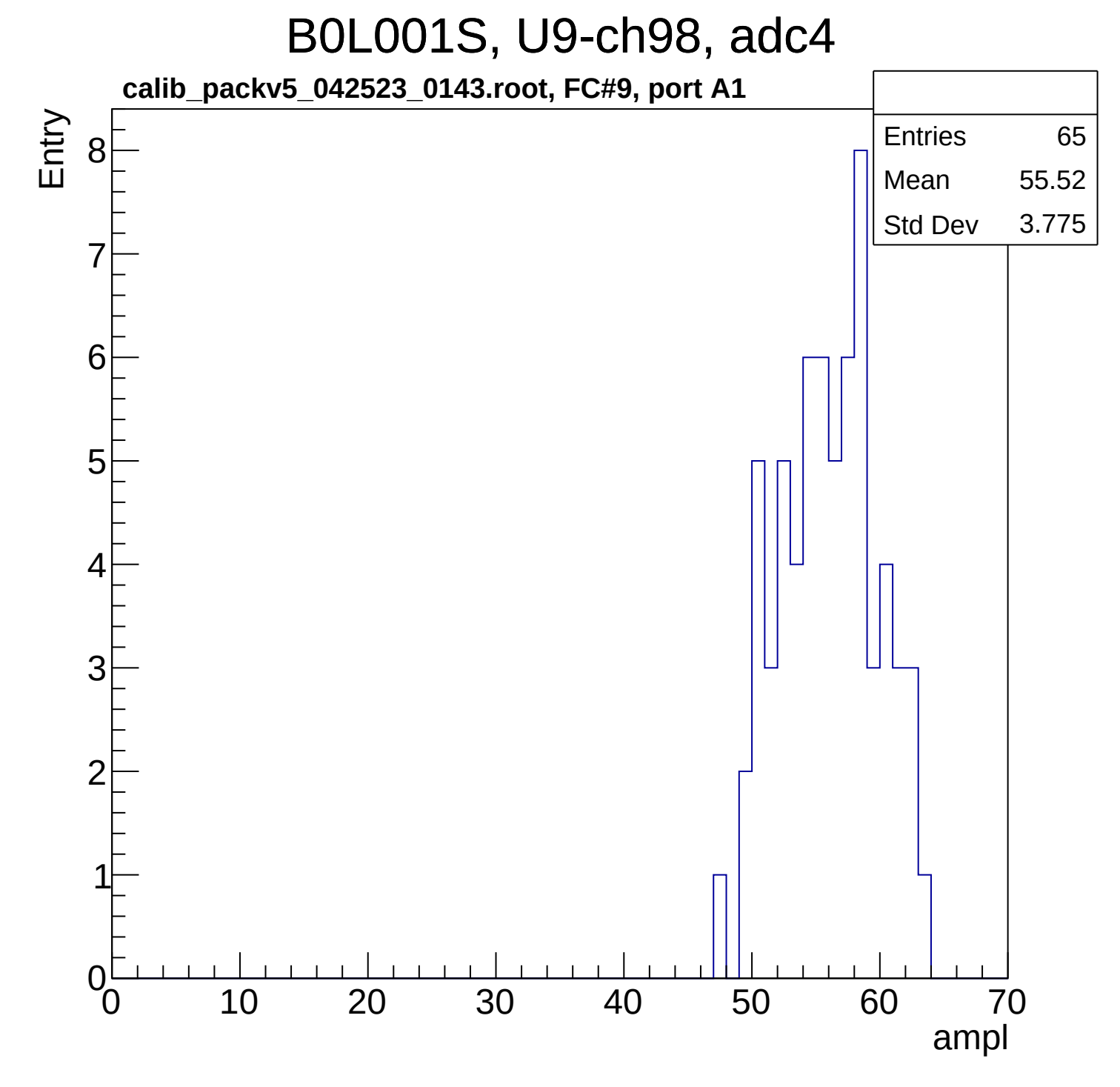

B0L001S, U9-ch97, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

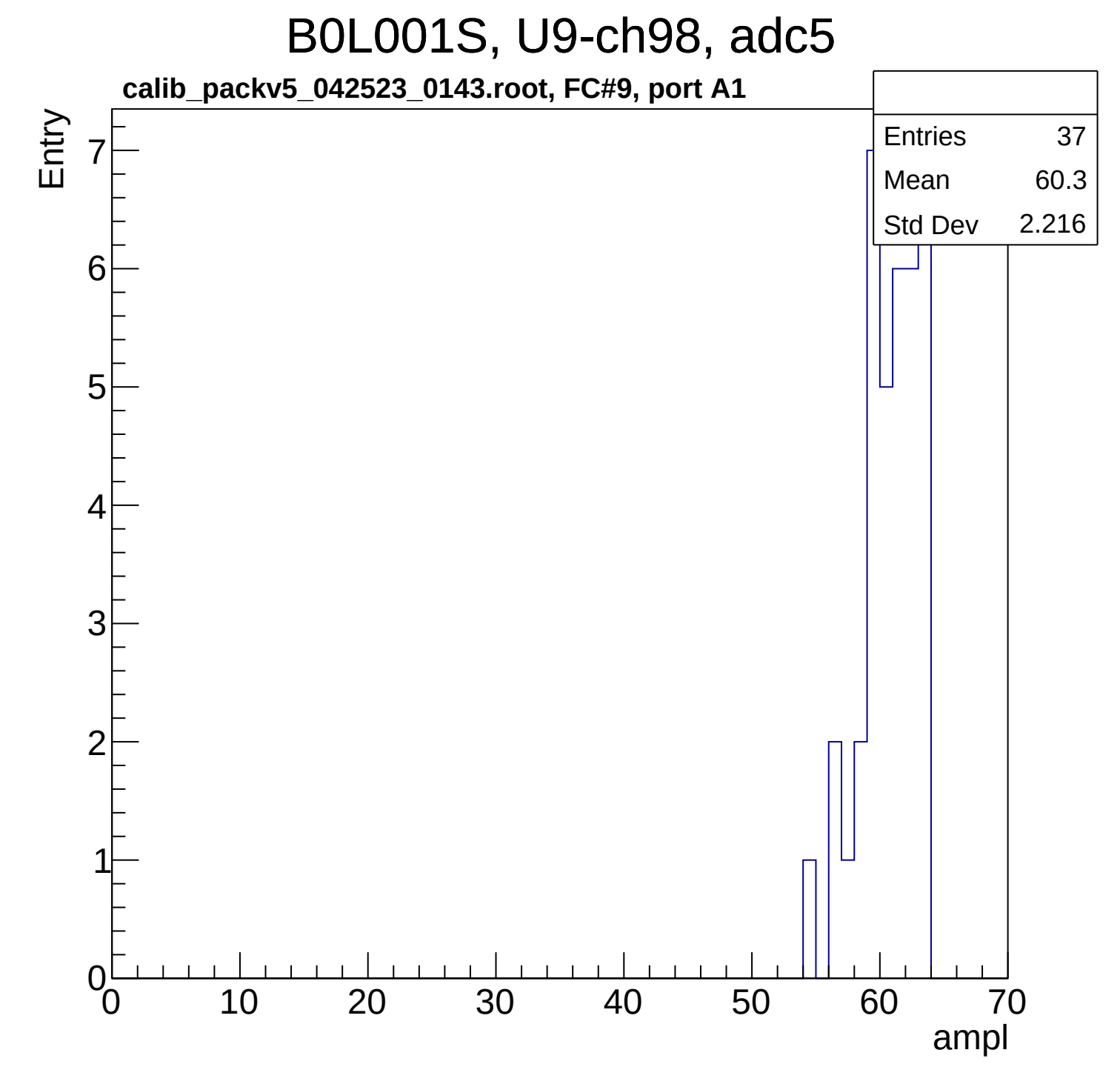

B0L001S, U9-ch98, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 30.7
Std Dev 3.934
Gaus mean : 31.5167
Gaus Width: 4.3423
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

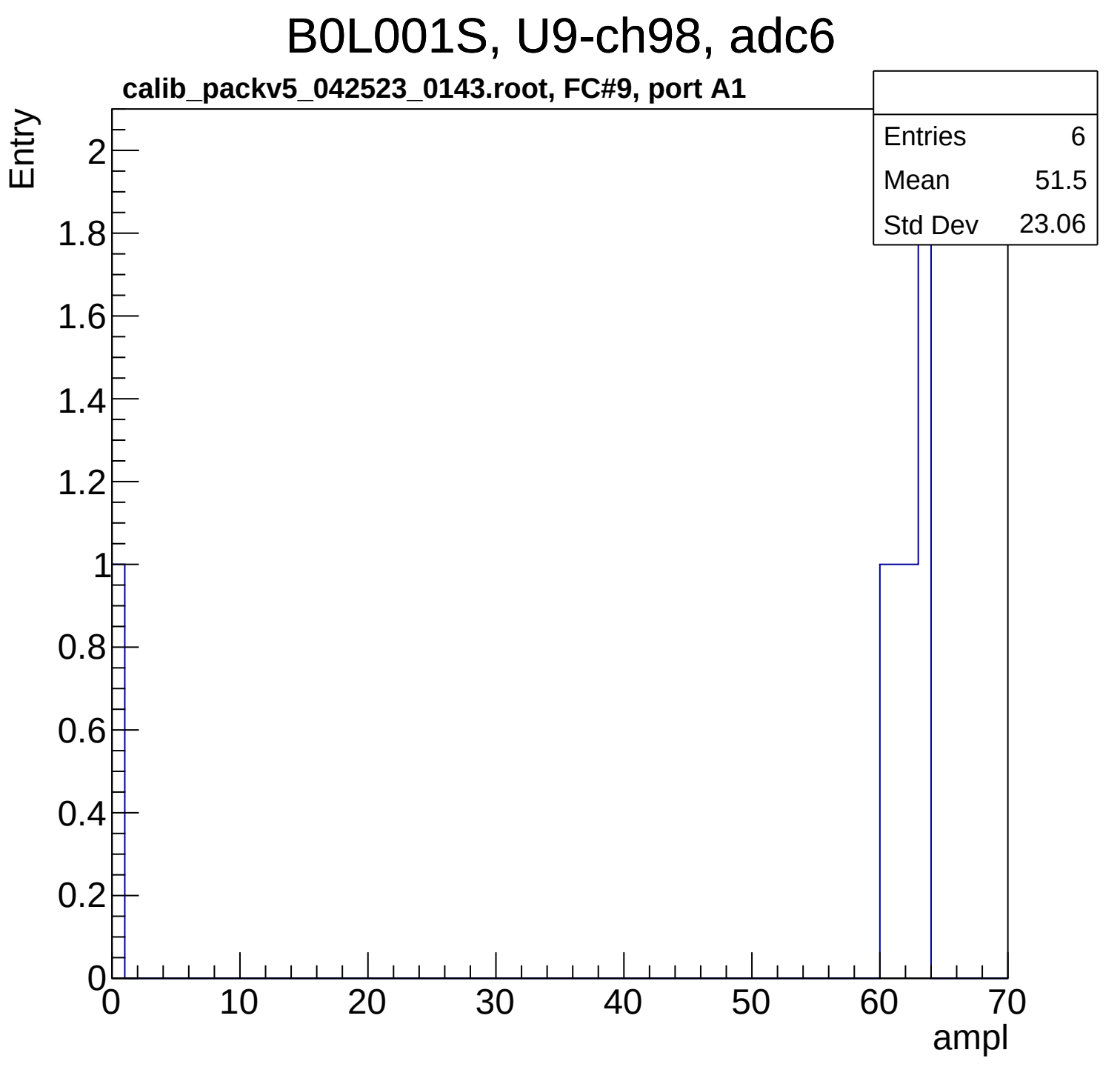

B0L001S, U9-ch98, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 36.33
Std Dev 3.757
Gaus mean : 36.9686
Gaus Width: 4.0108
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

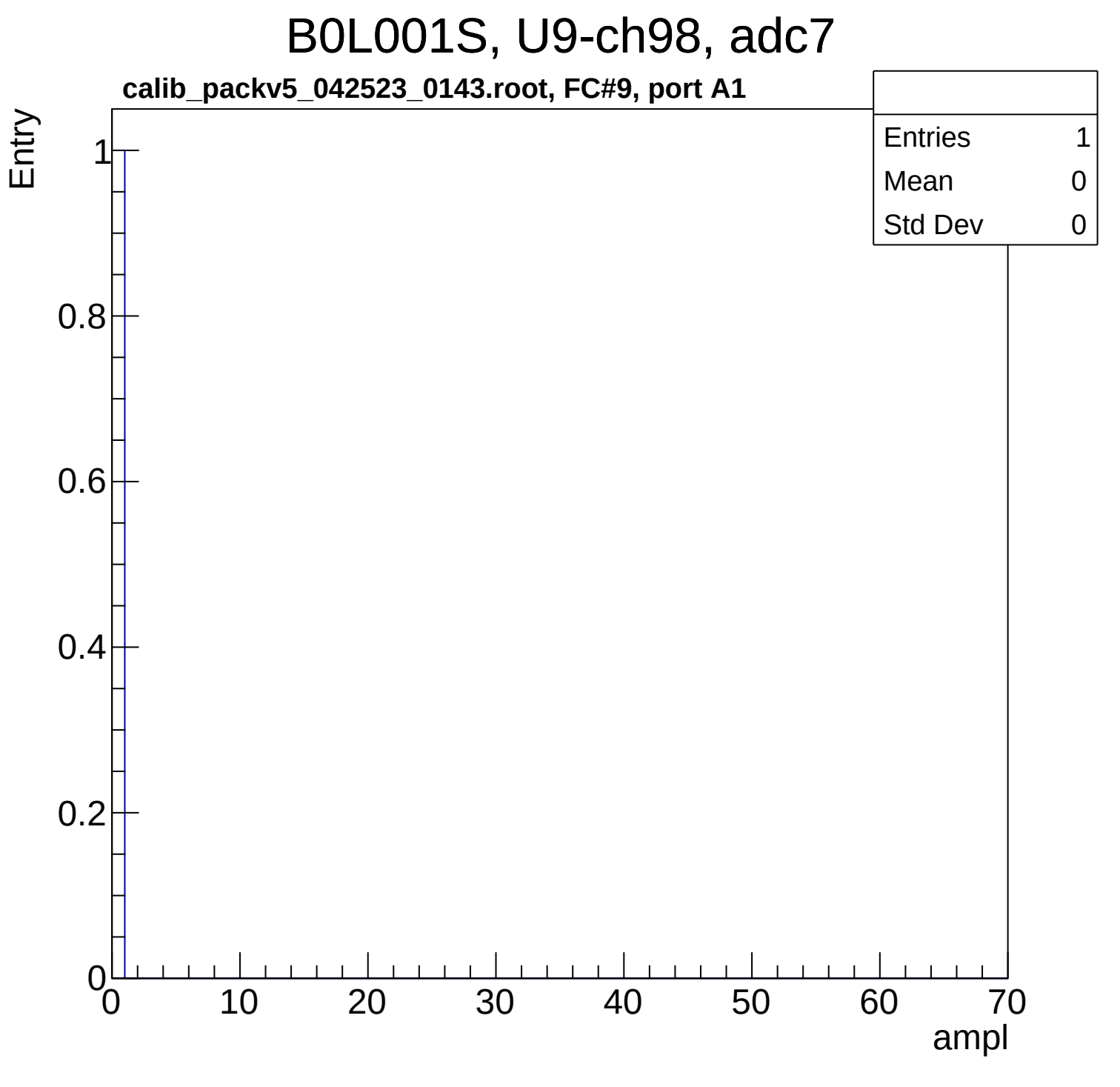

B0L001S, U9-ch98, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 43.91
Std Dev 3.635
Gaus mean : 44.4057
Gaus Width: 3.9719
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch98, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 51.28
Std Dev 3.443
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U9-ch98, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 55.52
Std Dev 3.775
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch98, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 60.3
Std Dev 2.216
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch98, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.5
Std Dev 23.06
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch98, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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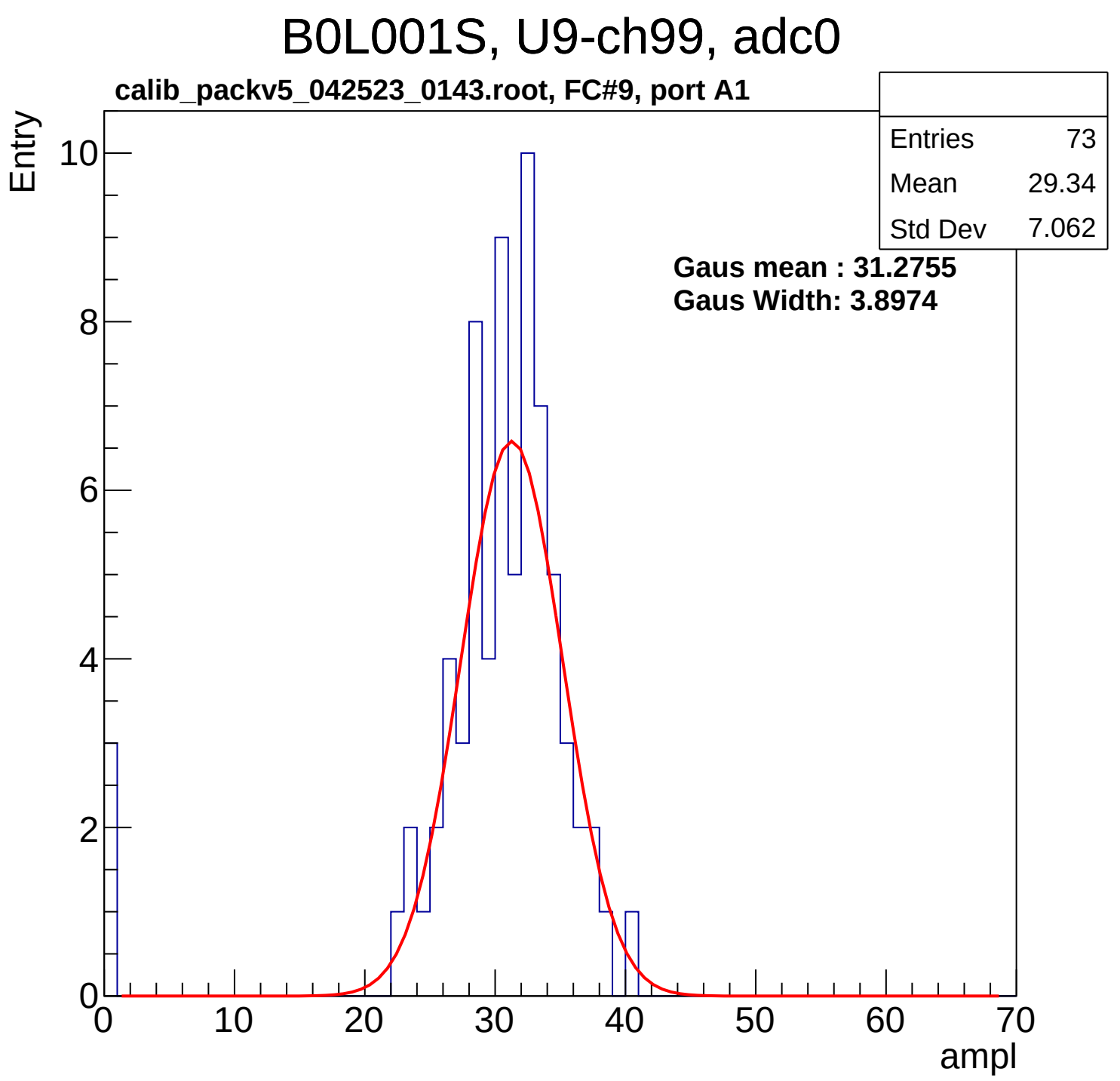
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch99, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.34
Std Dev 7.062
Gaus mean : 31.2755
Gaus Width: 3.8974
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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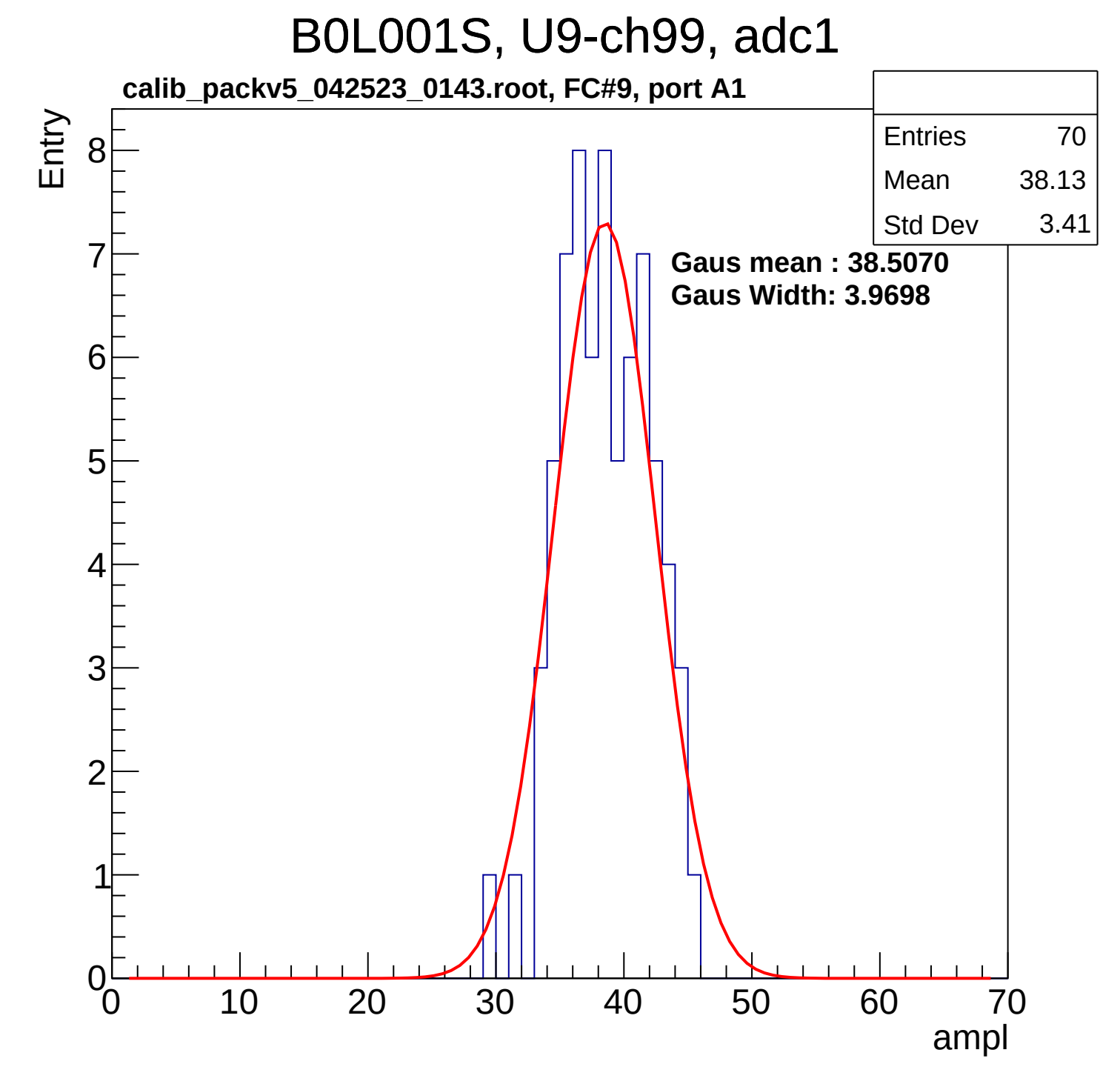
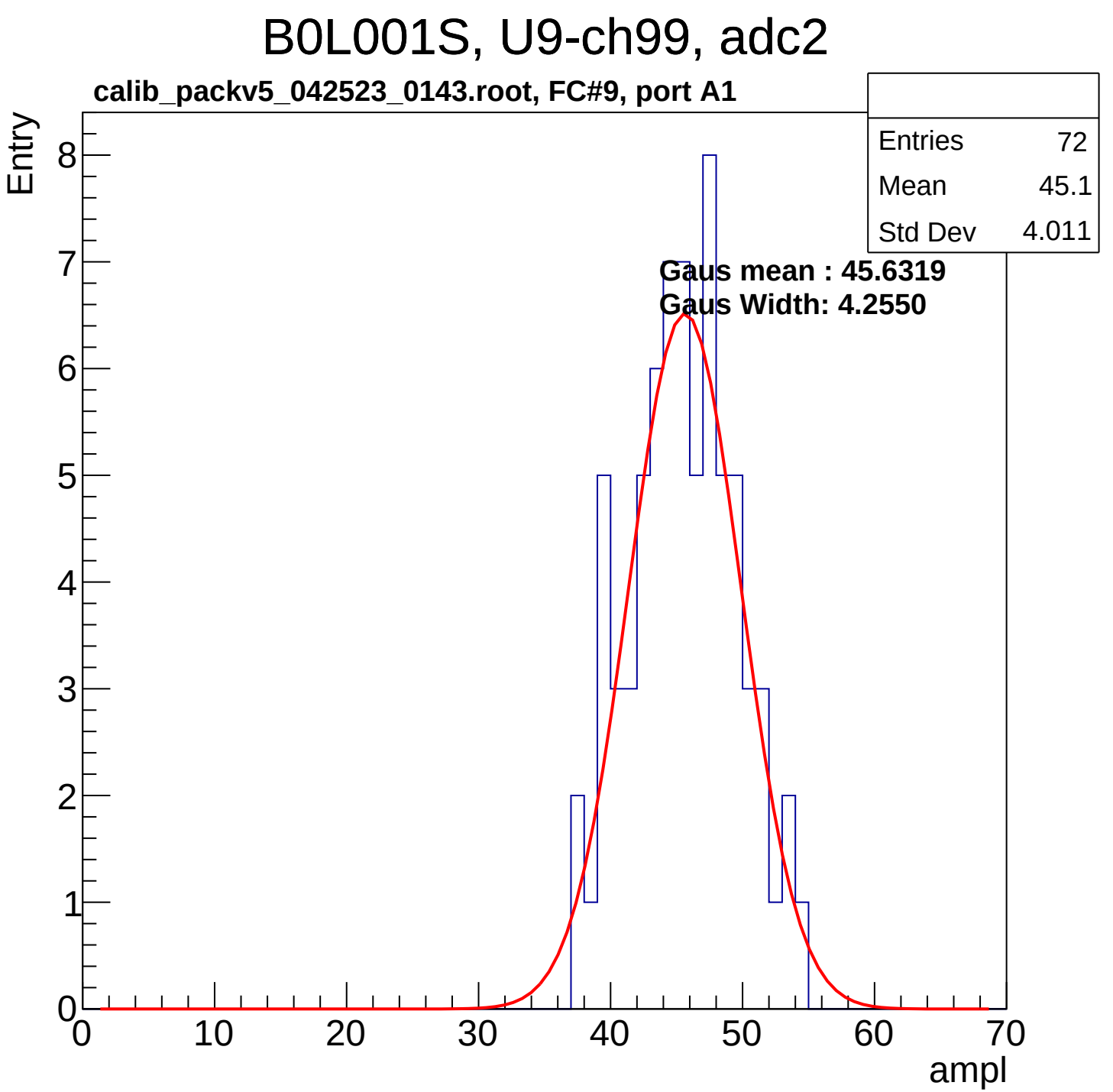
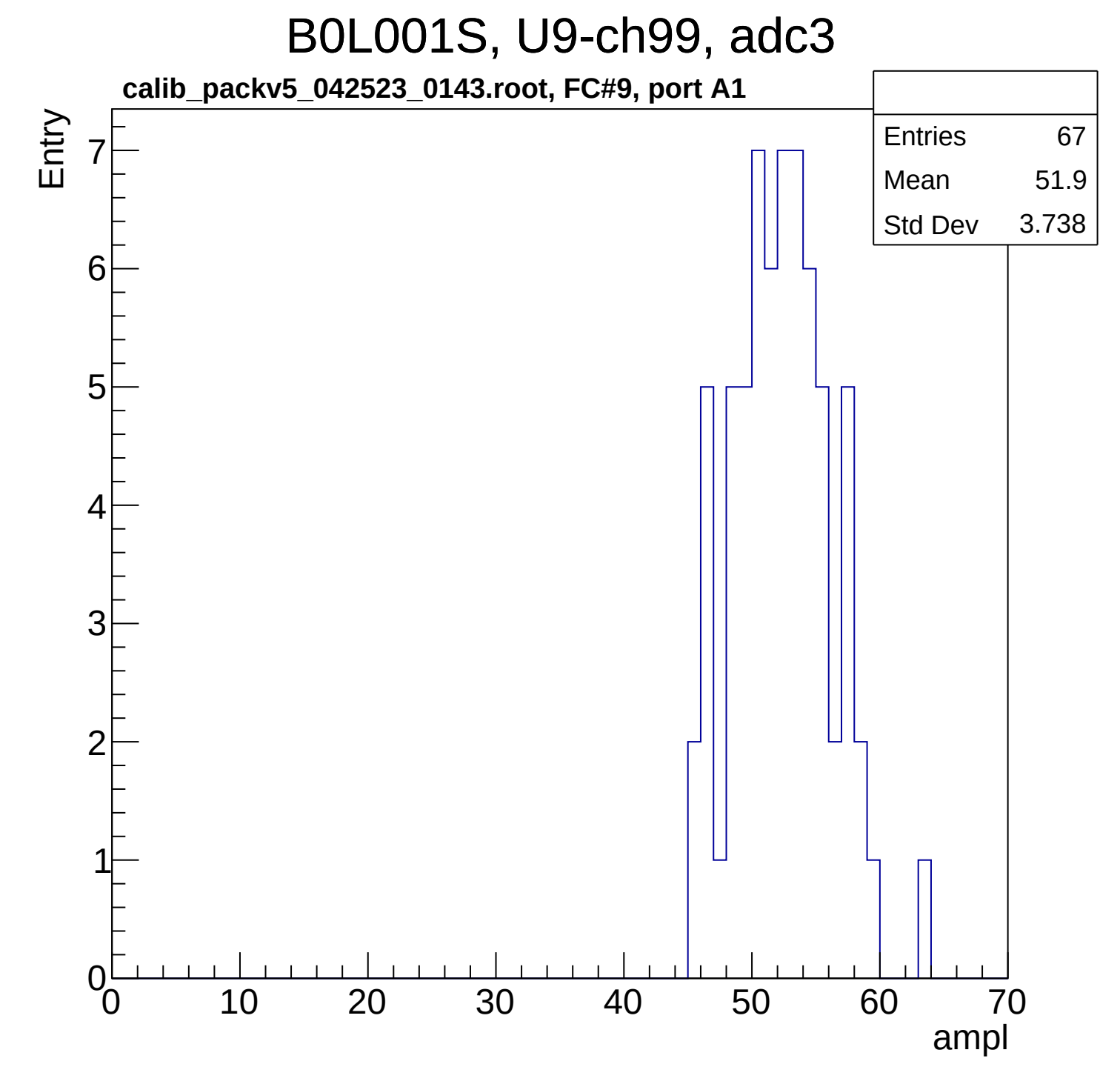

B0L001S, U9-ch99, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 38.13
Std Dev 3.41
Gaus mean : 38.5070
Gaus Width: 3.9698
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch99, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 45.1
Std Dev 4.011
Gaus mean : 45.6319
Gaus Width: 4.2550
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch99, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 51.9
Std Dev 3.738
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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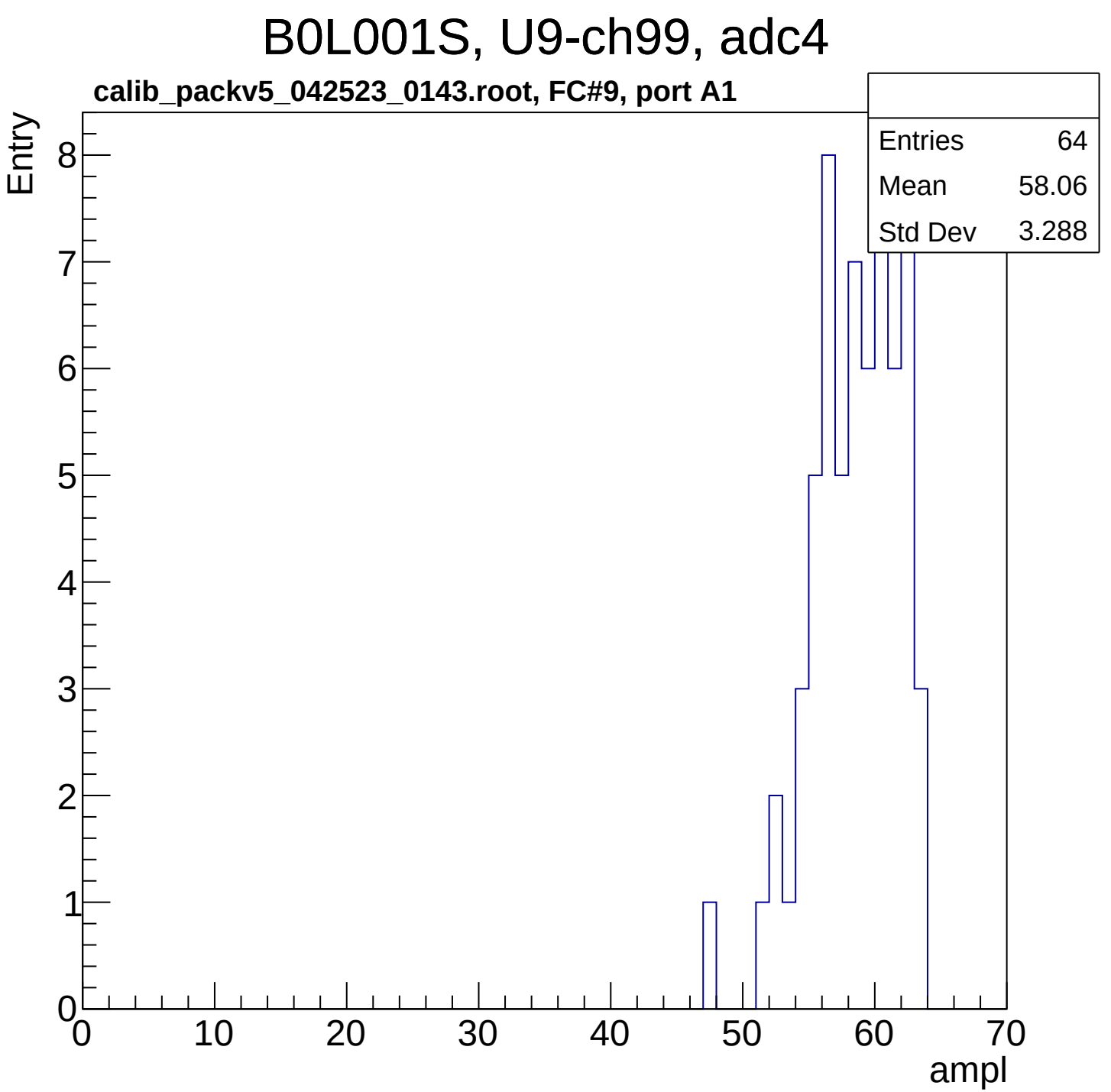
B0L001S, U9-ch99, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 58.06
Std Dev 3.288
Entry
ampl
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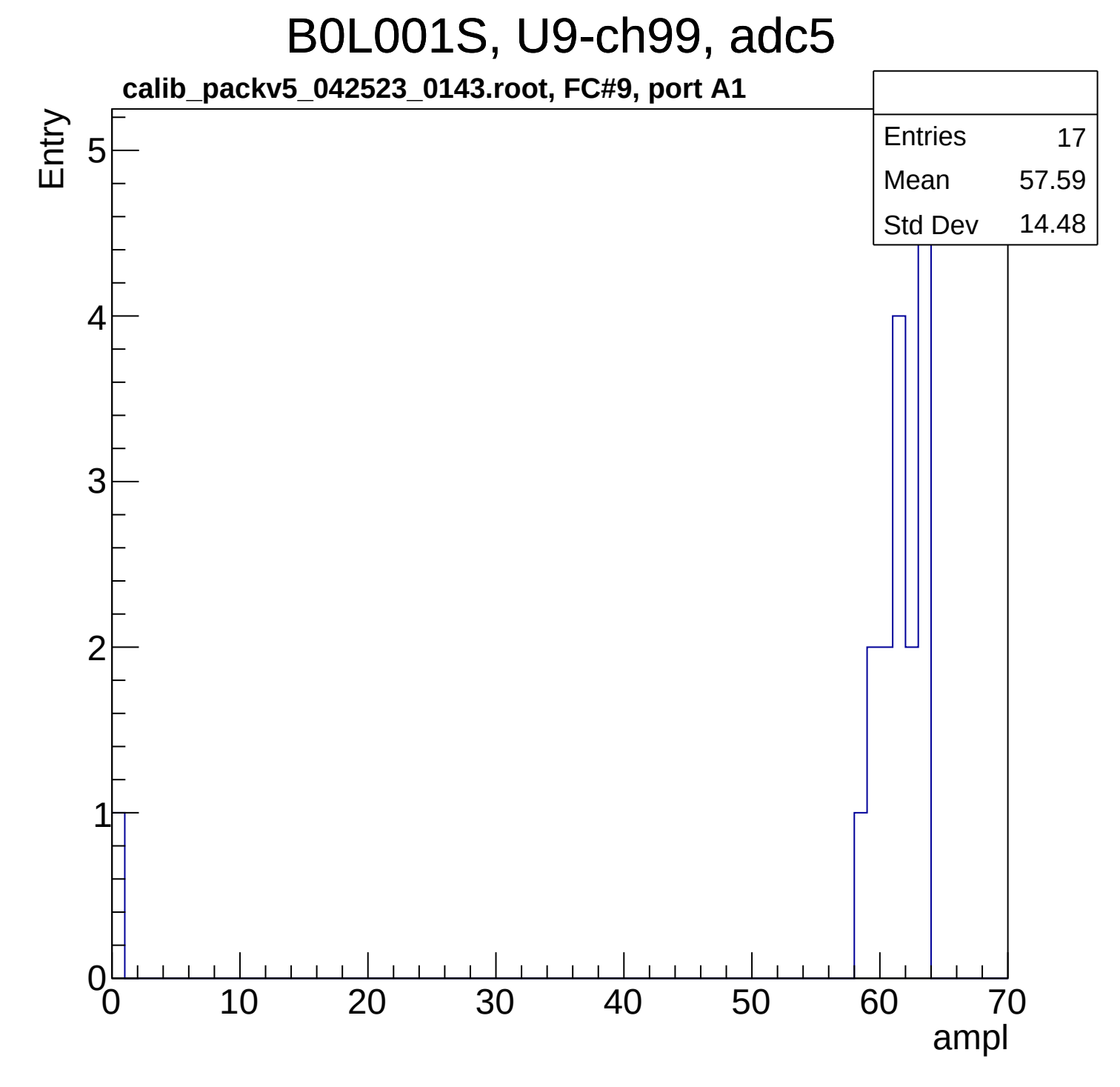
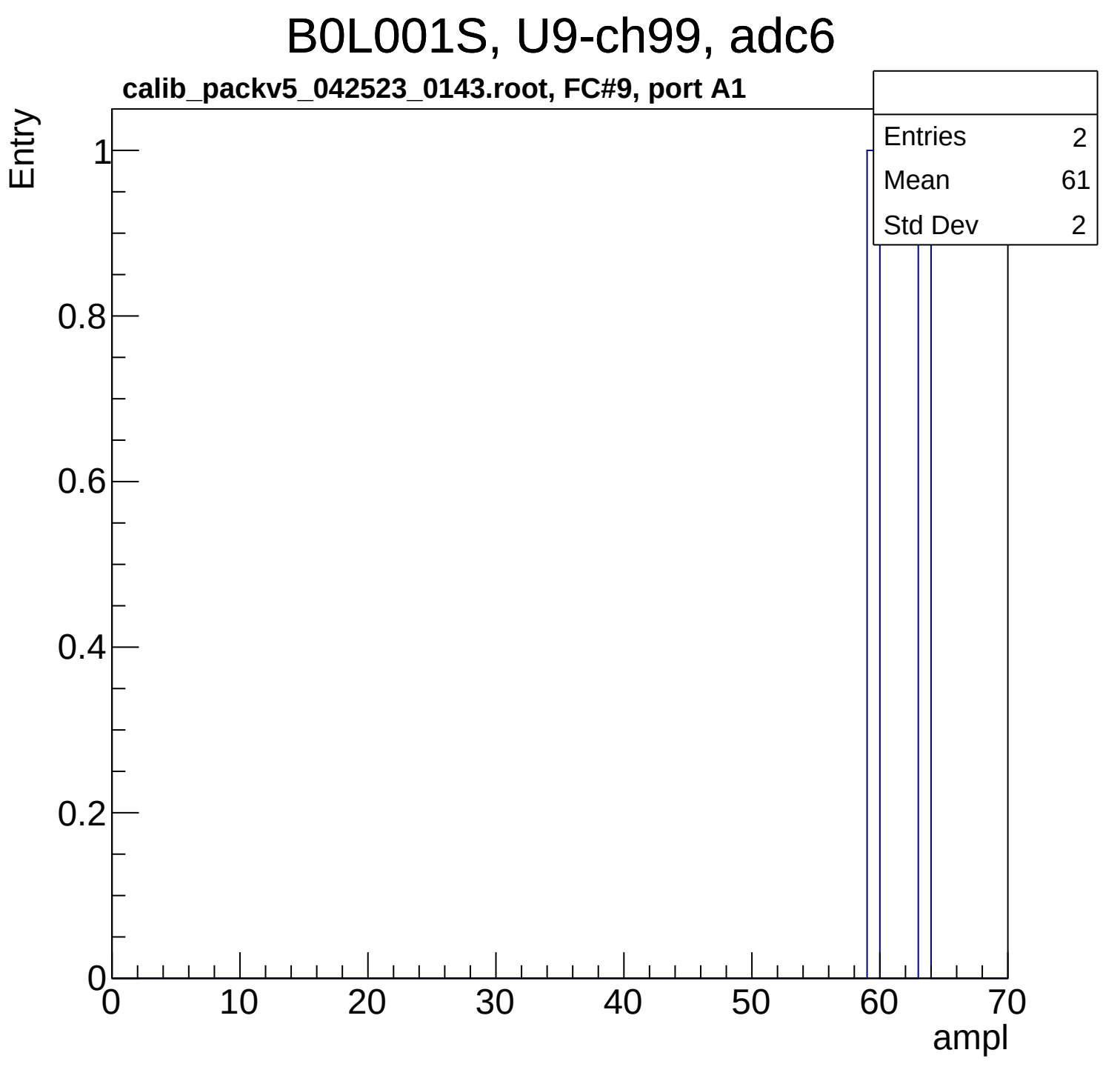
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch99, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 17
Mean 57.59
Std Dev 14.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch99, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 61
Std Dev 2
Entry
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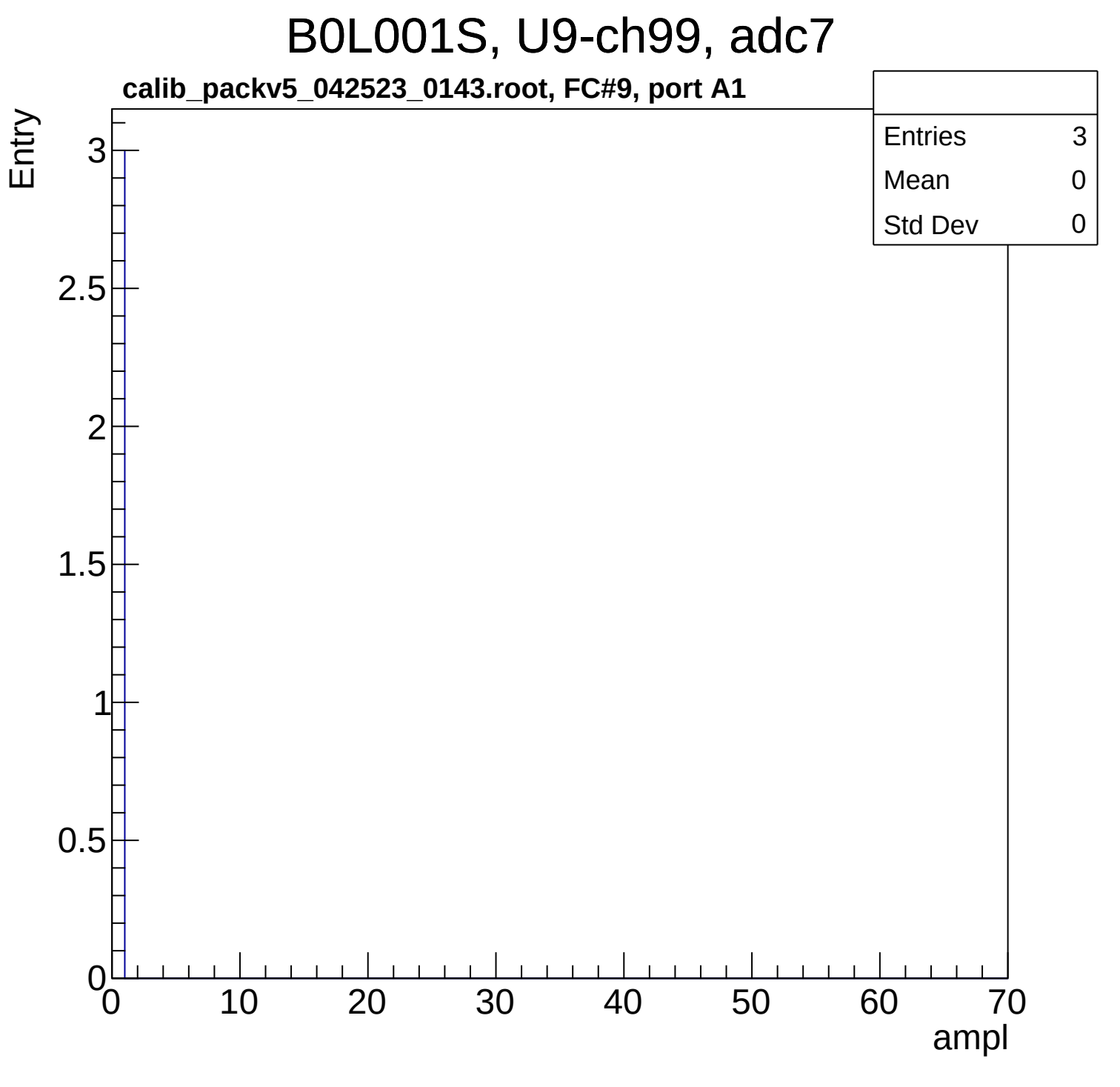
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch99, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
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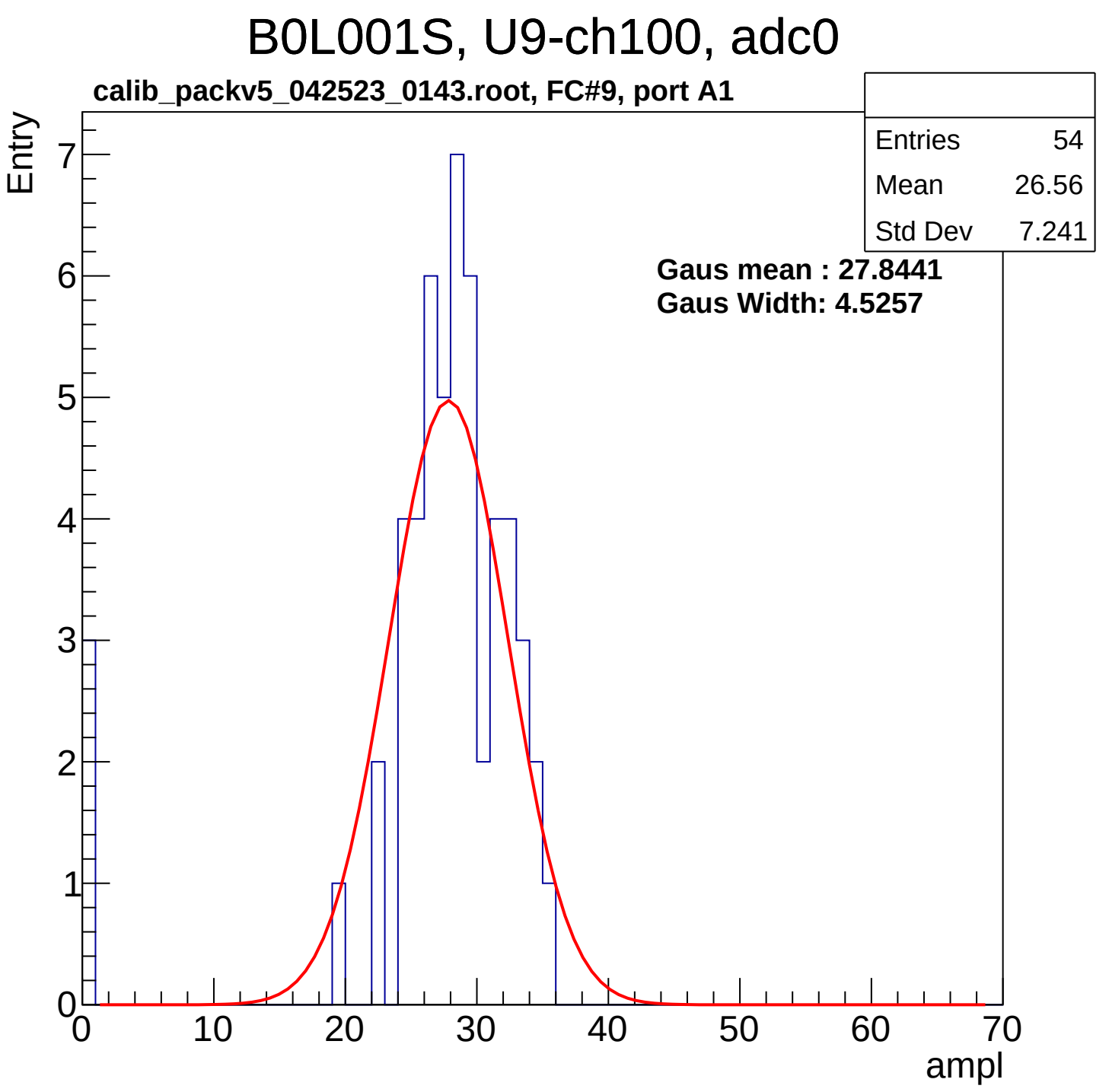
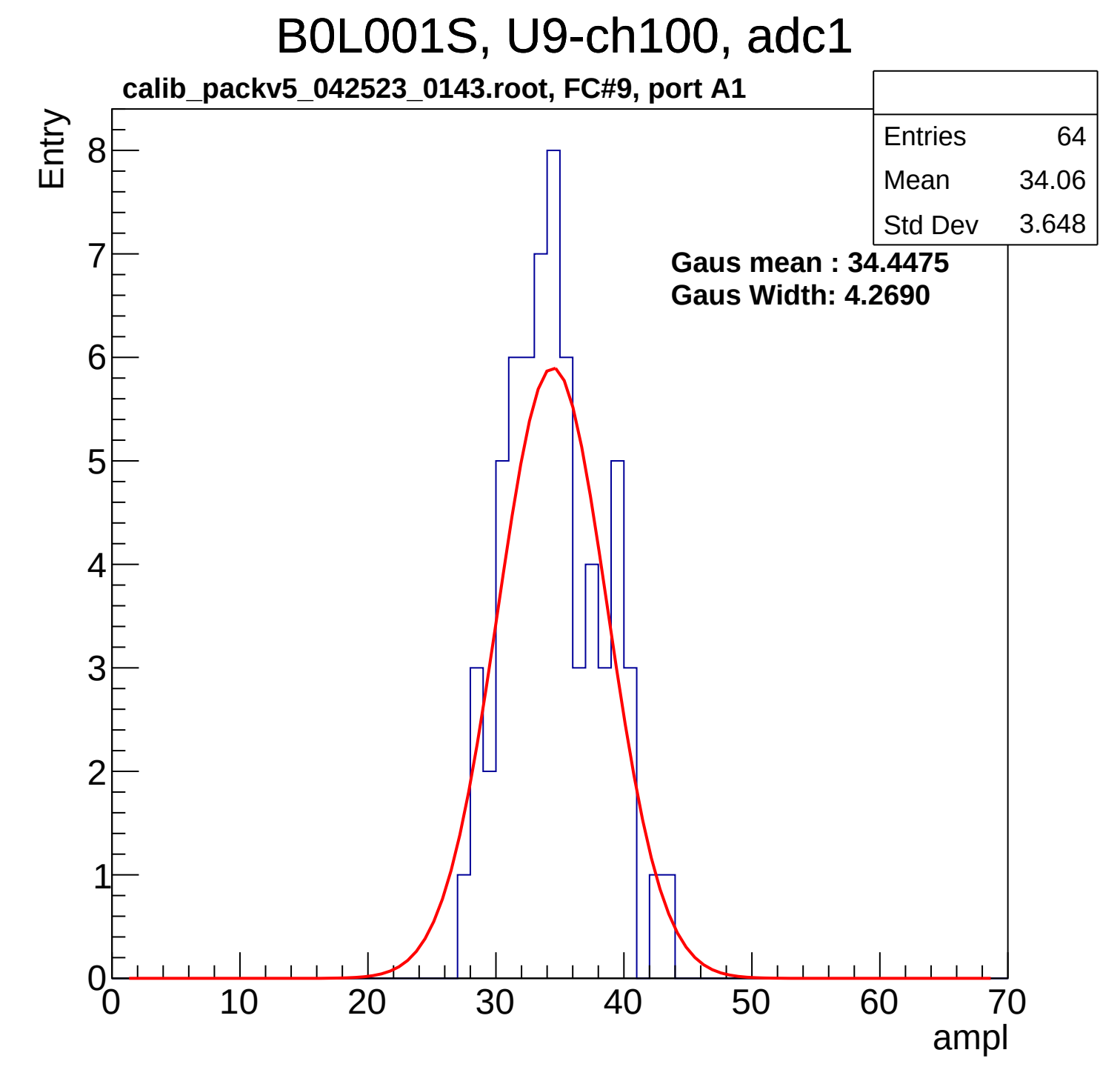
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch100, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 26.56
Std Dev 7.241
Gaus mean : 27.8441
Gaus Width: 4.5257
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch100, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 34.06
Std Dev 3.648
Gaus mean : 34.4475
Gaus Width: 4.2690
Entry
ampl
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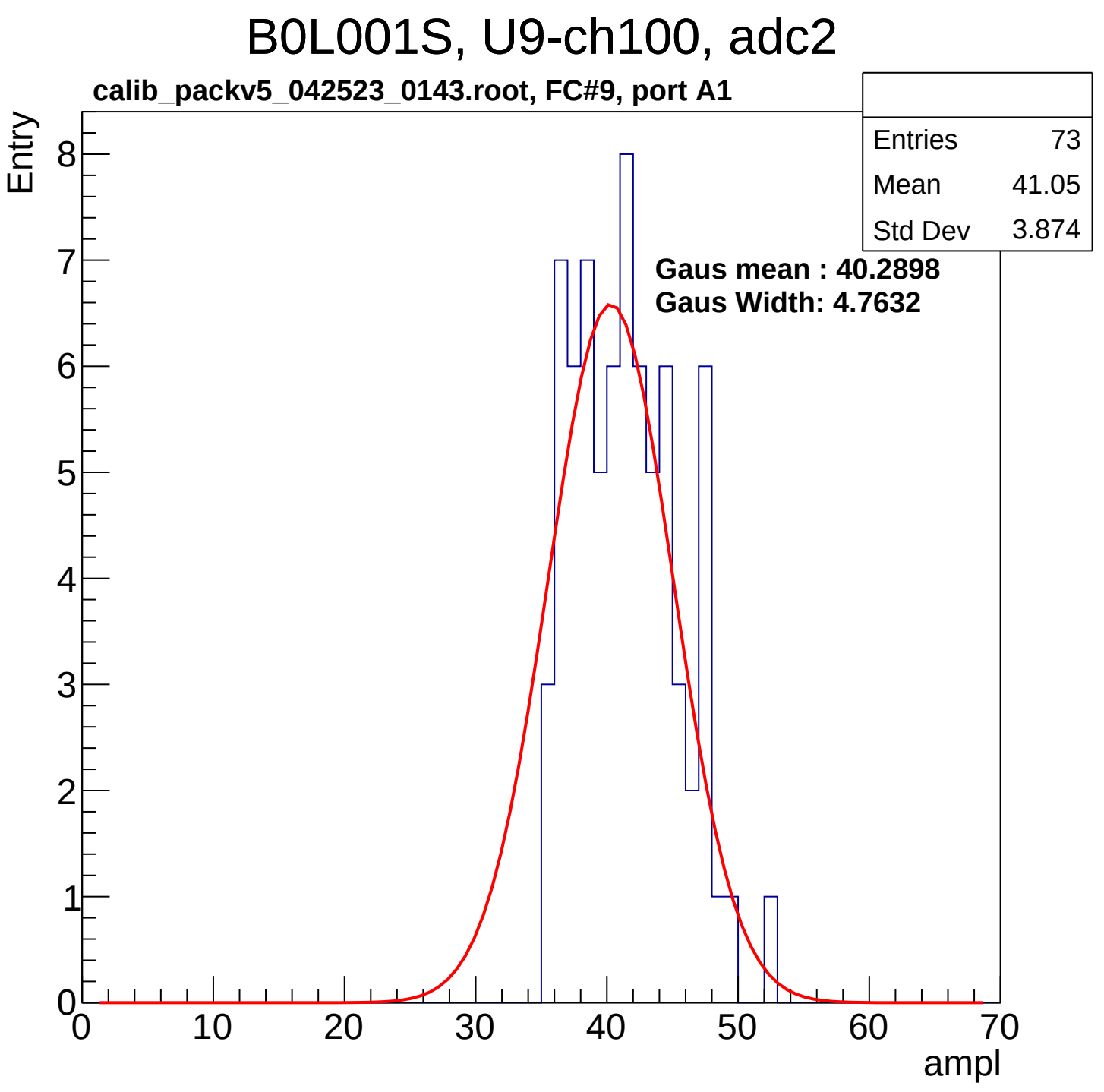
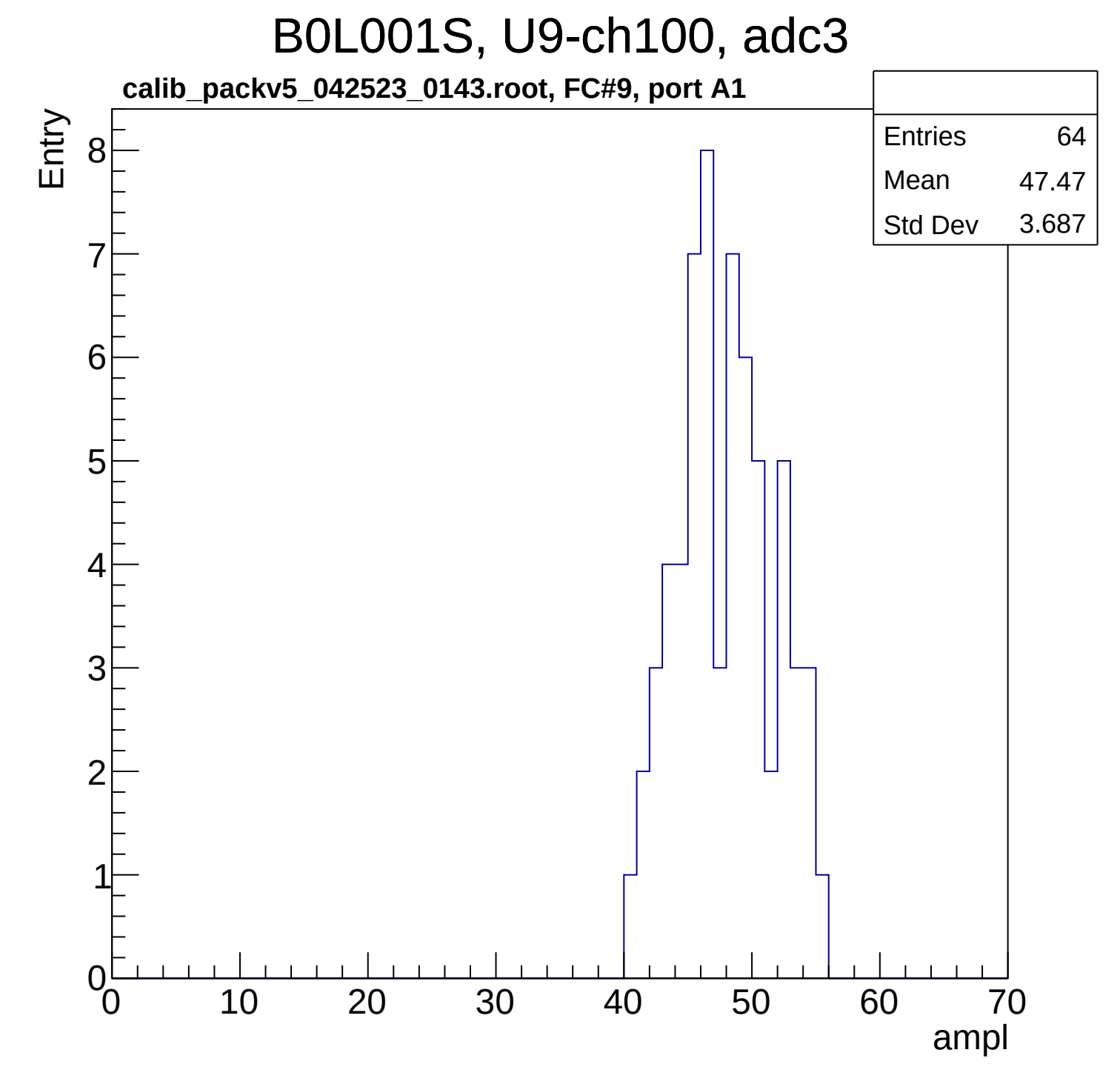
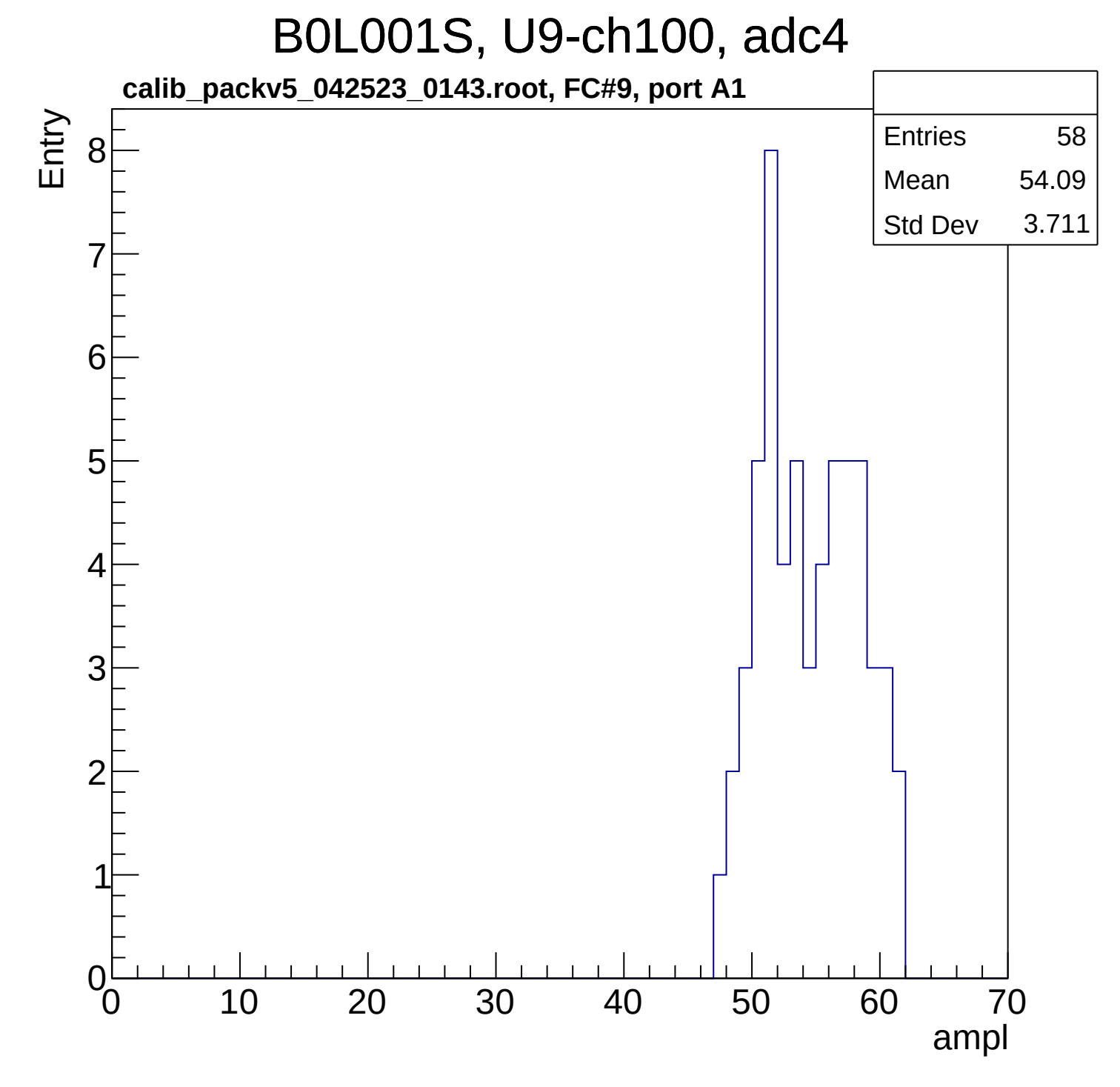
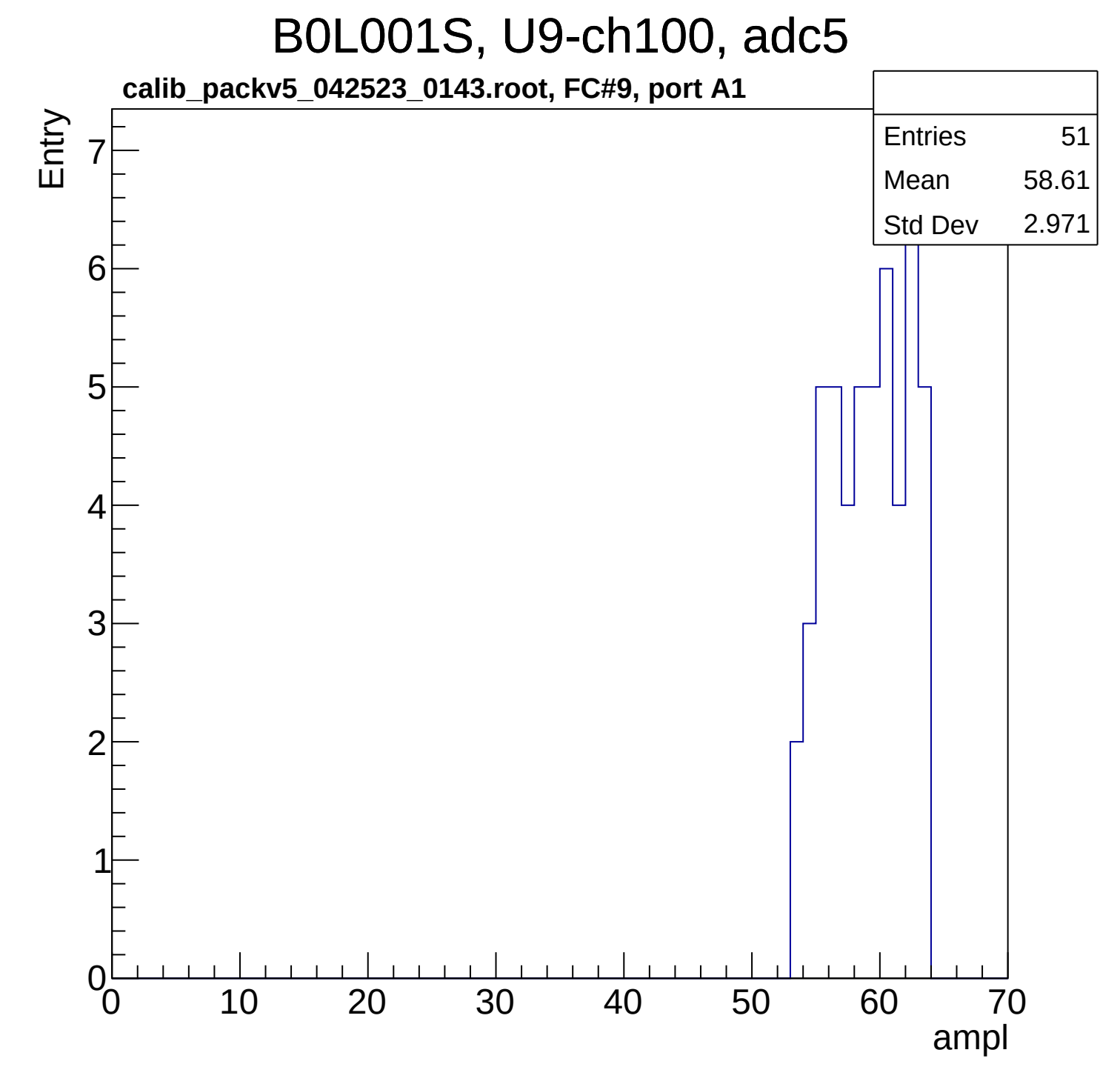
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch100, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 41.05
Std Dev 3.874
Gaus mean : 40.2898
Gaus Width: 4.7632
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch100, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 47.47
Std Dev 3.687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch100, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 54.09
Std Dev 3.711
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch100, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.61
Std Dev 2.971
Entry
ampl
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1
2
3
4
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20
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40
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60
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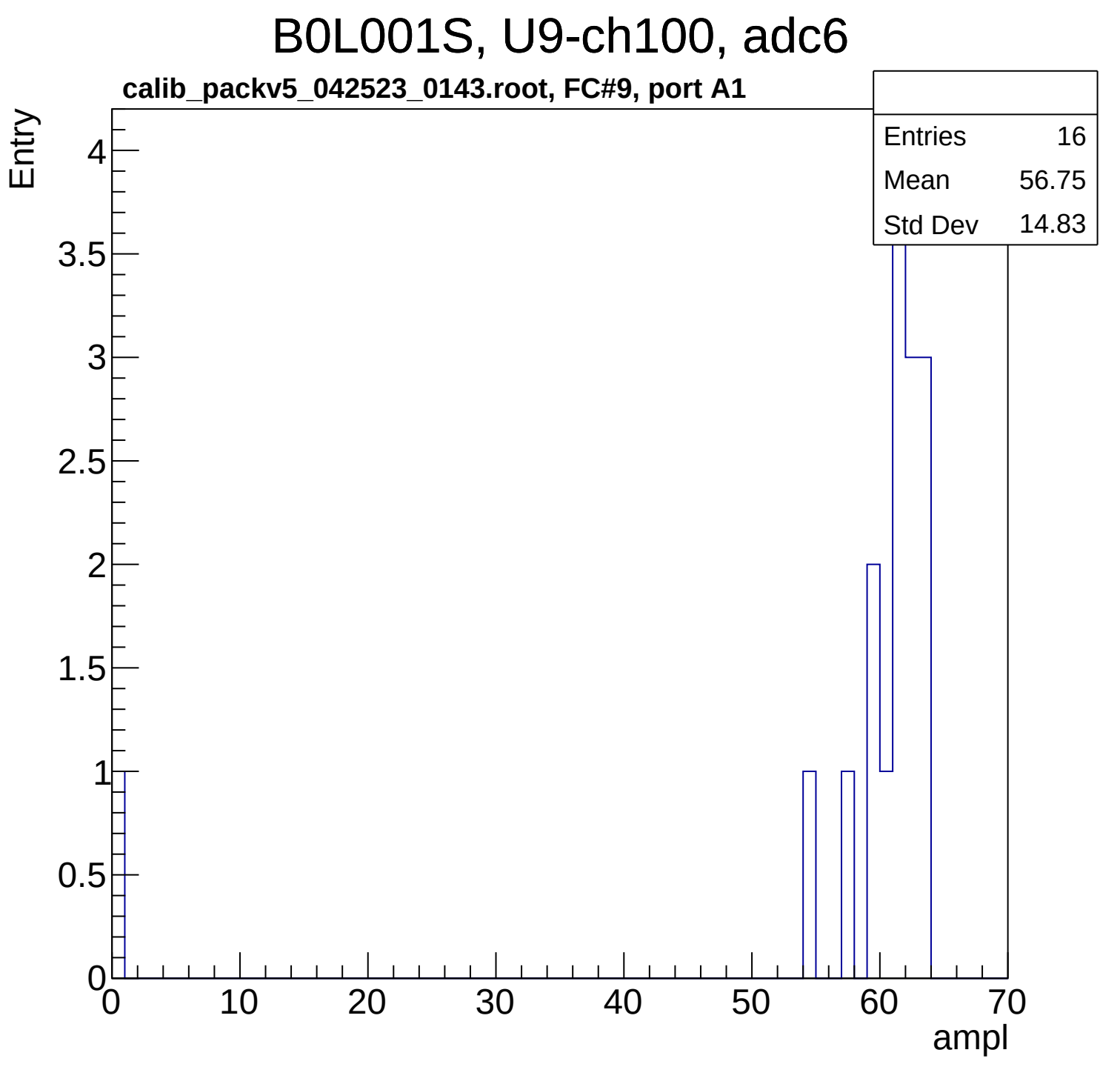
B0L001S, U9-ch100, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 56.75
Std Dev 14.83
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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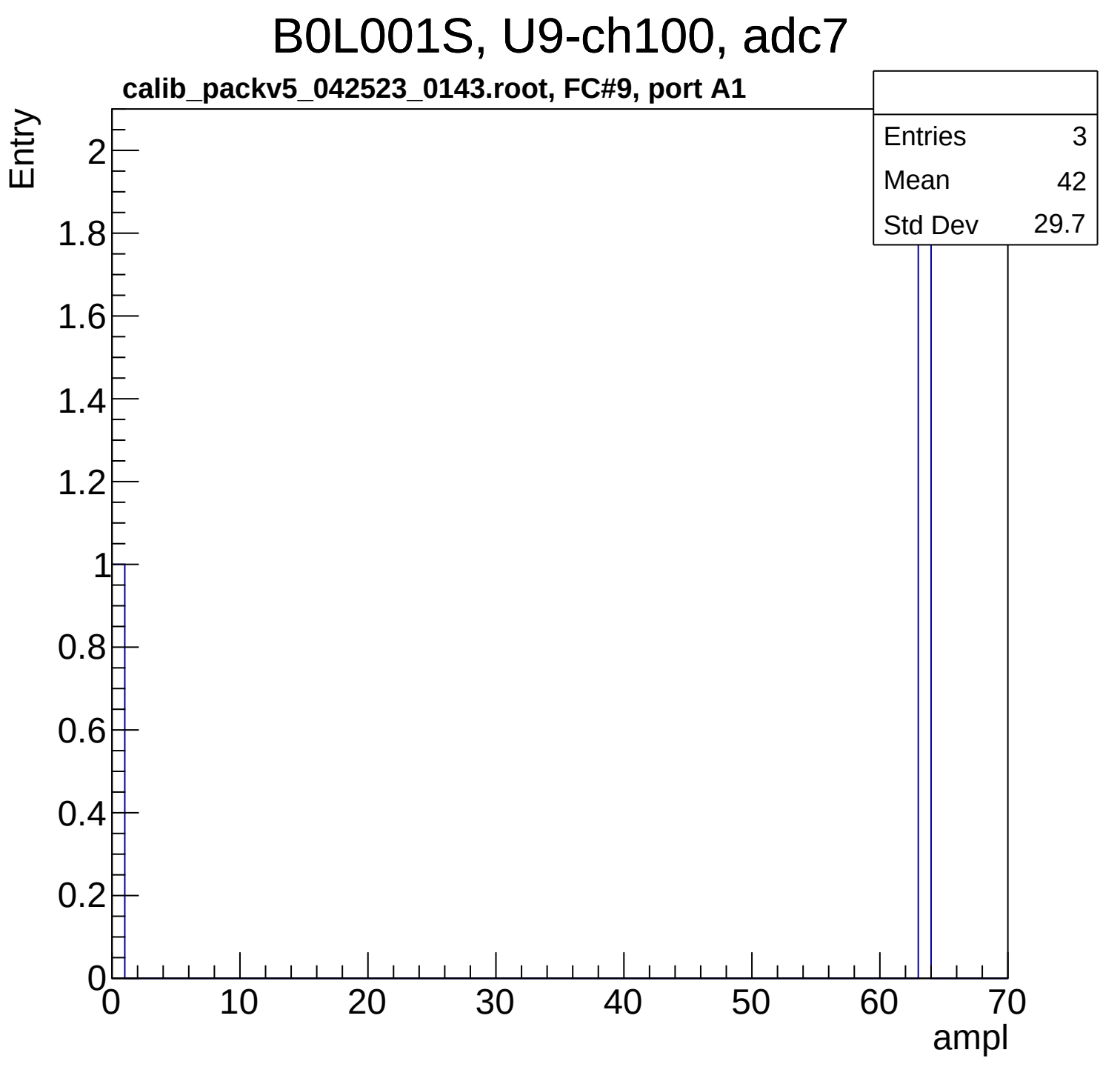
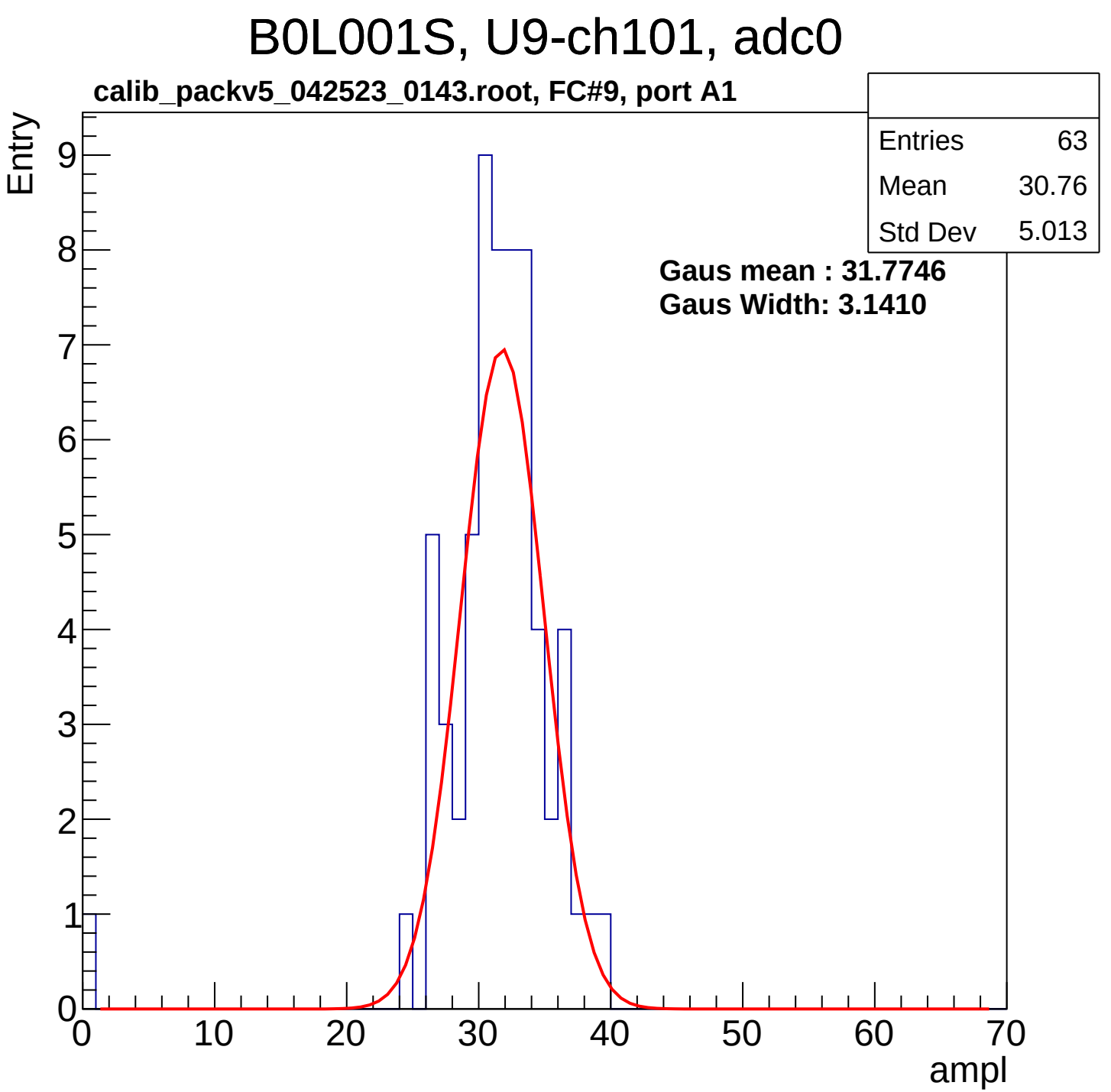
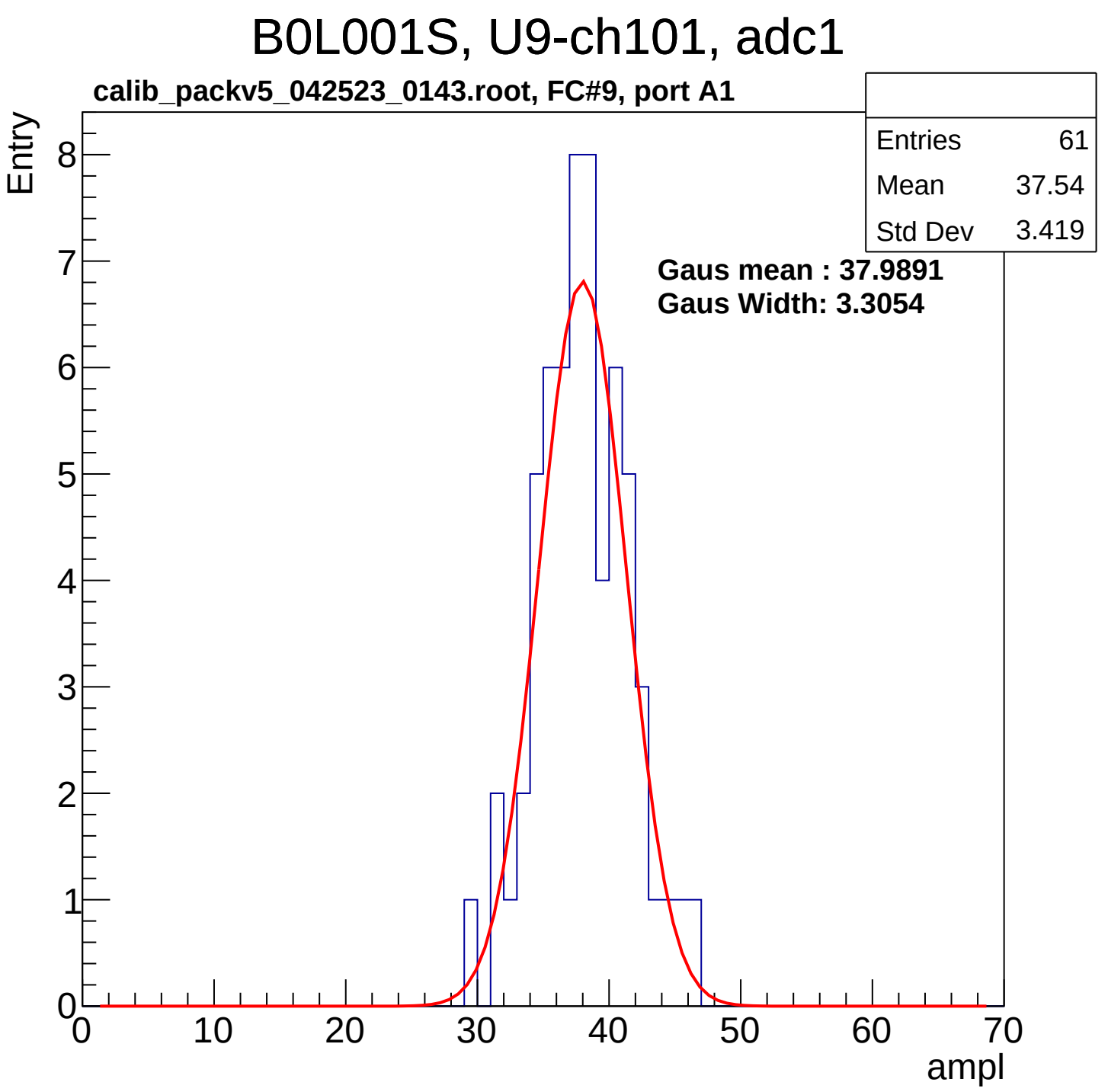
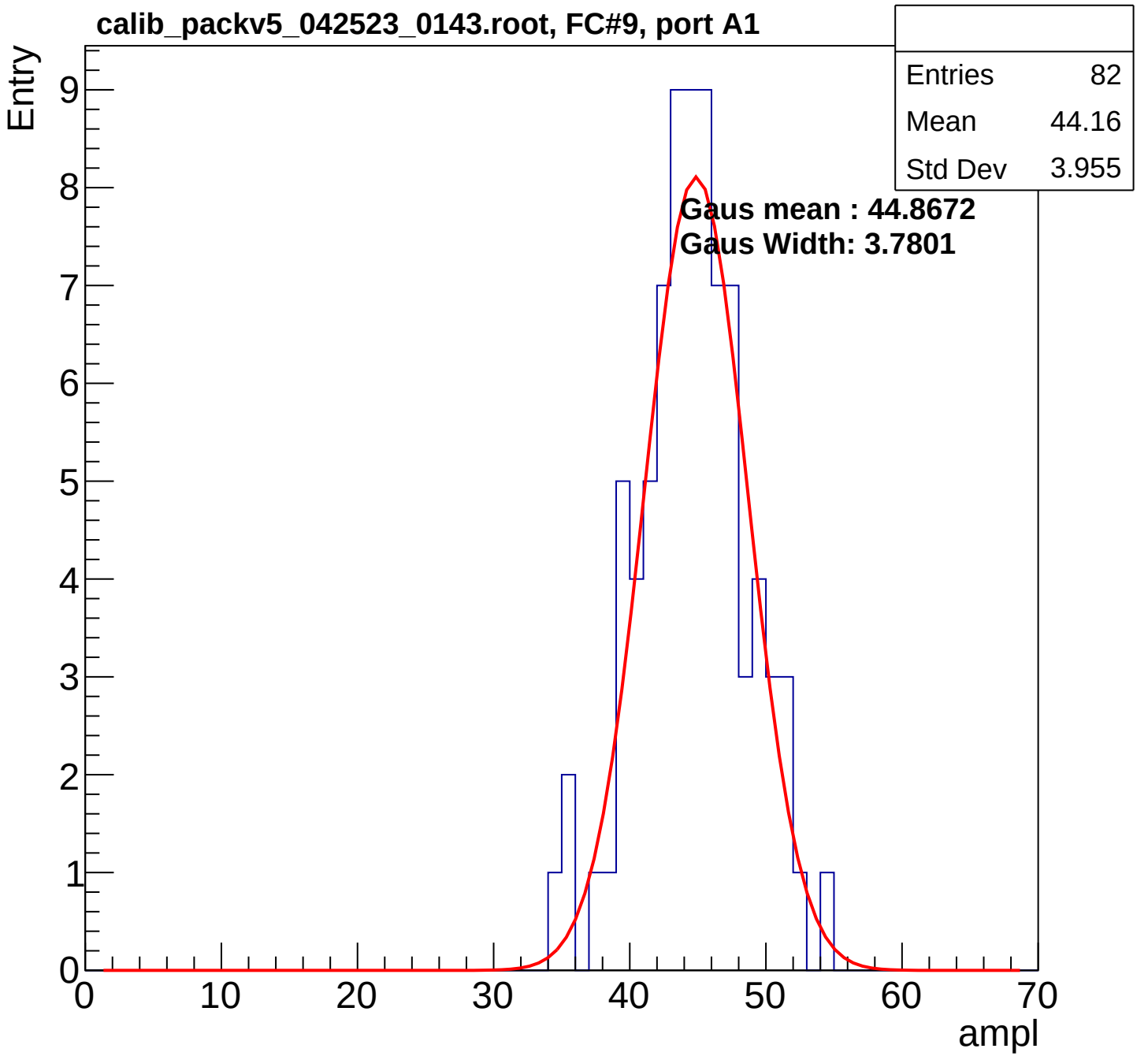

B0L001S, U9-ch100, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch101, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 30.76
Std Dev 5.013
Gaus mean : 31.7746
Gaus Width: 3.1410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch101, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.54
Std Dev 3.419
Gaus mean : 37.9891
Gaus Width: 3.3054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch101, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 44.16
Std Dev 3.955
Gaus mean : 44.8672
Gaus Width: 3.7801
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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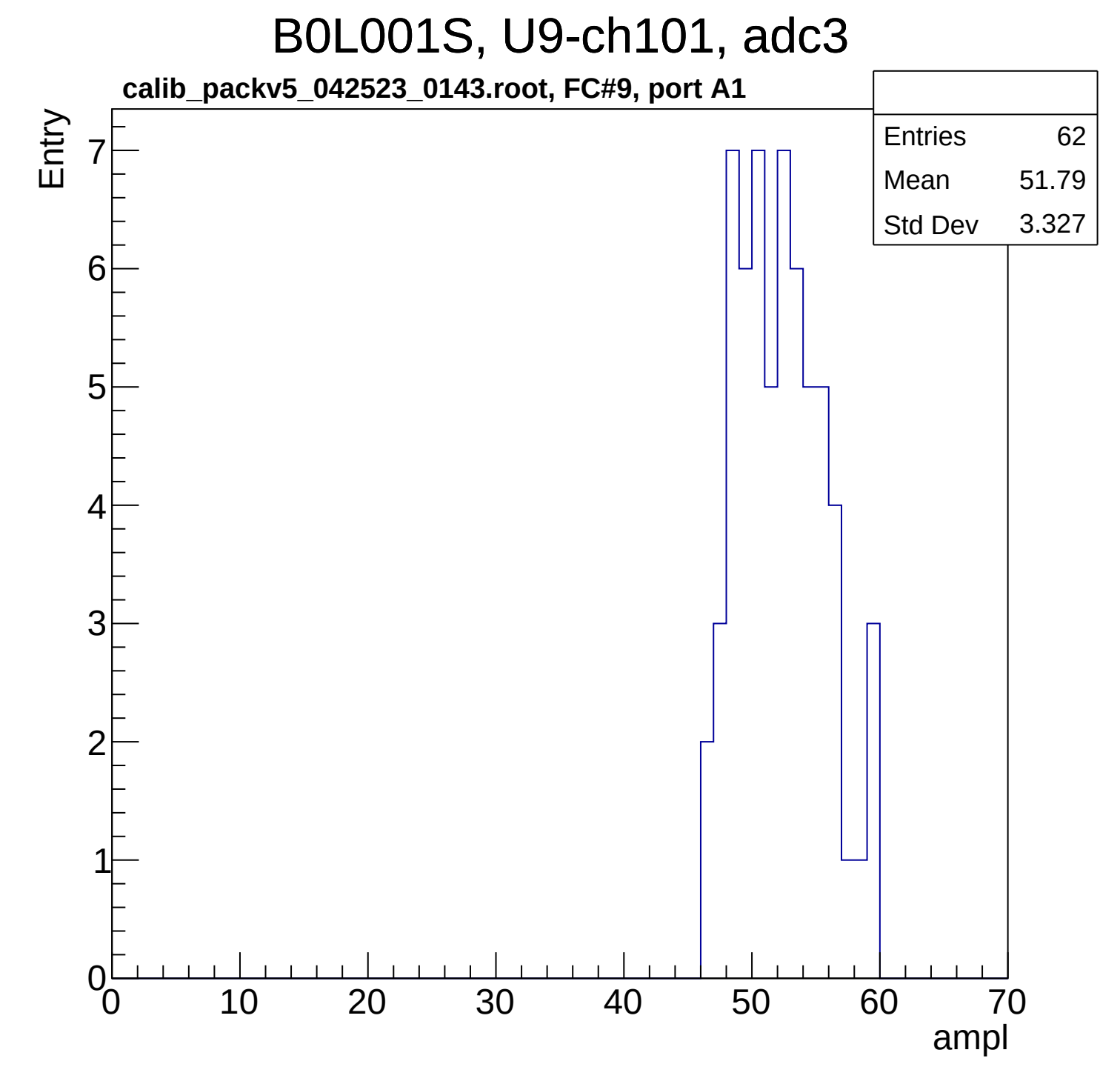
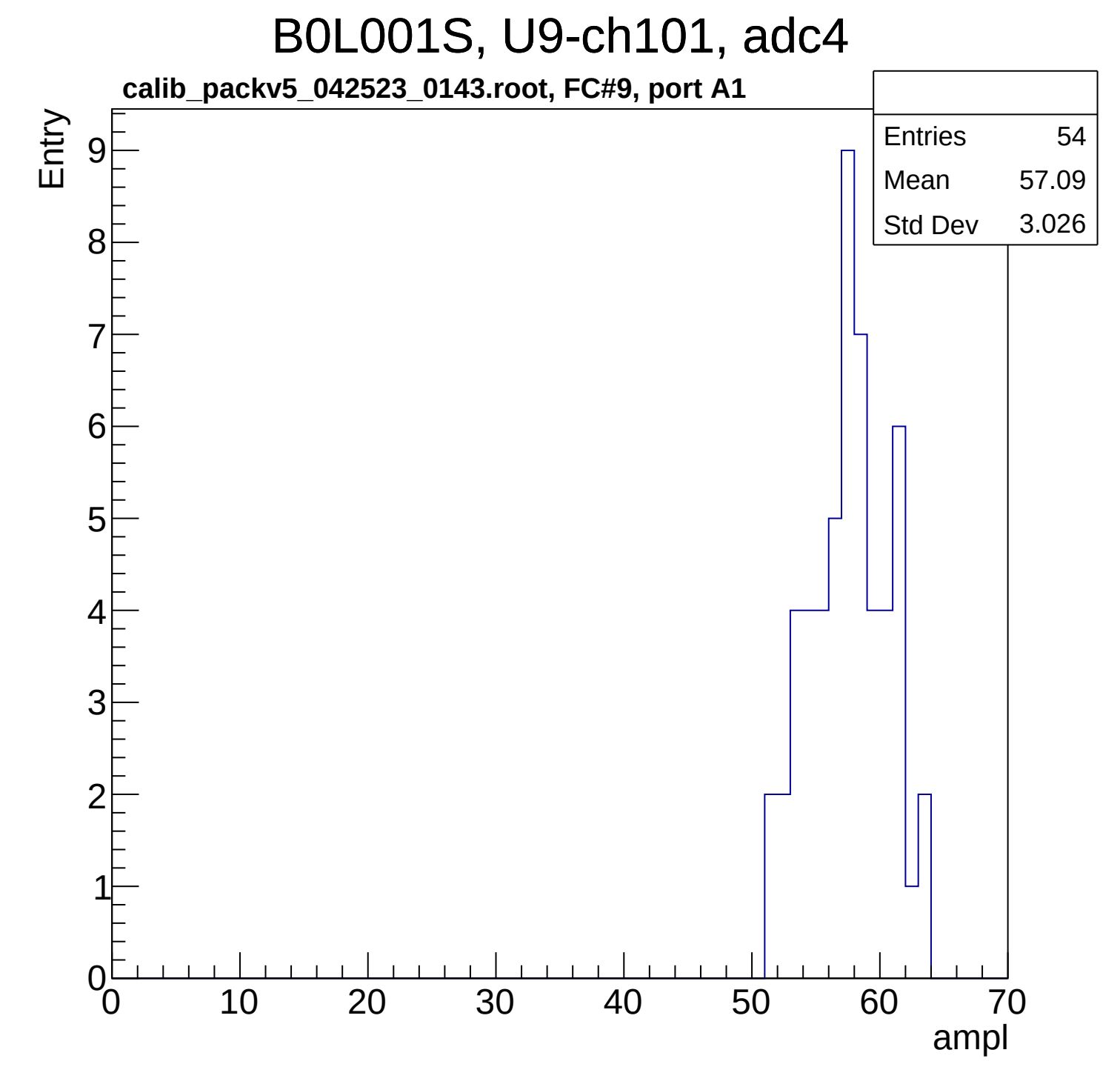
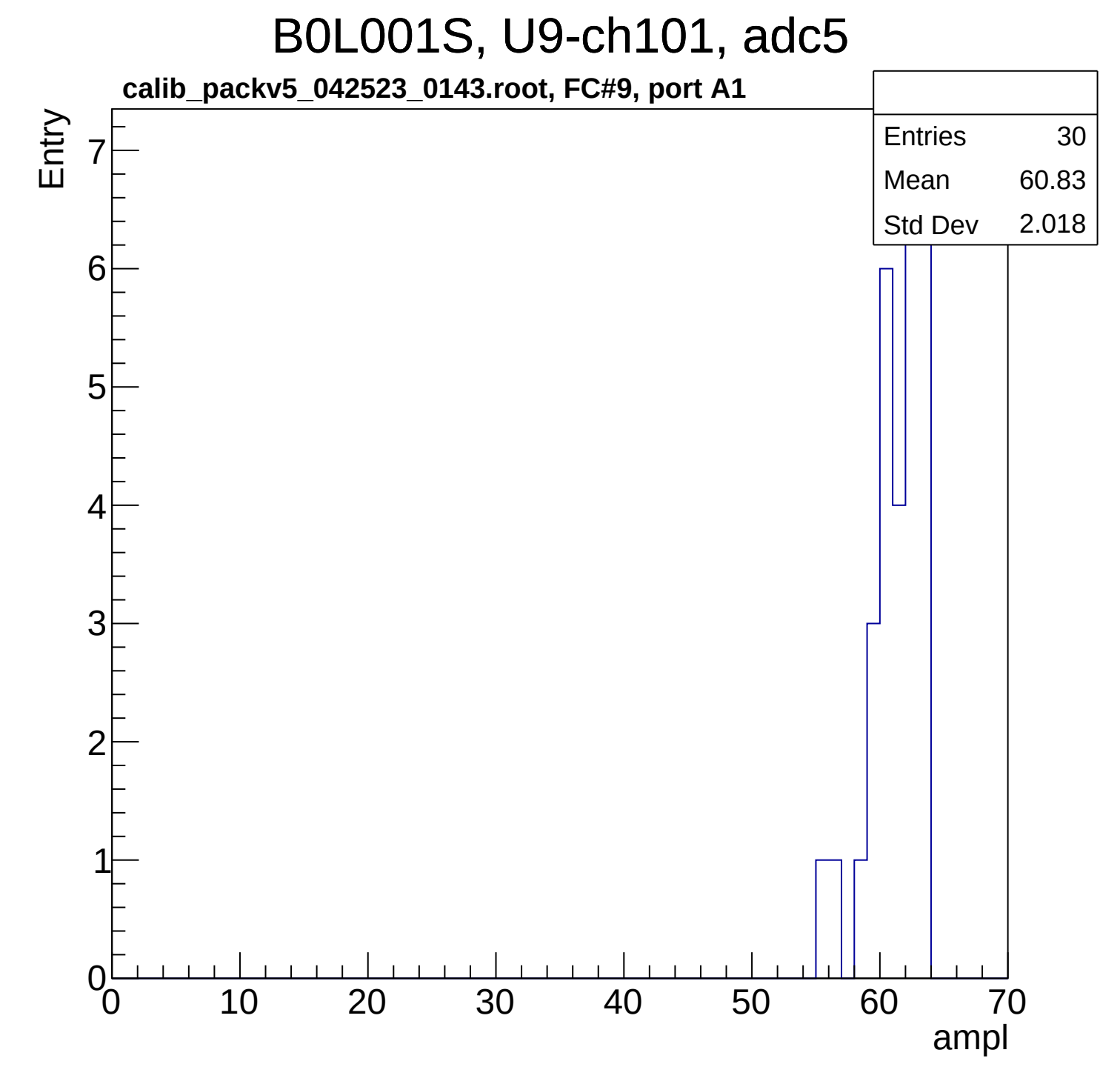
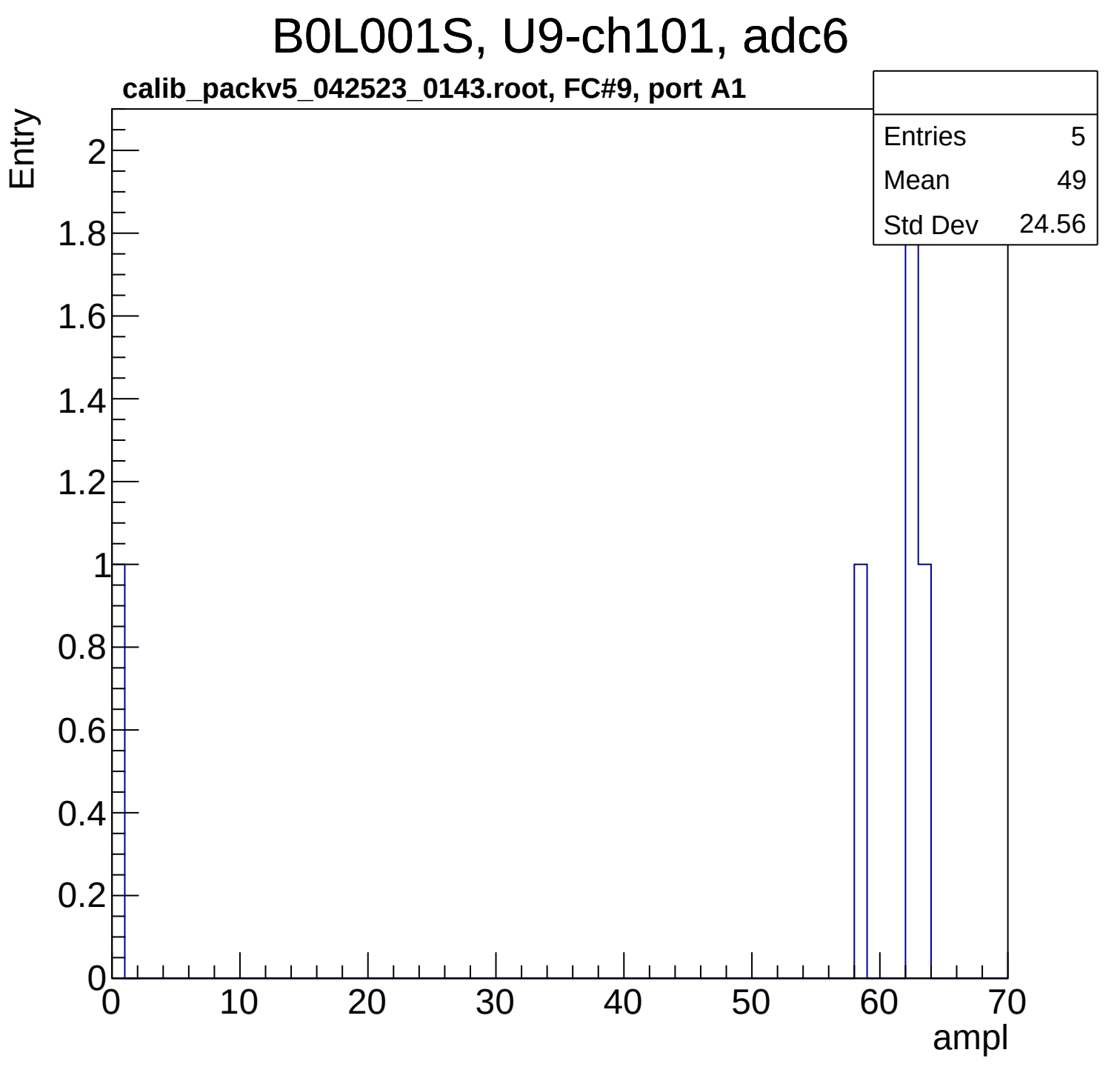
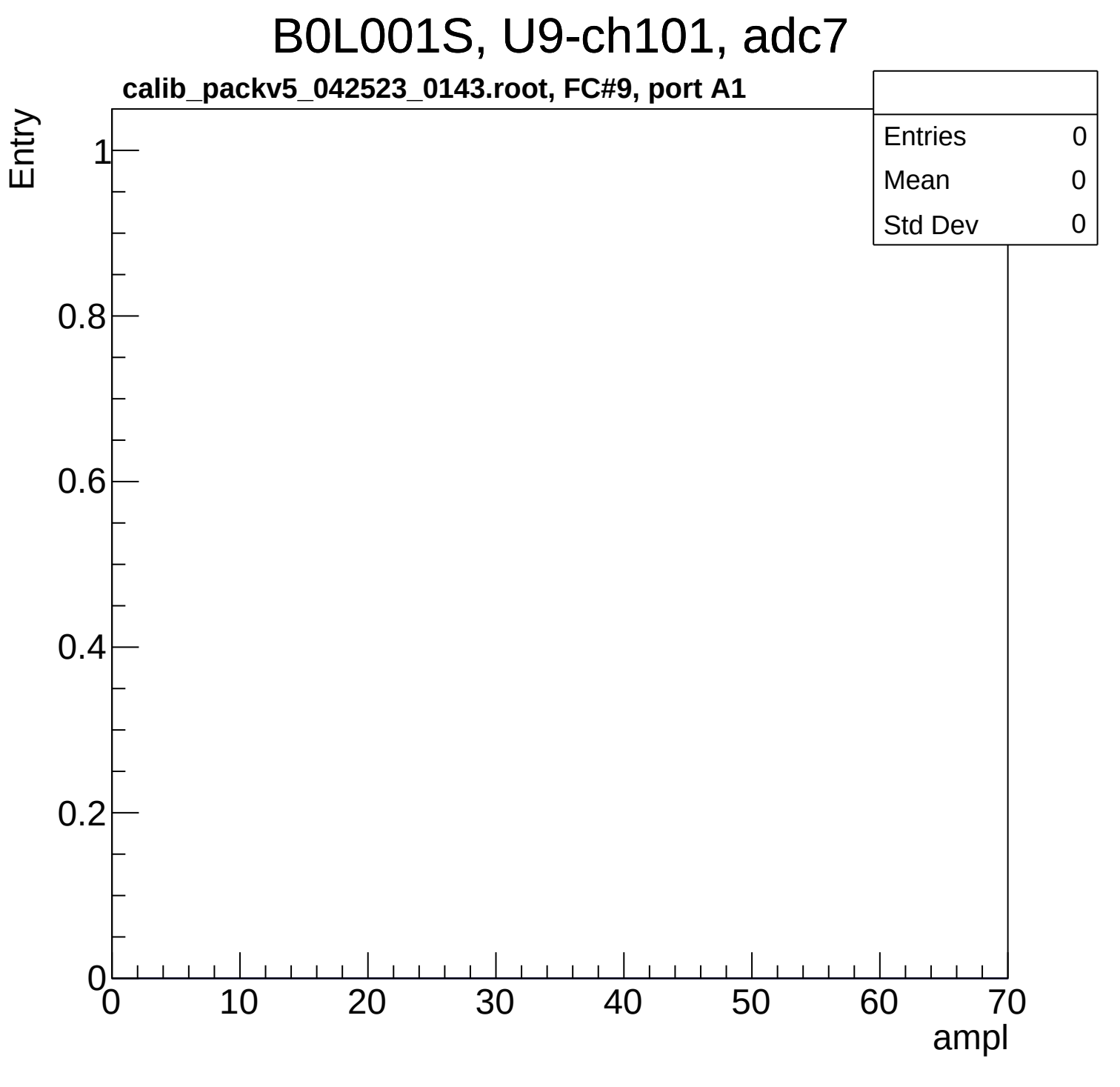
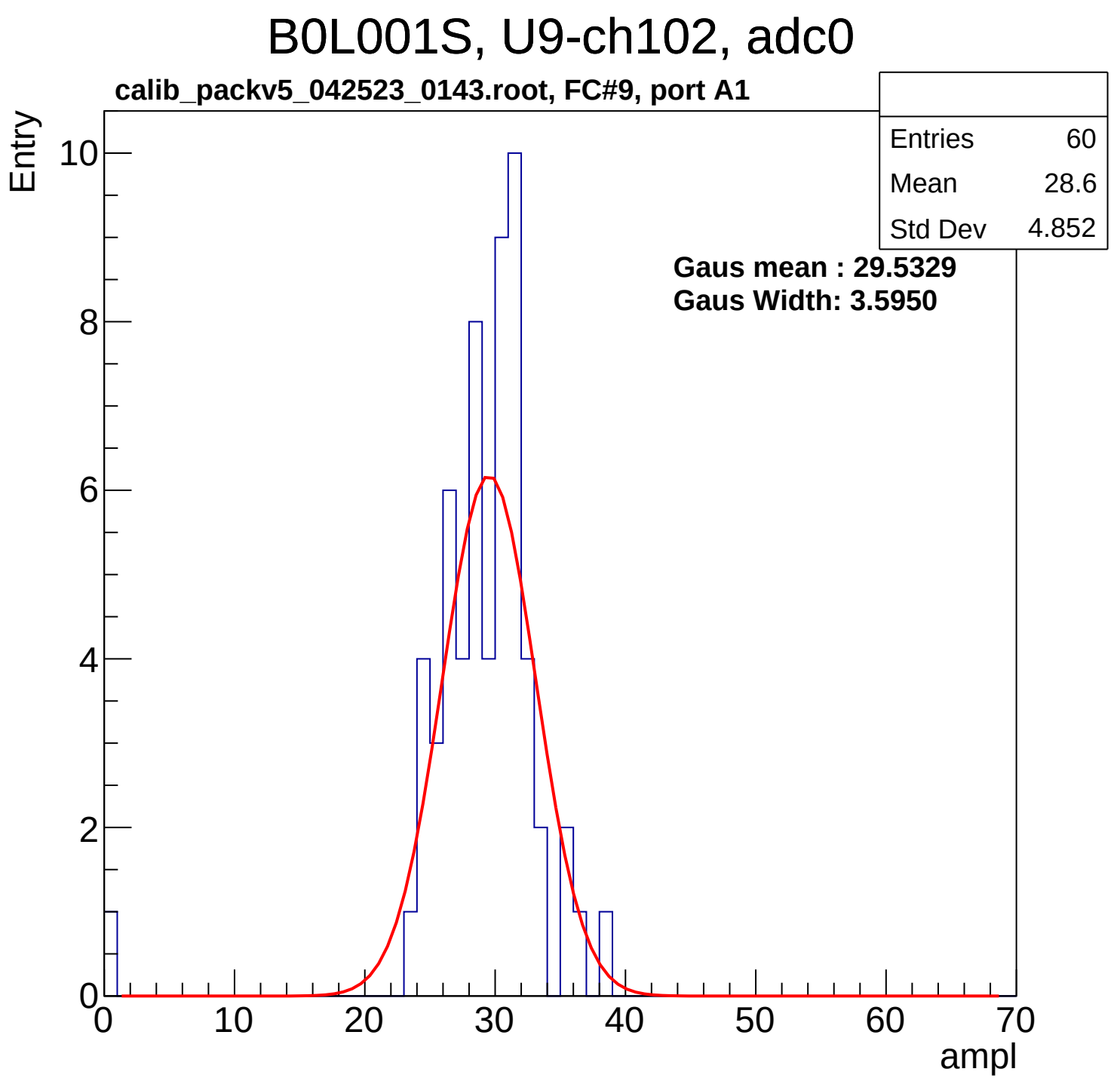

B0L001S, U9-ch101, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.79
Std Dev 3.327
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

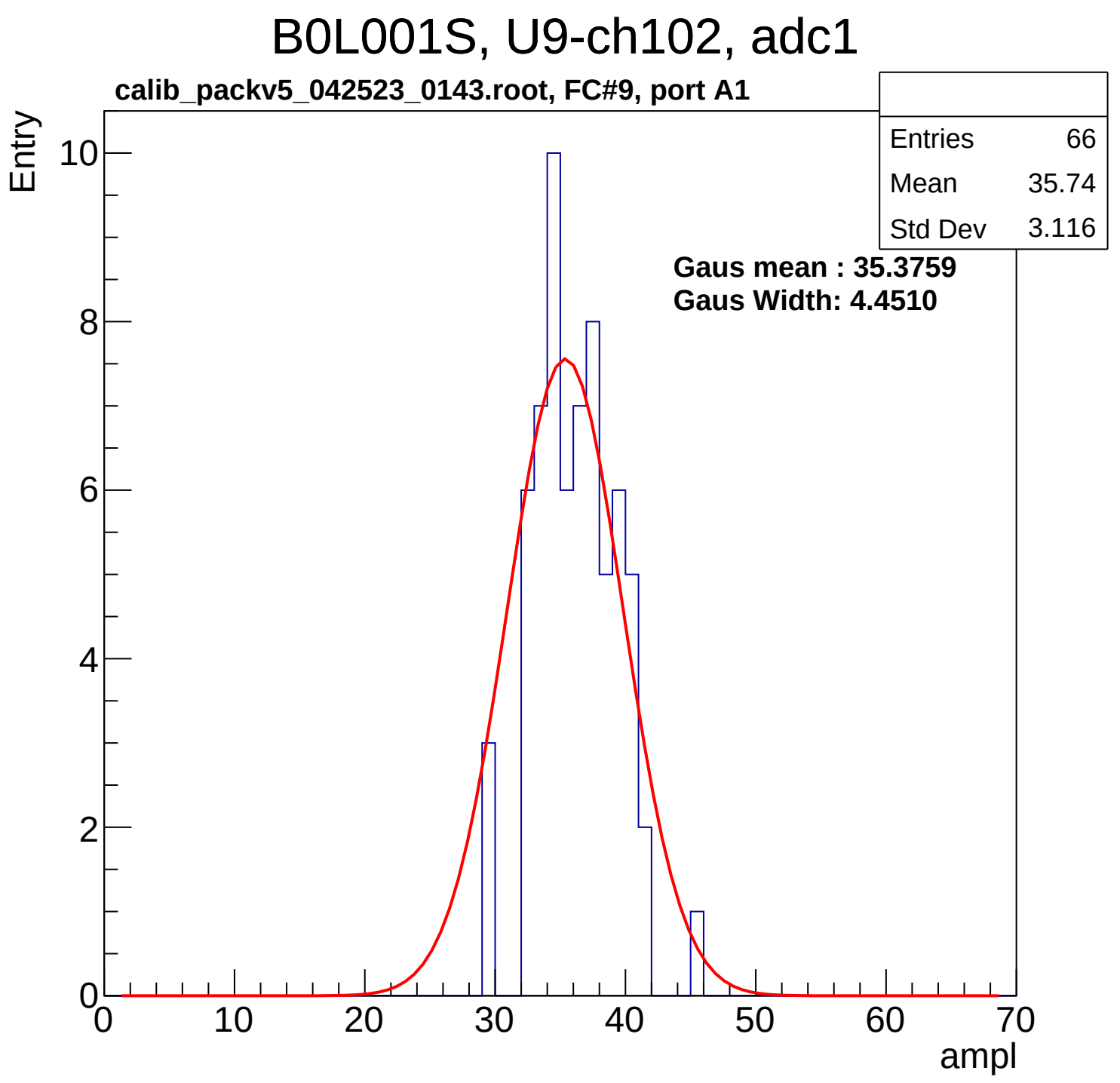

B0L001S, U9-ch101, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.09
Std Dev 3.026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

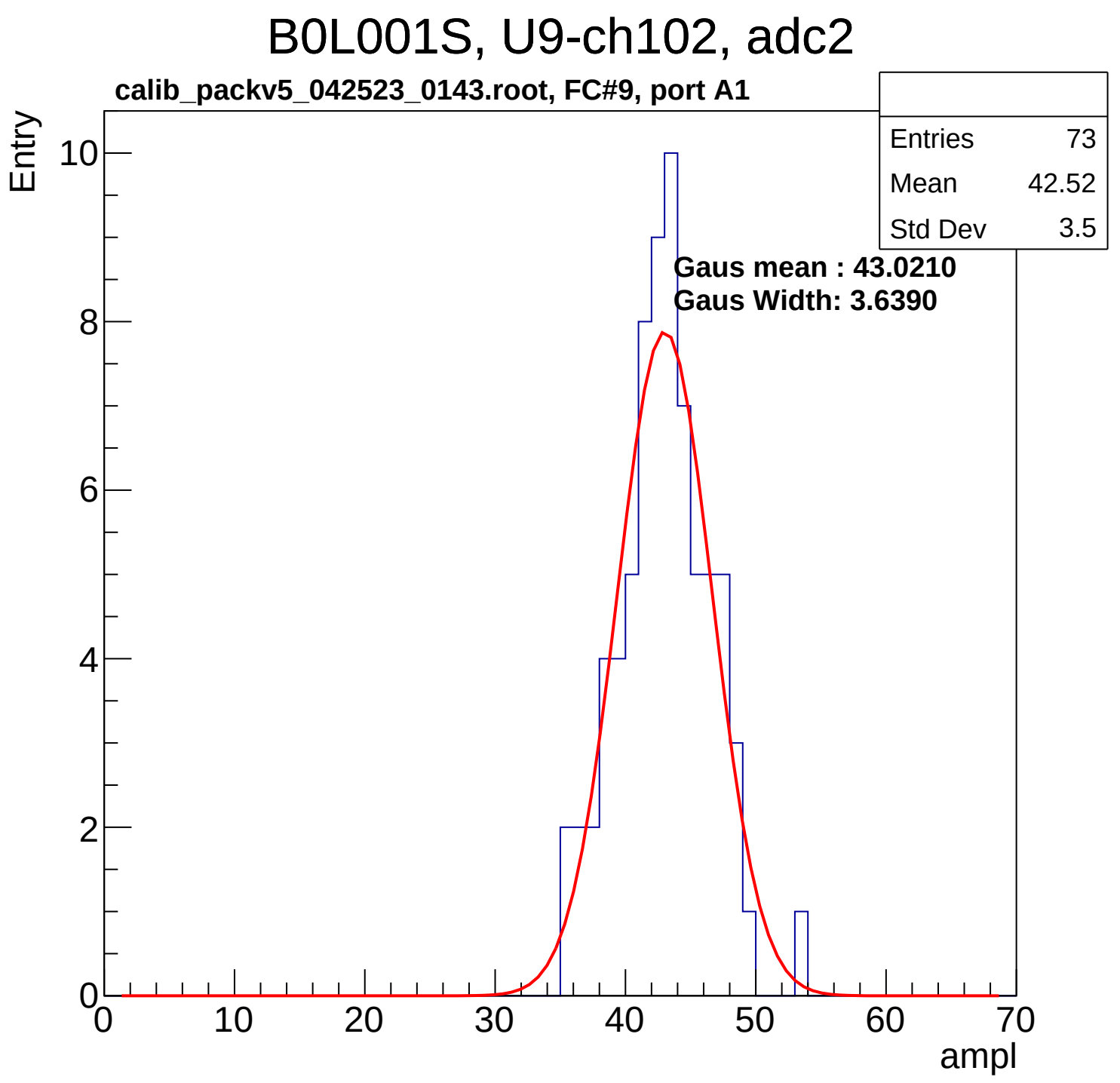

B0L001S, U9-ch101, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 60.83
Std Dev 2.018
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

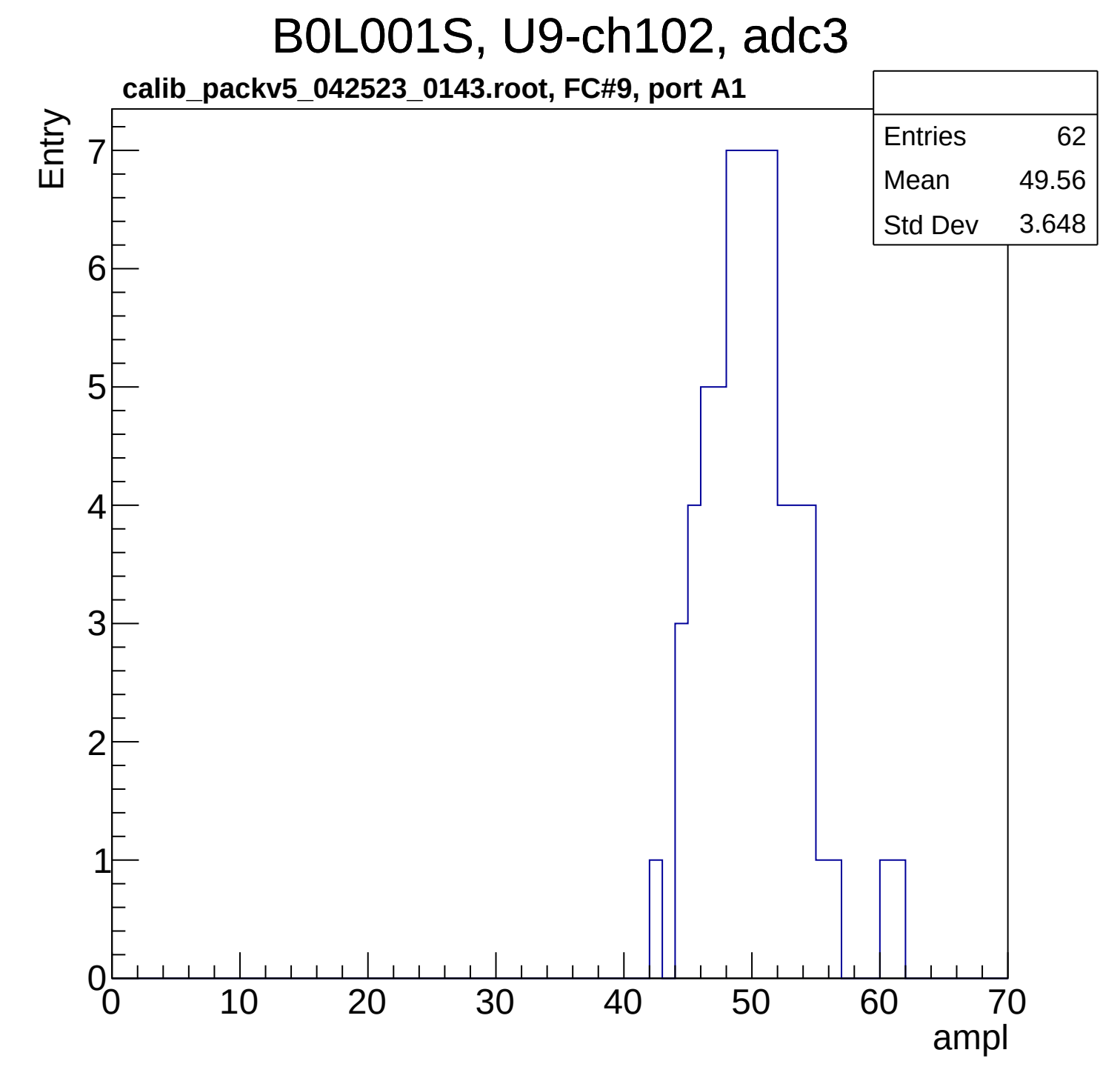

B0L001S, U9-ch101, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49
Std Dev 24.56
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

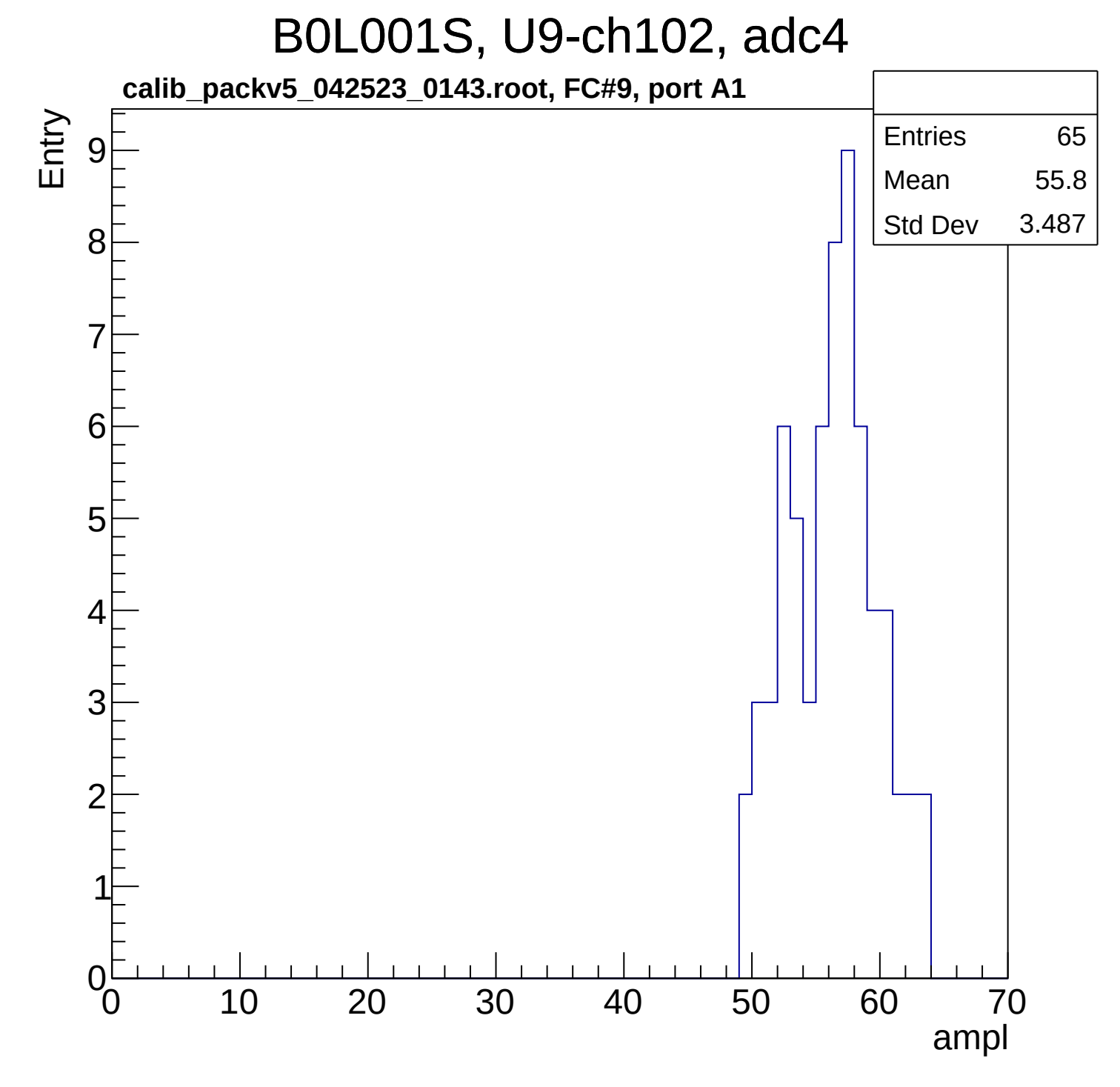

B0L001S, U9-ch101, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

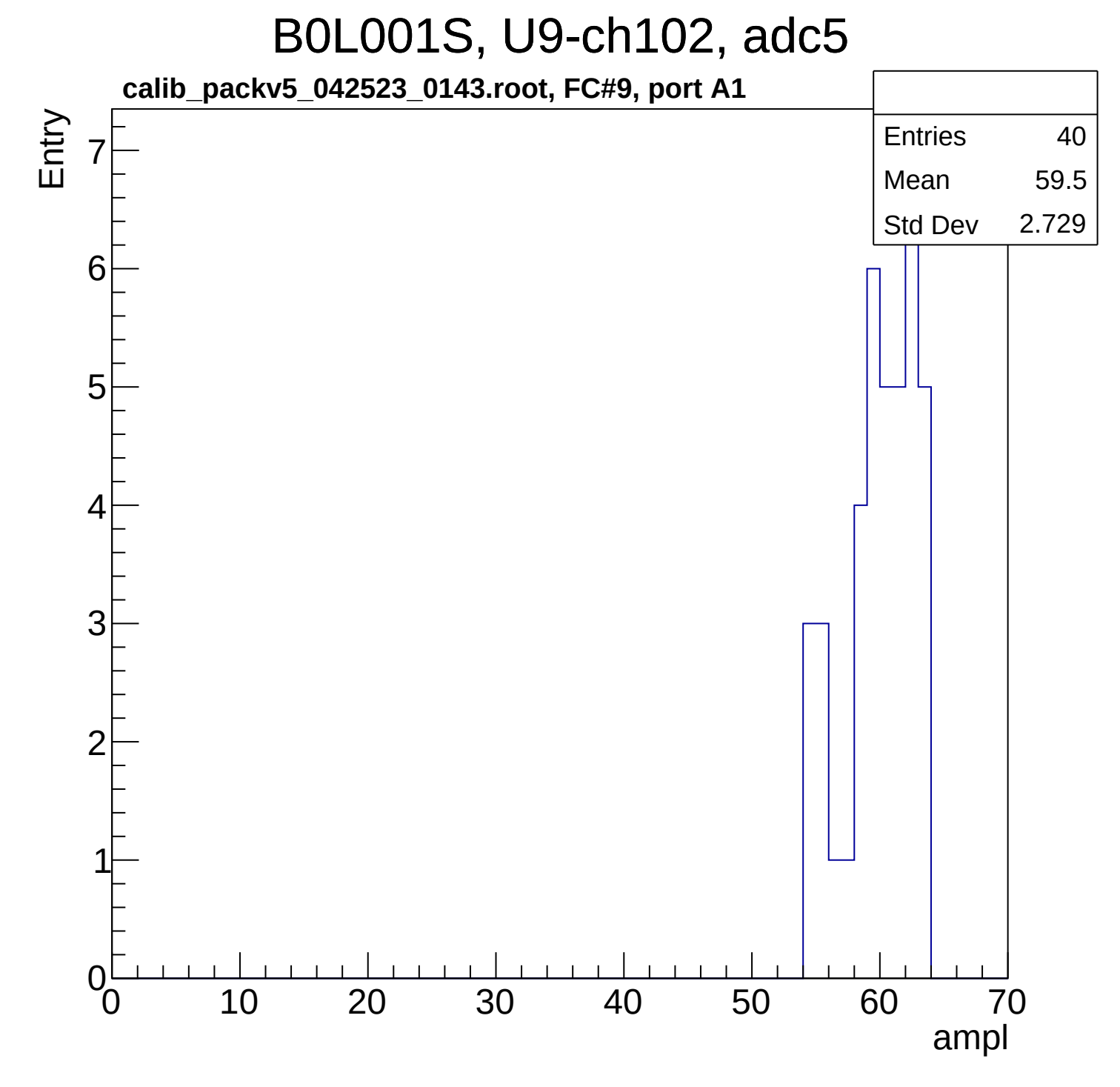

B0L001S, U9-ch102, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 28.6
Std Dev 4.852
Gaus mean : 29.5329
Gaus Width: 3.5950
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

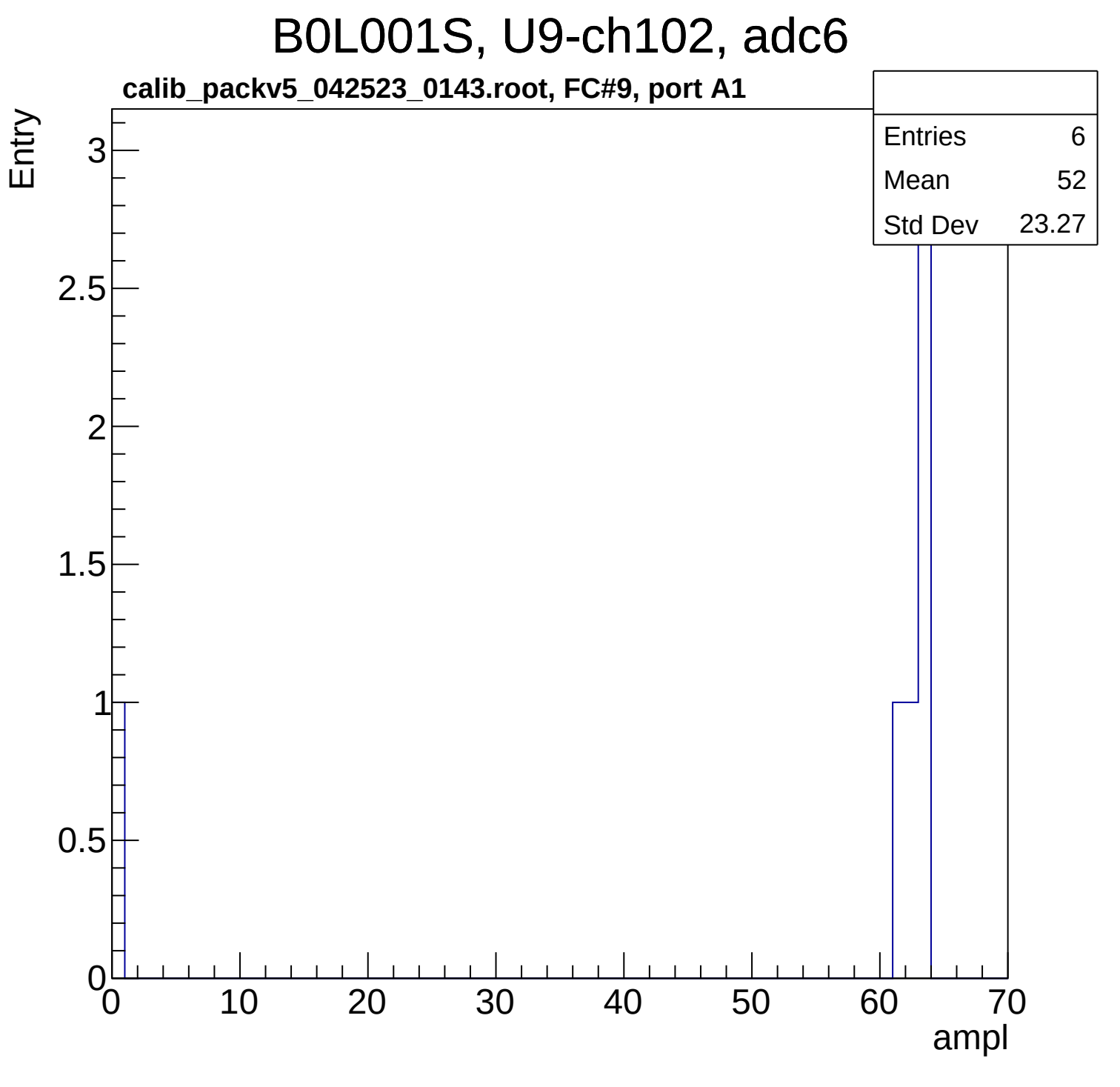

B0L001S, U9-ch102, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 35.74
Std Dev 3.116
Gaus mean : 35.3759
Gaus Width: 4.4510
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch102, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 42.52
Std Dev 3.5
Gaus mean : 43.0210
Gaus Width: 3.6390
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch102, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 49.56
Std Dev 3.648
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch102, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 55.8
Std Dev 3.487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch102, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.5
Std Dev 2.729
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch102, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
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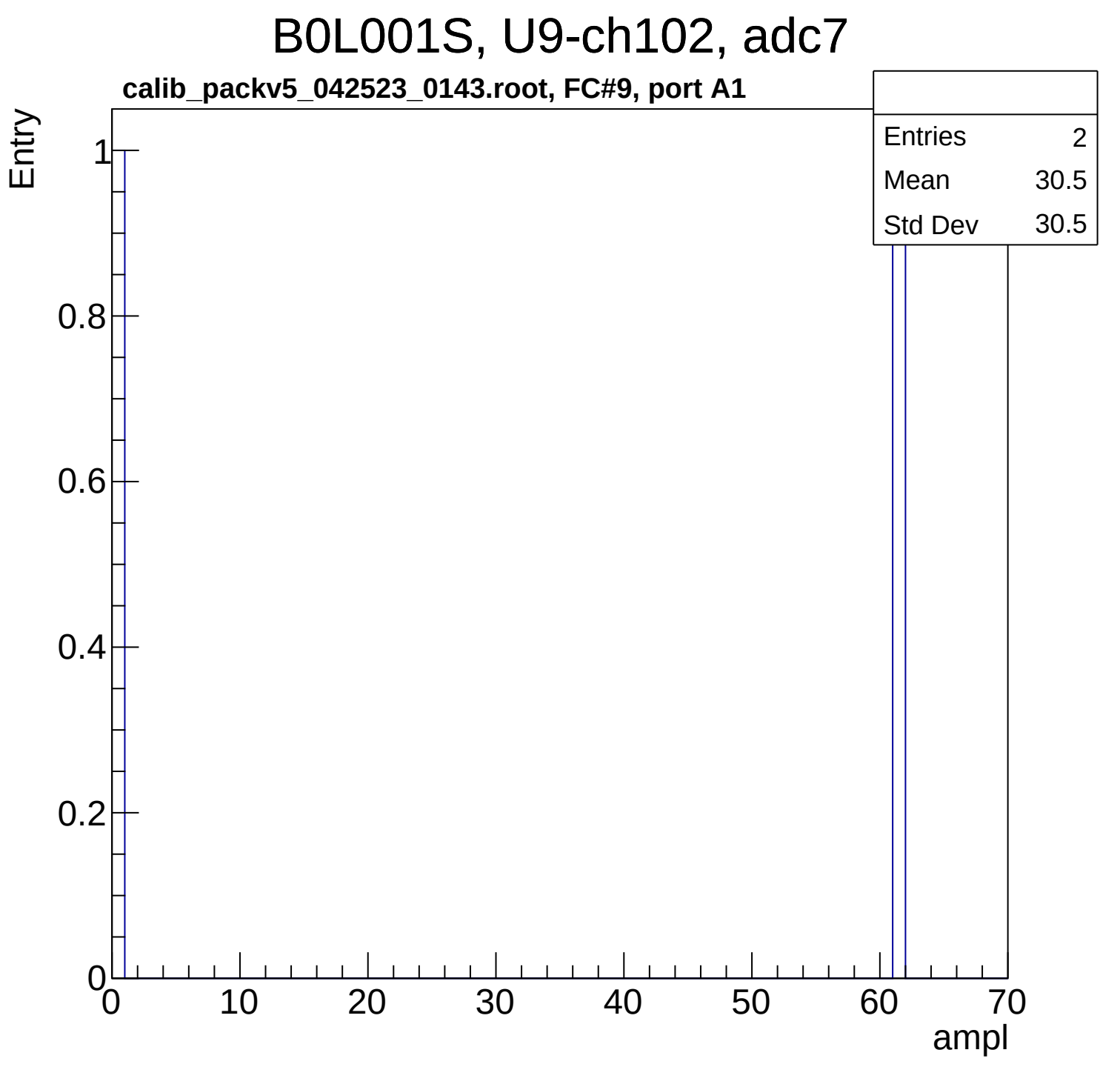
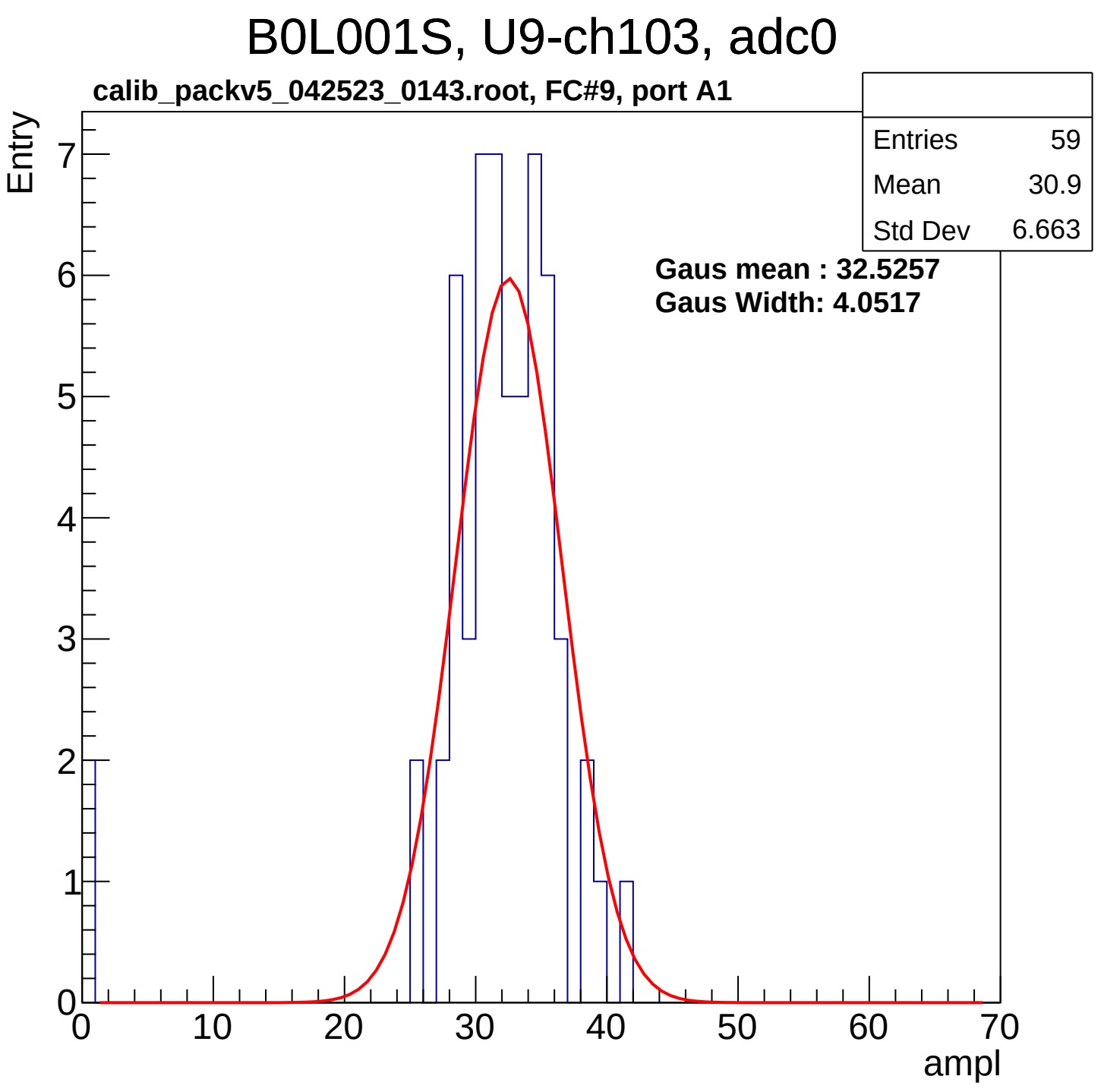
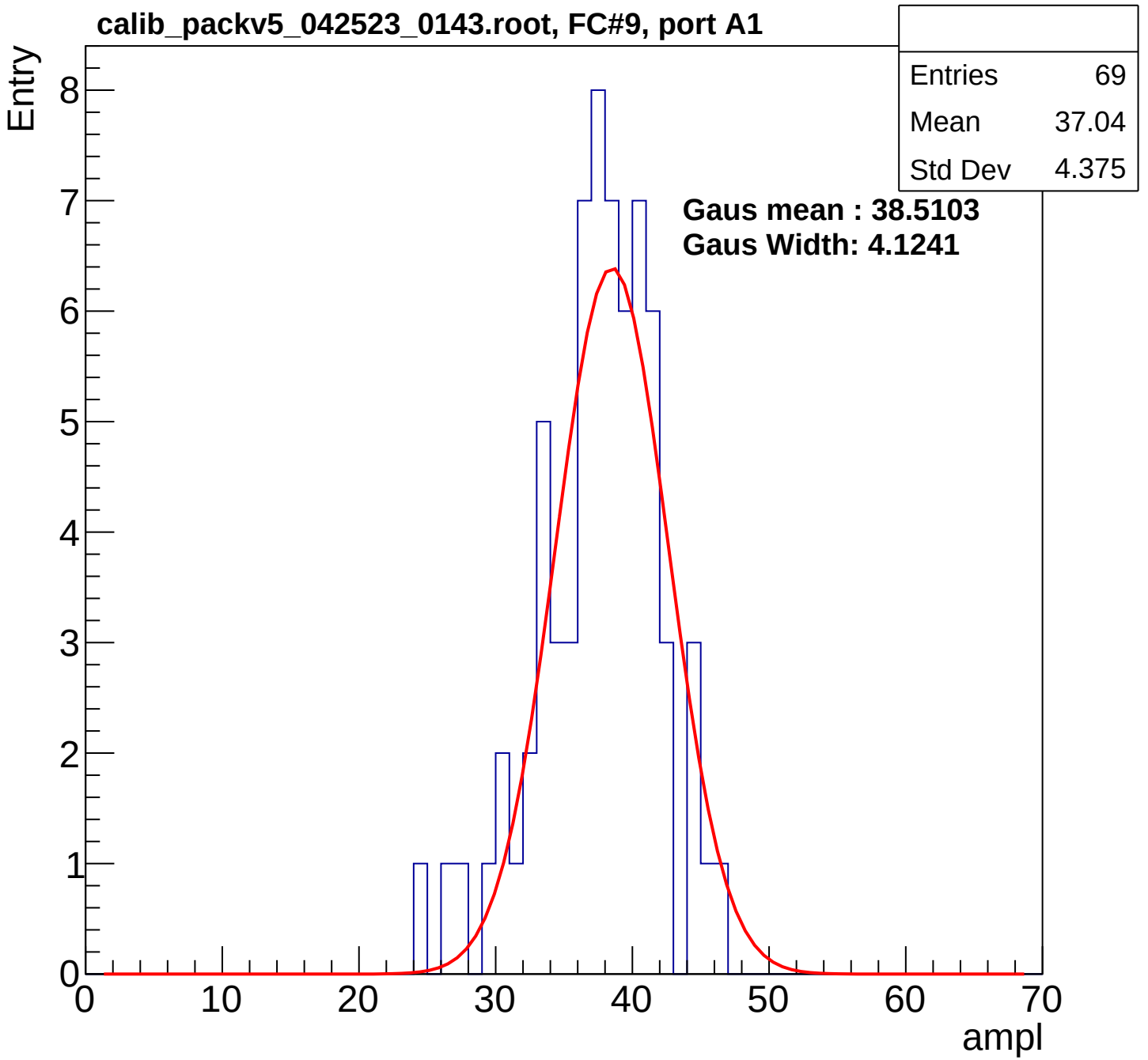
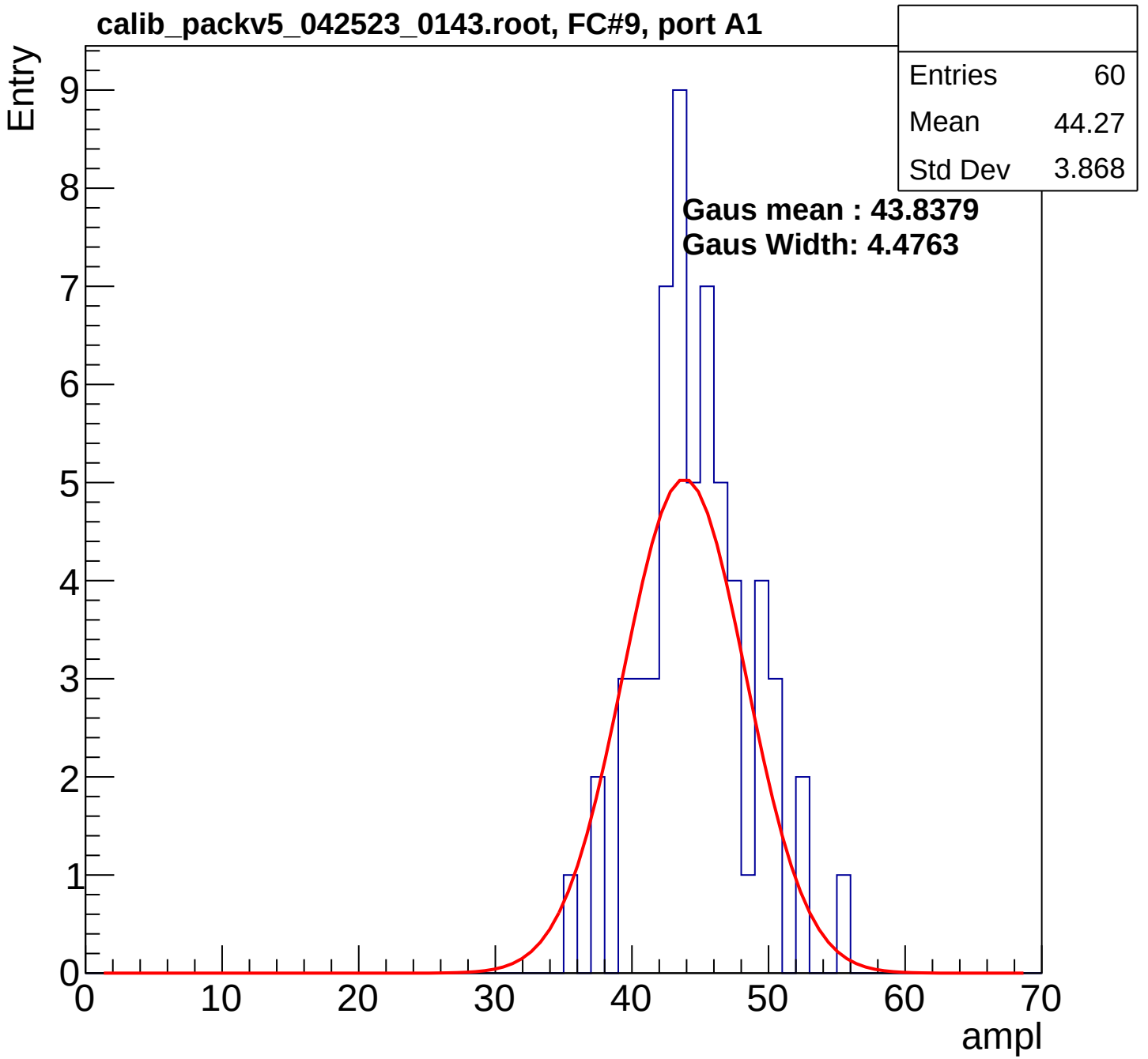
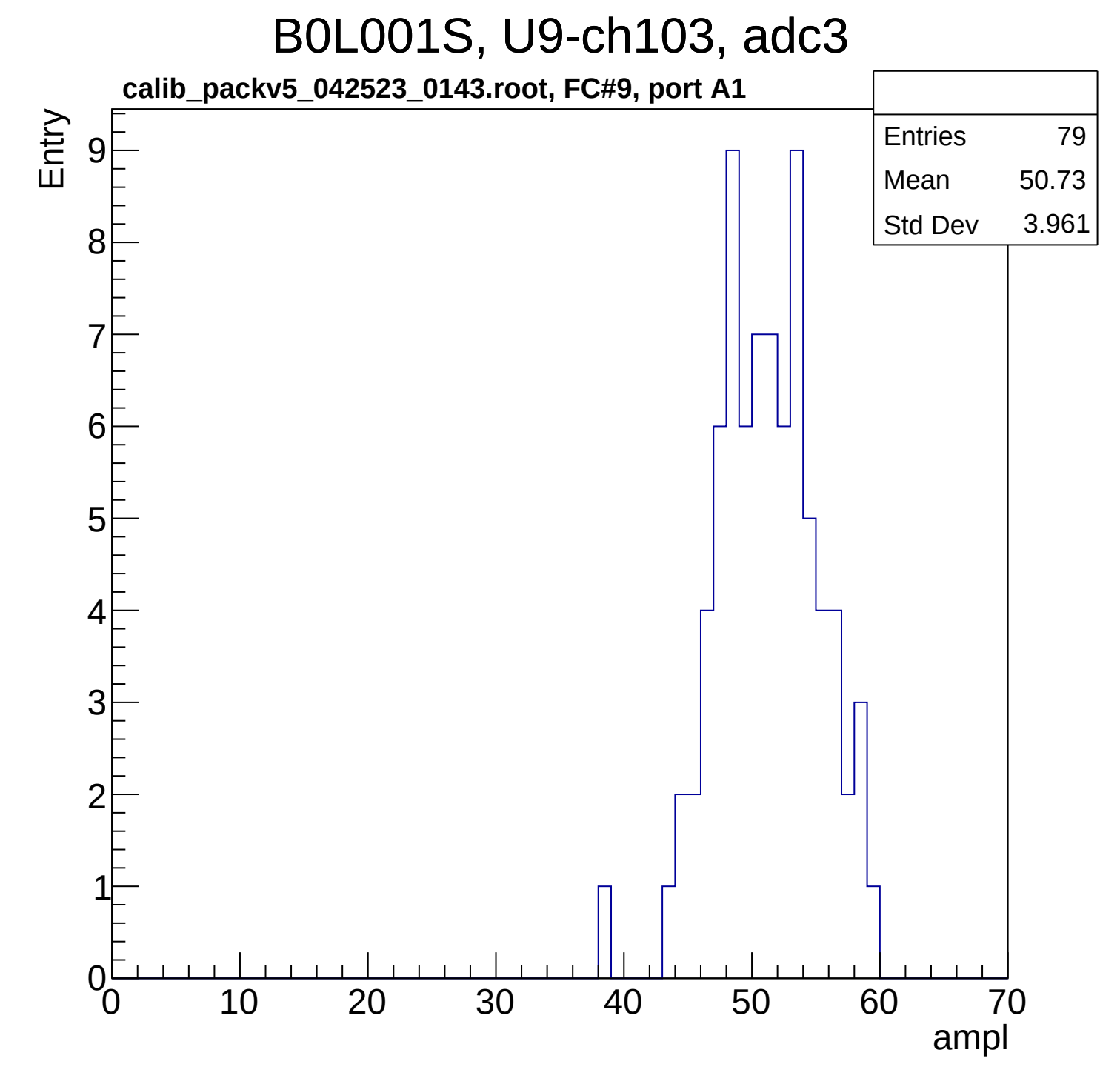
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch102, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch103, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.9
Std Dev 6.663
Gaus mean : 32.5257
Gaus Width: 4.0517
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch103, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.04
Std Dev 4.375
Gaus mean : 38.5103
Gaus Width: 4.1241
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch103, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 44.27
Std Dev 3.868
Gaus mean : 43.8379
Gaus Width: 4.4763
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch103, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 50.73
Std Dev 3.961
Entry
ampl
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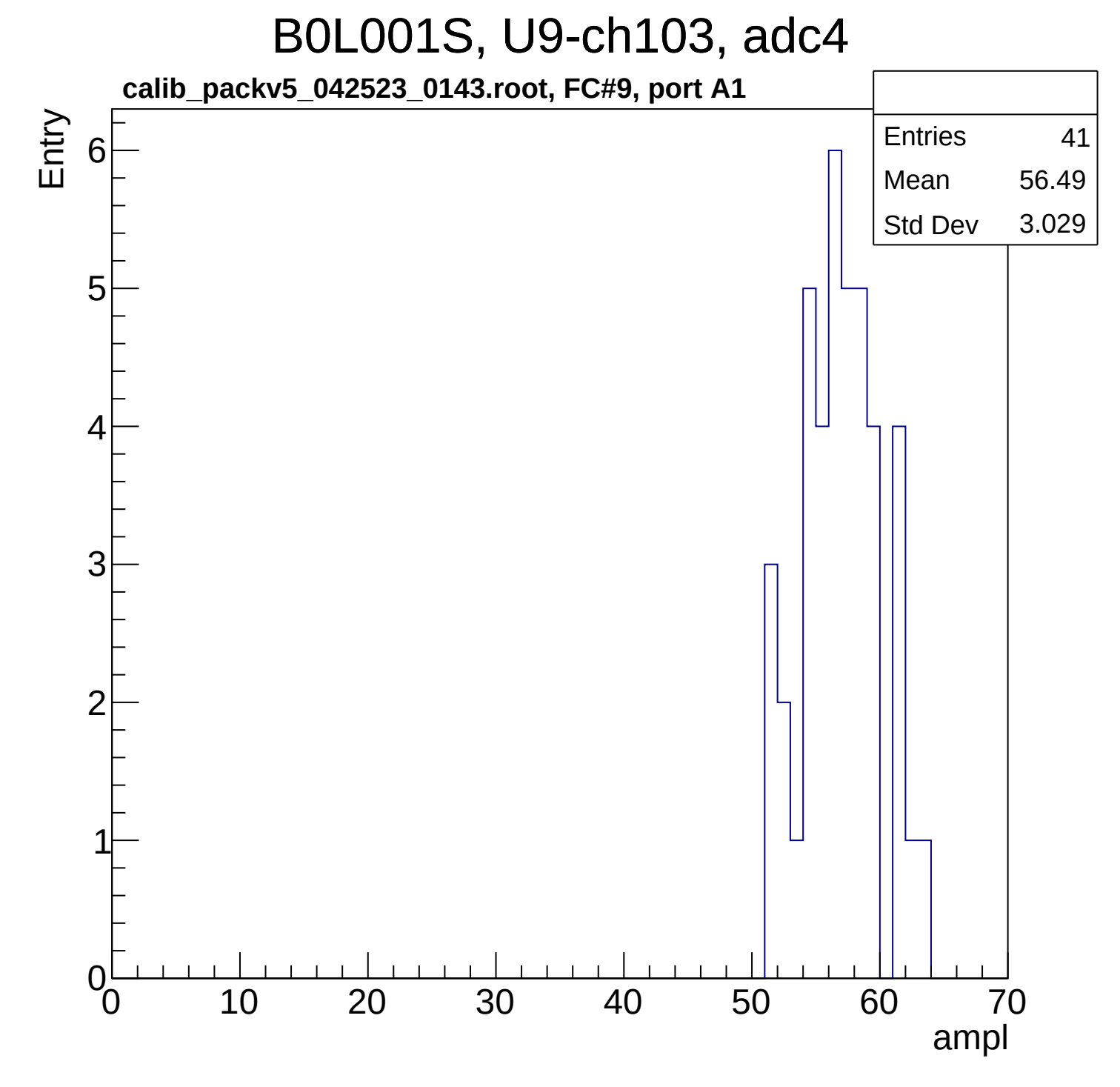
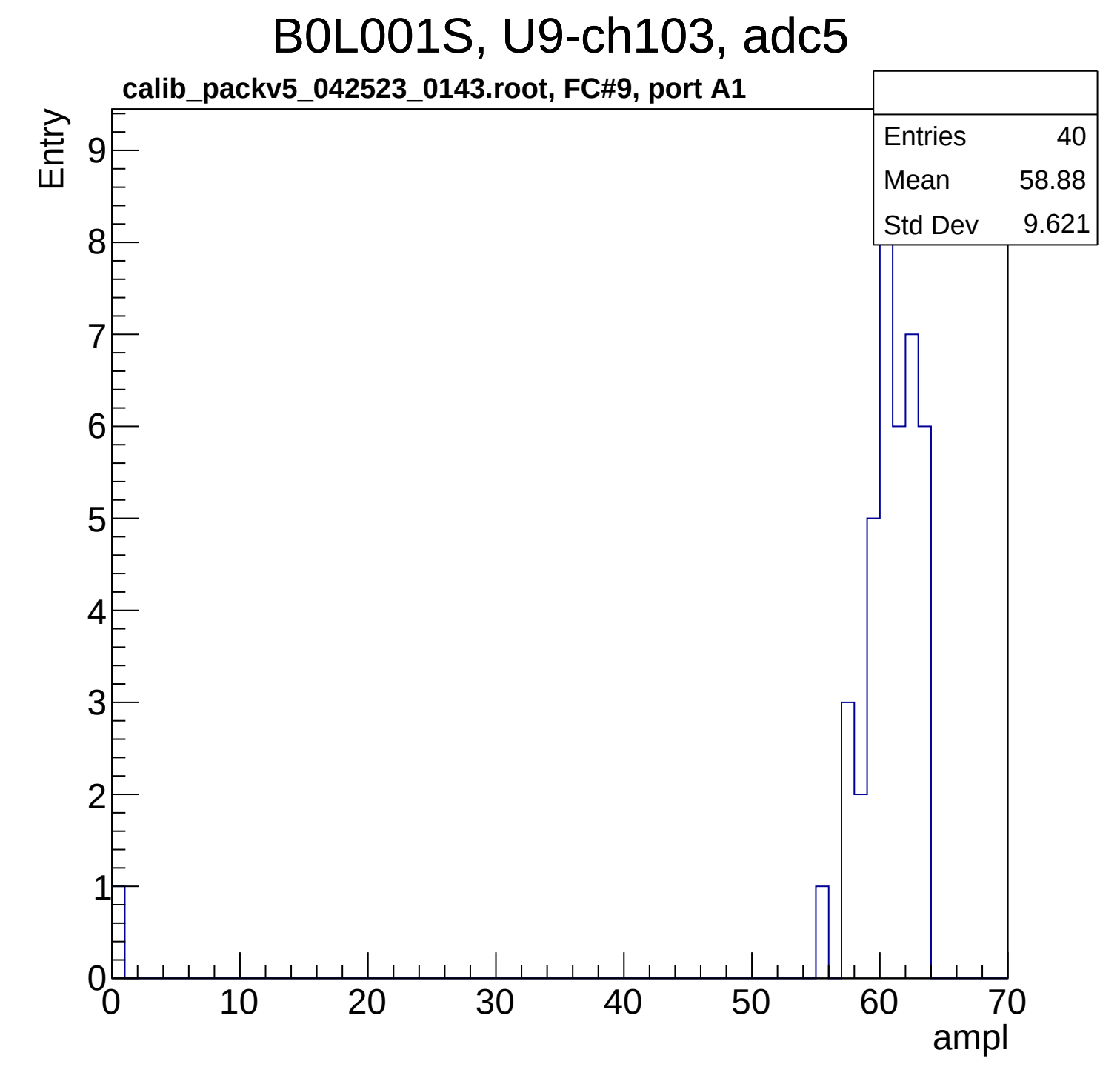
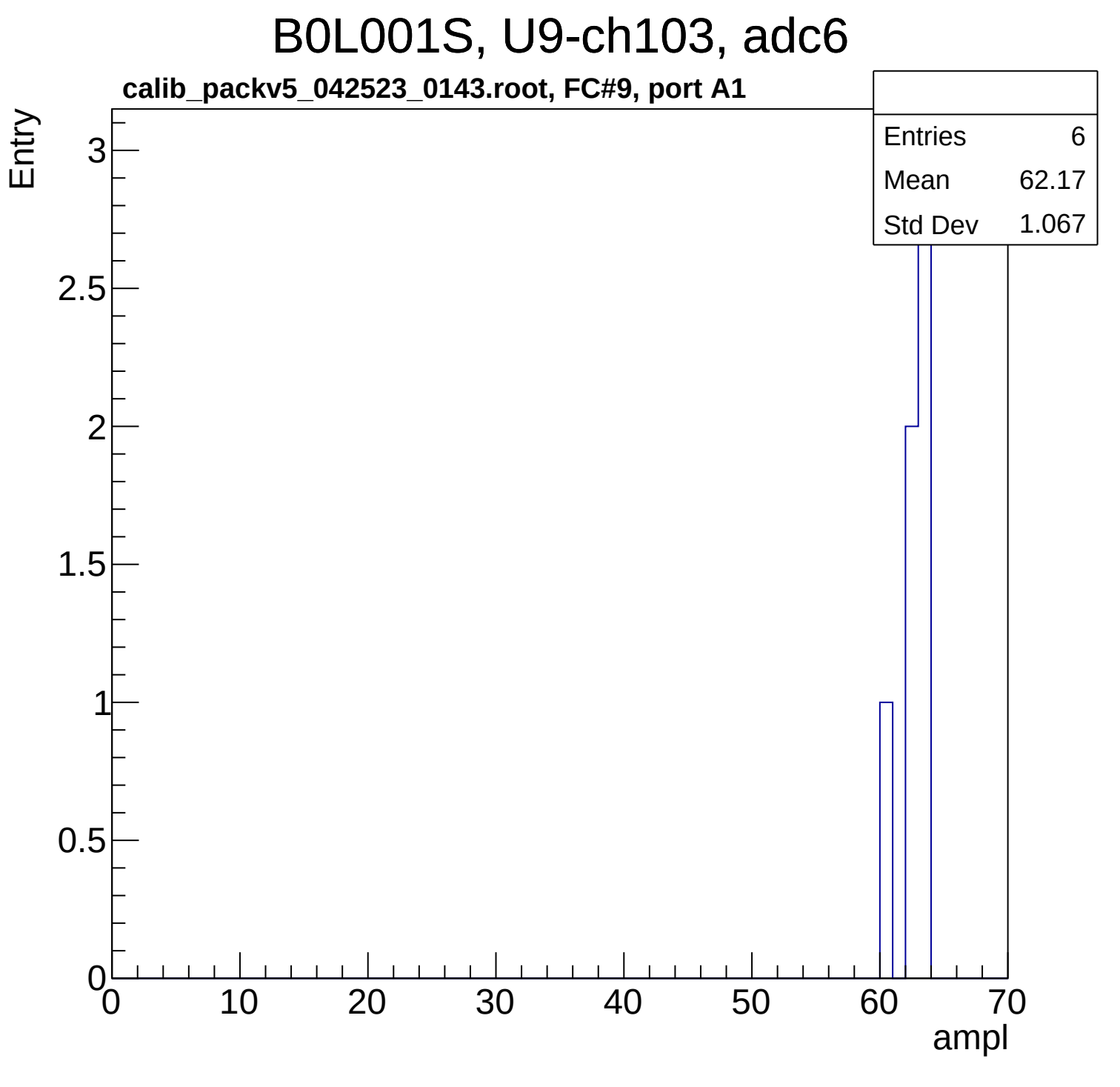
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch103, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 56.49
Std Dev 3.029
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch103, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.88
Std Dev 9.621
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch103, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
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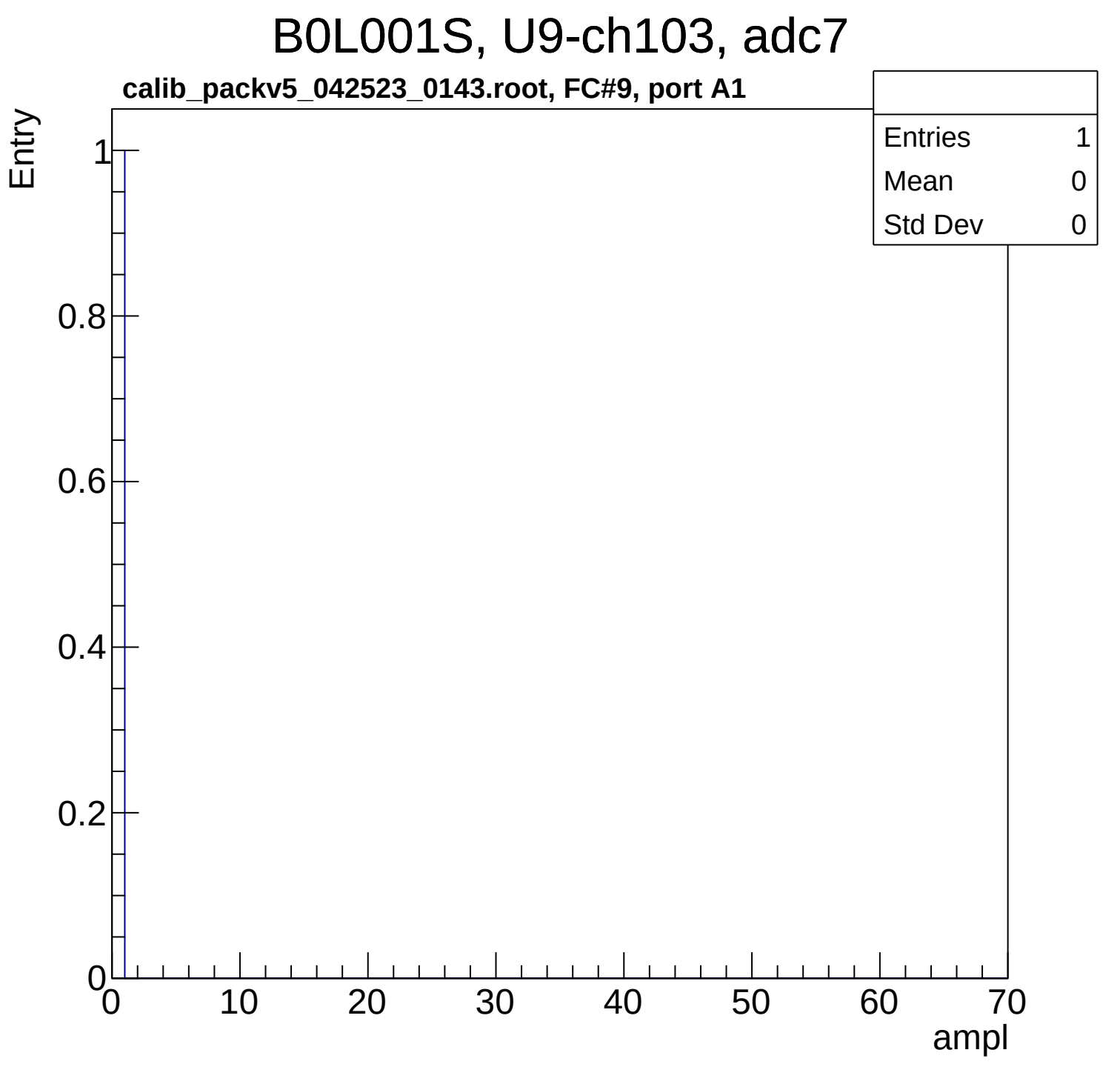
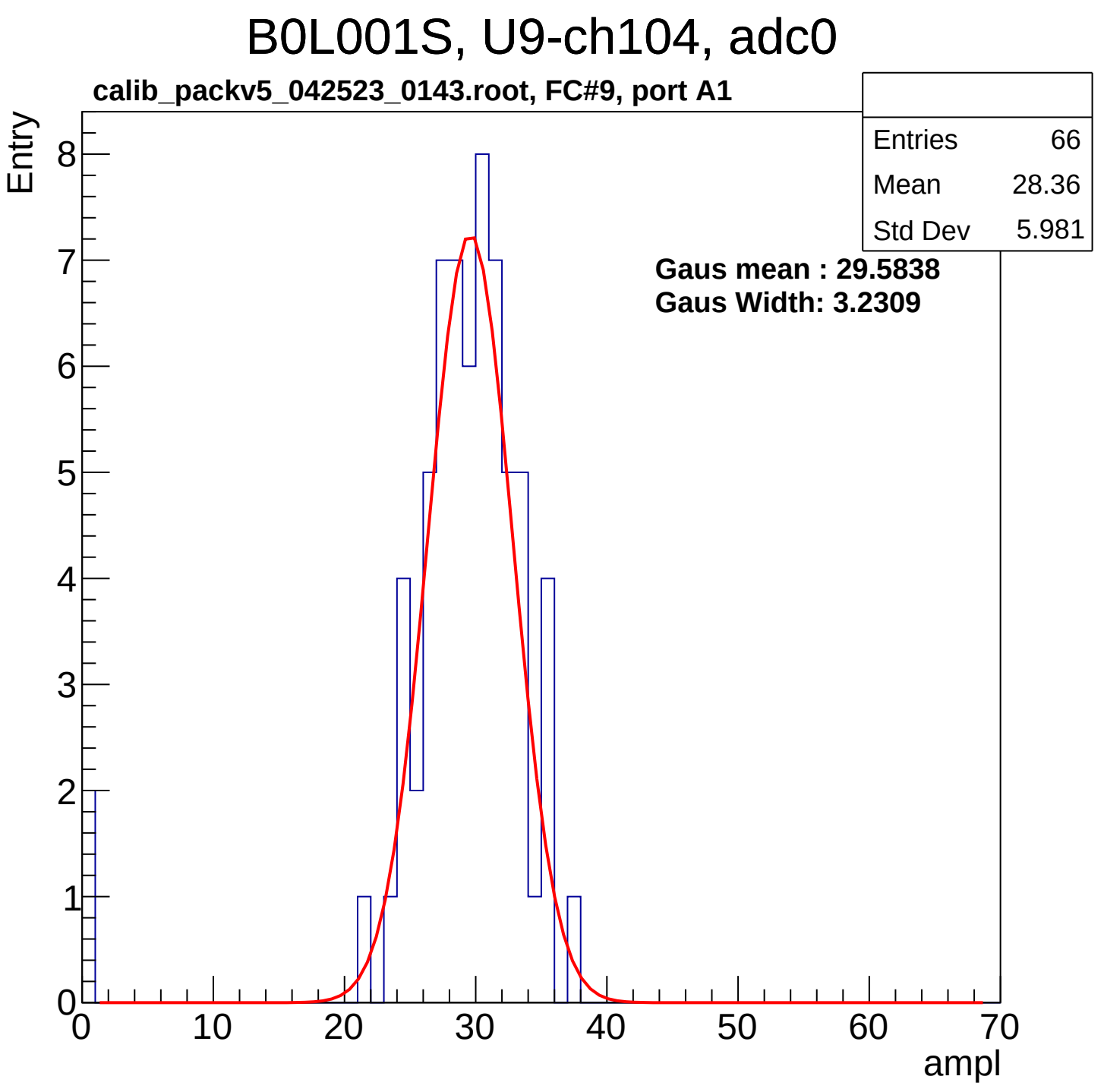
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch103, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch104, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 28.36
Std Dev 5.981
Gaus mean : 29.5838
Gaus Width: 3.2309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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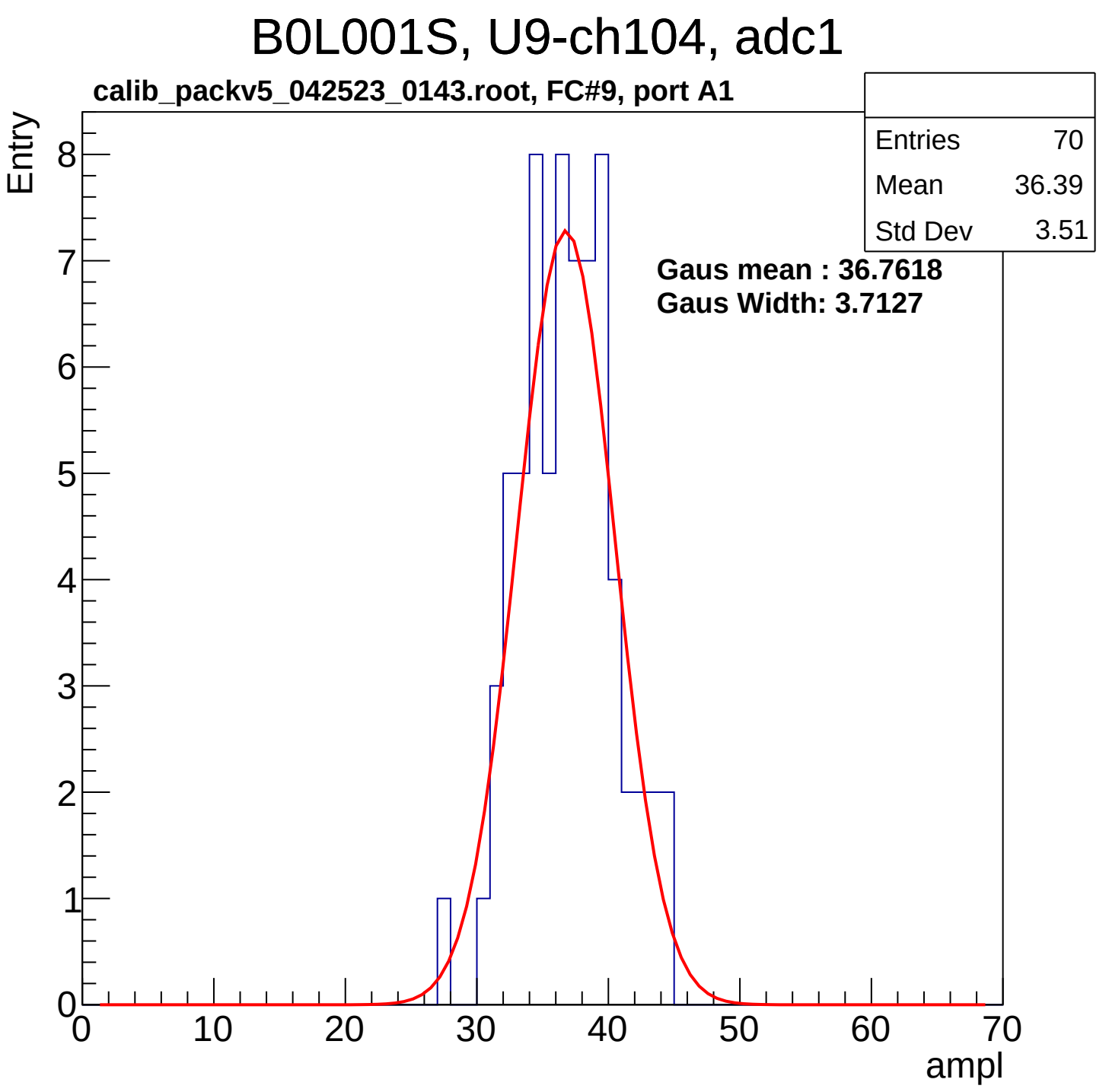
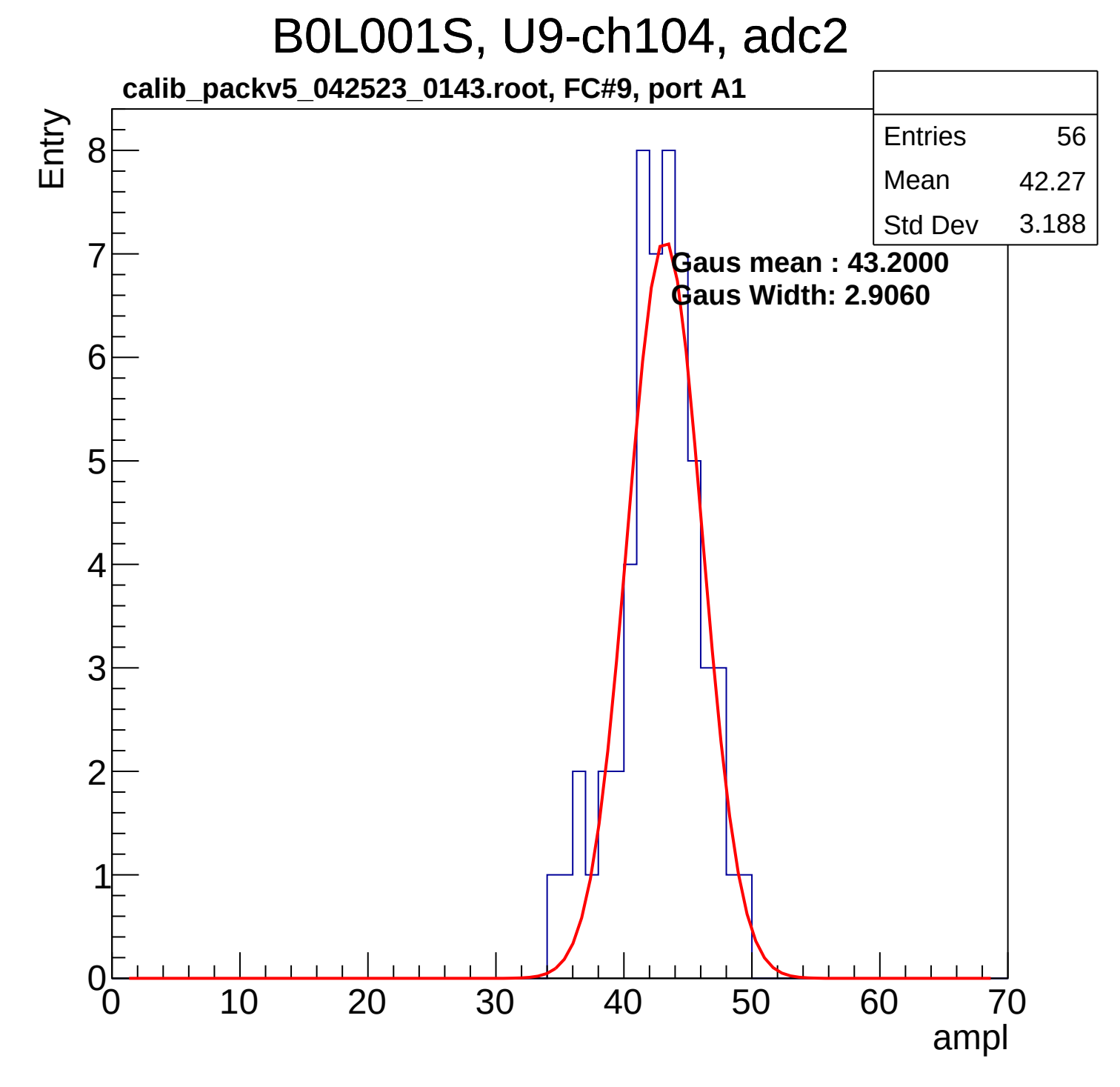
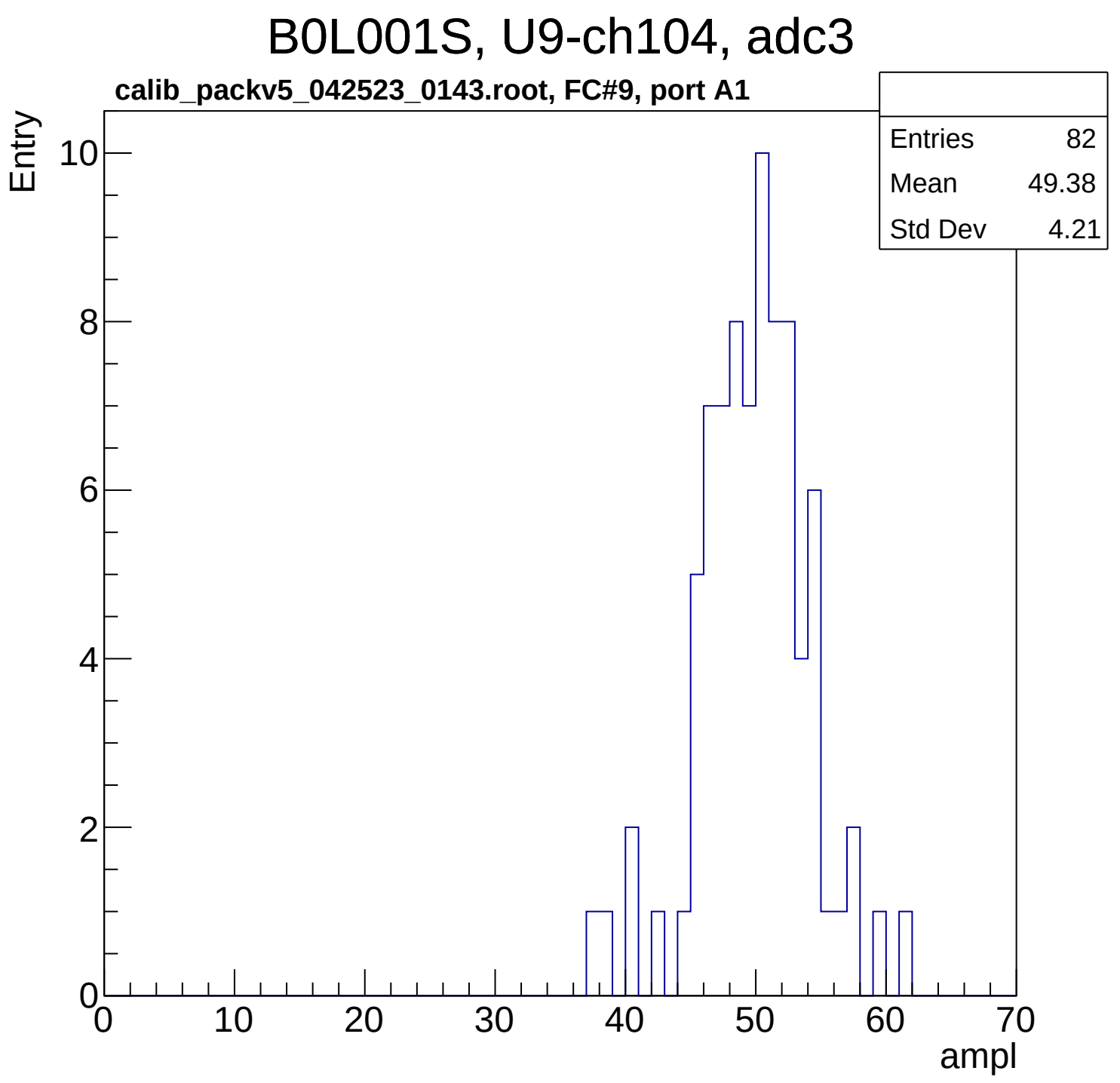
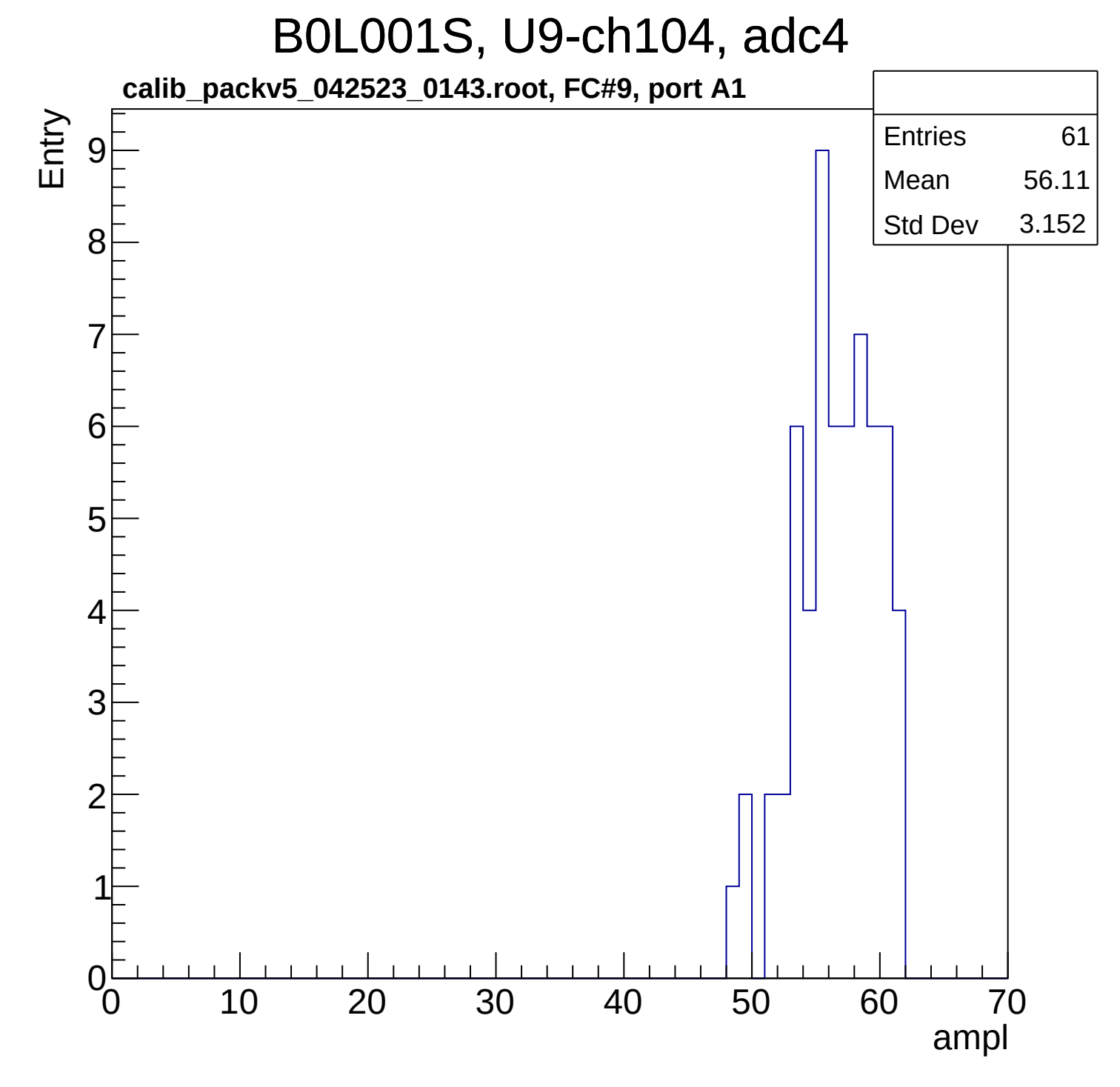
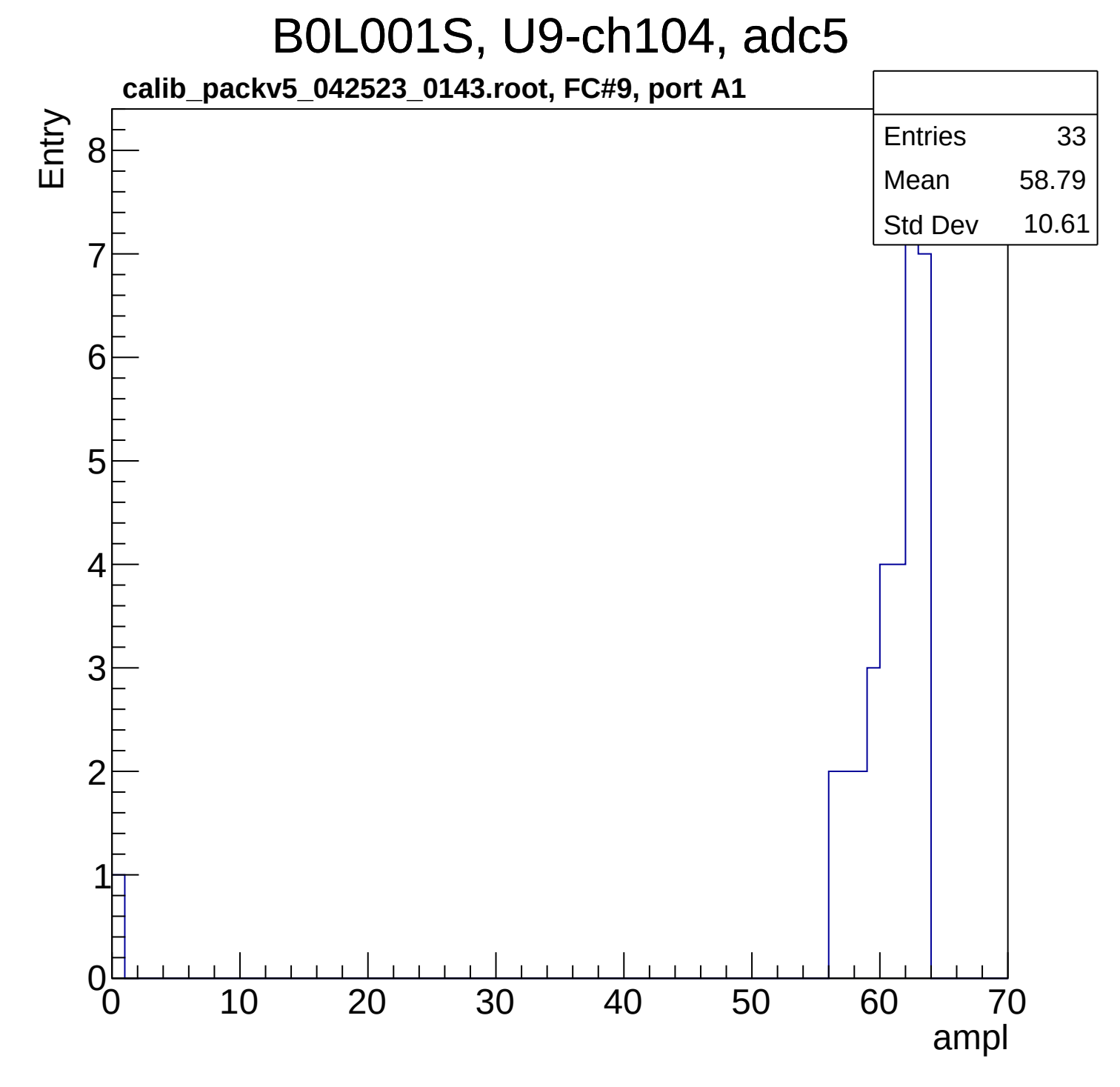
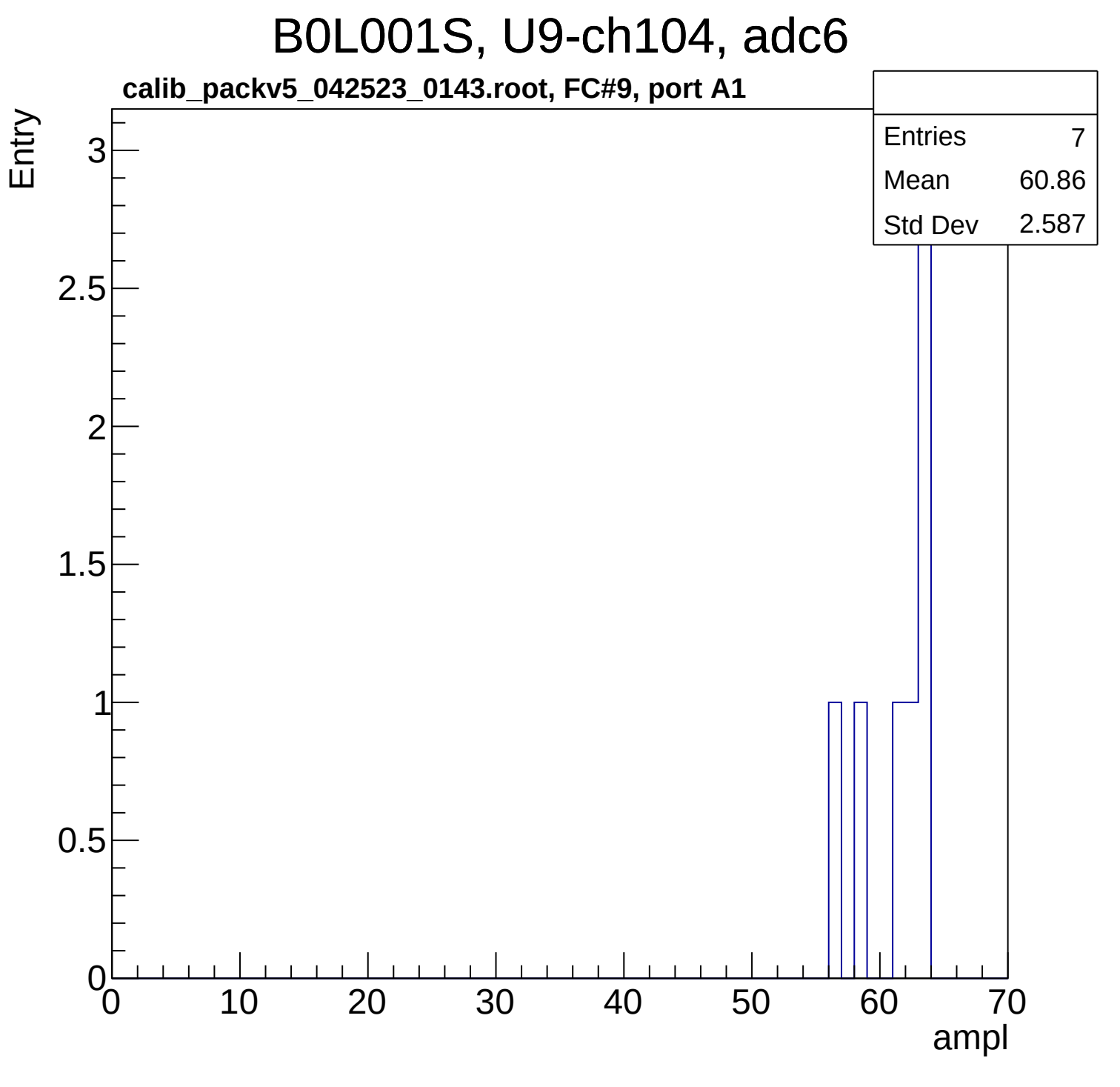

B0L001S, U9-ch104, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.39
Std Dev 3.51
Gaus mean : 36.7618
Gaus Width: 3.7127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

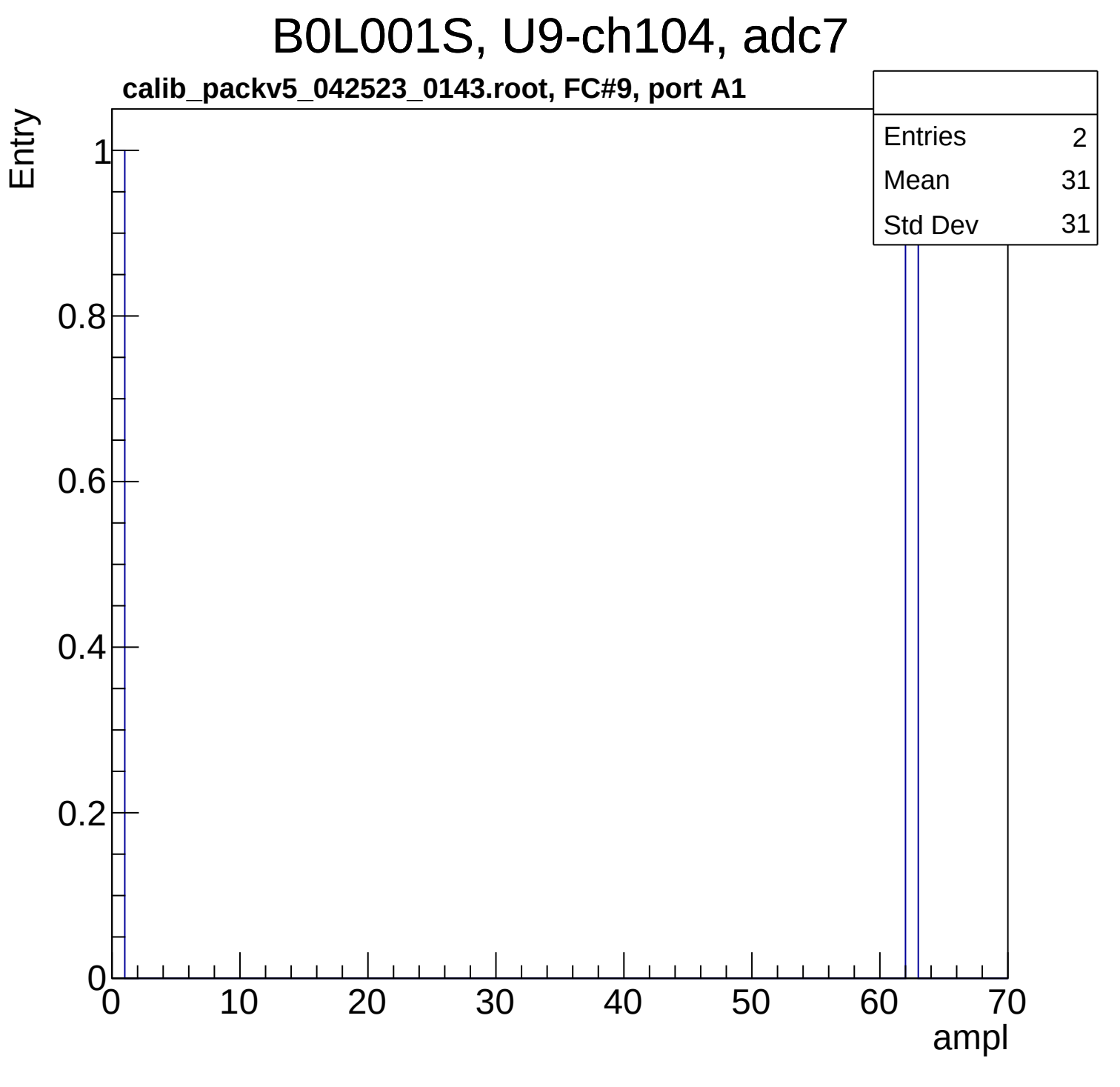

B0L001S, U9-ch104, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 42.27
Std Dev 3.188
Gaus mean : 43.2000
Gaus Width: 2.9060
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

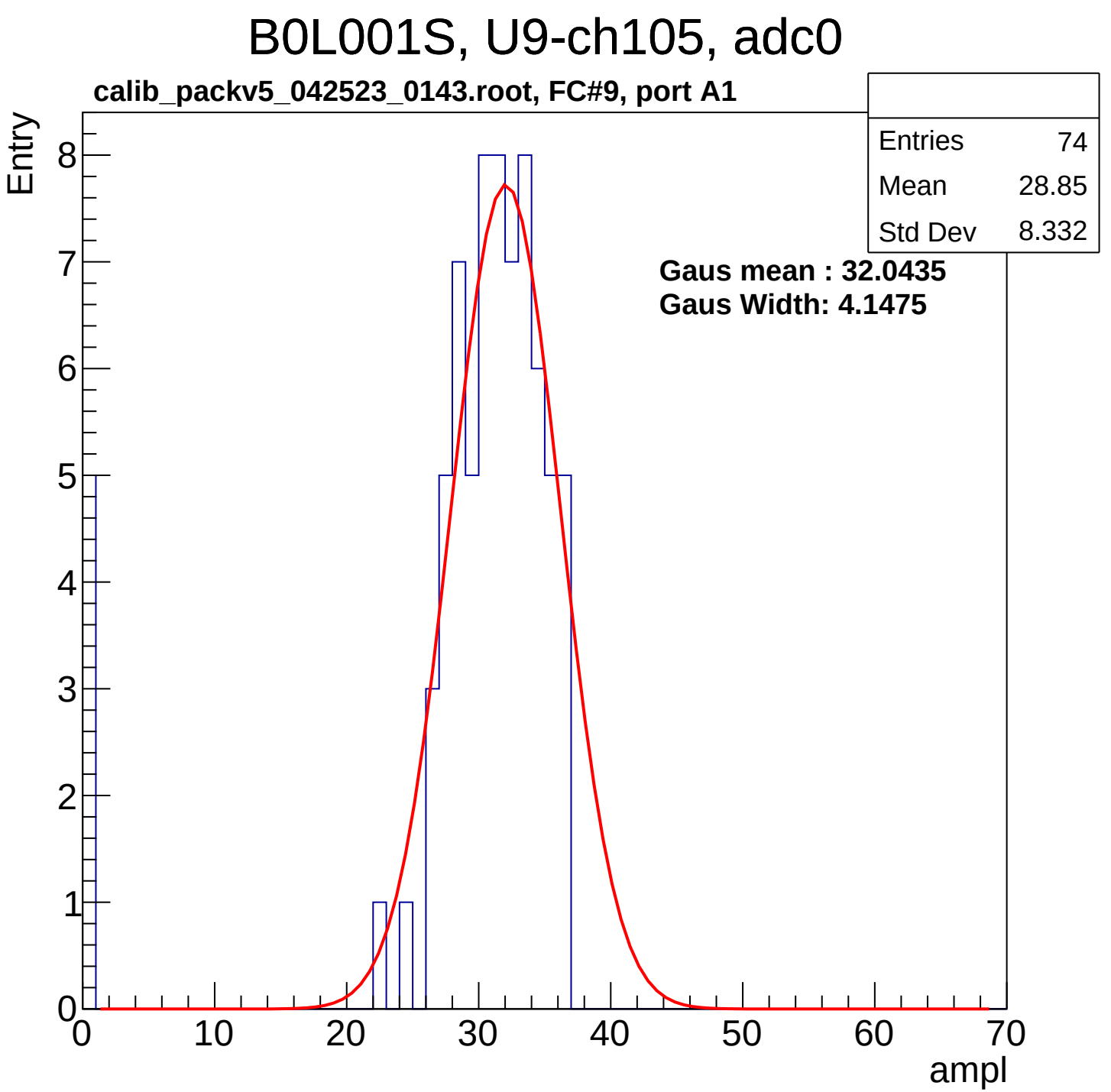

B0L001S, U9-ch104, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 49.38
Std Dev 4.21
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

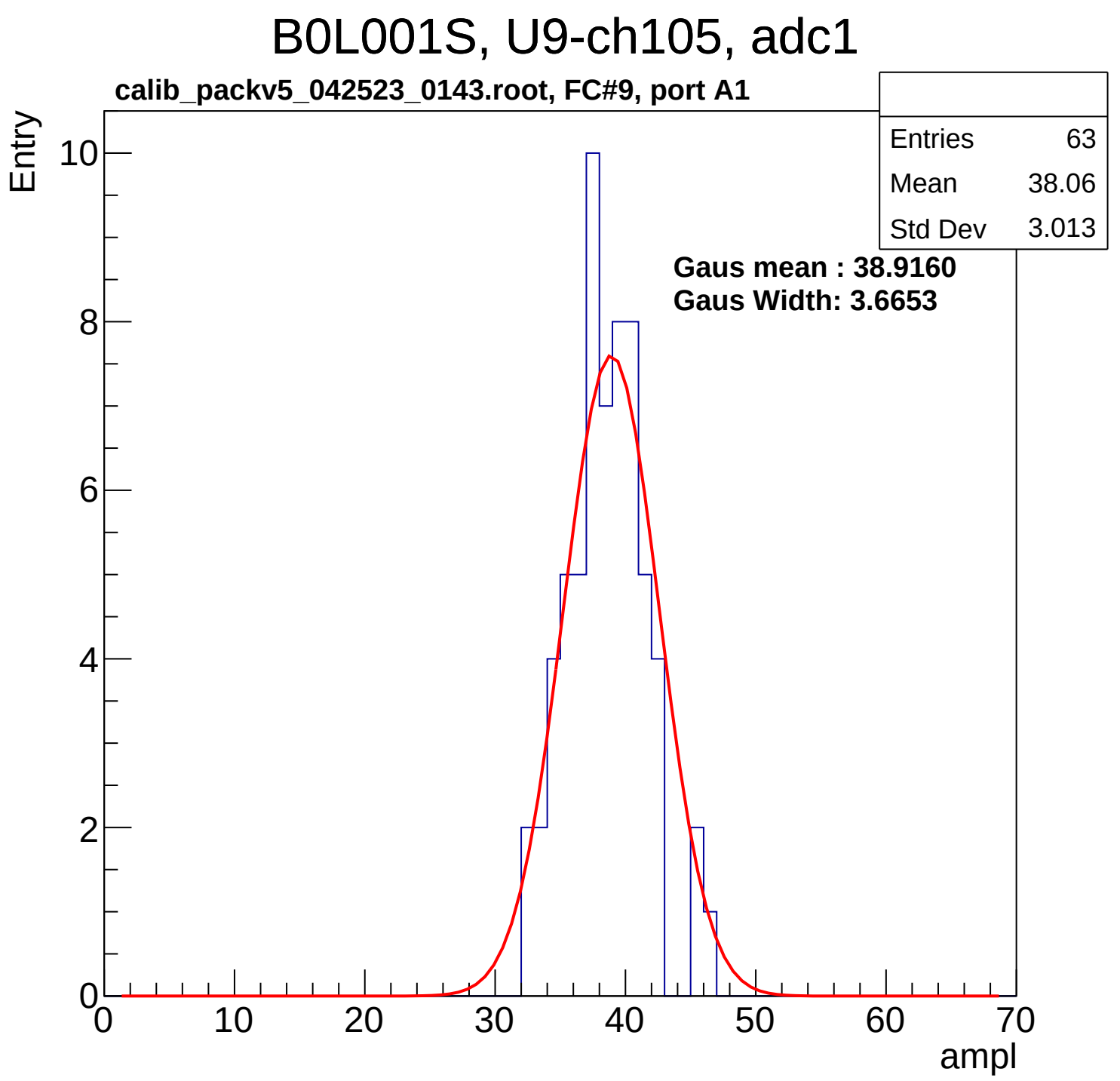

B0L001S, U9-ch104, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.11
Std Dev 3.152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

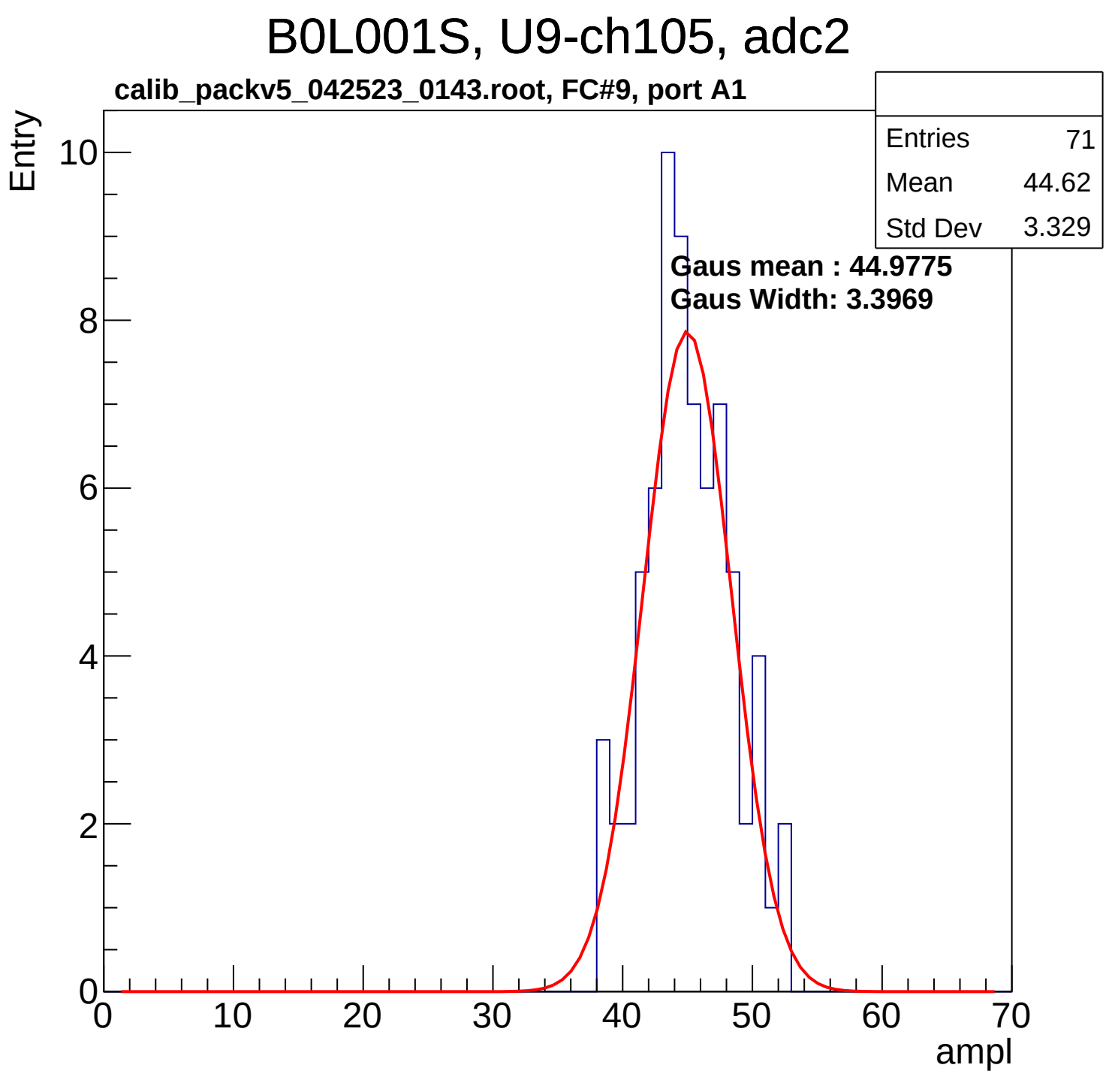

B0L001S, U9-ch104, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.79
Std Dev 10.61
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

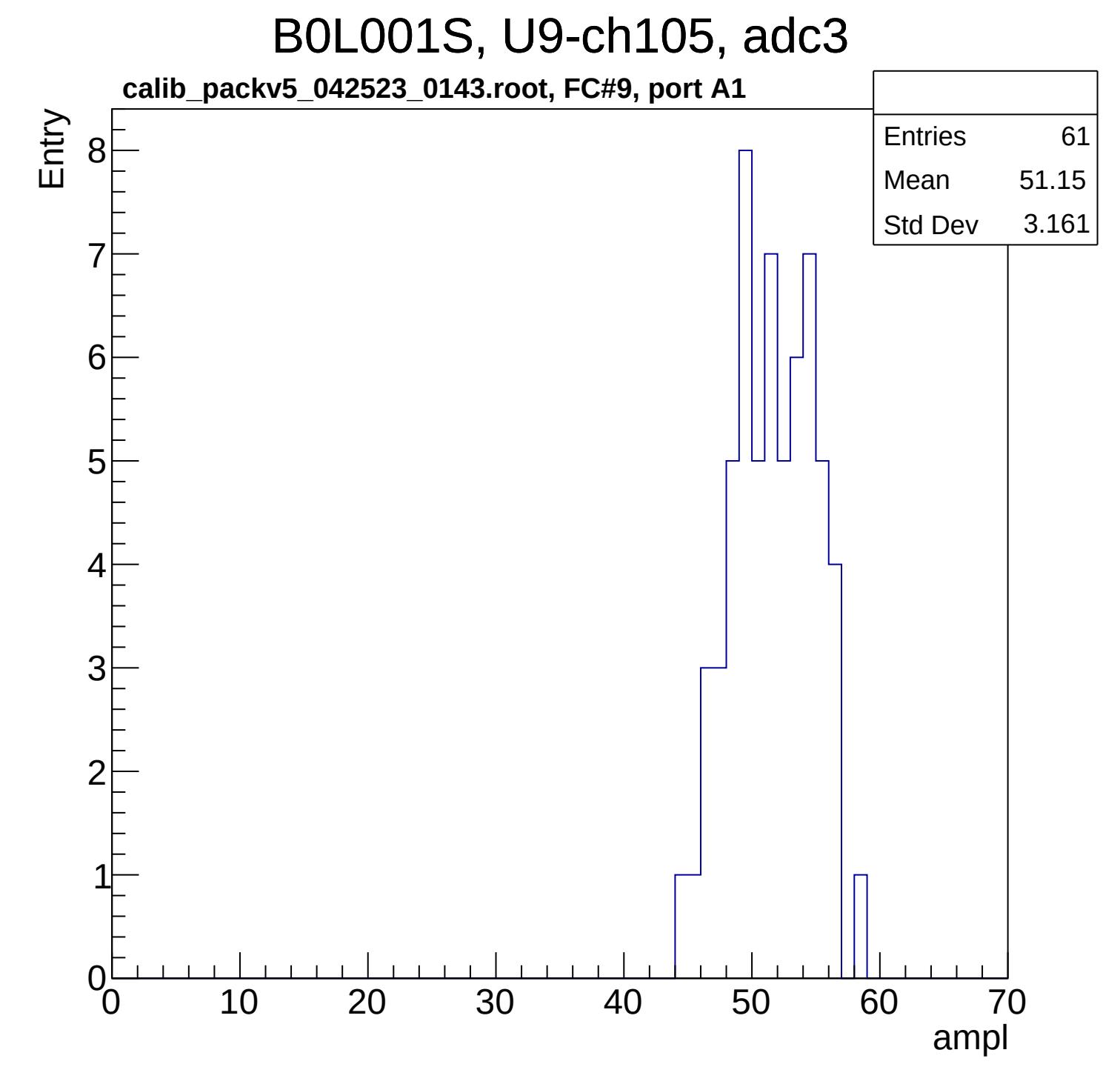

B0L001S, U9-ch104, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 60.86
Std Dev 2.587
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

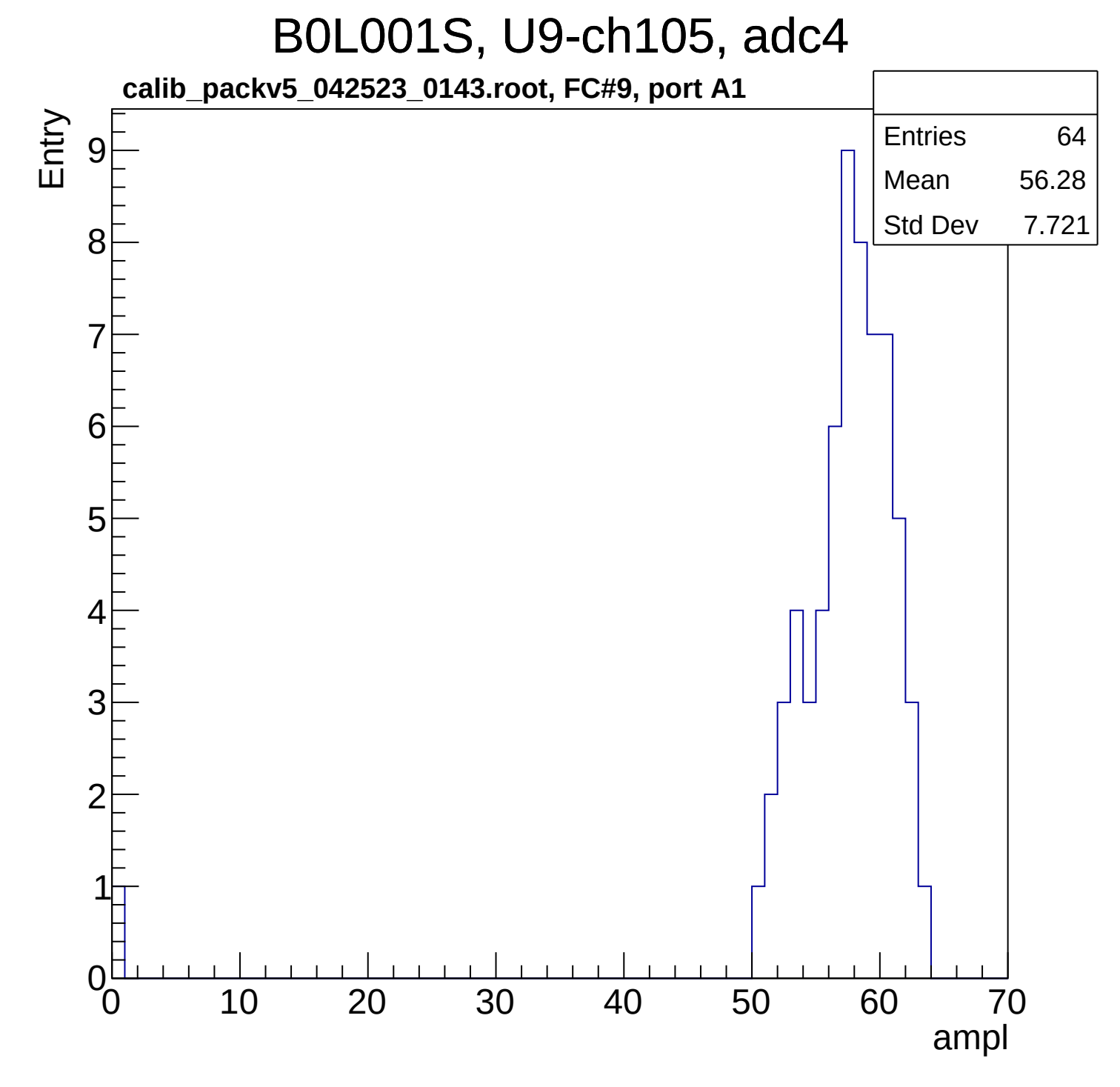

B0L001S, U9-ch104, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

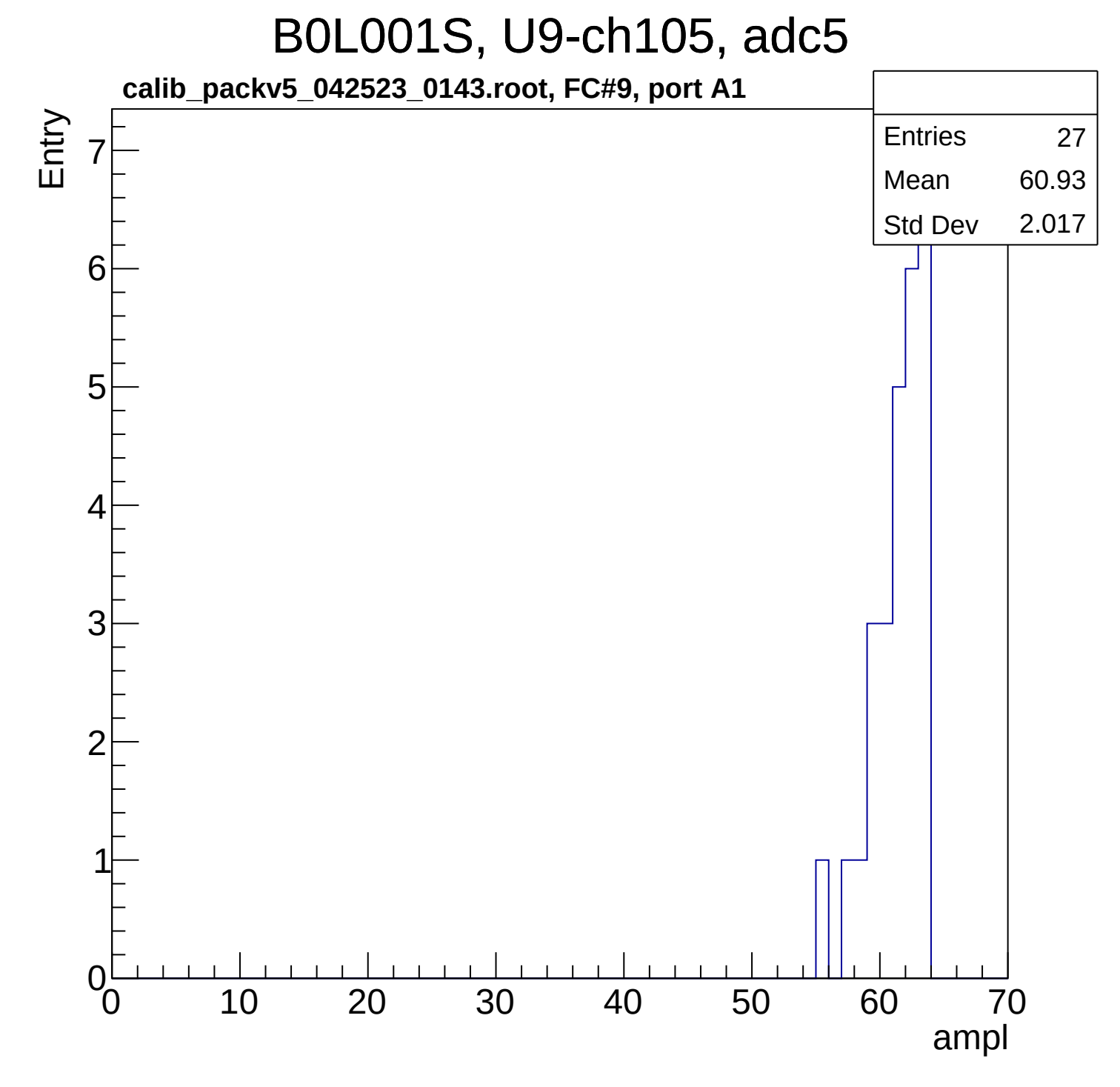

B0L001S, U9-ch105, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 28.85
Std Dev 8.332
Gaus mean : 32.0435
Gaus Width: 4.1475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

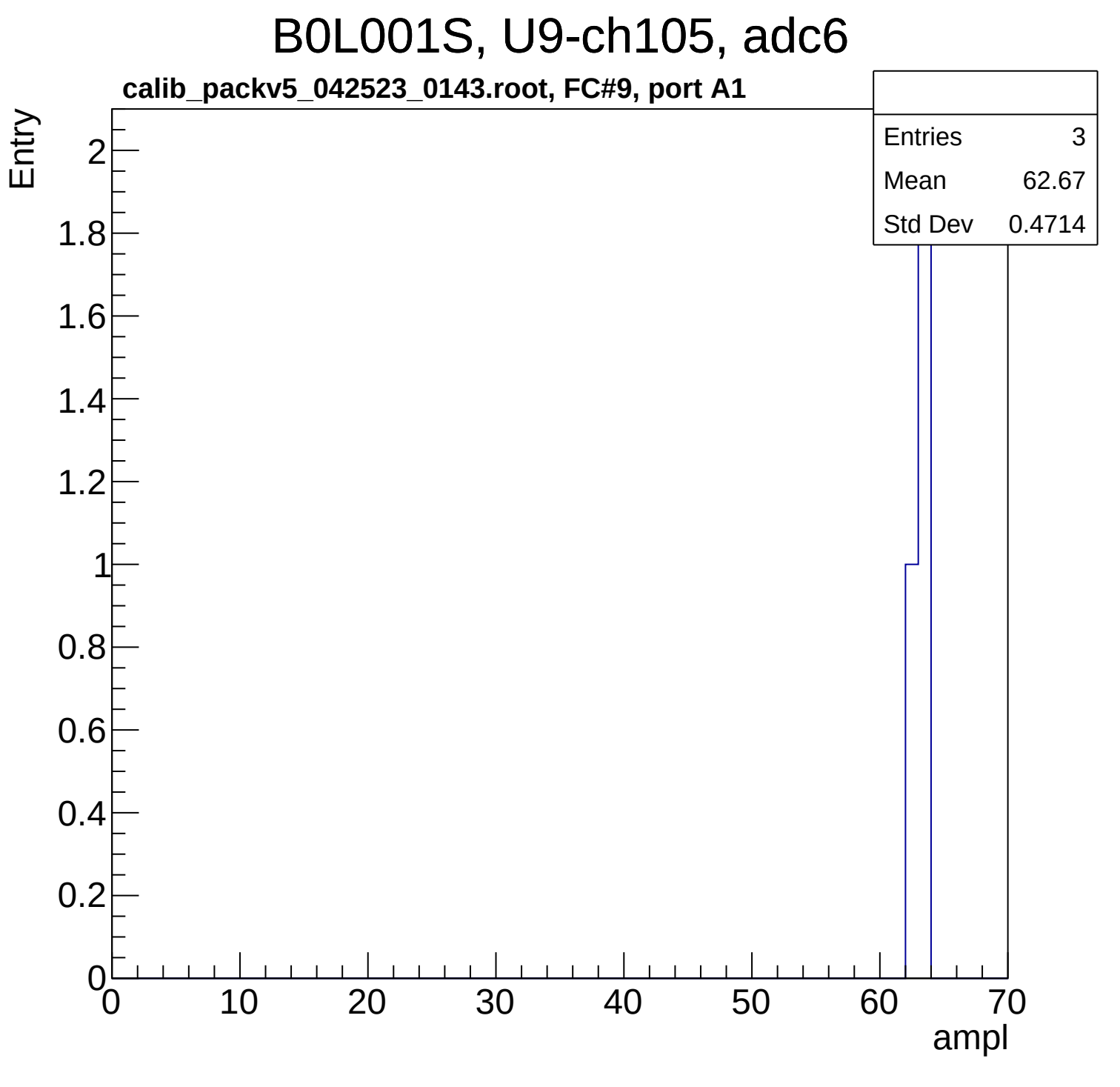

B0L001S, U9-ch105, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 38.06
Std Dev 3.013
Gaus mean : 38.9160
Gaus Width: 3.6653
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

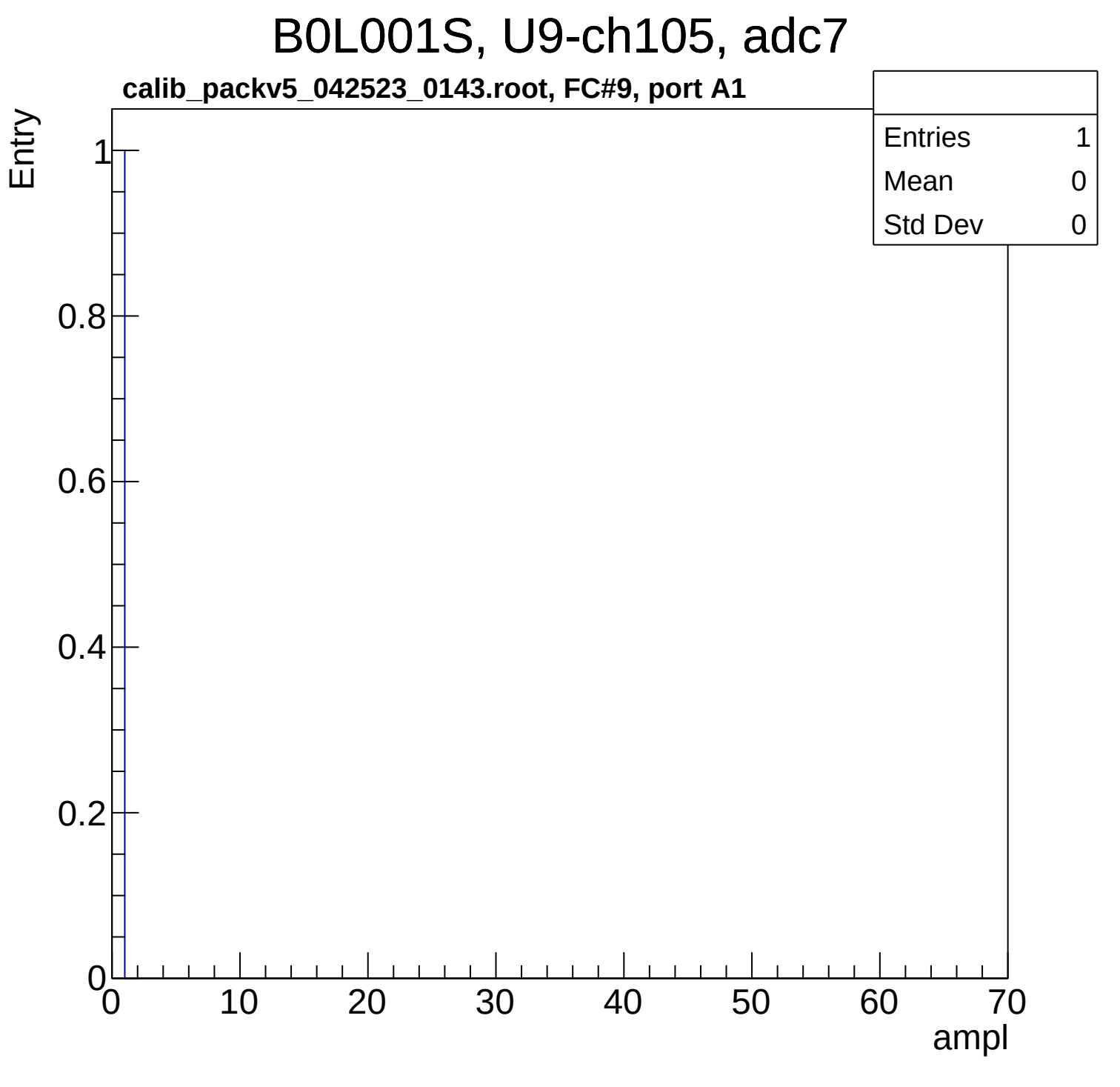

B0L001S, U9-ch105, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.62
Std Dev 3.329
Gaus mean : 44.9775
Gaus Width: 3.3969
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

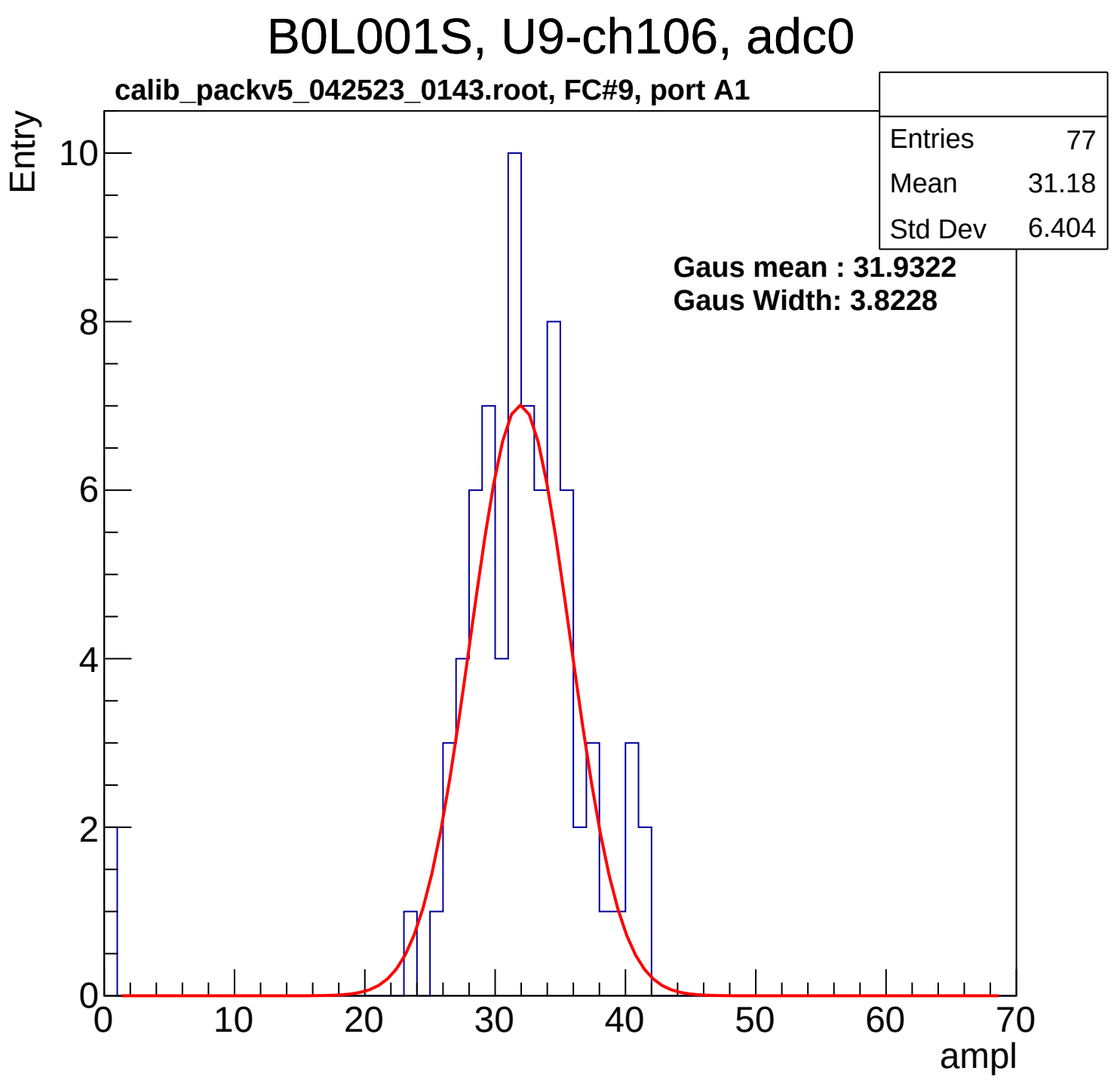

B0L001S, U9-ch105, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.15
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

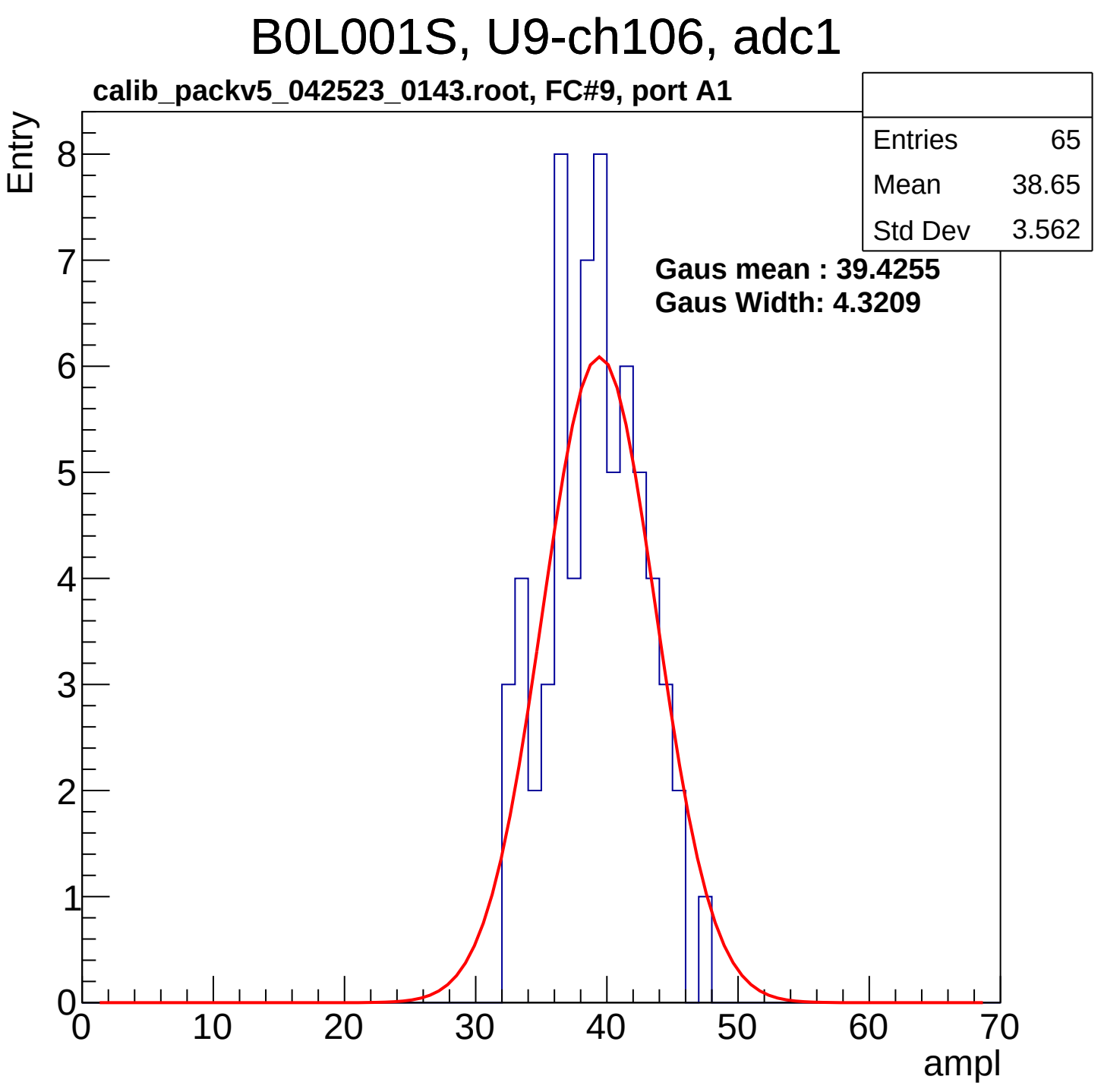

B0L001S, U9-ch105, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.28
Std Dev 7.721
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

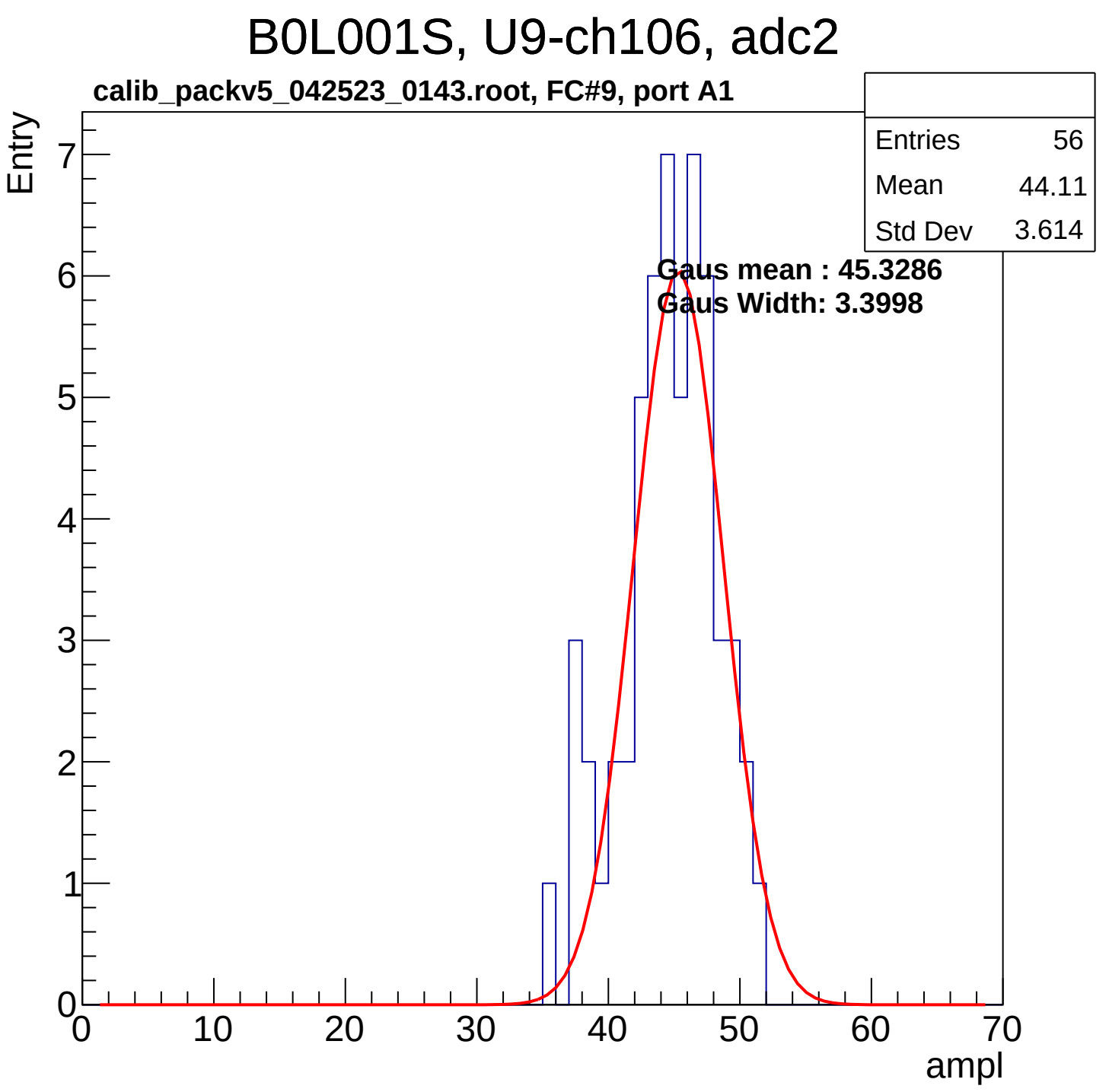

B0L001S, U9-ch105, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 60.93
Std Dev 2.017
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

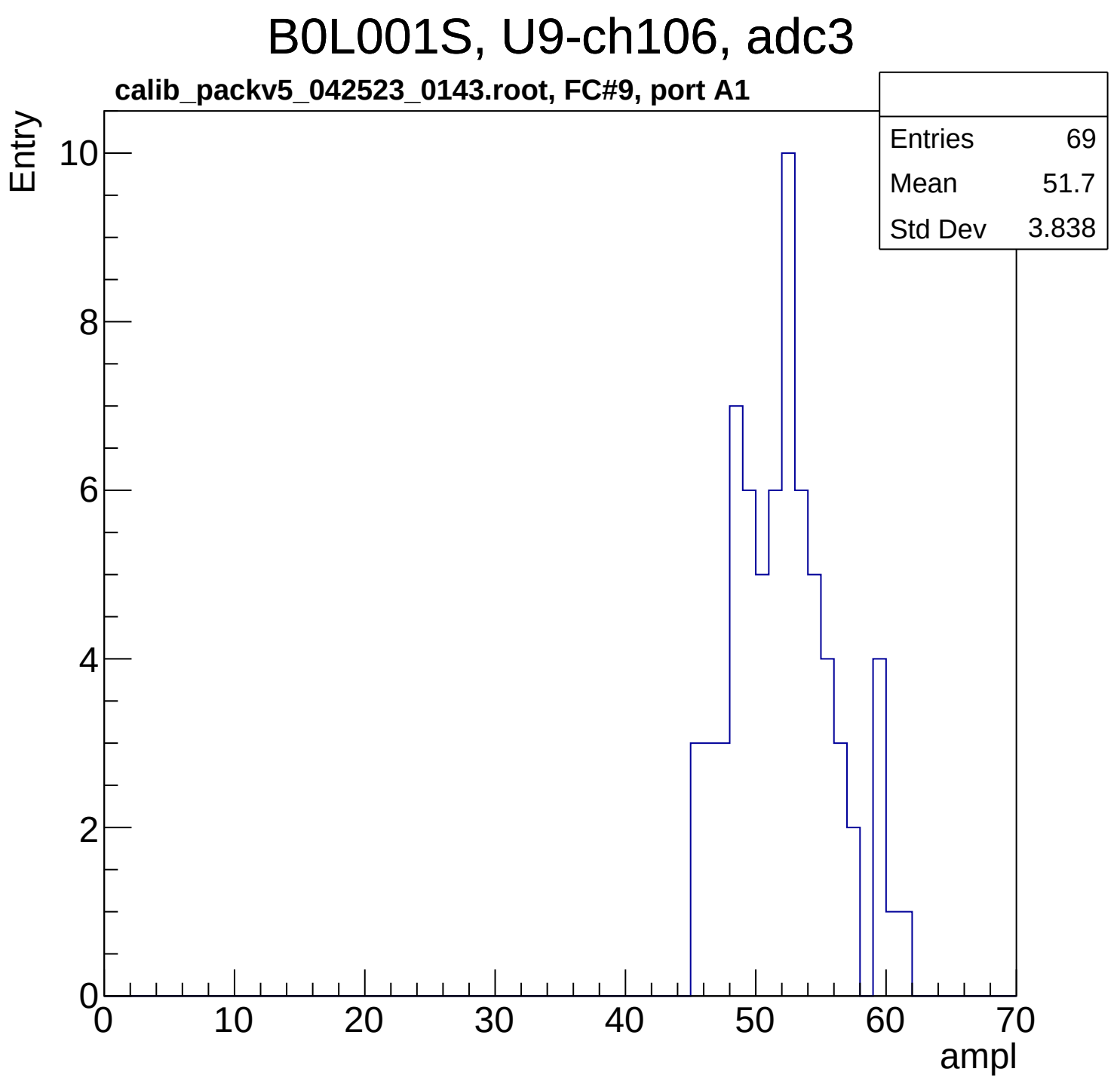

B0L001S, U9-ch105, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

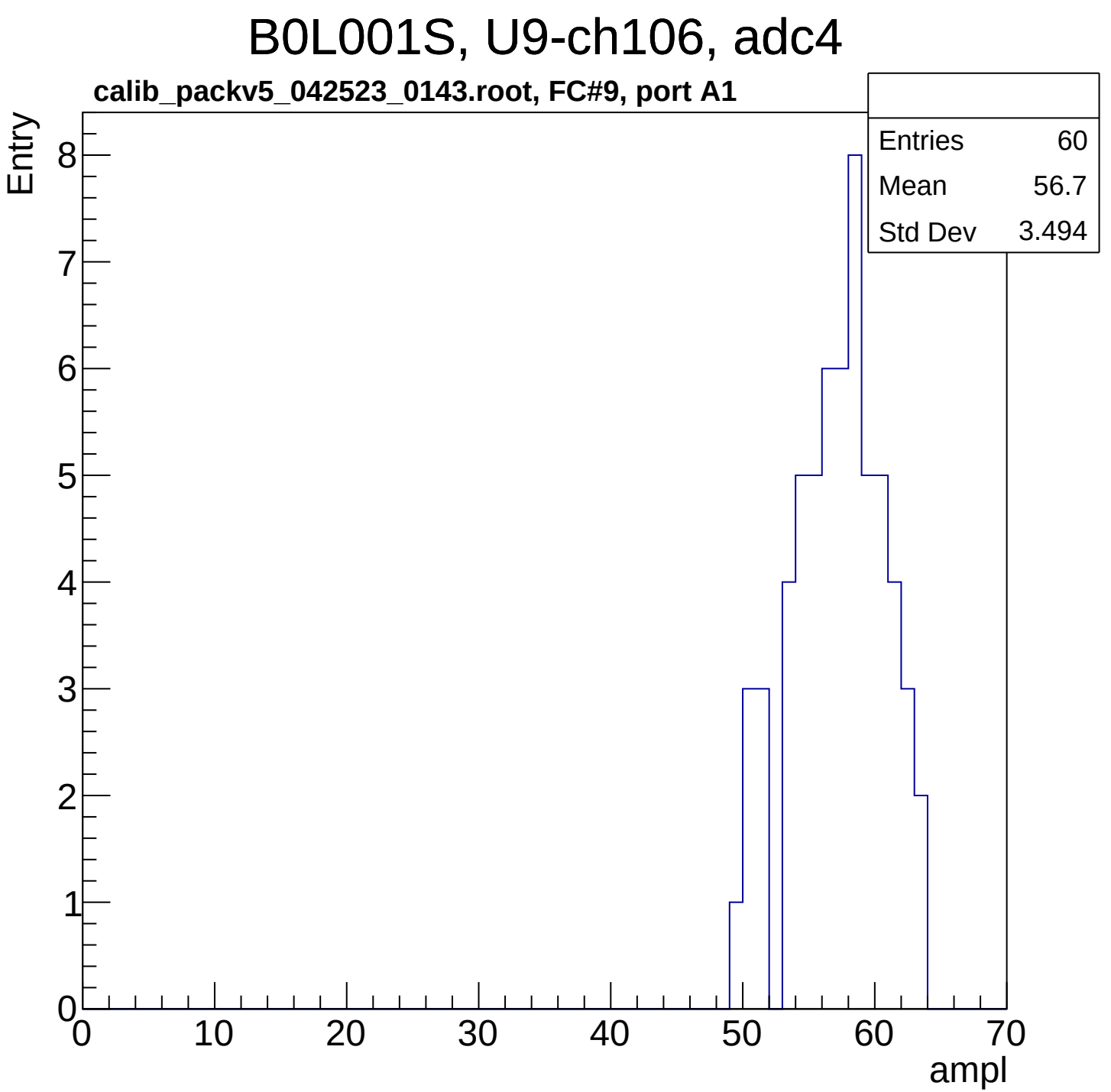

B0L001S, U9-ch105, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

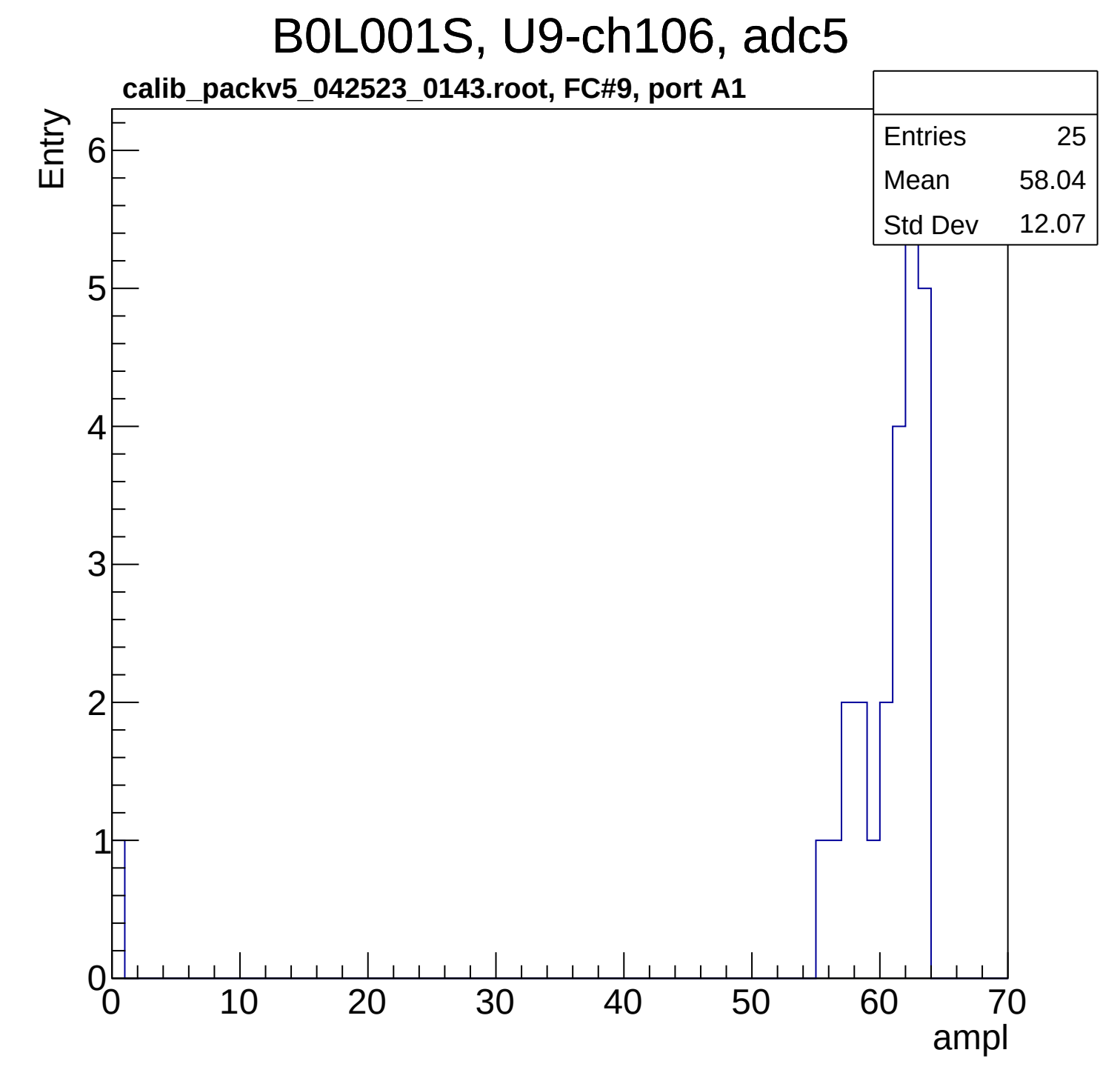

B0L001S, U9-ch106, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 31.18
Std Dev 6.404
Gaus mean : 31.9322
Gaus Width: 3.8228
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

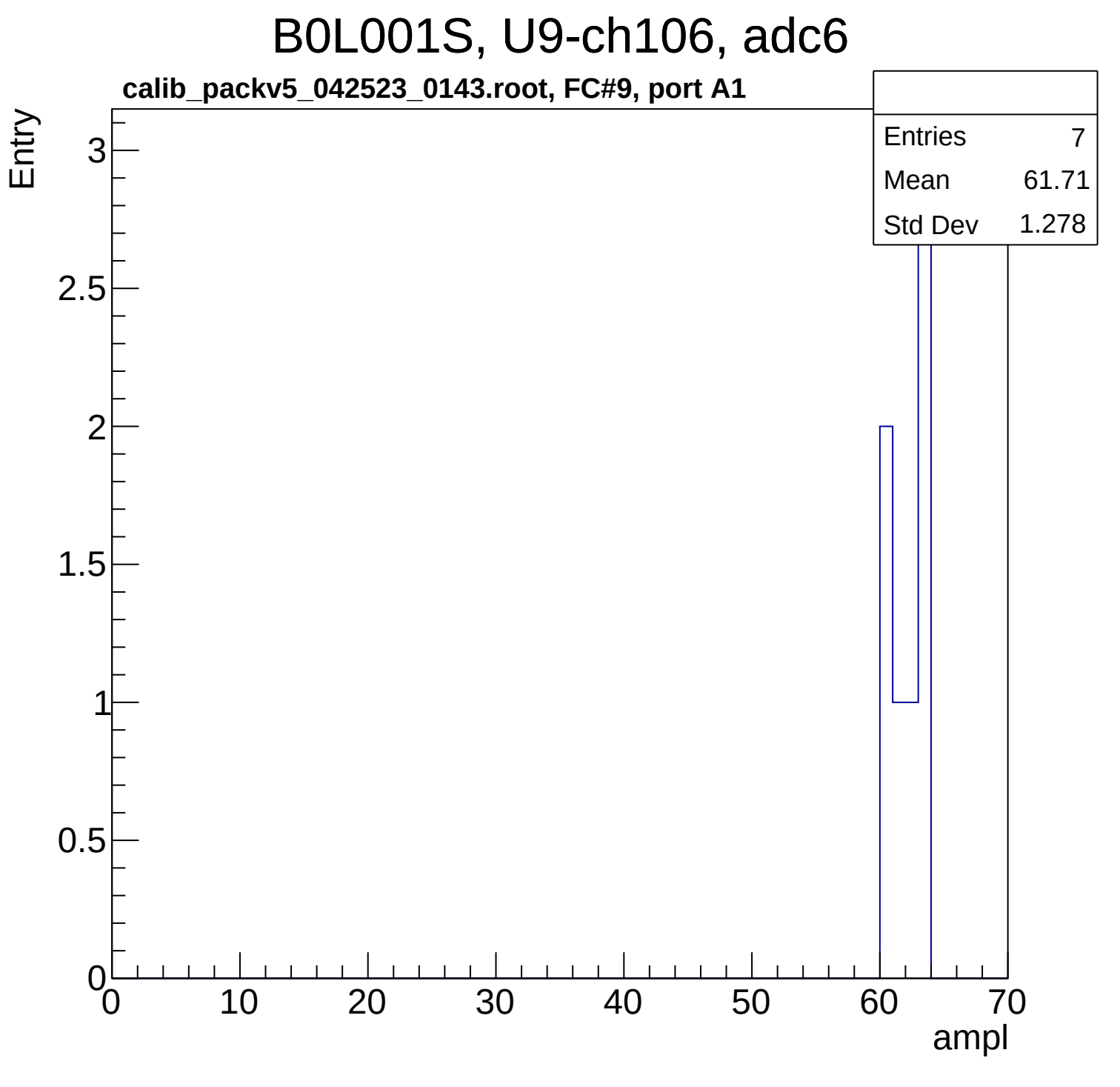

B0L001S, U9-ch106, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 38.65
Std Dev 3.562
Gaus mean : 39.4255
Gaus Width: 4.3209
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

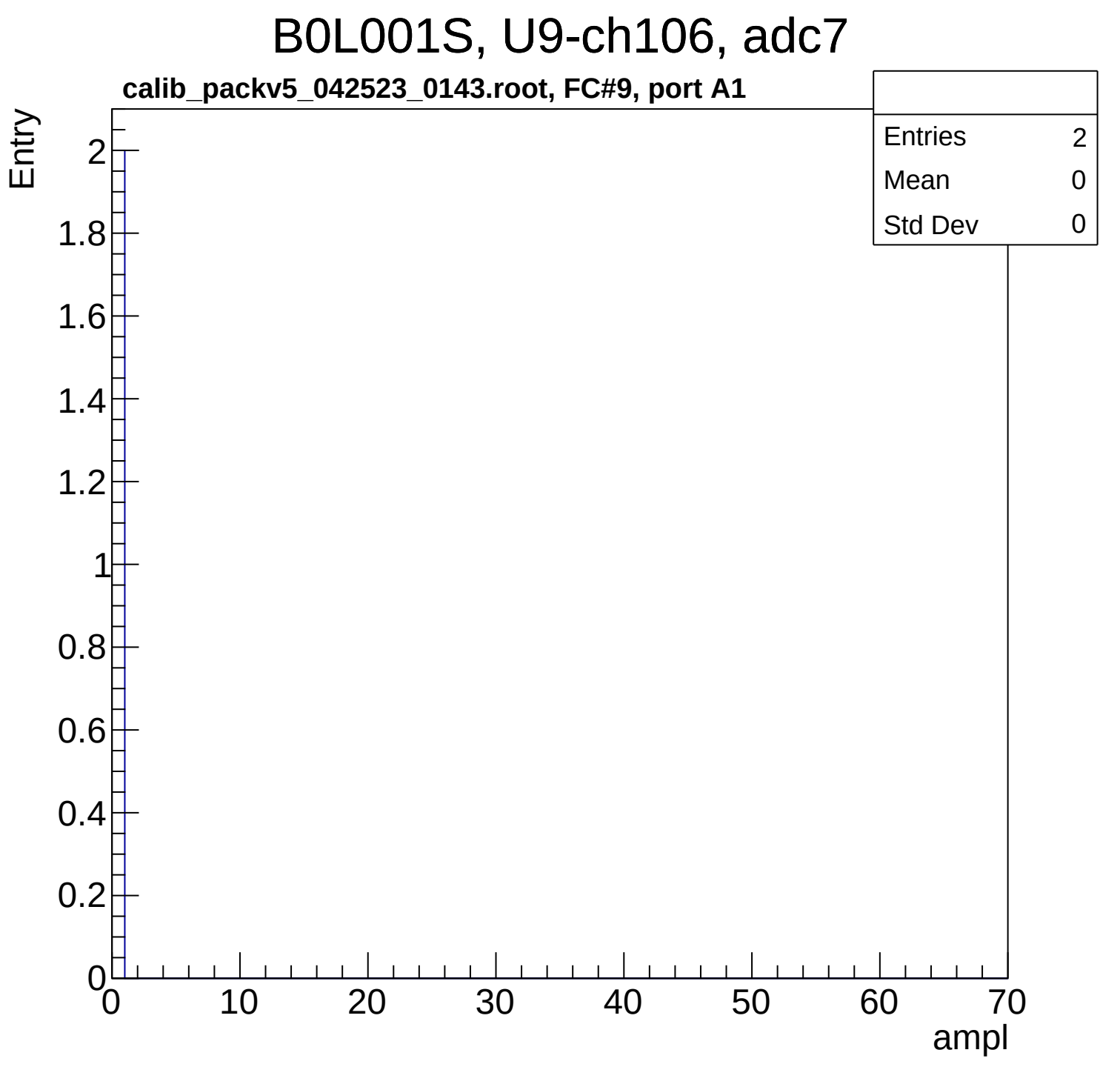

B0L001S, U9-ch106, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 44.11
Std Dev 3.614
Gaus mean : 45.3286
Gaus Width: 3.3998
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

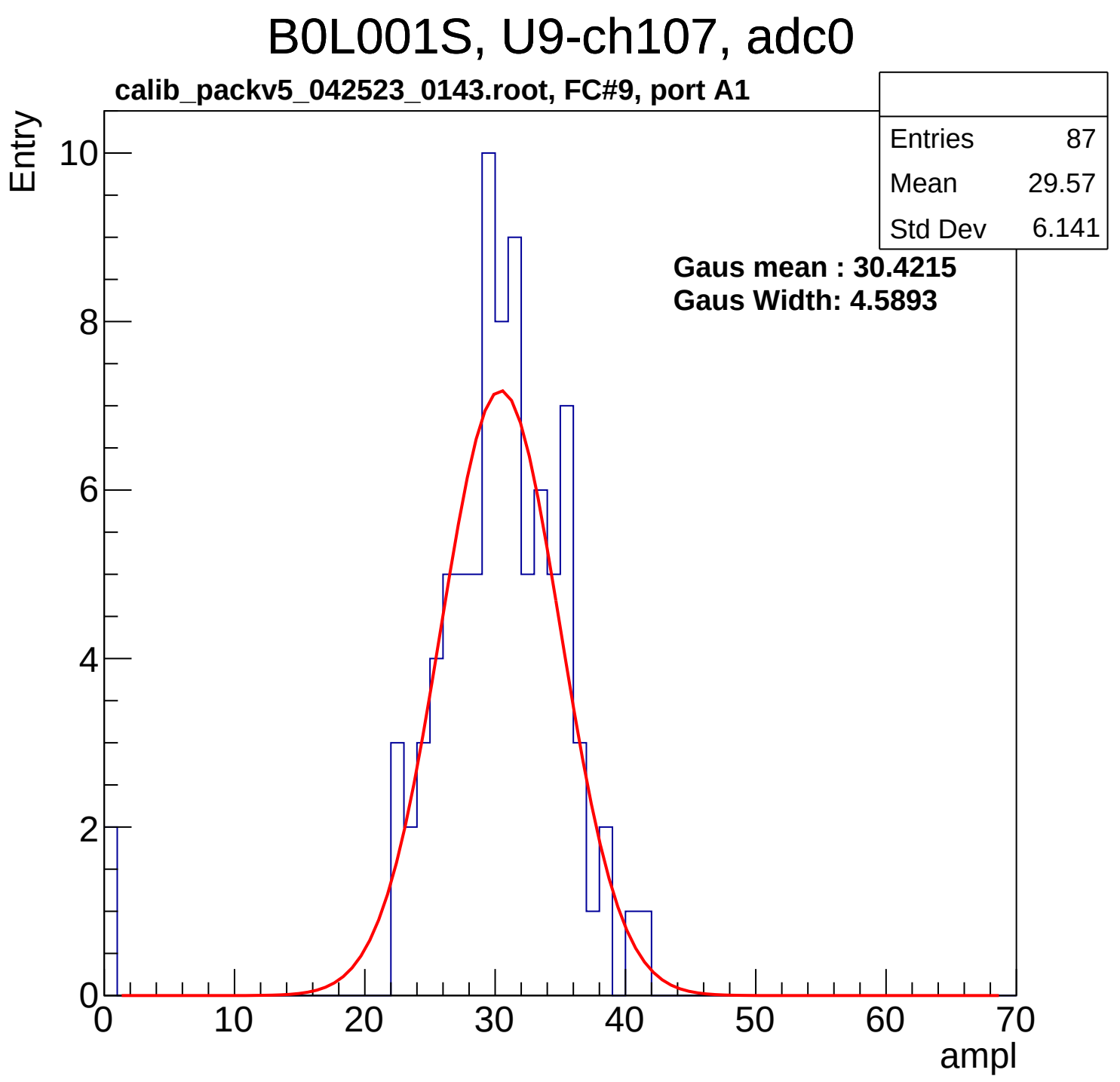

B0L001S, U9-ch106, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.7
Std Dev 3.838
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

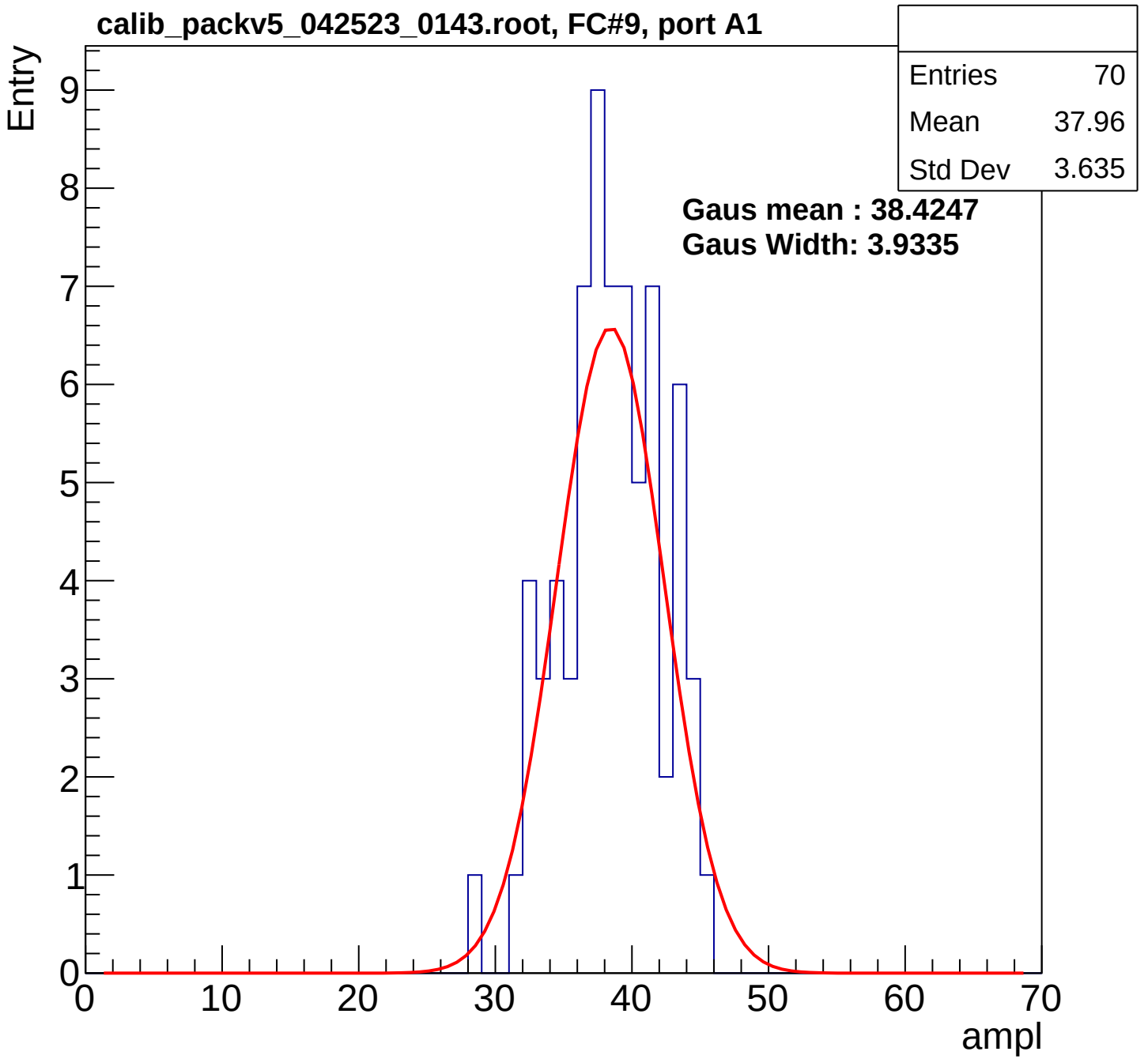

B0L001S, U9-ch106, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 56.7
Std Dev 3.494
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

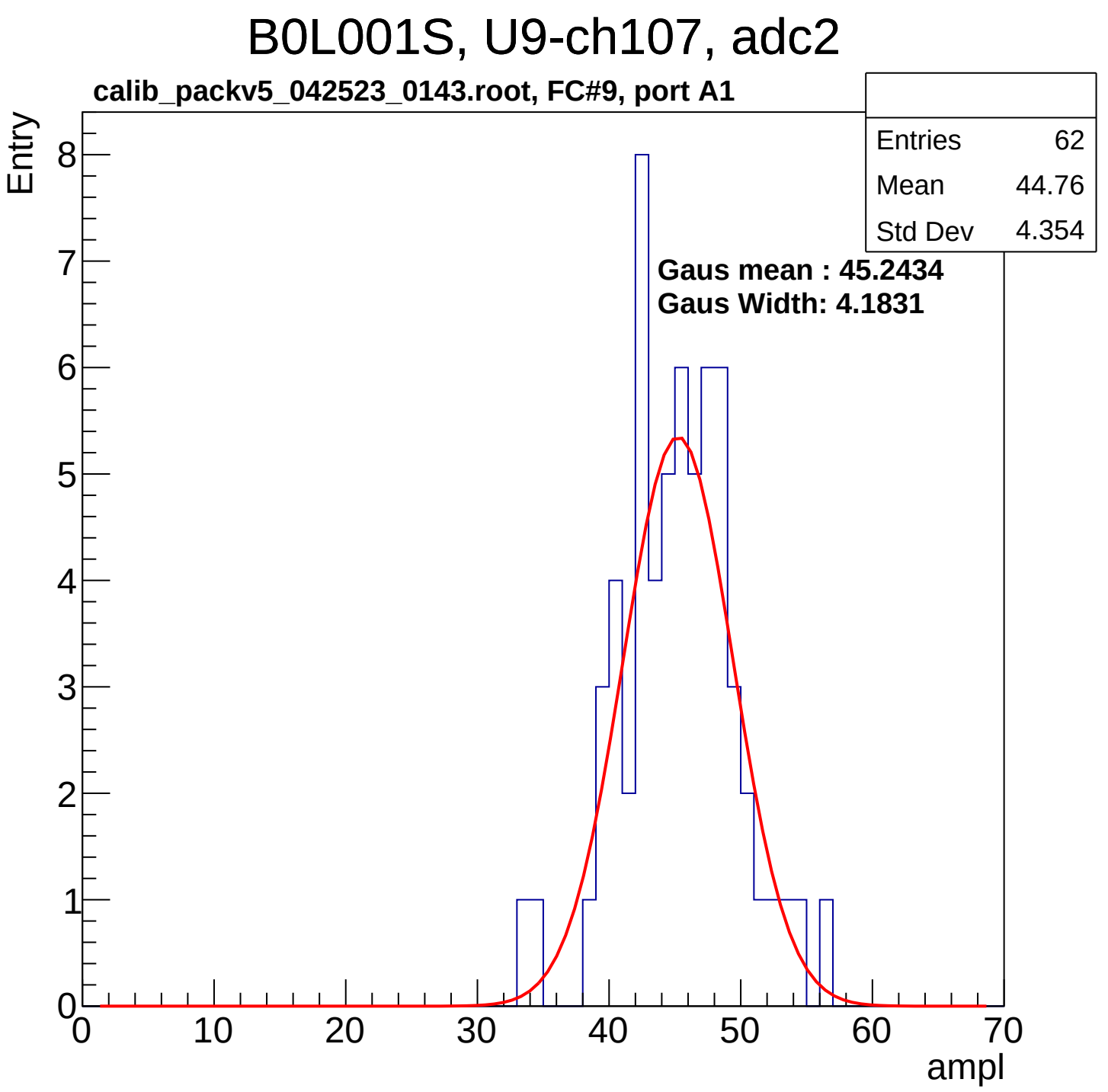

B0L001S, U9-ch106, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.04
Std Dev 12.07
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

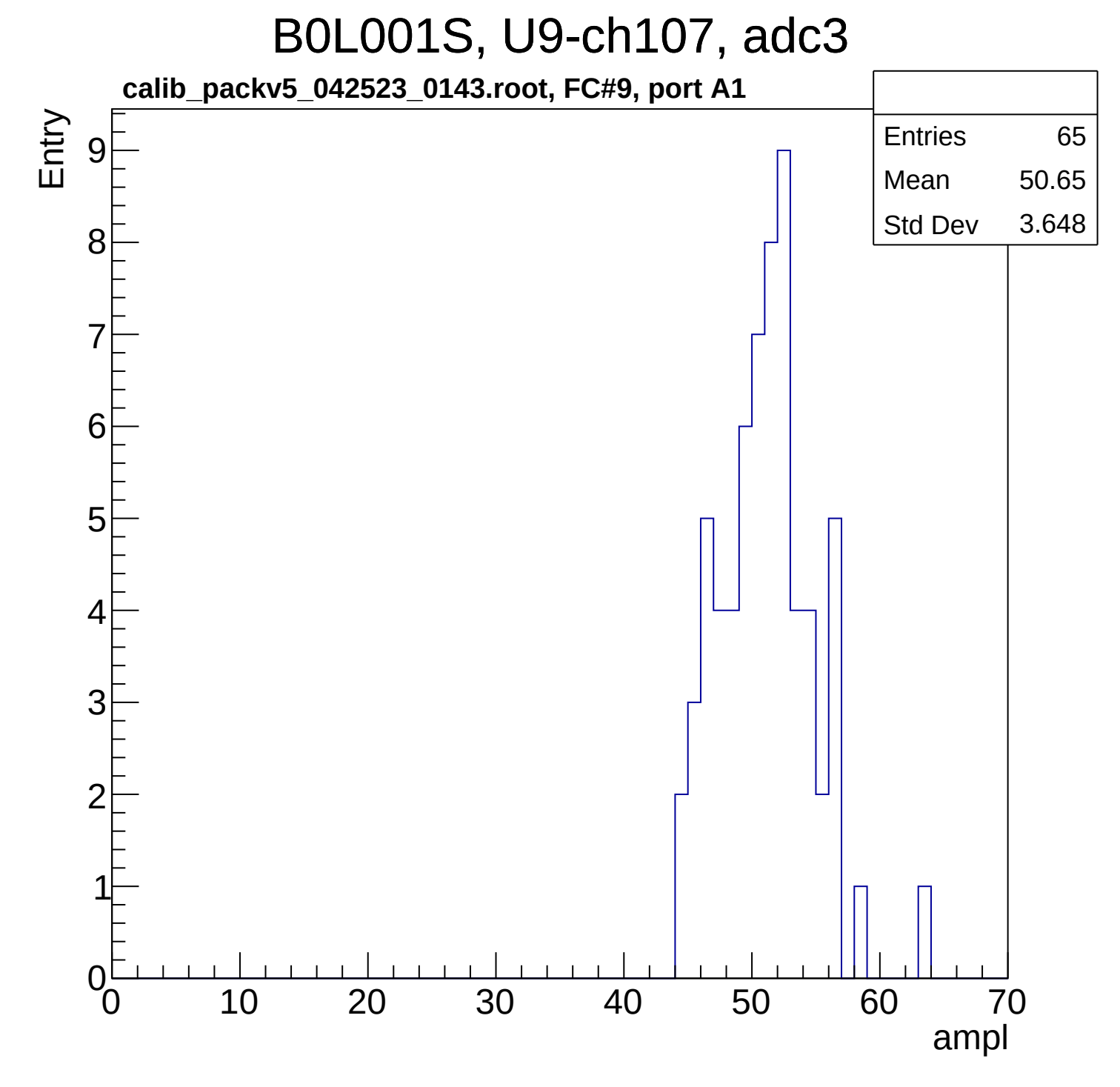

B0L001S, U9-ch106, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.71
Std Dev 1.278
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch106, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch107, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 29.57
Std Dev 6.141
Gaus mean : 30.4215
Gaus Width: 4.5893
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch107, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 37.96
Std Dev 3.635
Gaus mean : 38.4247
Gaus Width: 3.9335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch107, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.76
Std Dev 4.354
Gaus mean : 45.2434
Gaus Width: 4.1831
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch107, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 50.65
Std Dev 3.648
Entry
ampl
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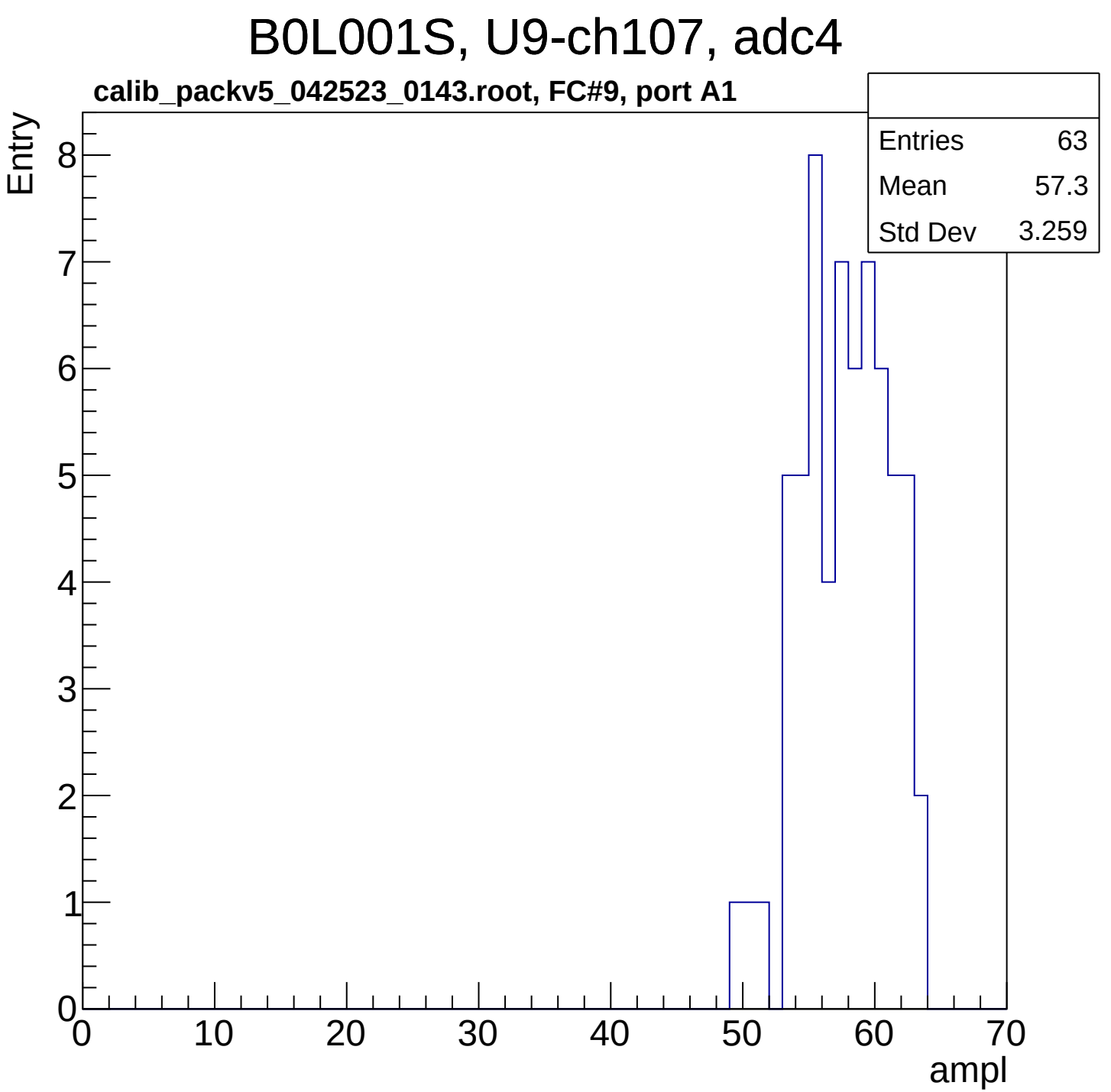
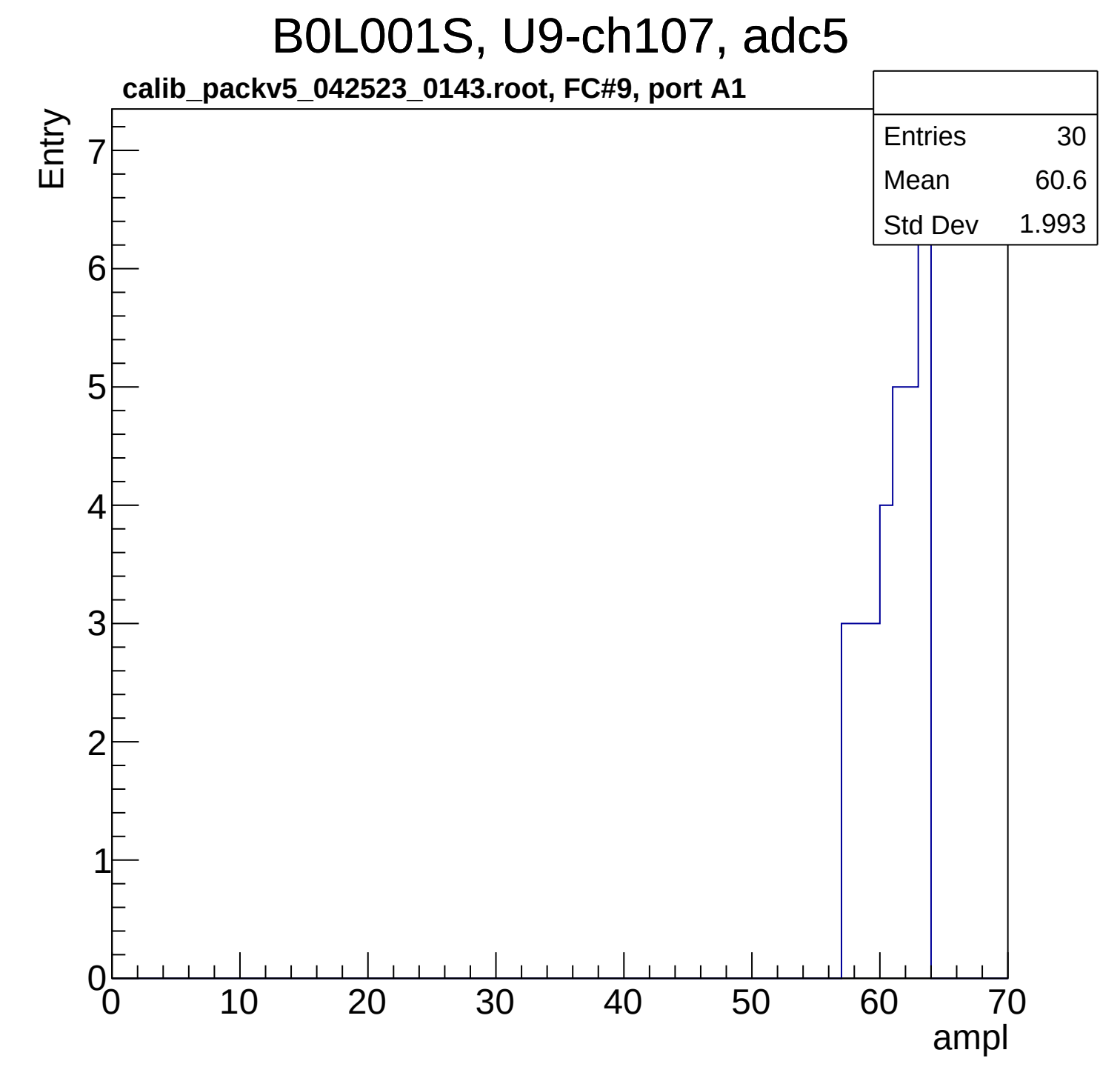
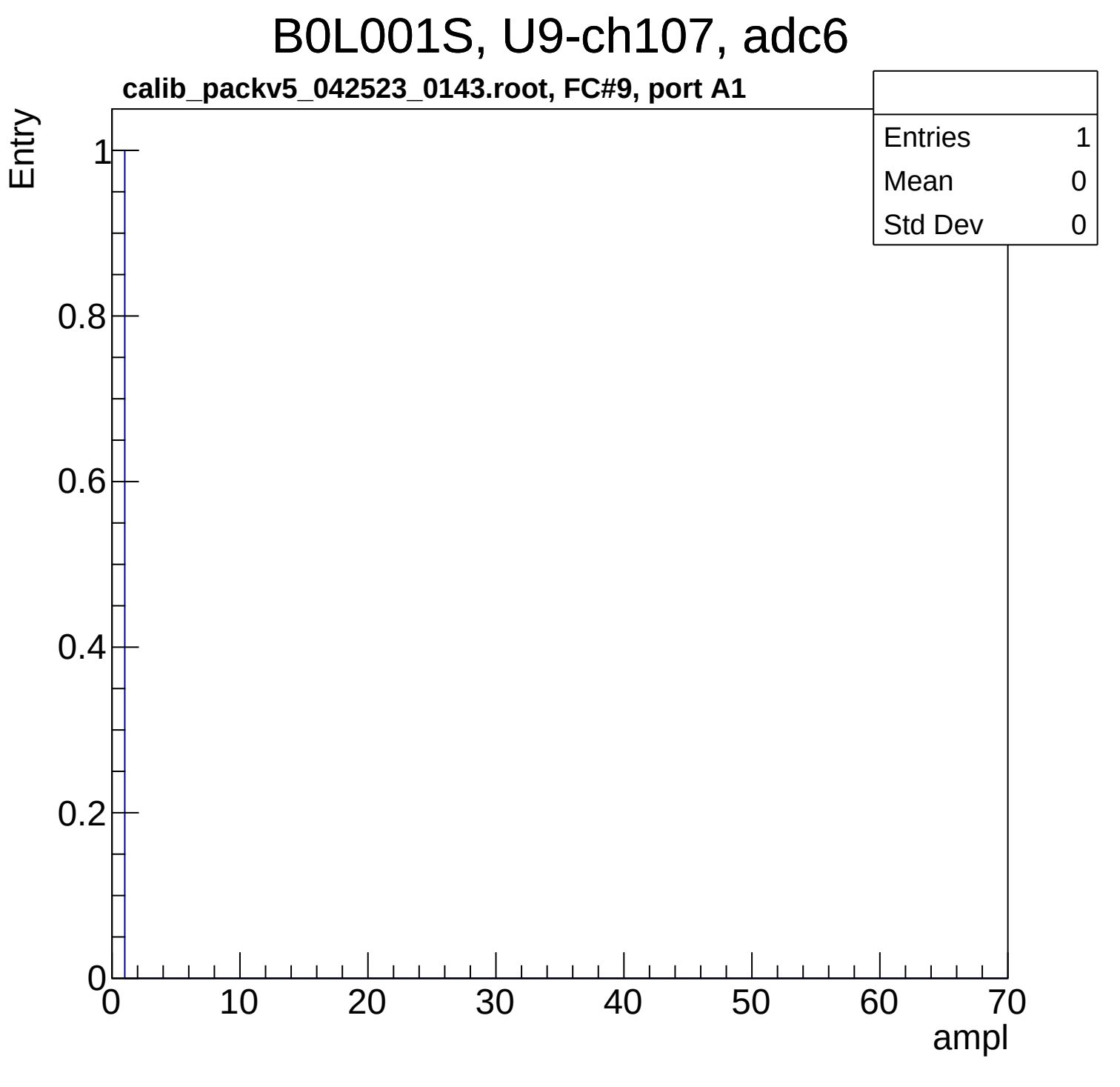
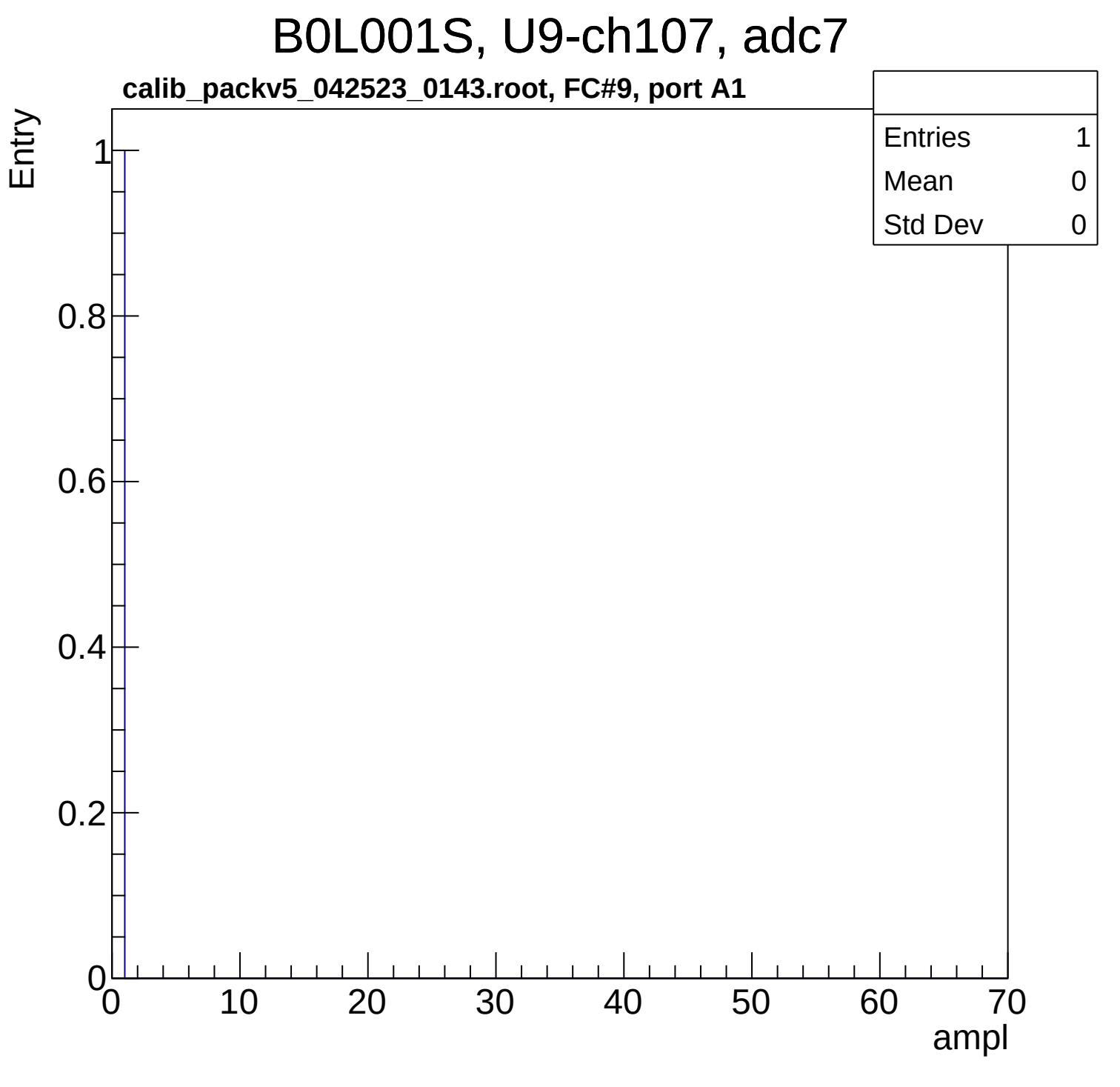
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch107, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 57.3
Std Dev 3.259
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch107, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 60.6
Std Dev 1.993
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch107, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch107, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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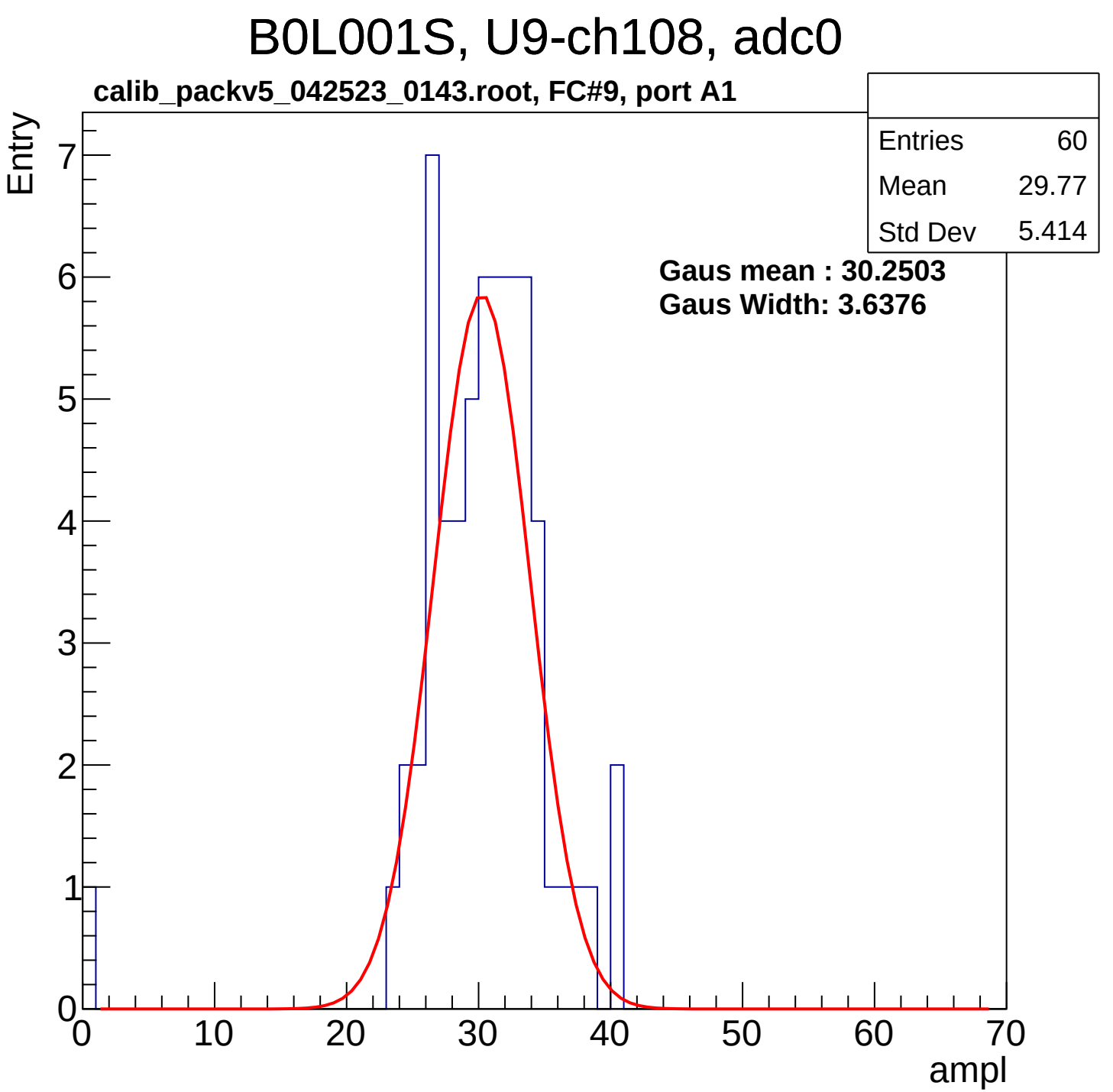
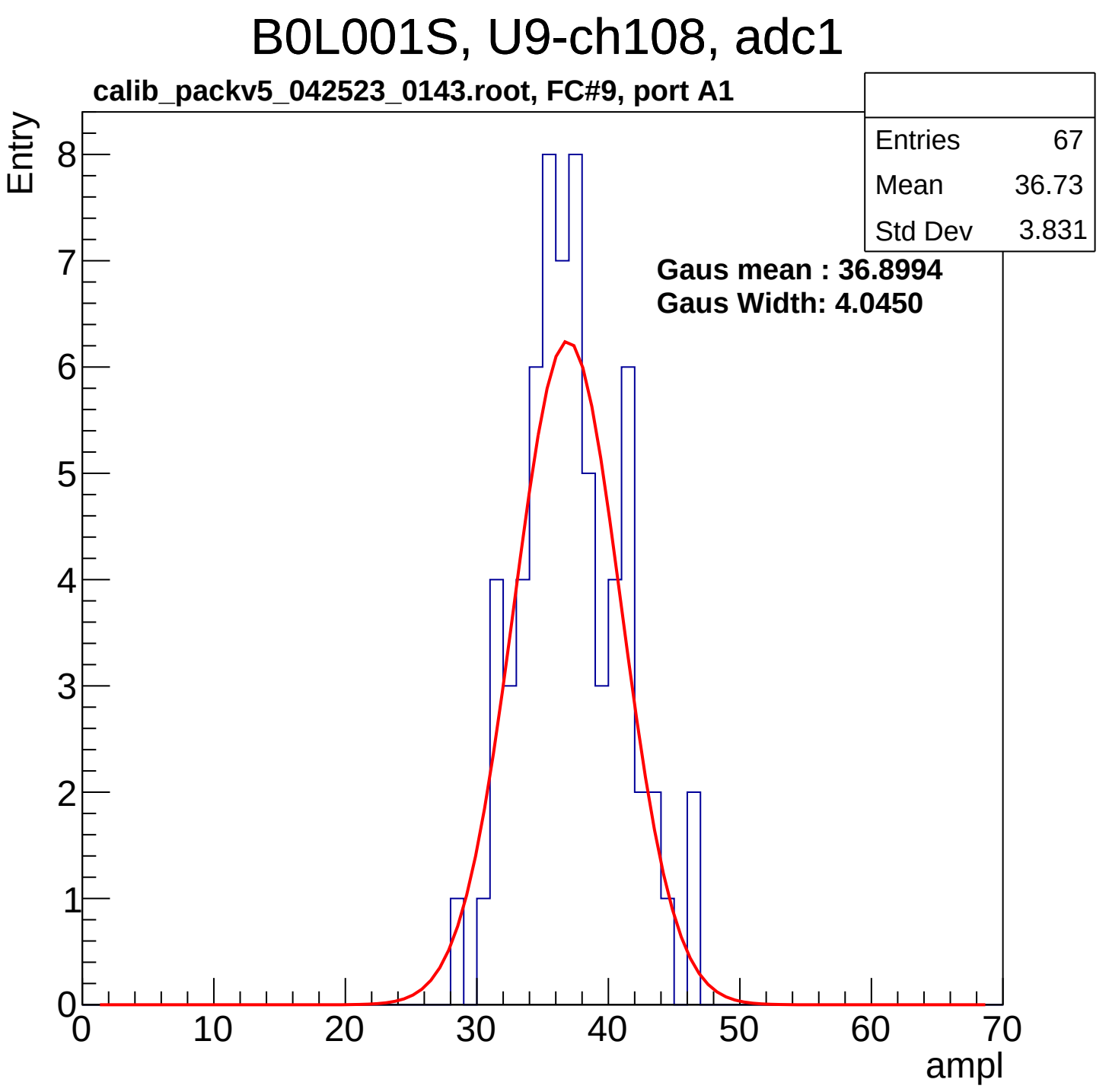
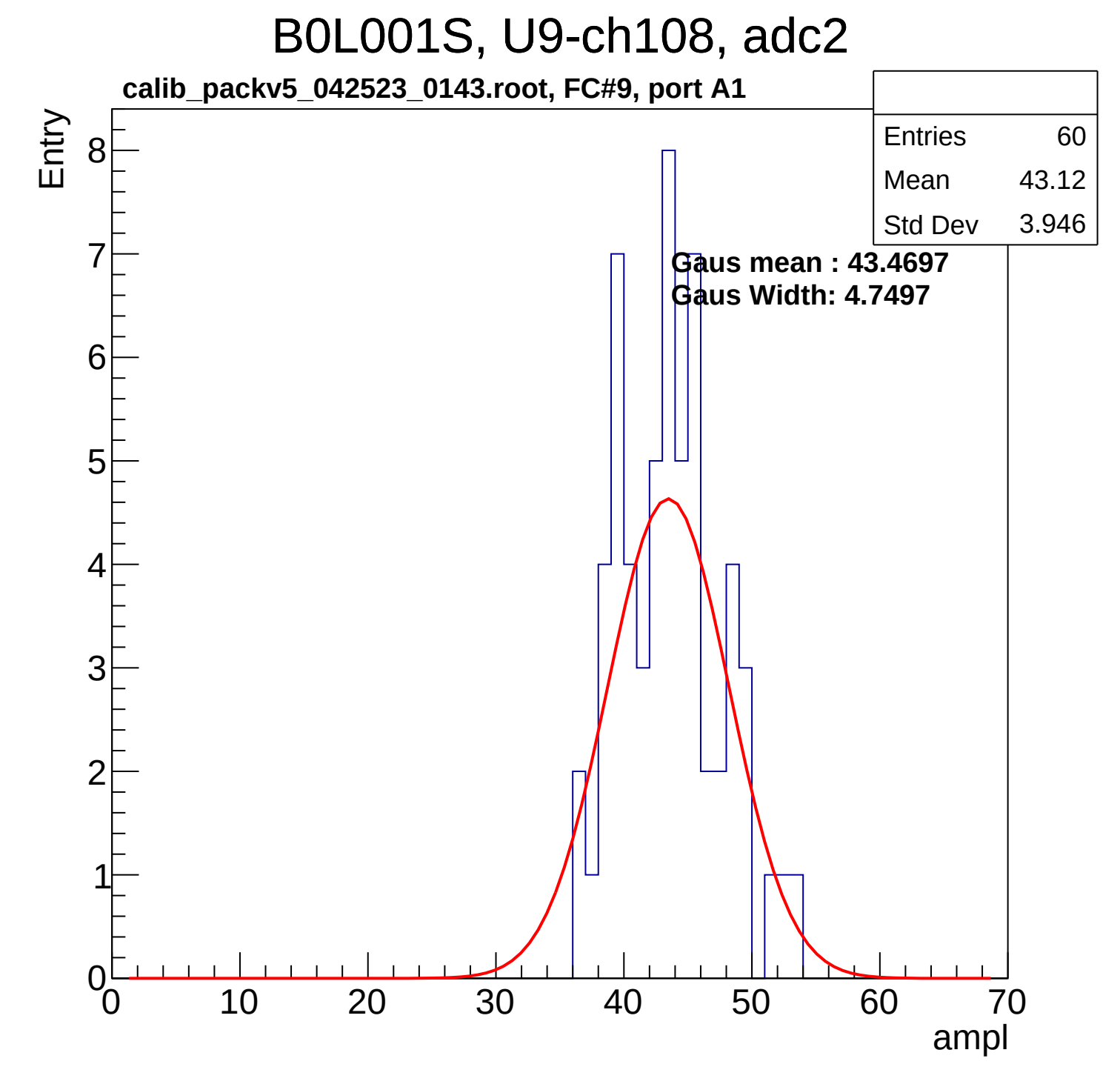
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch108, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 29.77
Std Dev 5.414
Gaus mean : 30.2503
Gaus Width: 3.6376
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch108, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.73
Std Dev 3.831
Gaus mean : 36.8994
Gaus Width: 4.0450
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch108, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 43.12
Std Dev 3.946
Gaus mean : 43.4697
Gaus Width: 4.7497
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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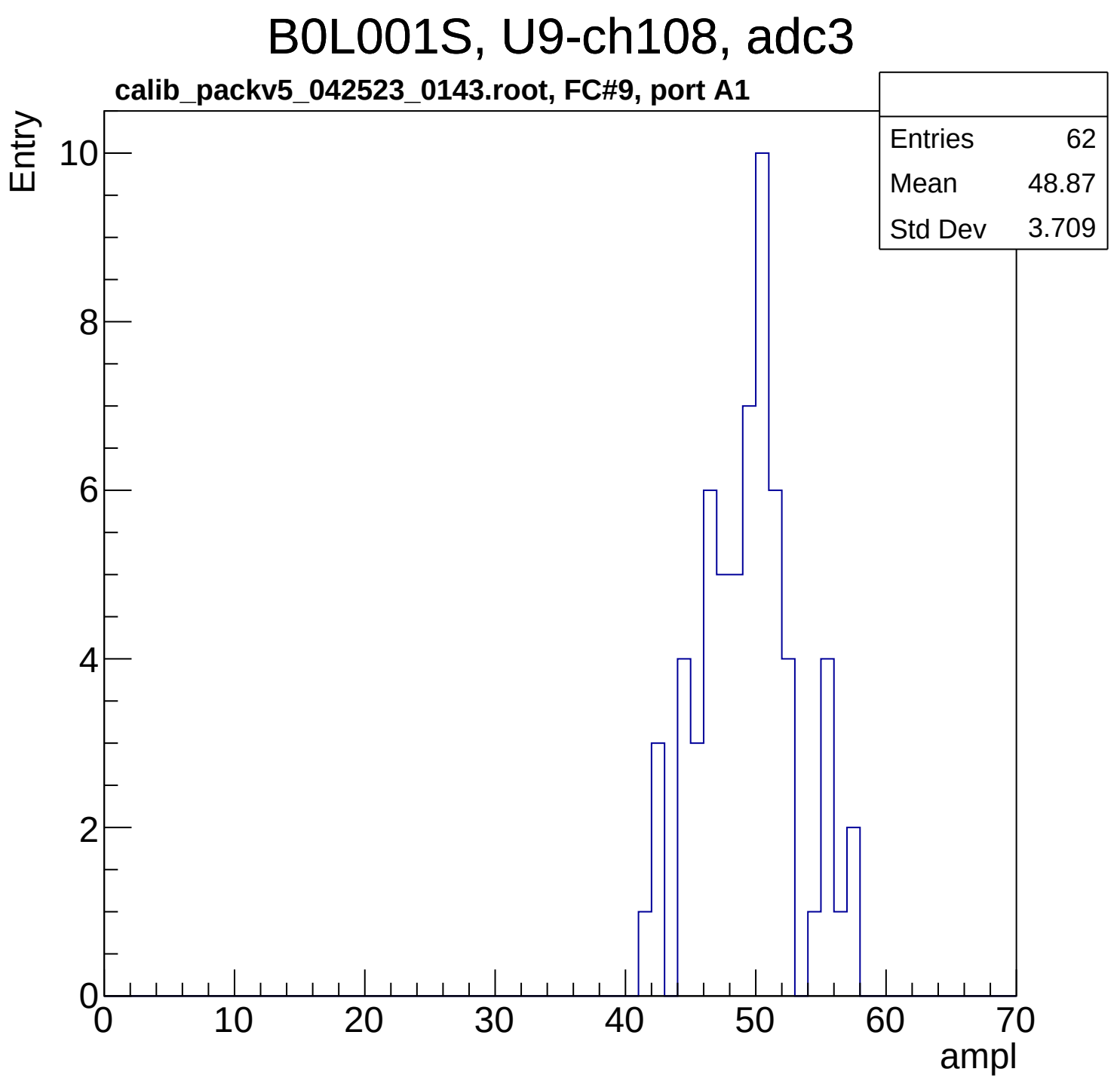
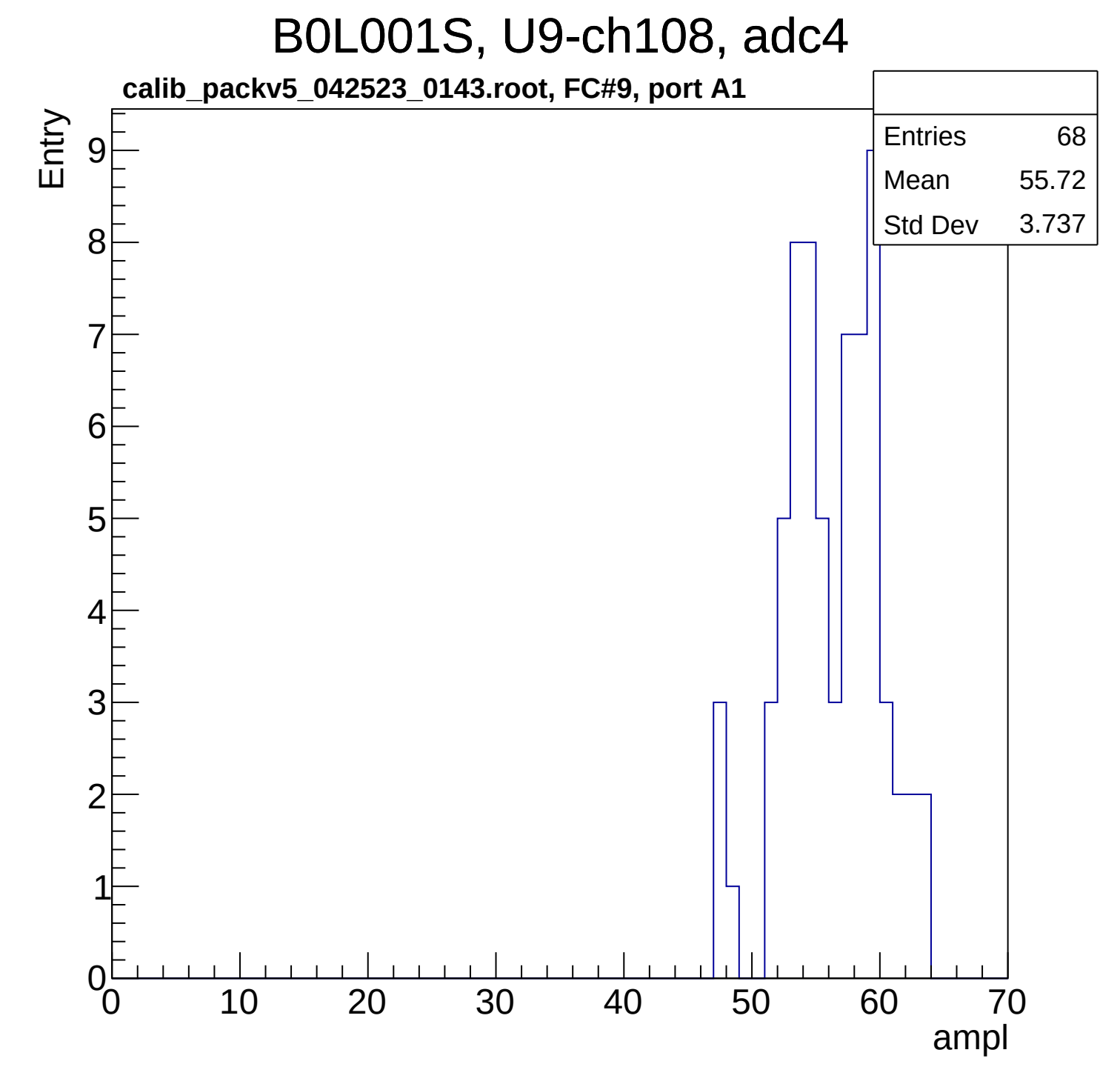

B0L001S, U9-ch108, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 48.87
Std Dev 3.709
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch108, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 55.72
Std Dev 3.737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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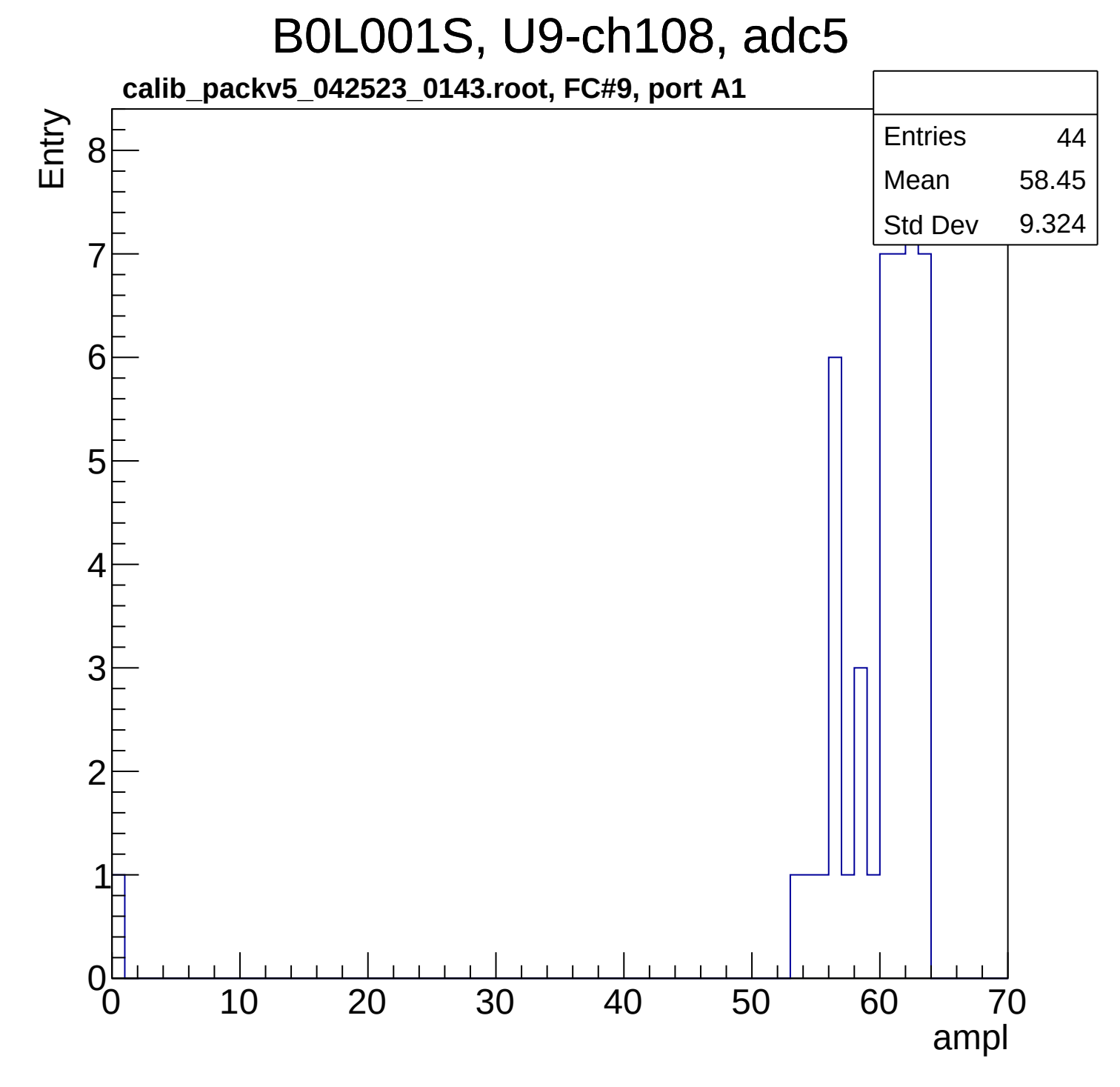
B0L001S, U9-ch108, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.45
Std Dev 9.324
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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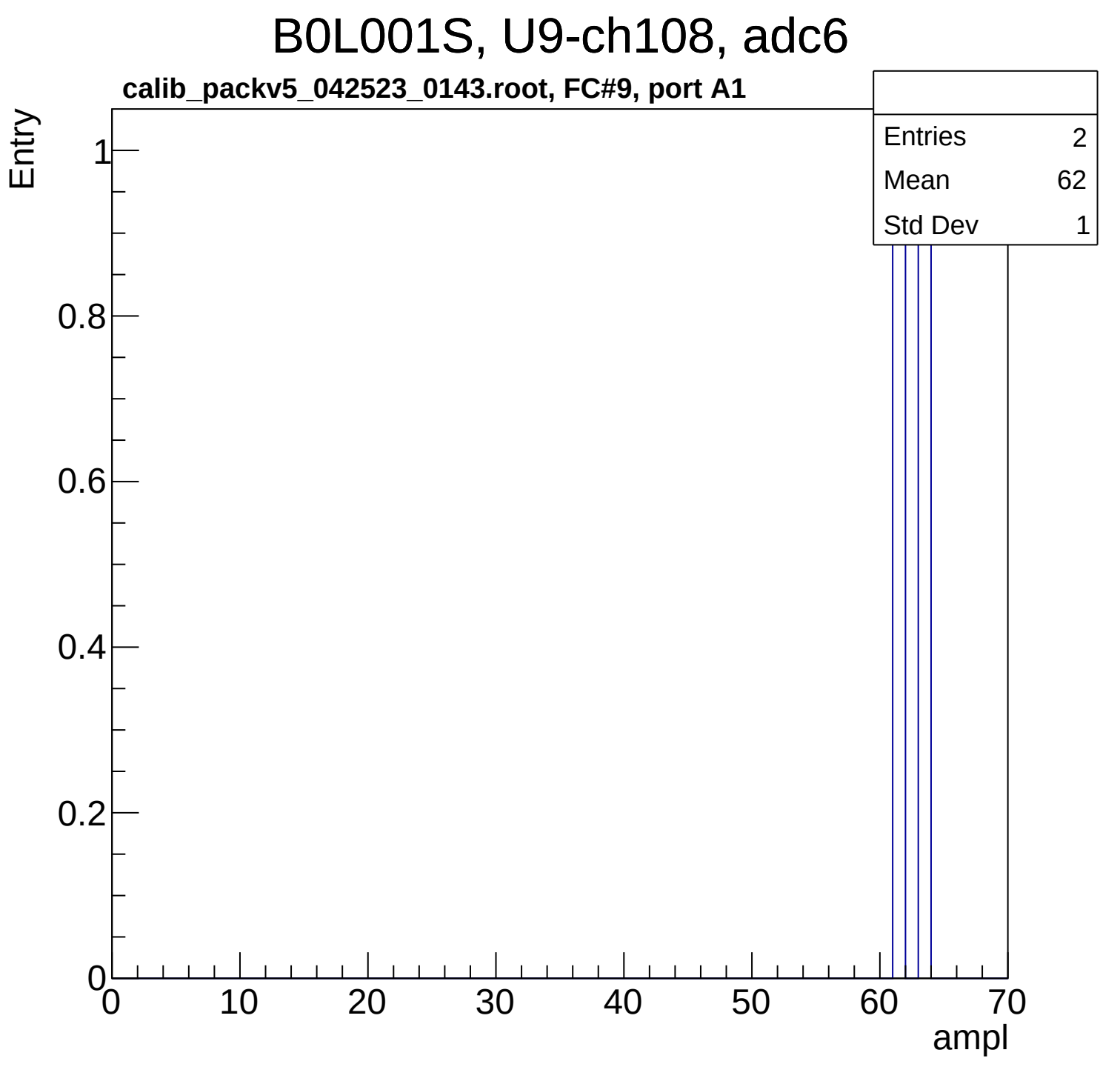
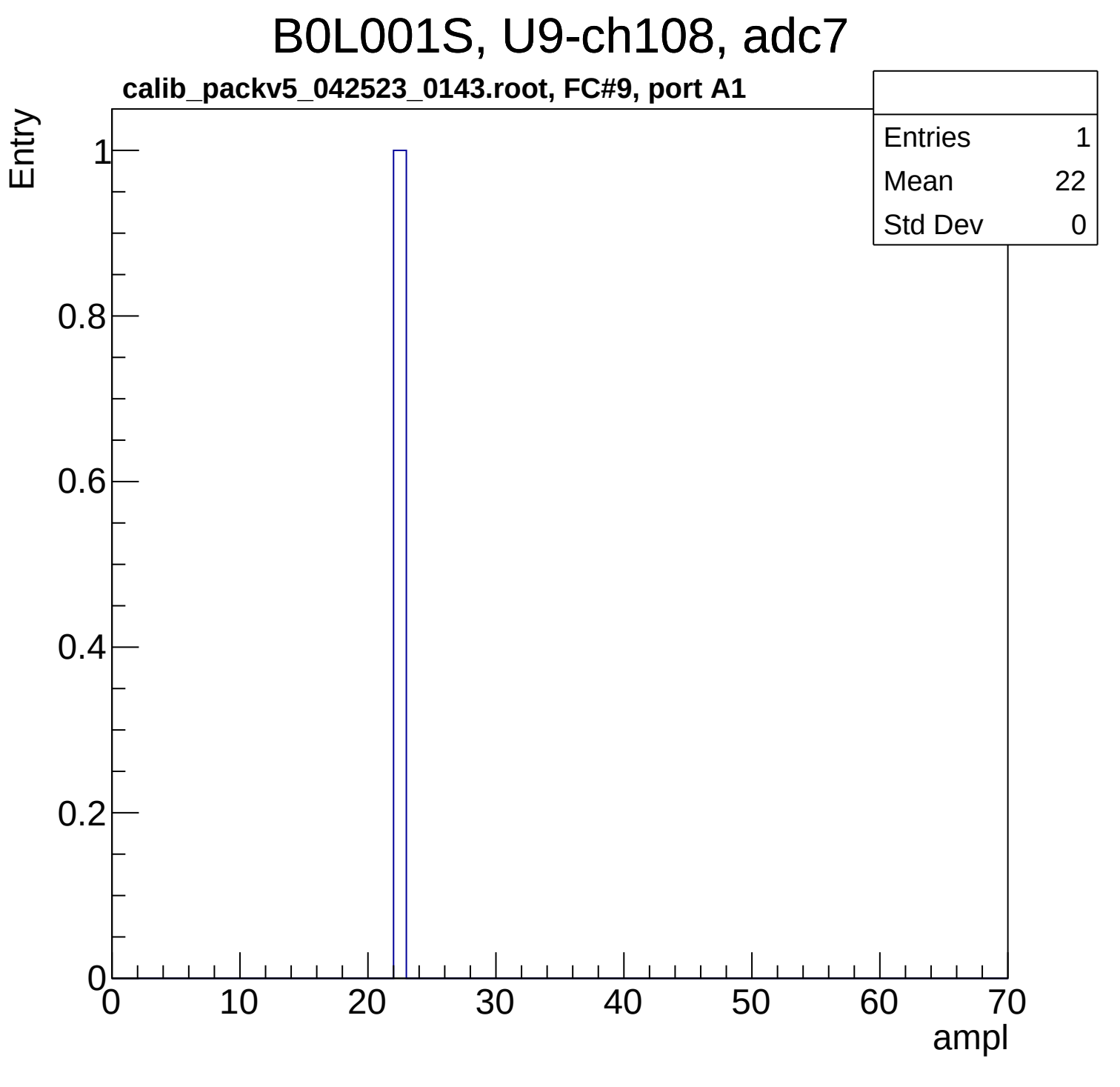
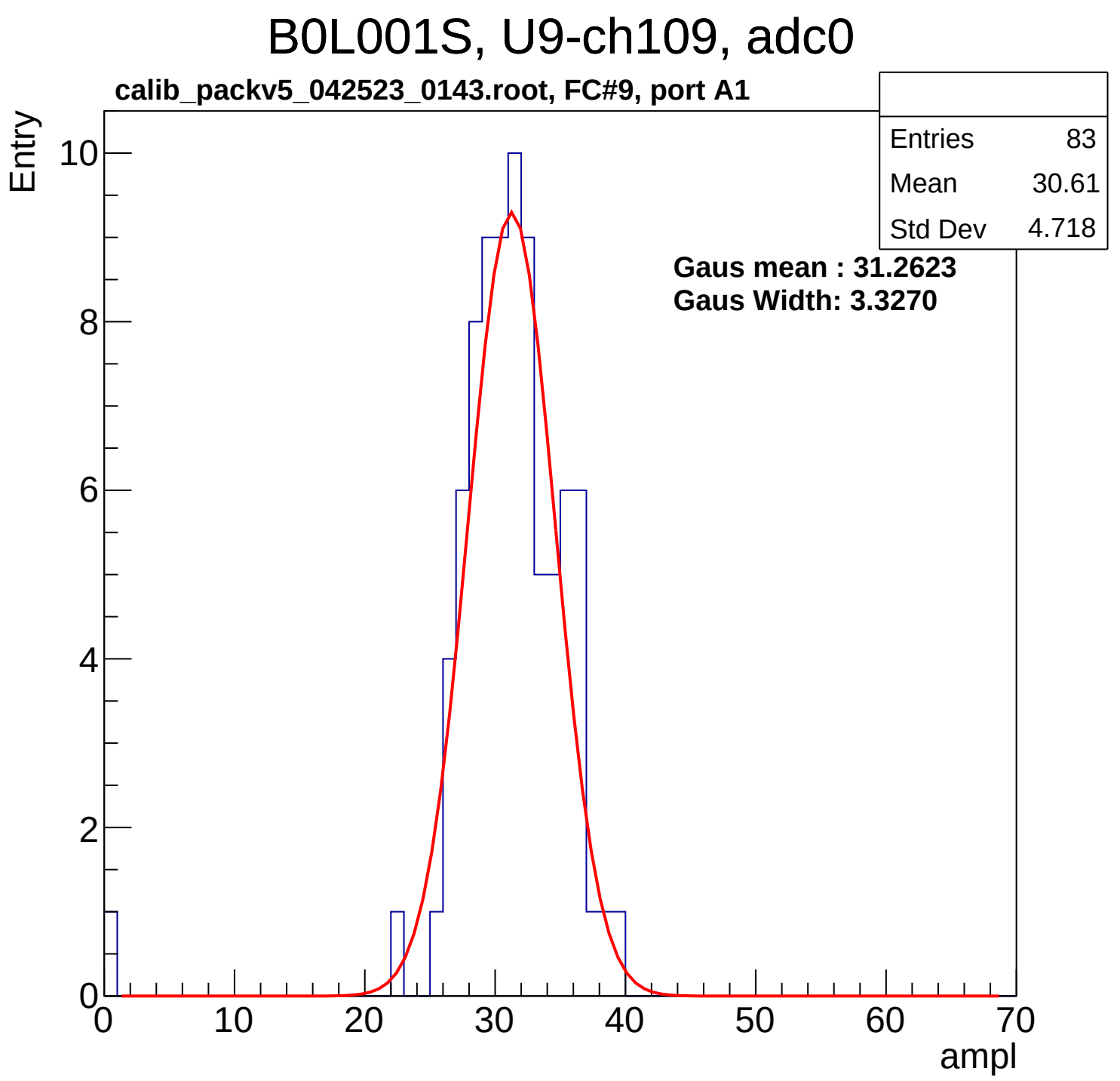
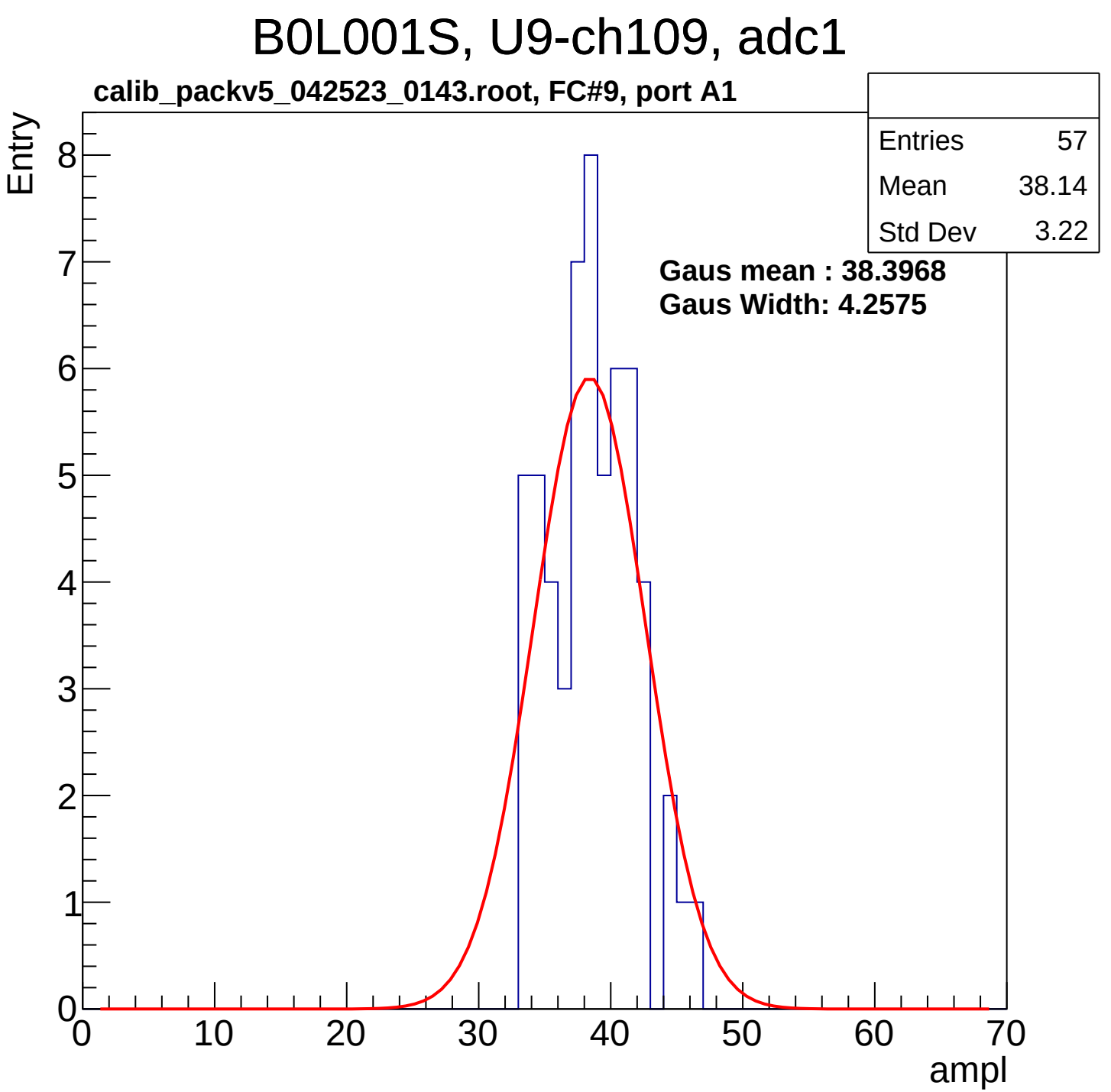
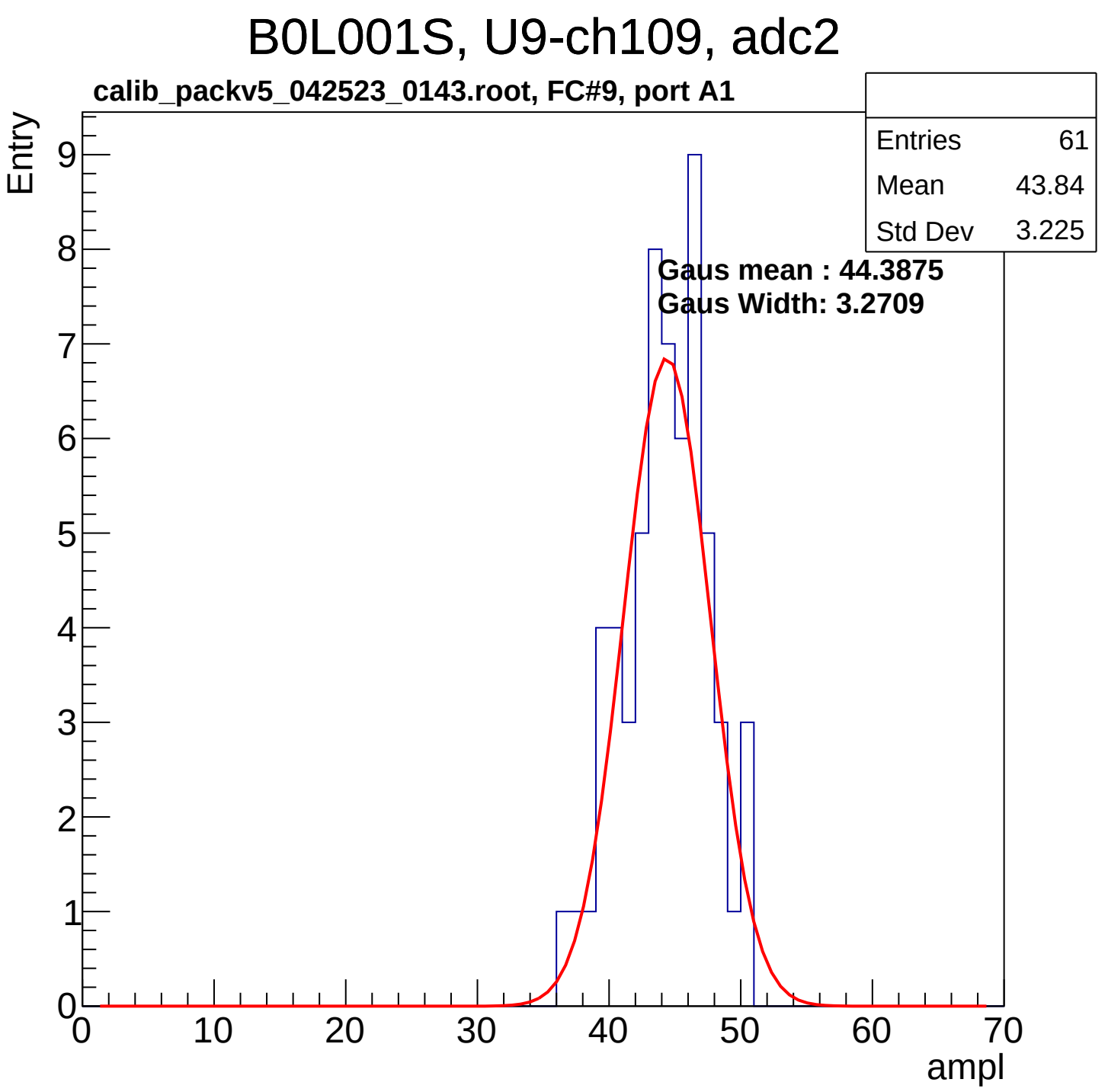
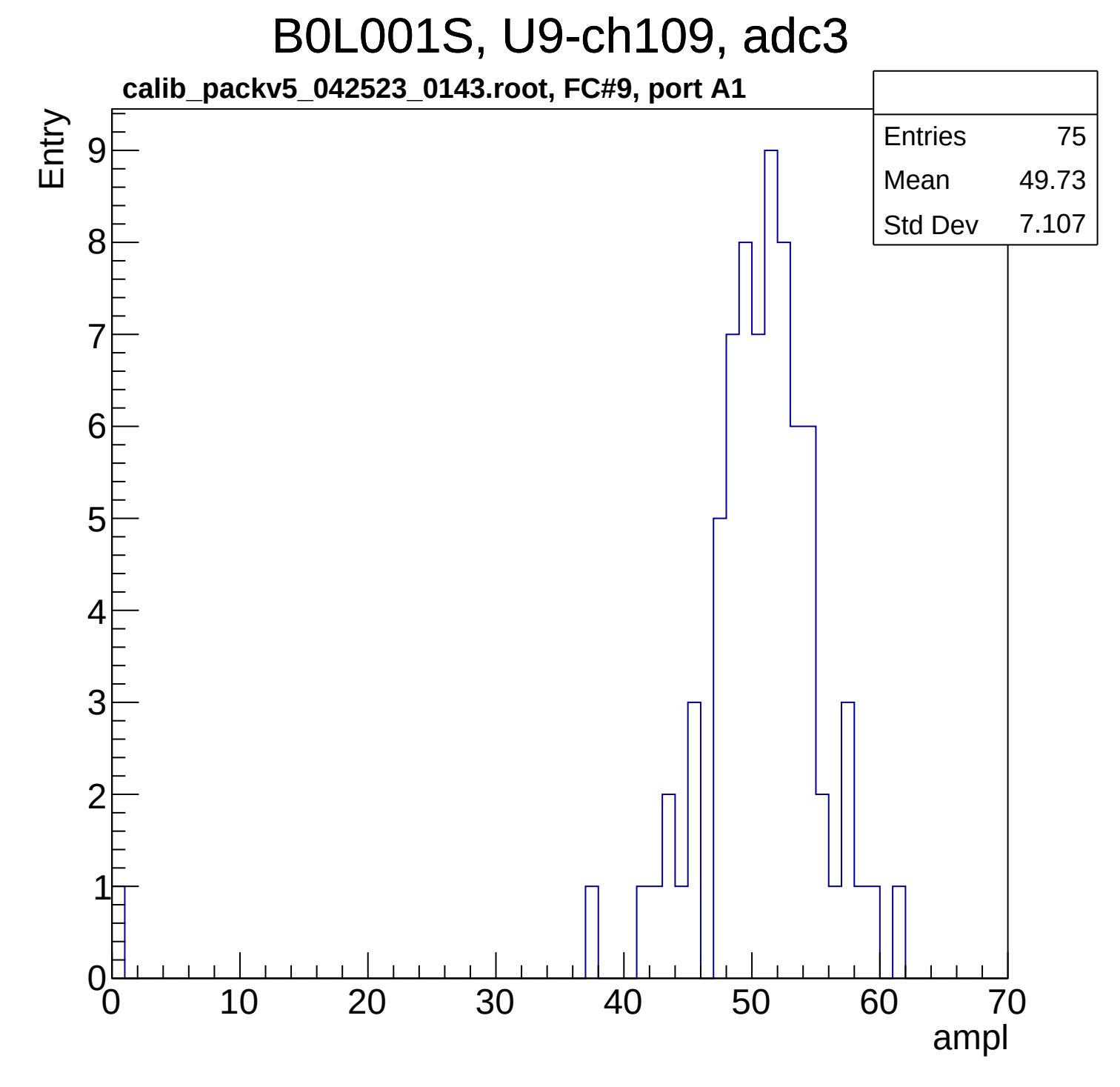

B0L001S, U9-ch108, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch108, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch109, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 30.61
Std Dev 4.718
Gaus mean : 31.2623
Gaus Width: 3.3270
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch109, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 38.14
Std Dev 3.22
Gaus mean : 38.3968
Gaus Width: 4.2575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch109, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.84
Std Dev 3.225
Gaus mean : 44.3875
Gaus Width: 3.2709
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch109, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 49.73
Std Dev 7.107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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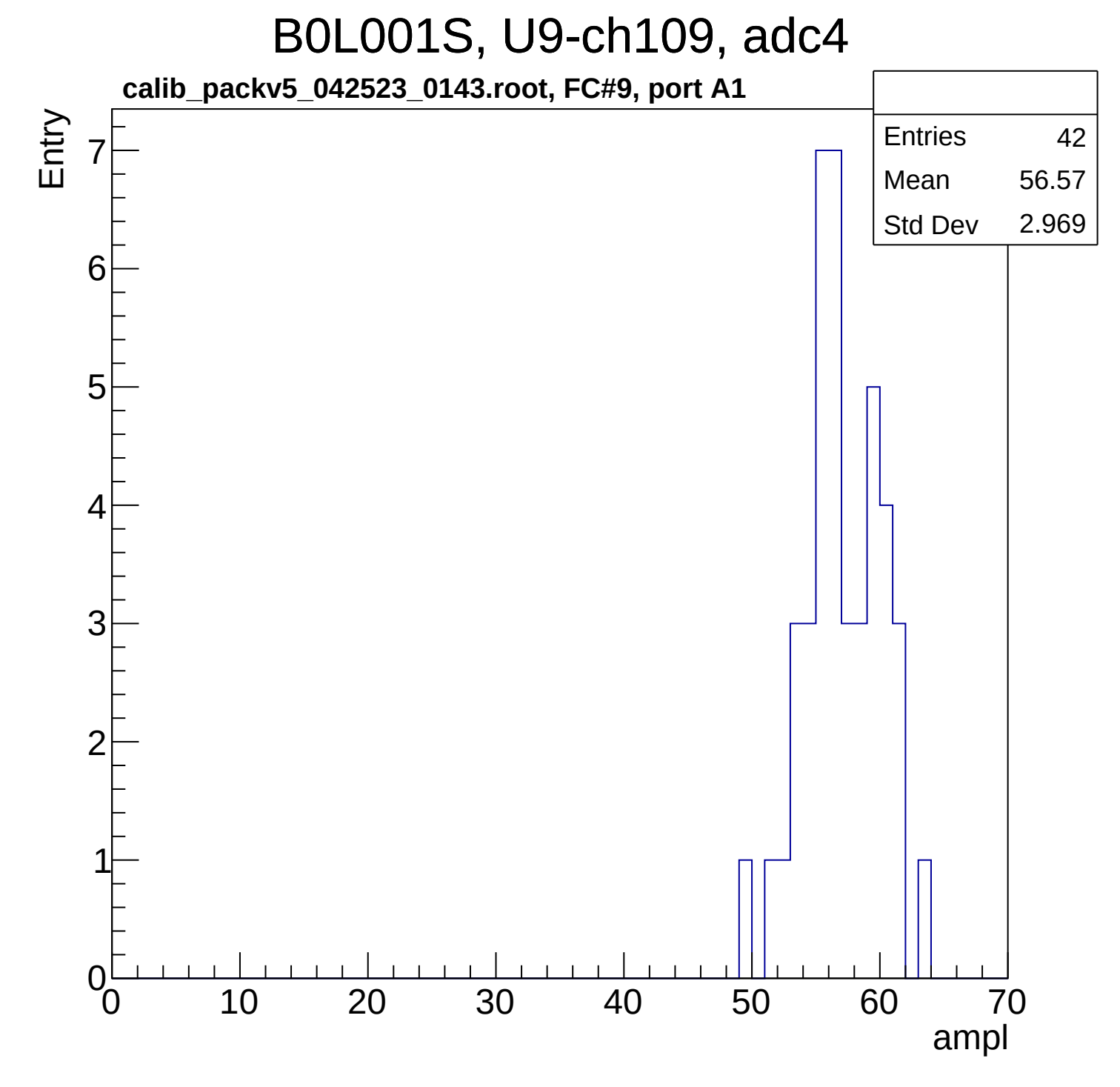
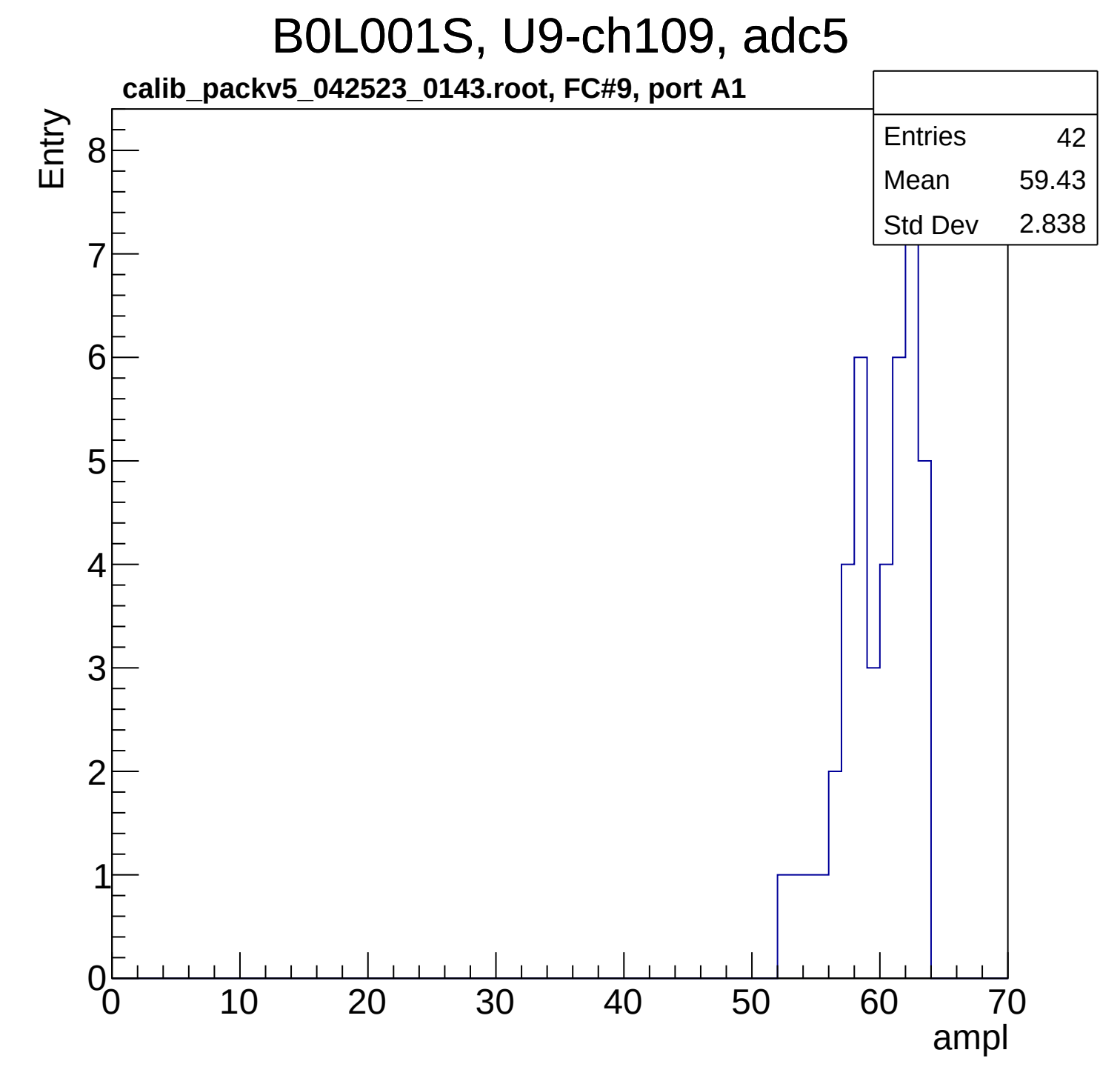

B0L001S, U9-ch109, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 56.57
Std Dev 2.969
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch109, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.43
Std Dev 2.838
Entry
ampl
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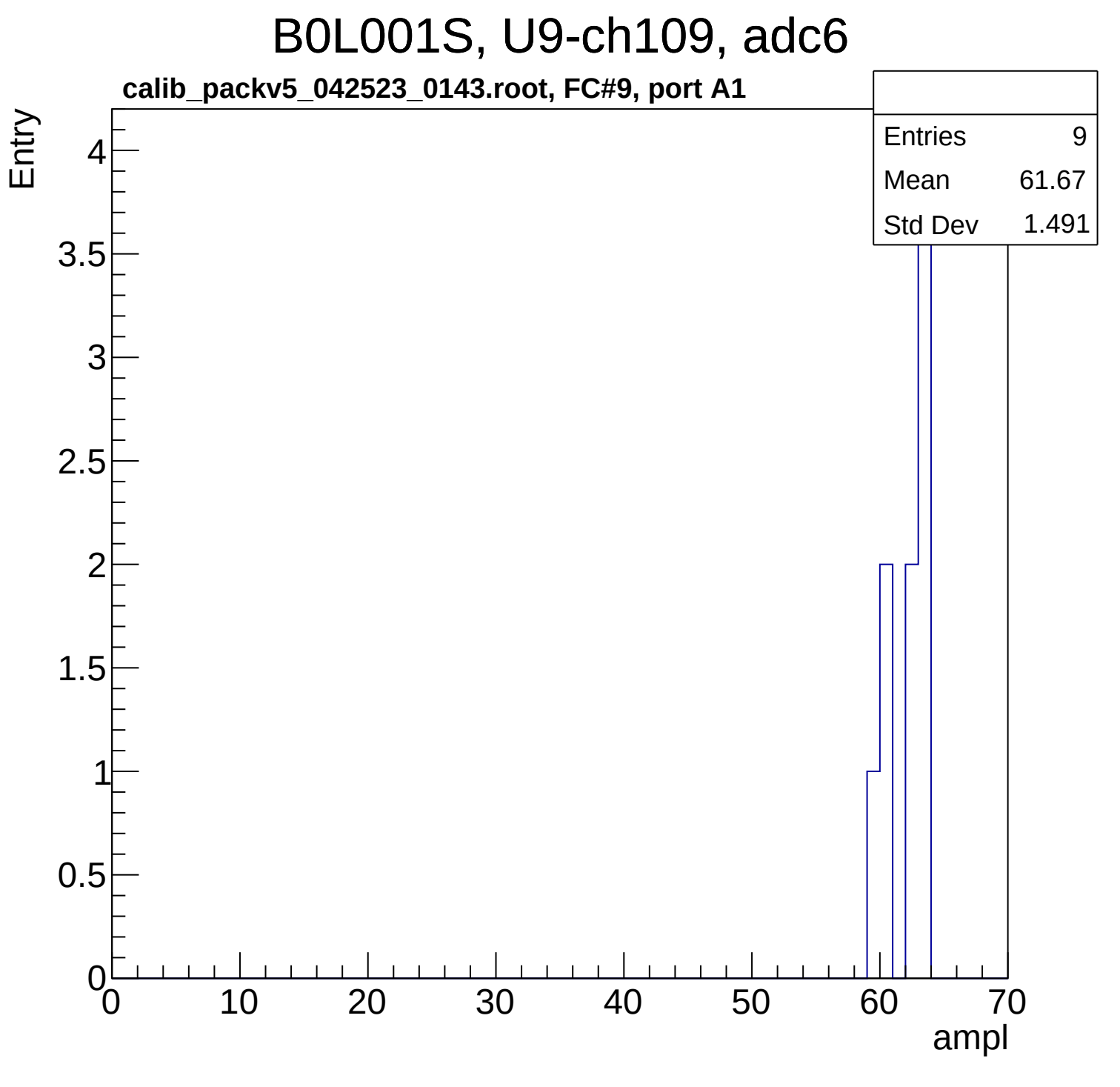
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch109, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.67
Std Dev 1.491
Entry
ampl
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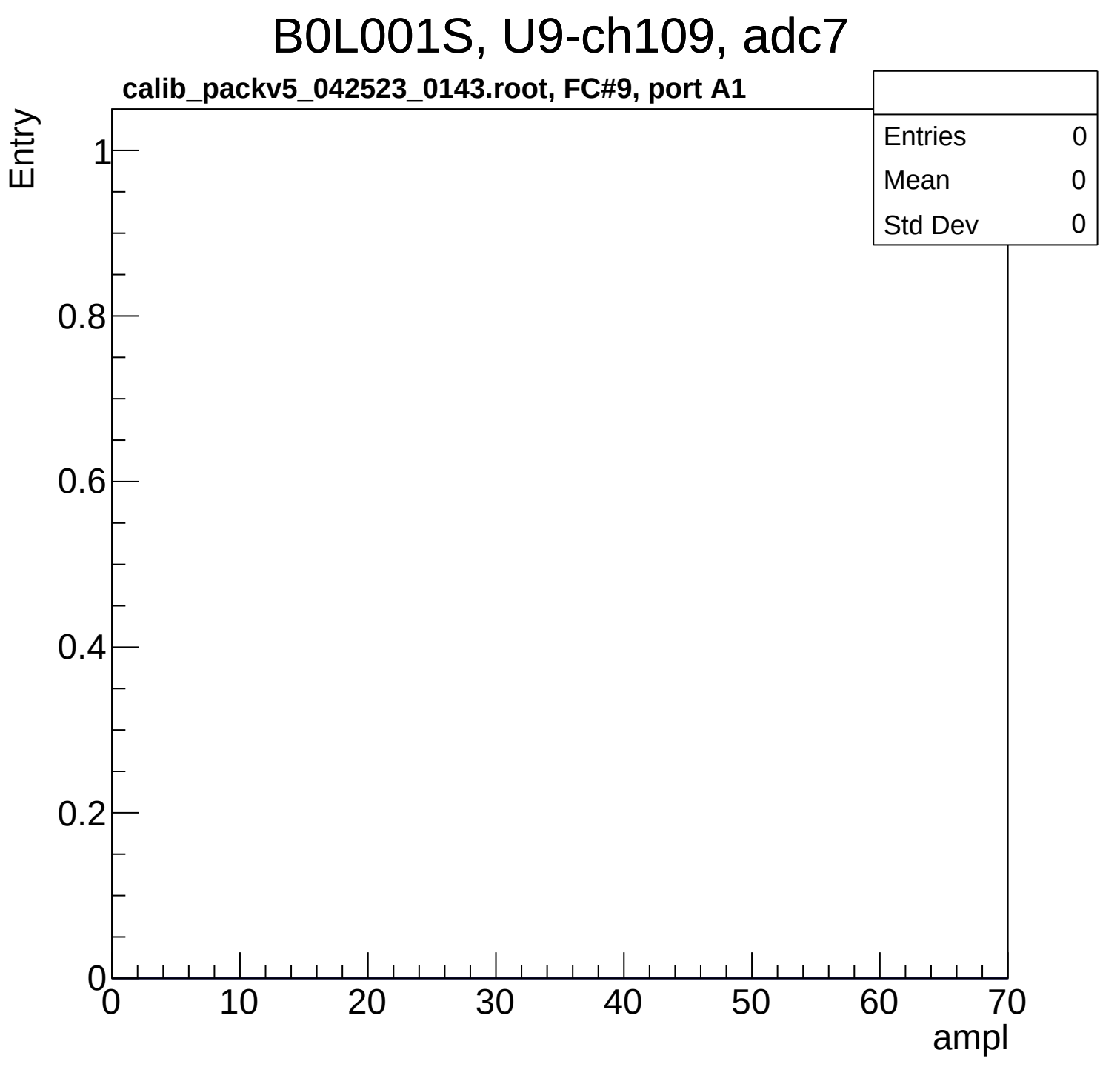
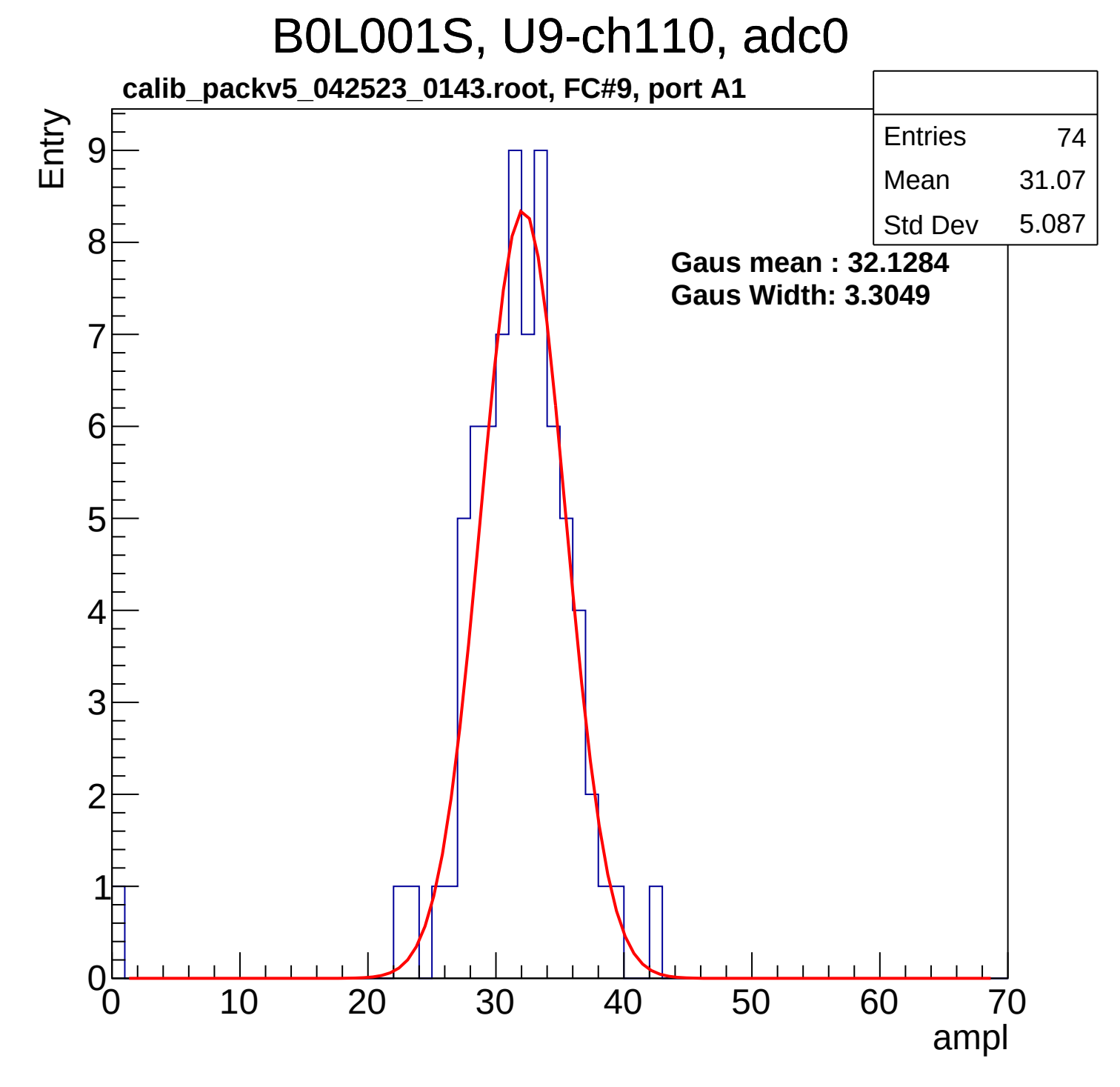
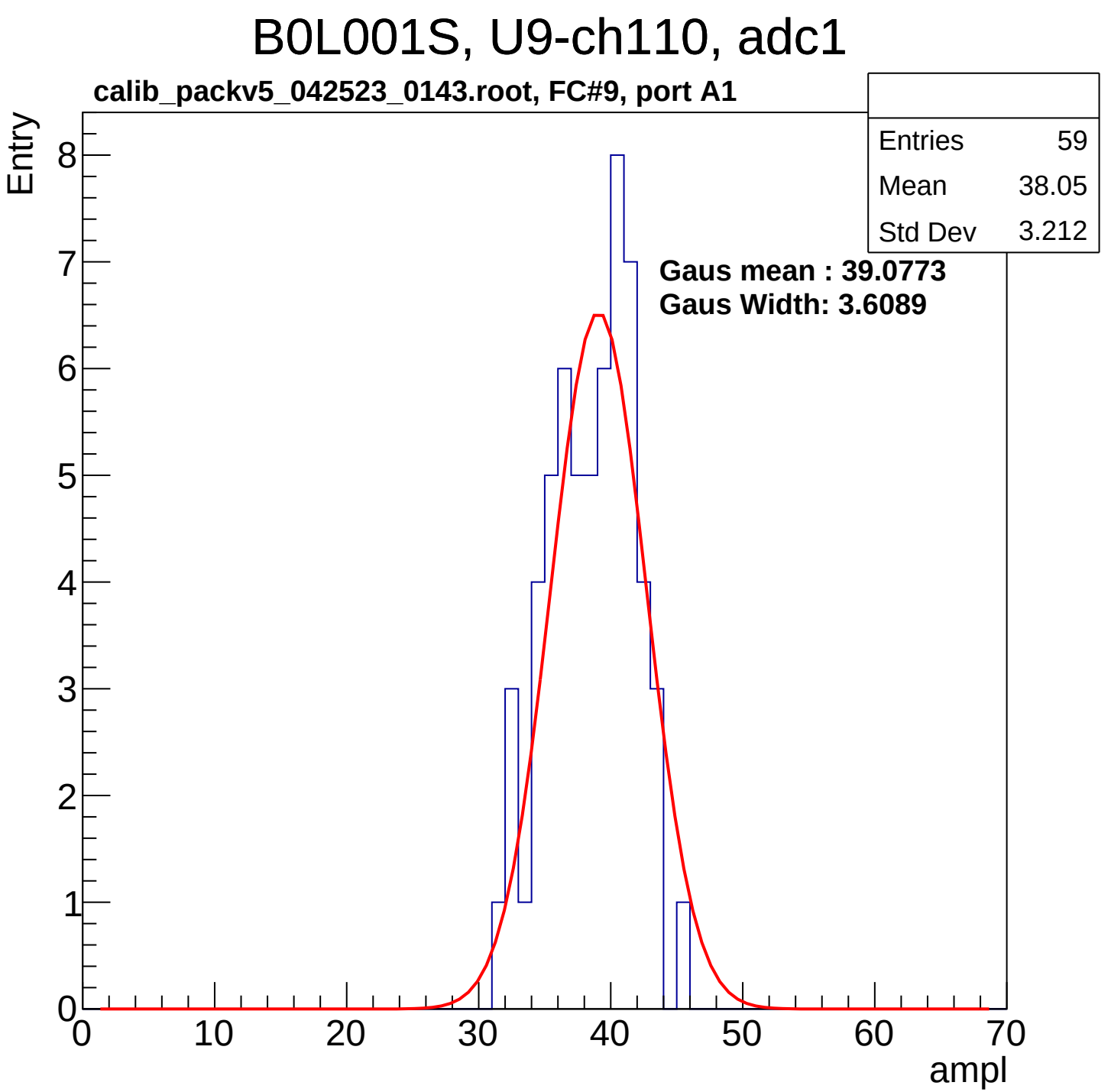
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U9-ch109, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch110, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 31.07
Std Dev 5.087
Gaus mean : 32.1284
Gaus Width: 3.3049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch110, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 38.05
Std Dev 3.212
Gaus mean : 39.0773
Gaus Width: 3.6089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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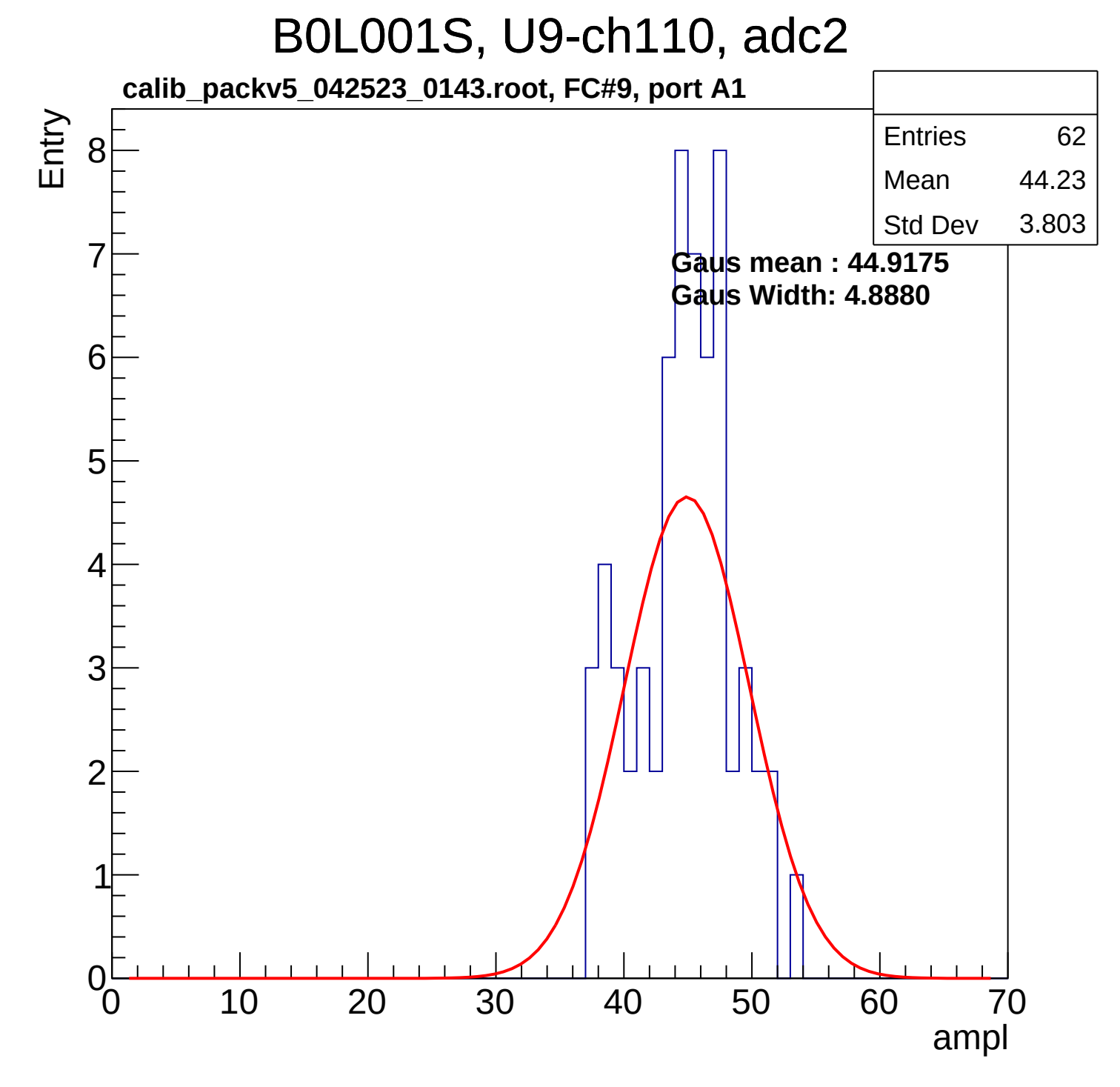
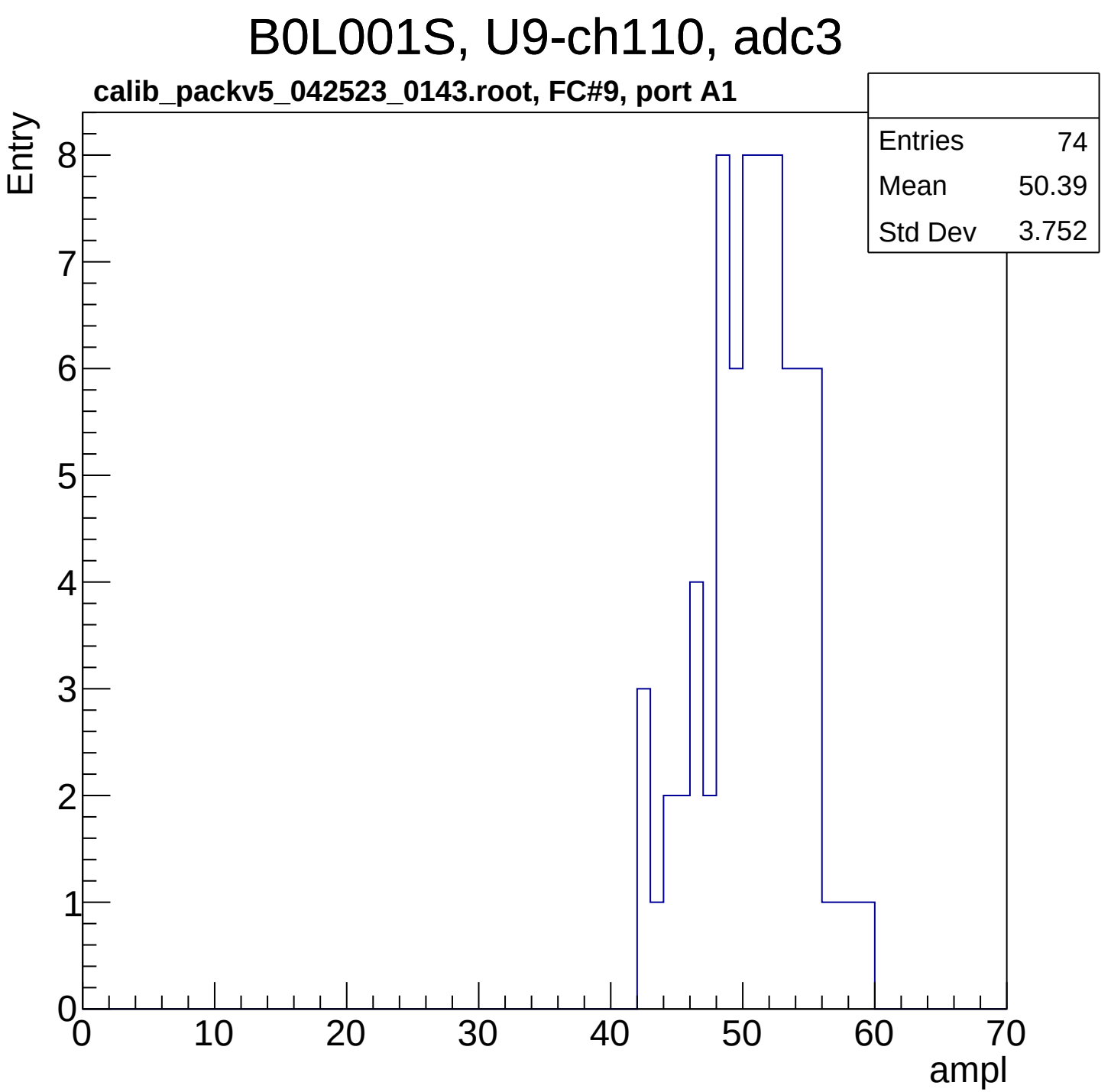

B0L001S, U9-ch110, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.23
Std Dev 3.803
Gaus mean : 44.9175
Gaus Width: 4.8880
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch110, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.39
Std Dev 3.752
Entry
ampl
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1
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3
4
5
6
7
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0
10
20
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40
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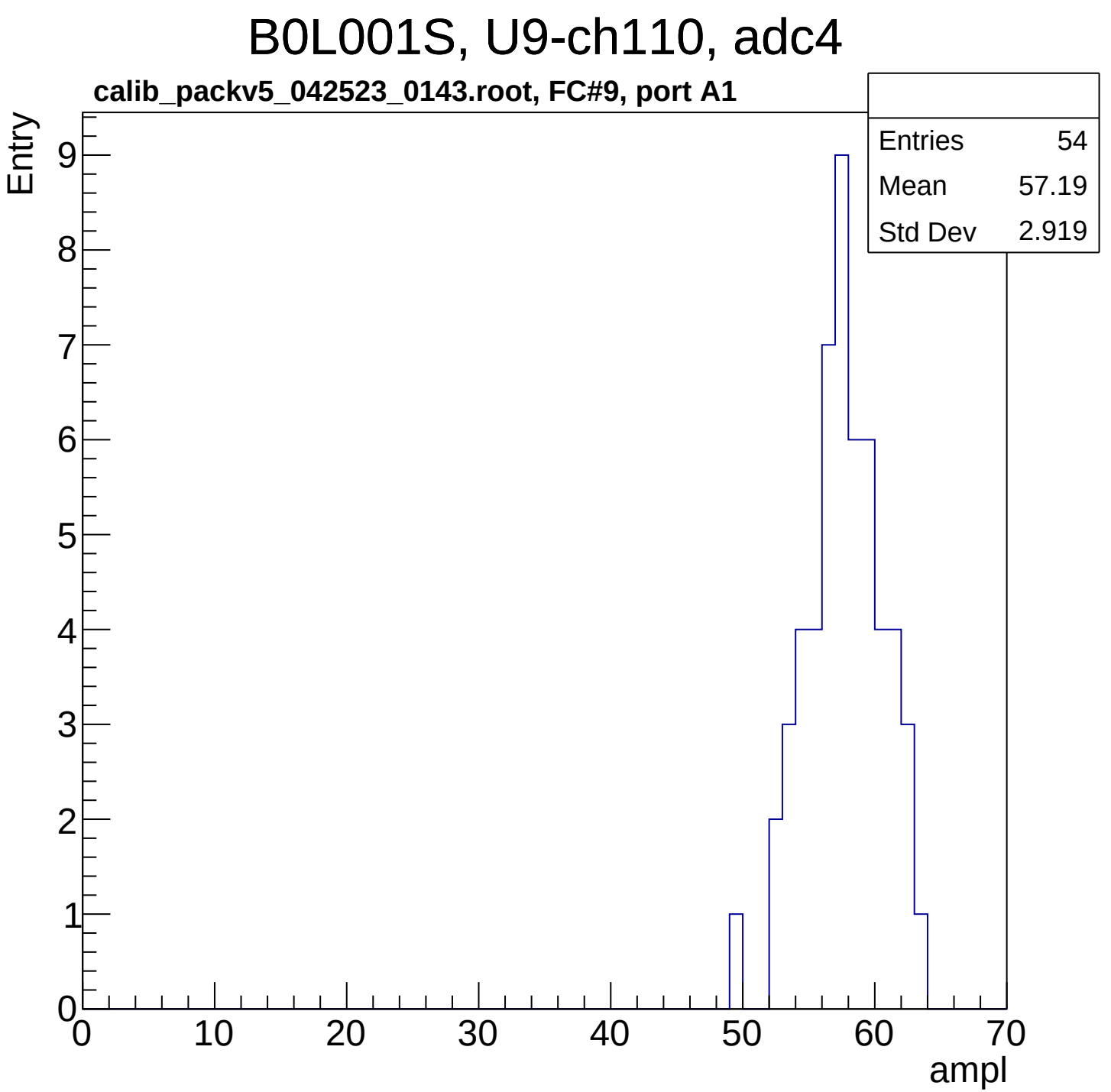
60
70


B0L001S, U9-ch110, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 57.19
Std Dev 2.919
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
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60
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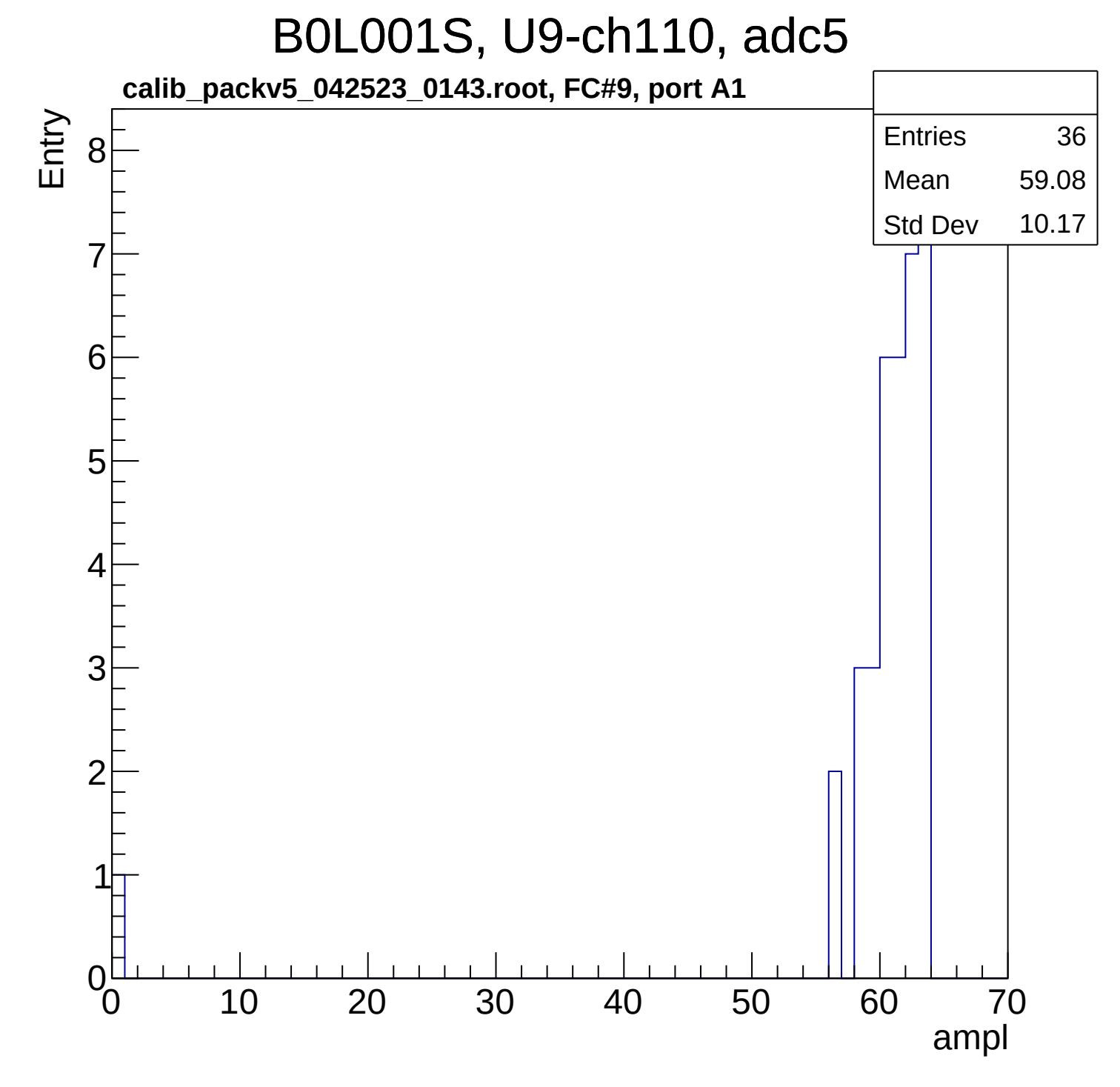
B0L001S, U9-ch110, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.08
Std Dev 10.17
Entry
ampl
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1
2
3
4
5
6
7
8
0
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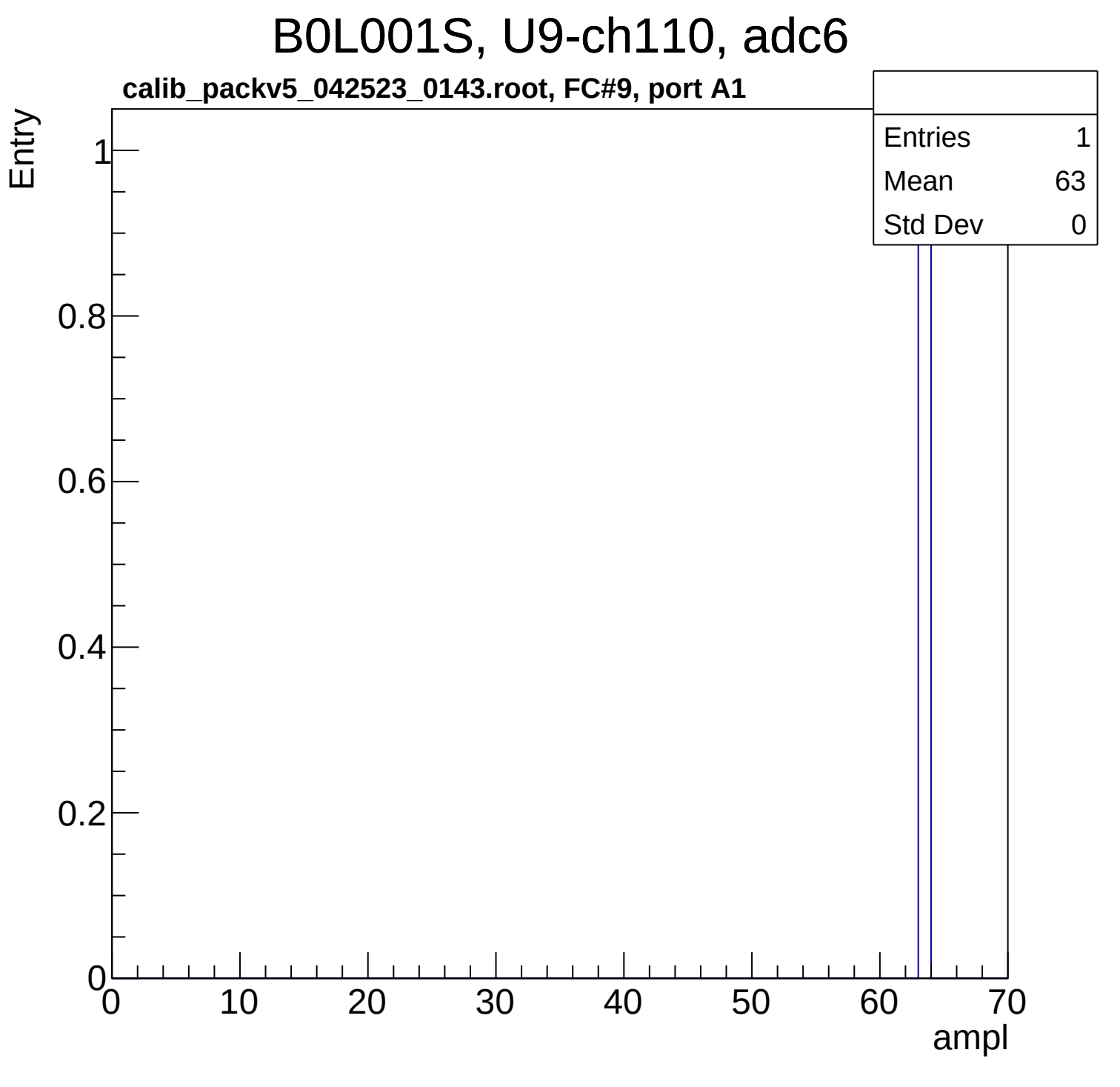
60
70


B0L001S, U9-ch110, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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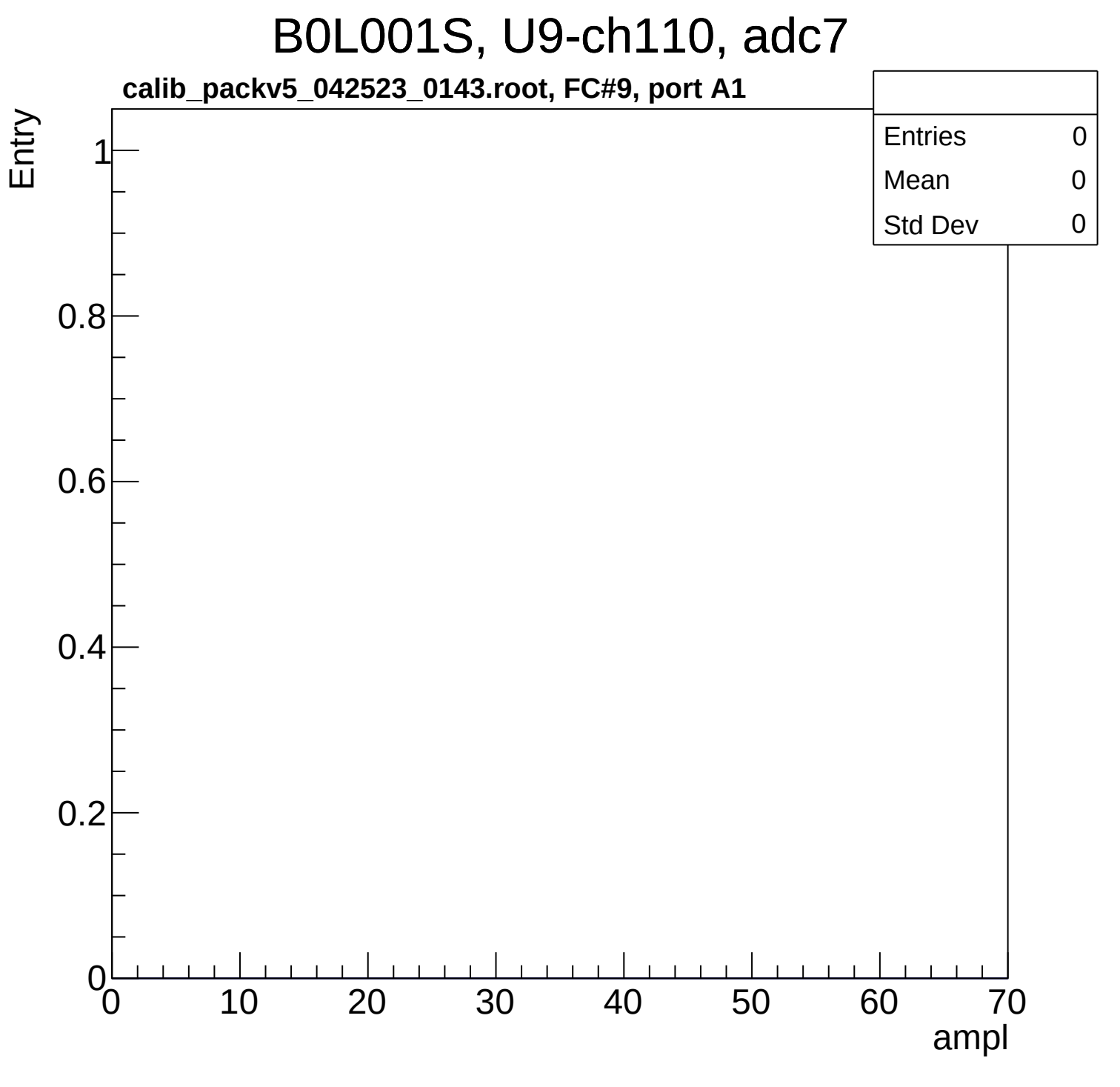
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch110, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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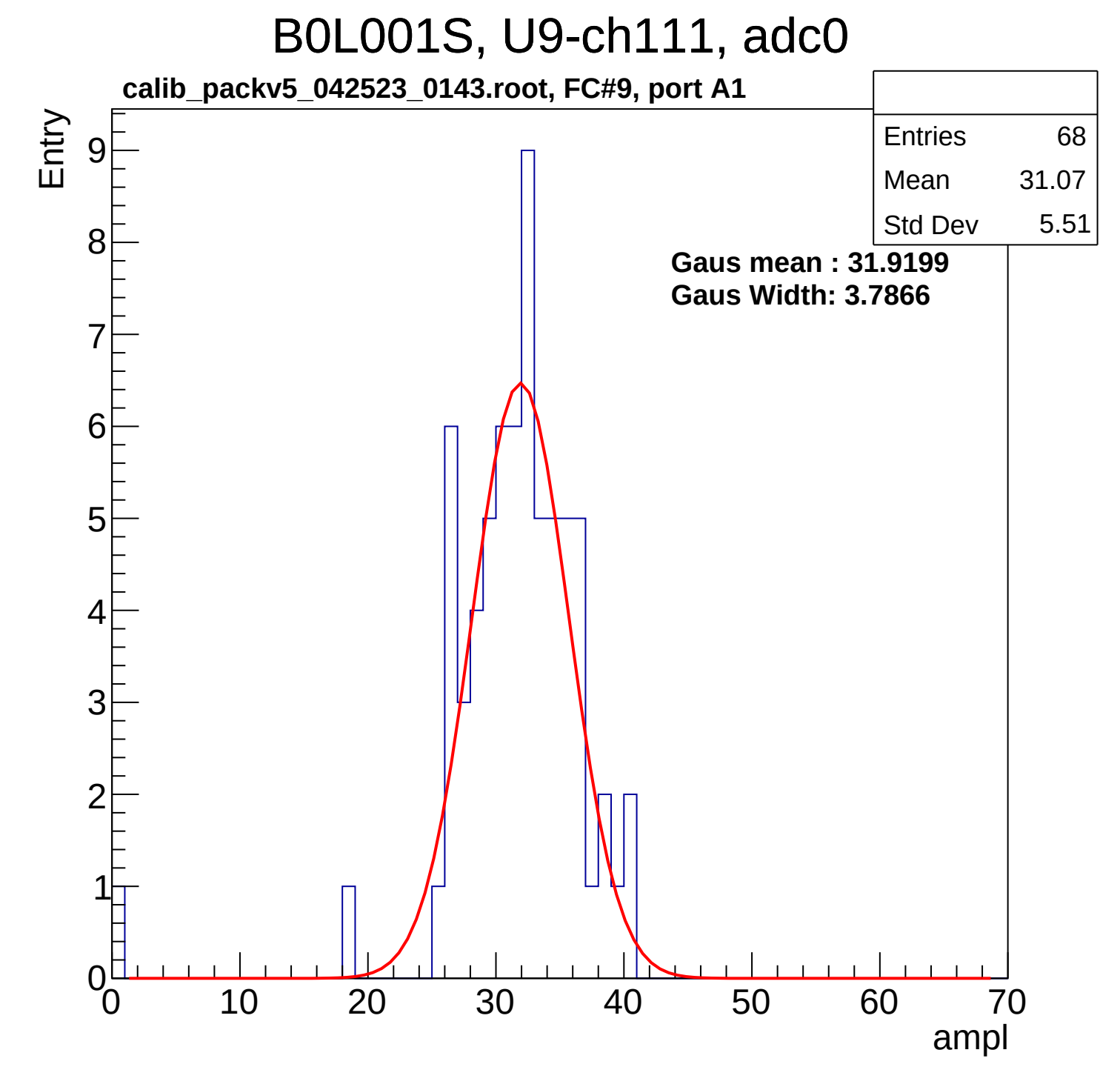
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch111, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.07
Std Dev 5.51
Gaus mean : 31.9199
Gaus Width: 3.7866
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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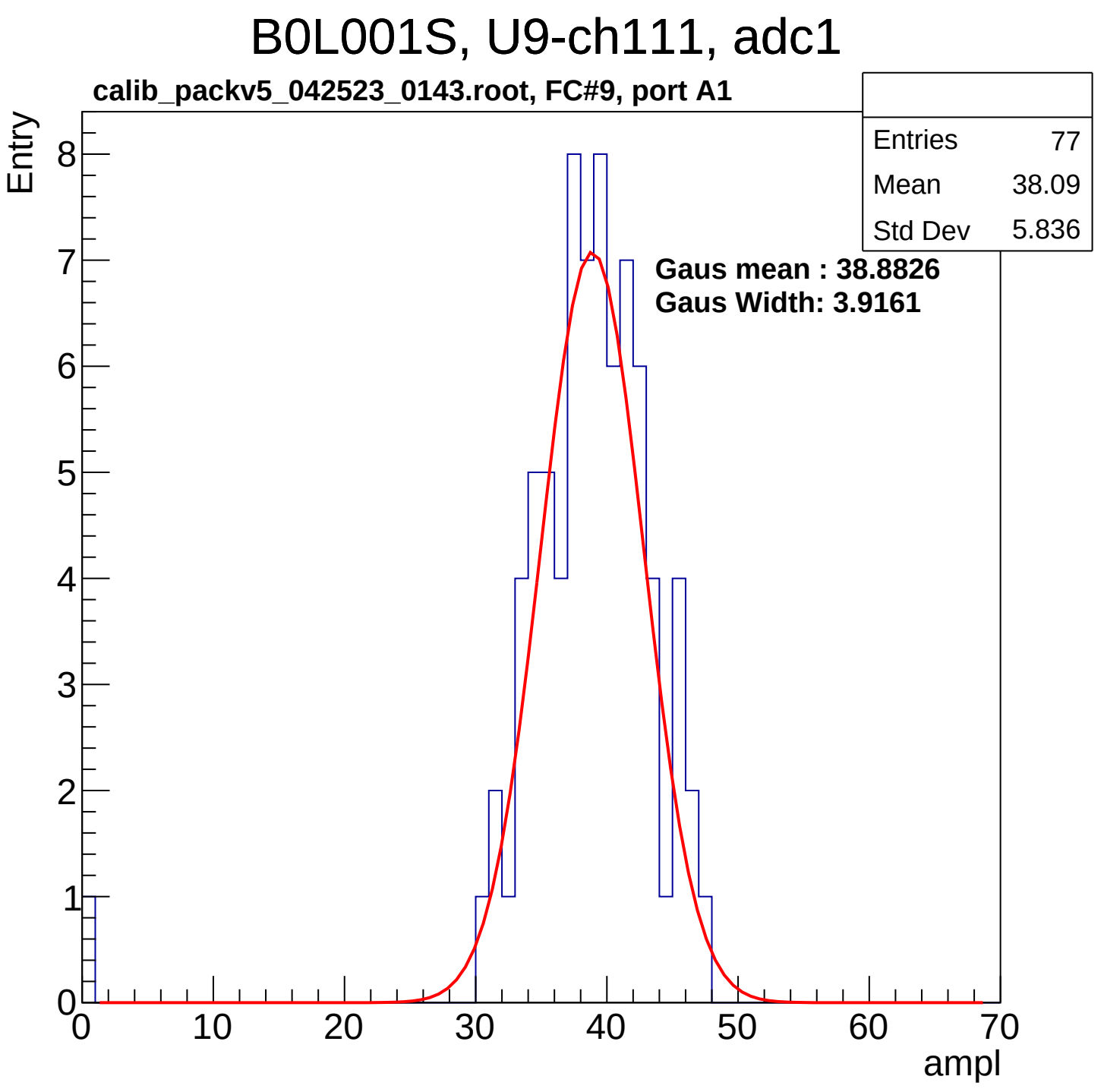
B0L001S, U9-ch111, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 38.09
Std Dev 5.836
Gaus mean : 38.8826
Gaus Width: 3.9161
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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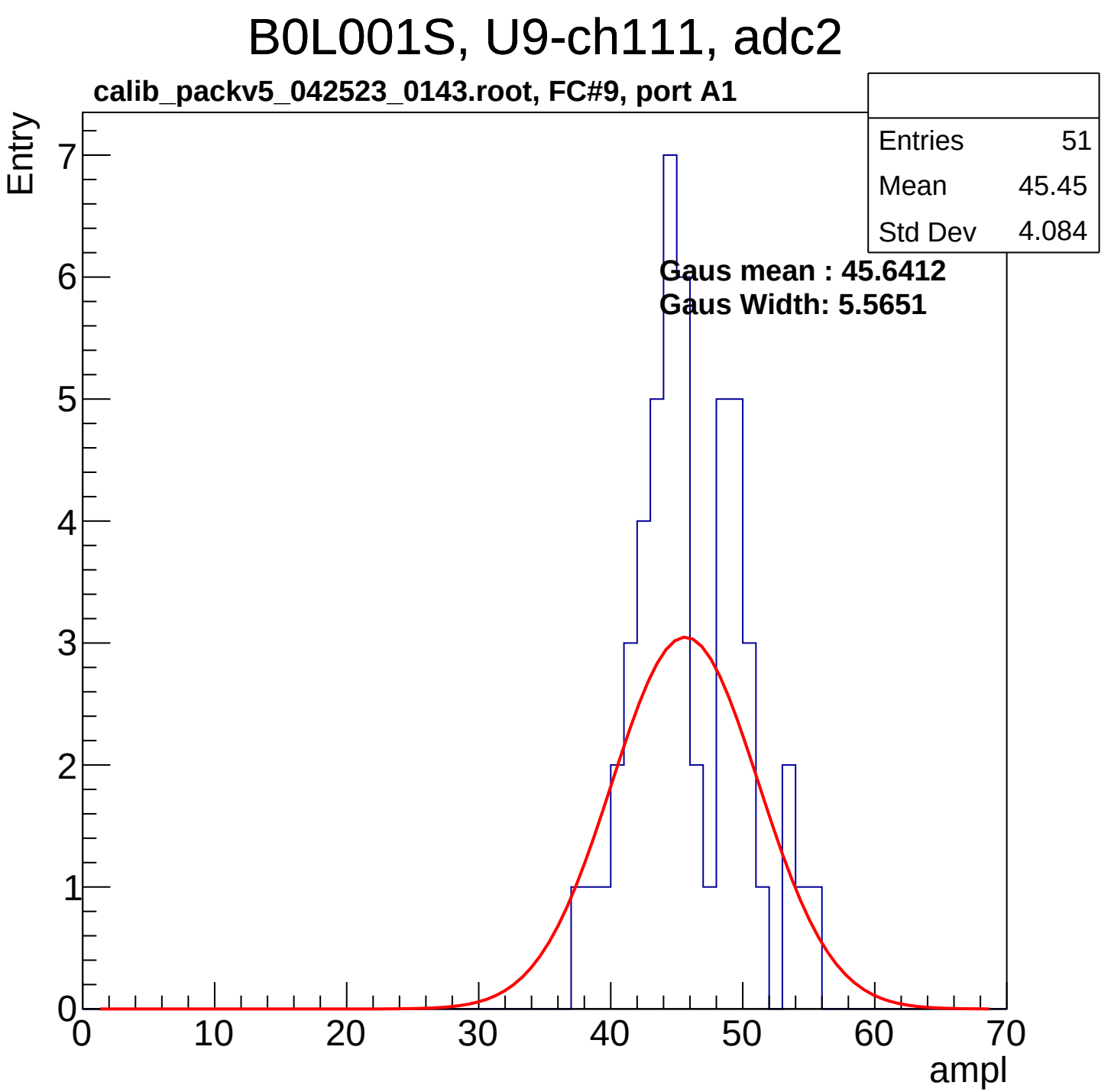
60
70


B0L001S, U9-ch111, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 45.45
Std Dev 4.084
Gaus mean : 45.6412
Gaus Width: 5.5651
Entry
ampl
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1
2
3
4
5
6
7
0
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50
60
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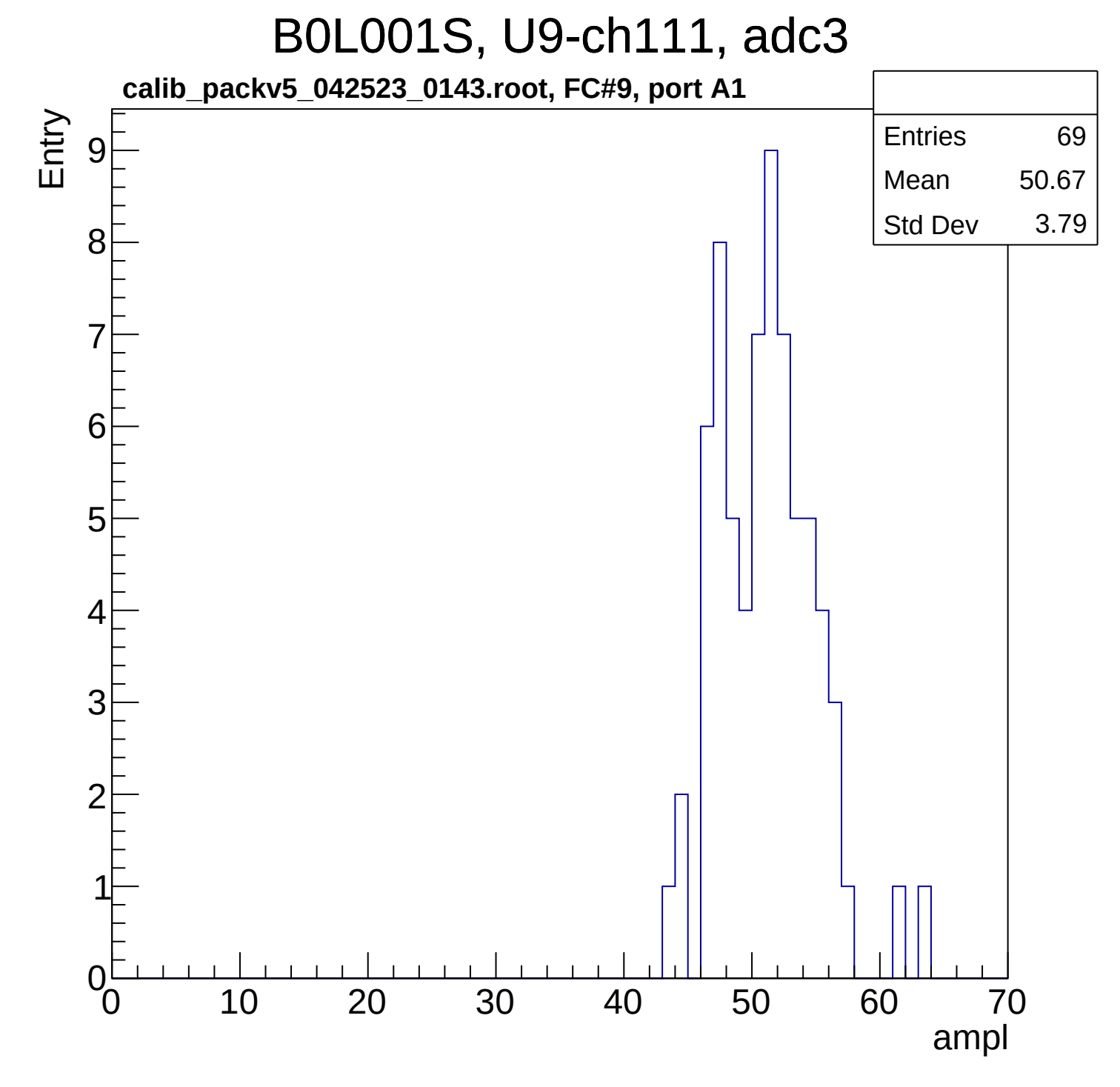
B0L001S, U9-ch111, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 50.67
Std Dev 3.79
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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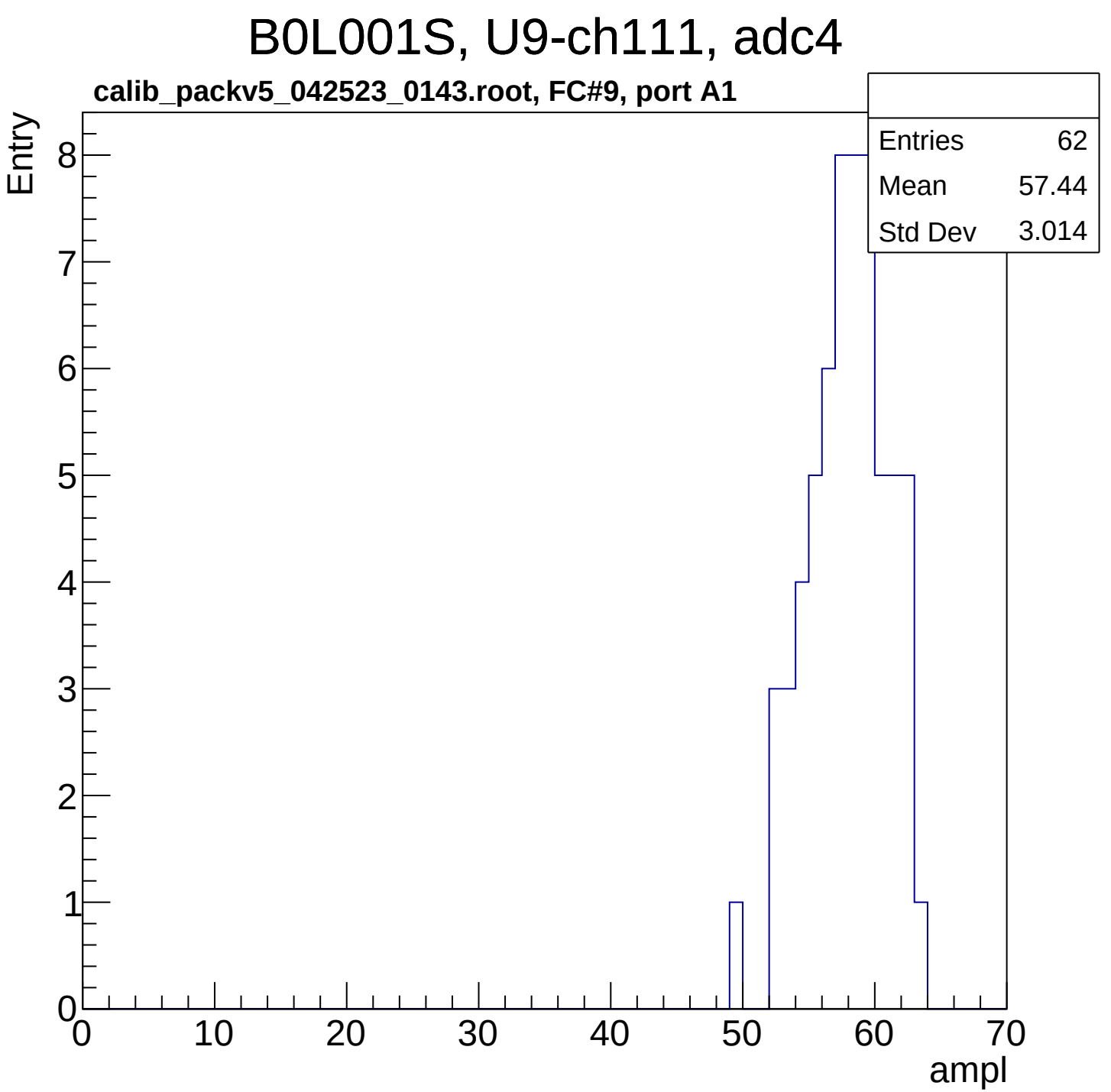
B0L001S, U9-ch111, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 57.44
Std Dev 3.014
Entry
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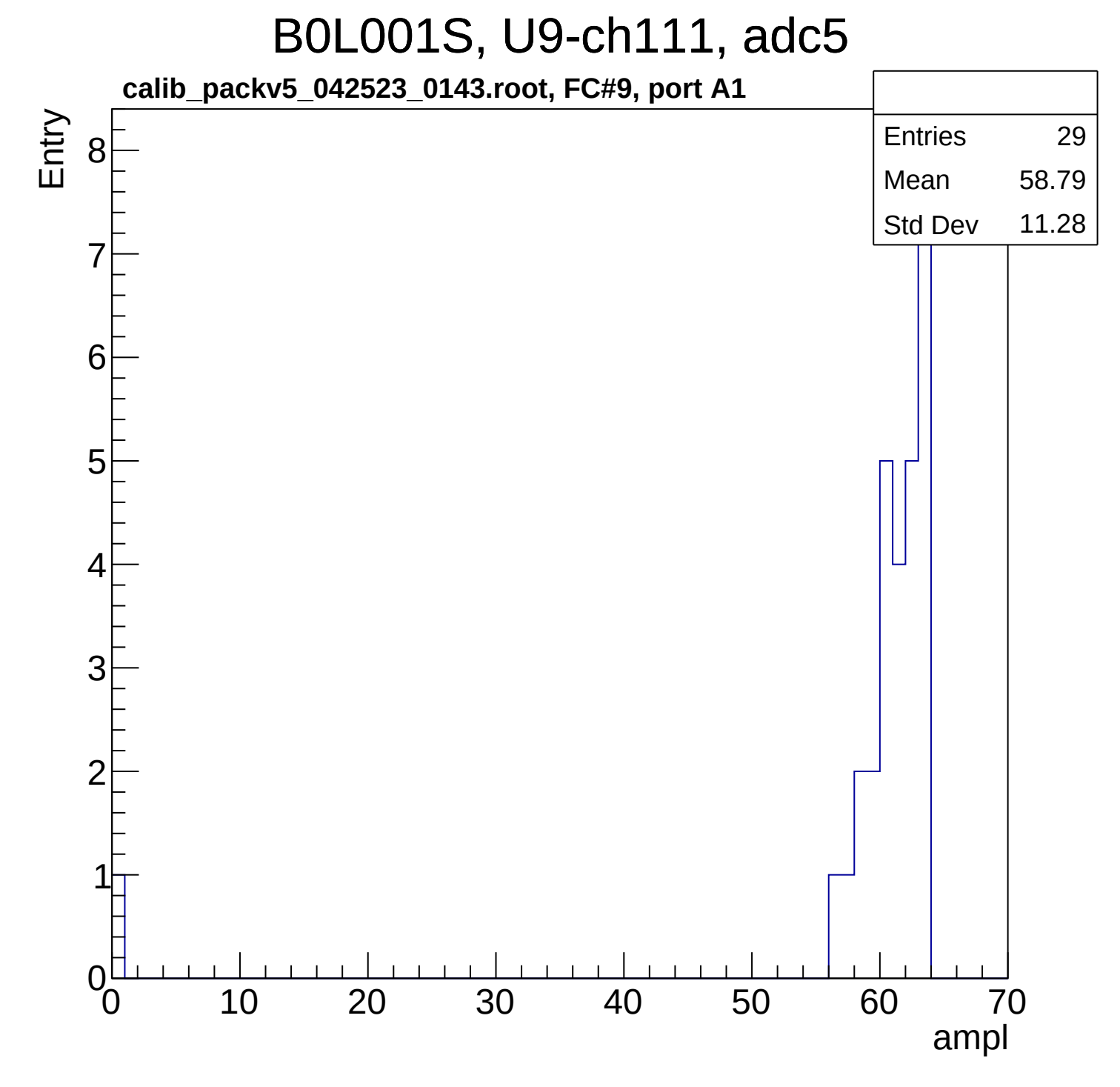
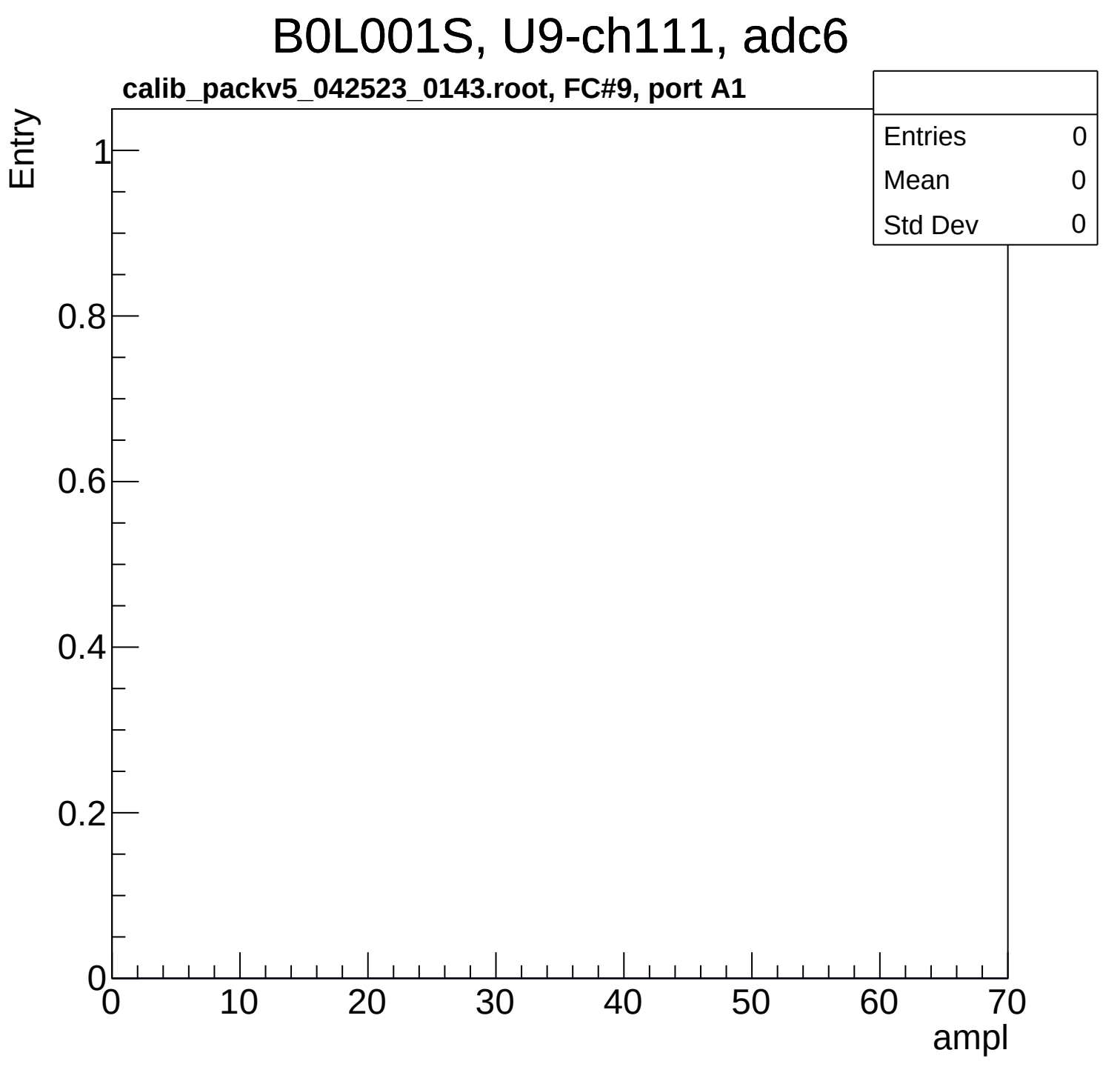
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch111, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 29
Mean 58.79
Std Dev 11.28
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch111, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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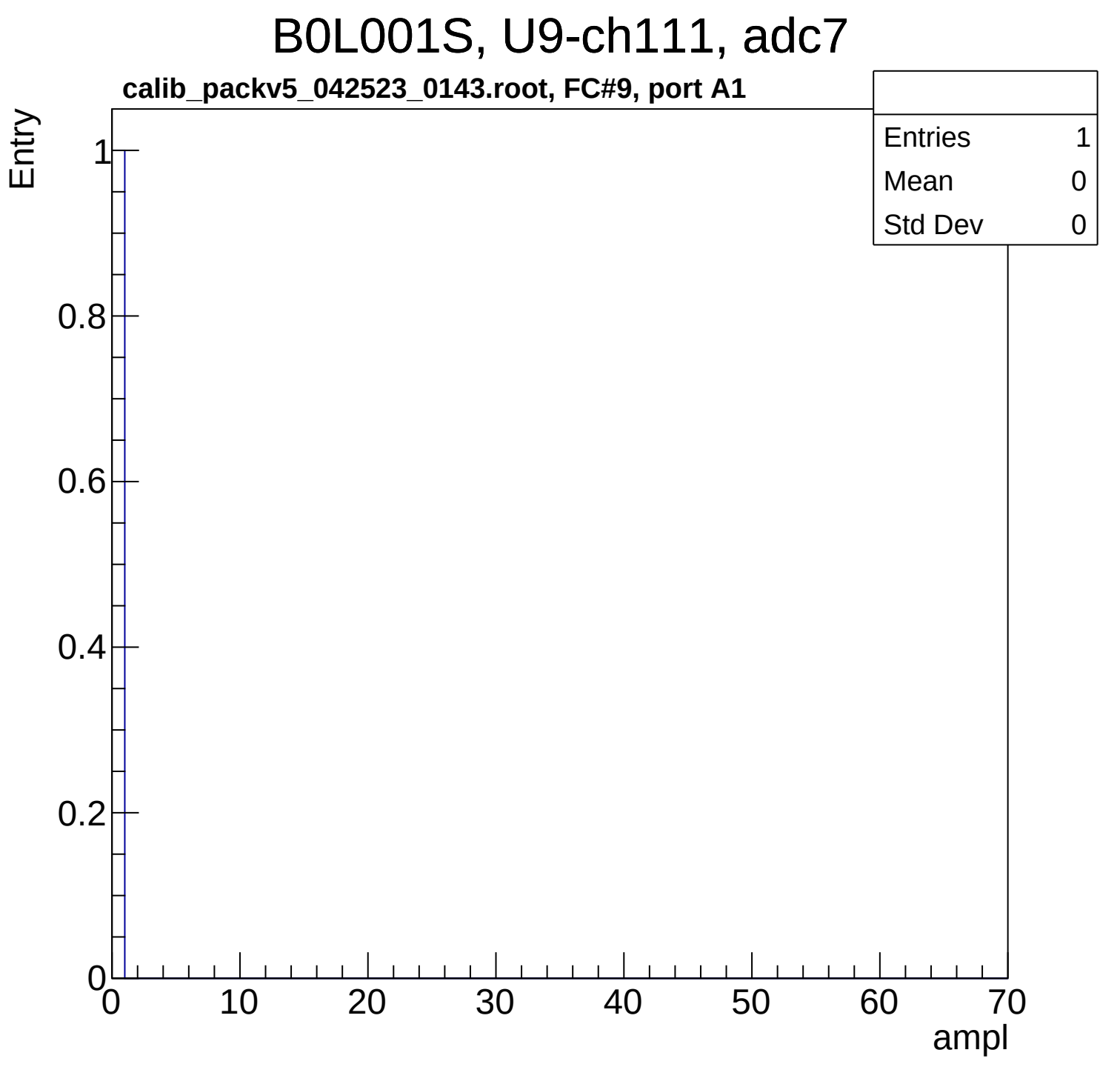
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch111, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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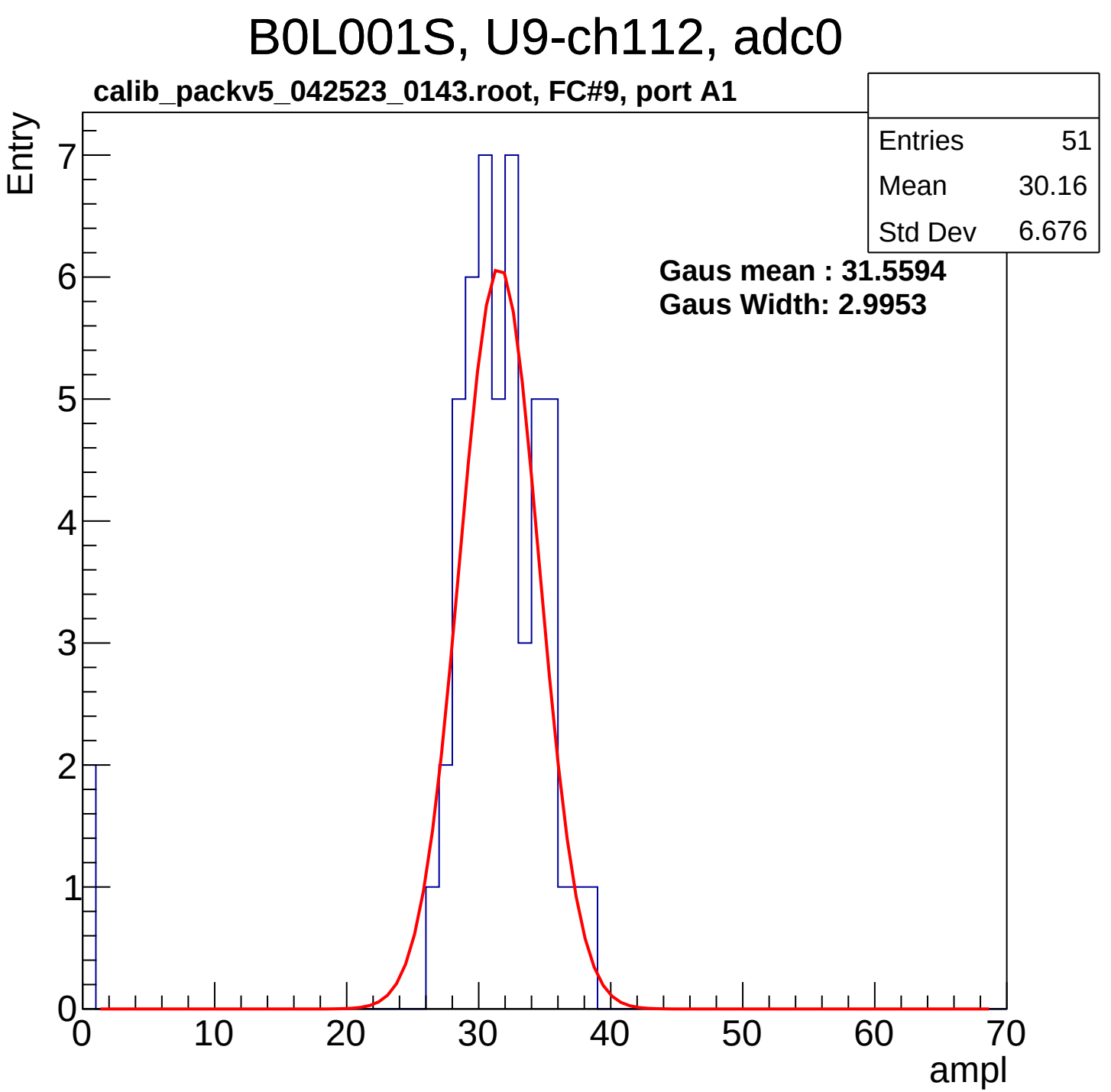
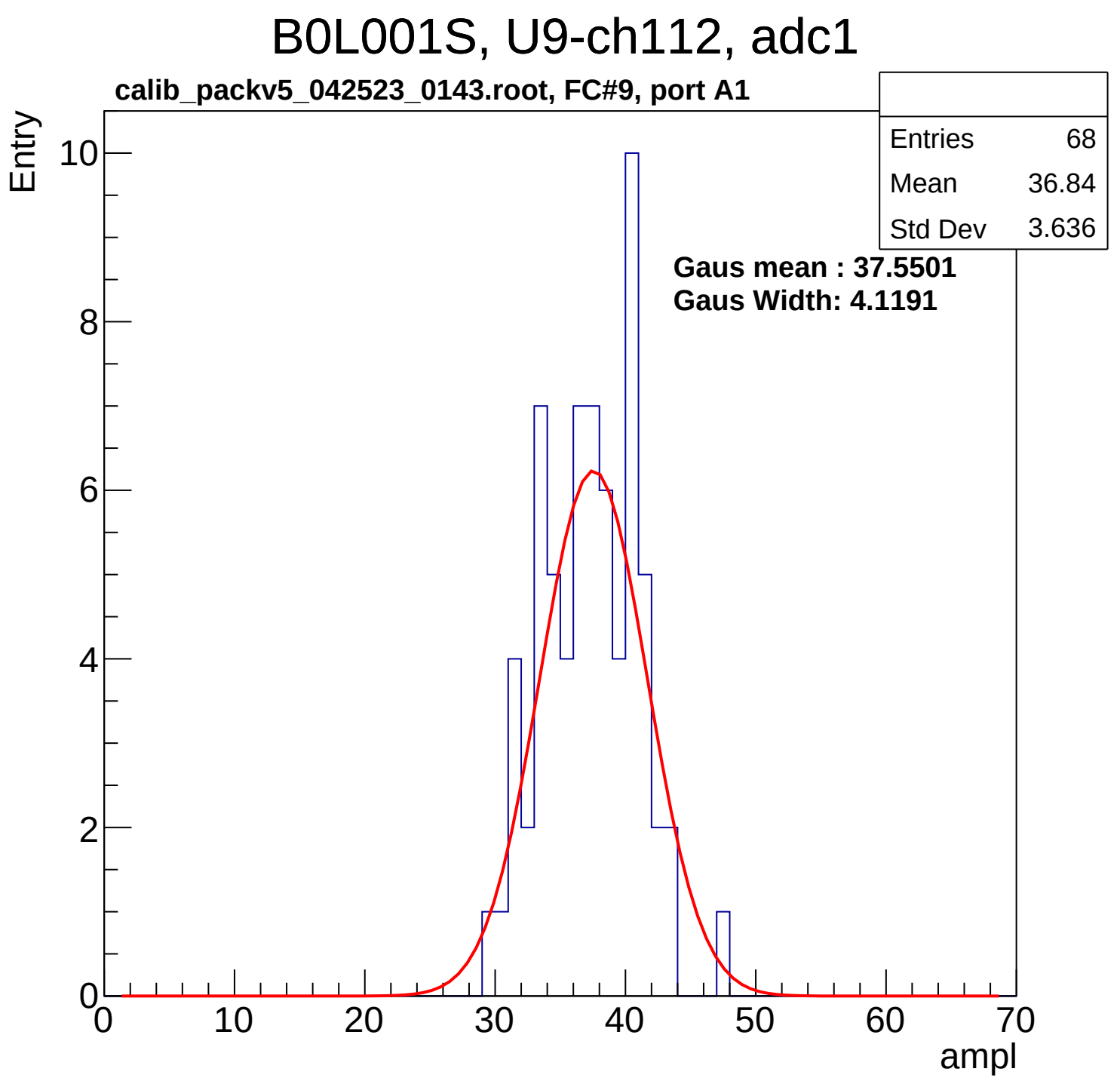
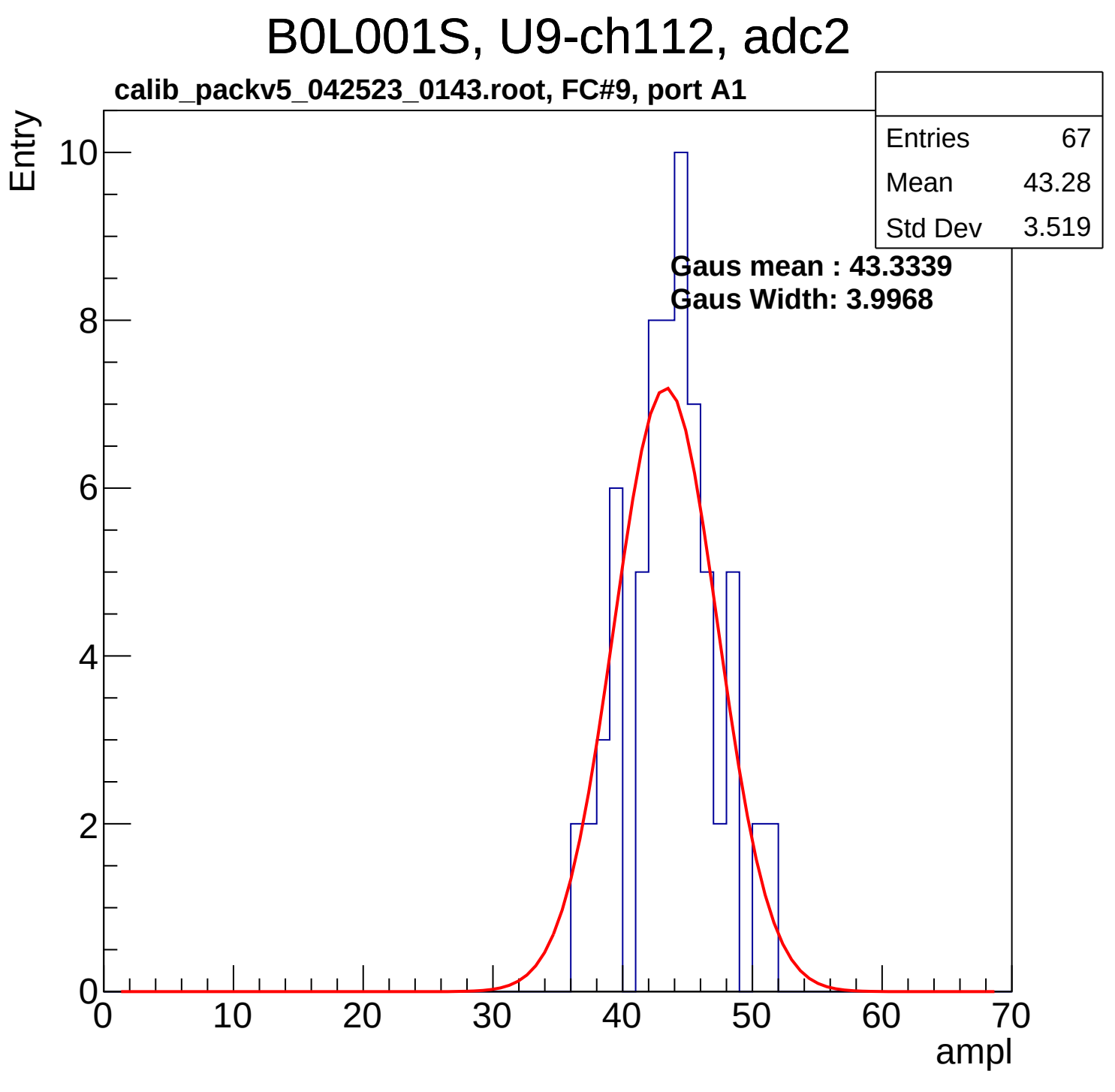
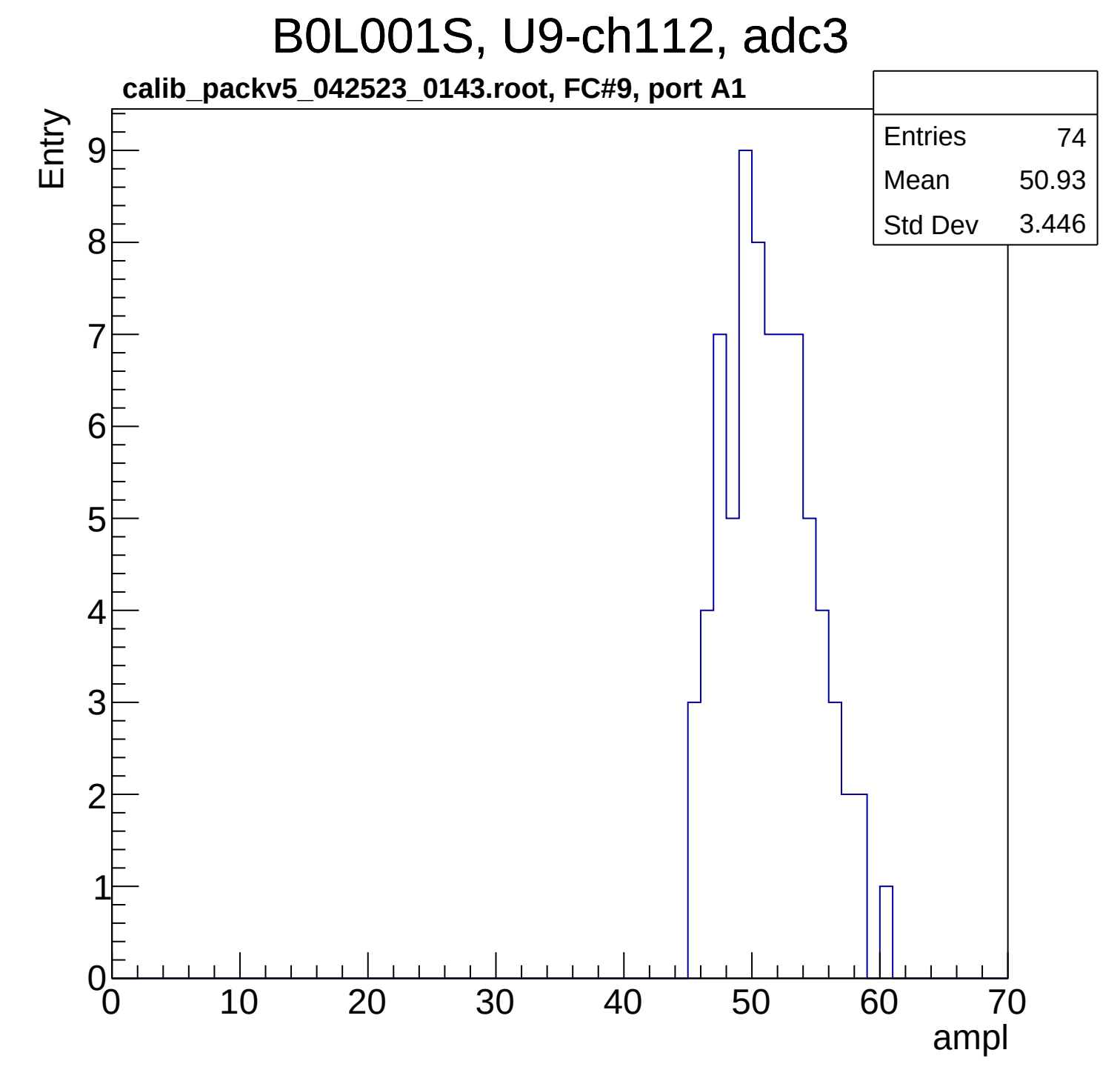
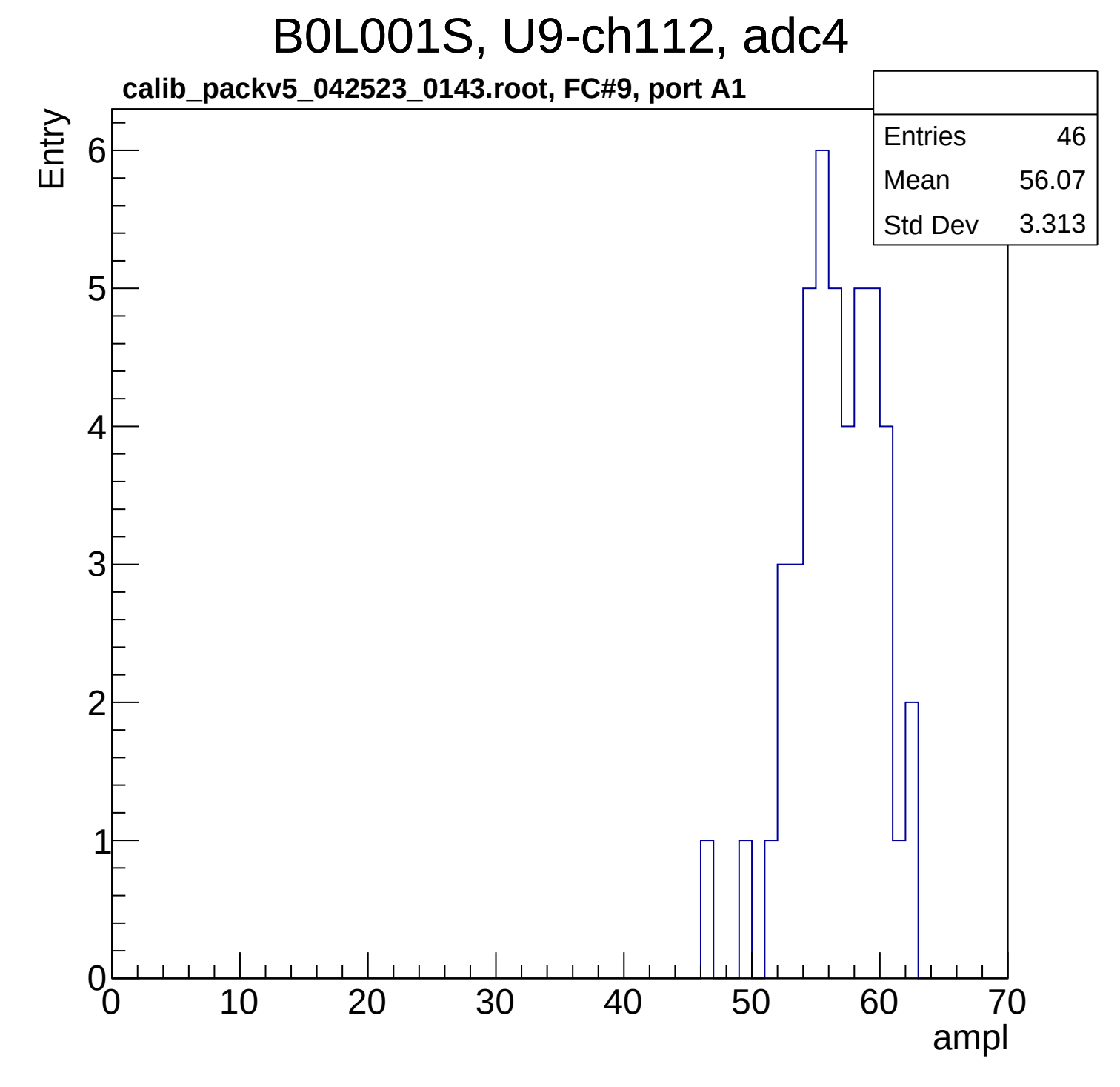
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

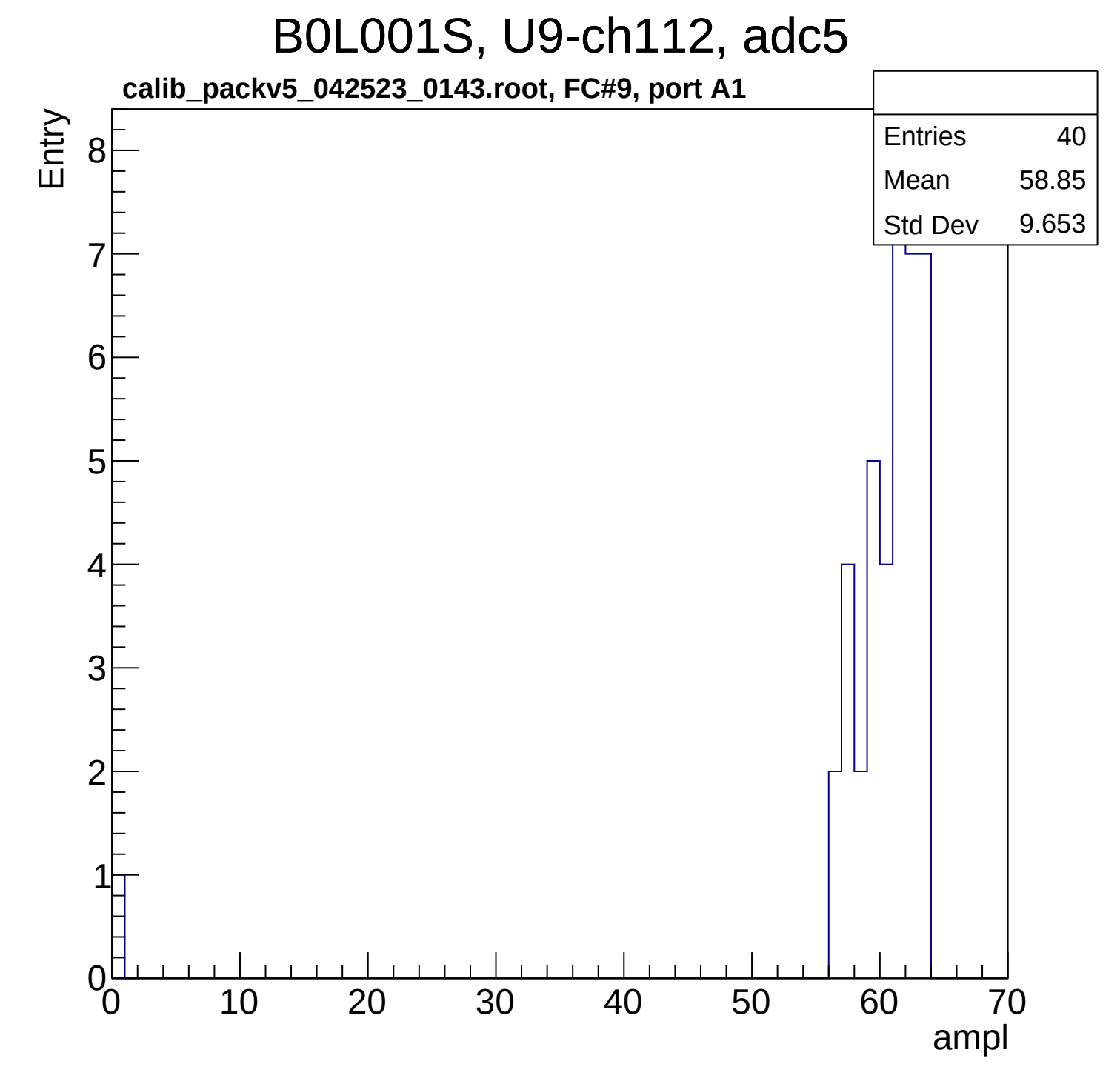

B0L001S, U9-ch112, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 30.16
Std Dev 6.676
Gaus mean : 31.5594
Gaus Width: 2.9953
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

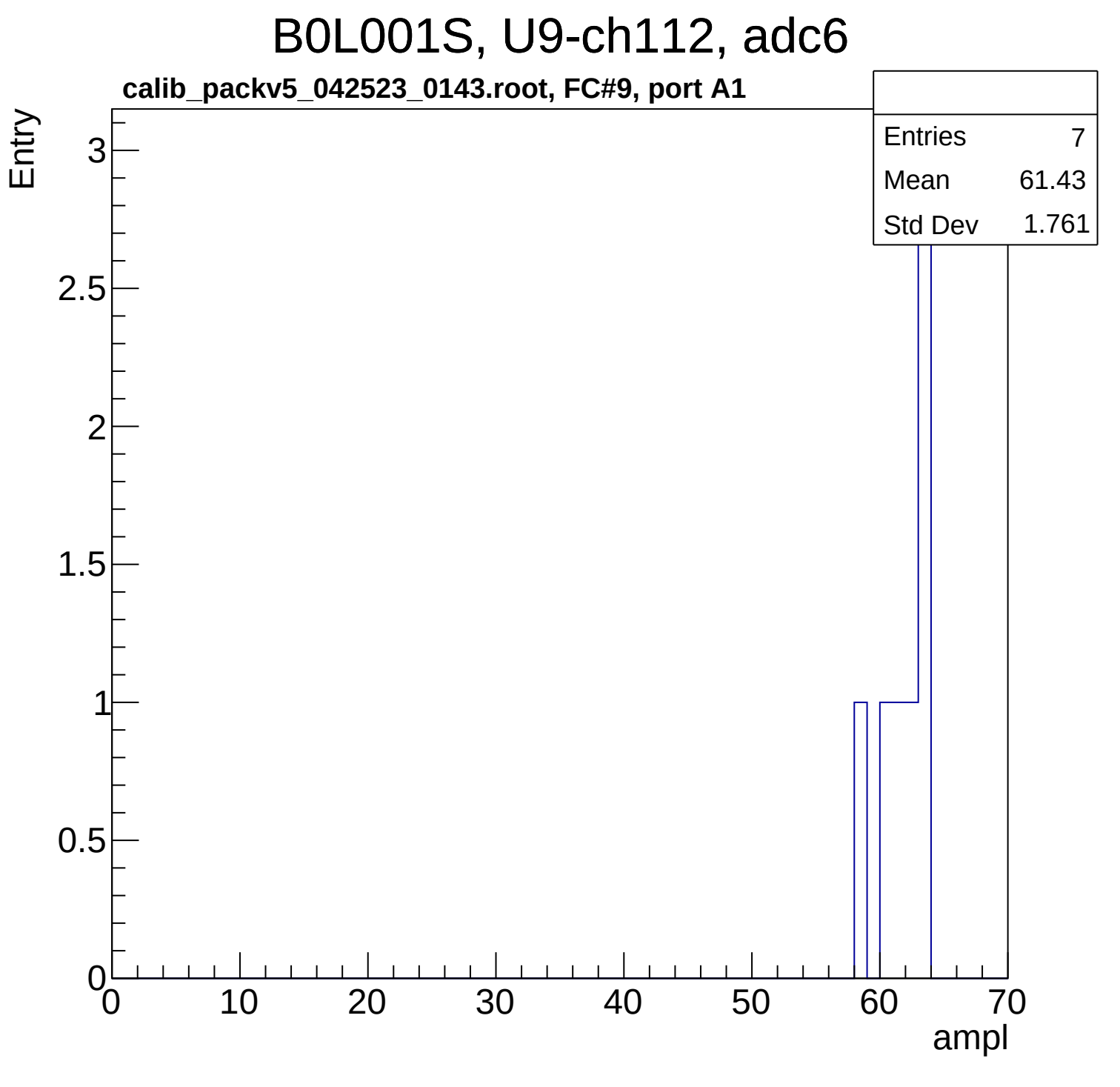

B0L001S, U9-ch112, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.84
Std Dev 3.636
Gaus mean : 37.5501
Gaus Width: 4.1191
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

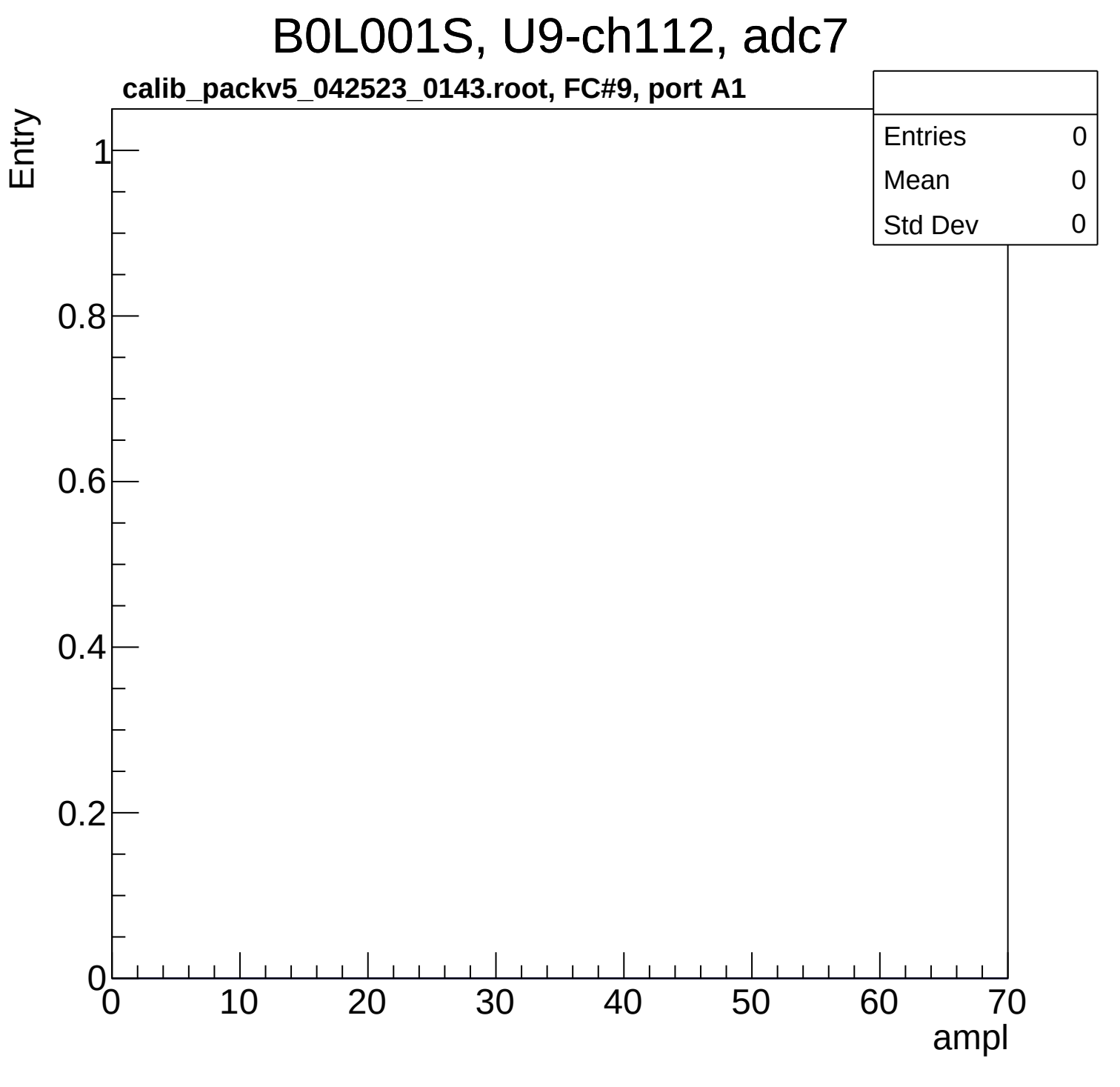

B0L001S, U9-ch112, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.28
Std Dev 3.519
Gaus mean : 43.3339
Gaus Width: 3.9968
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

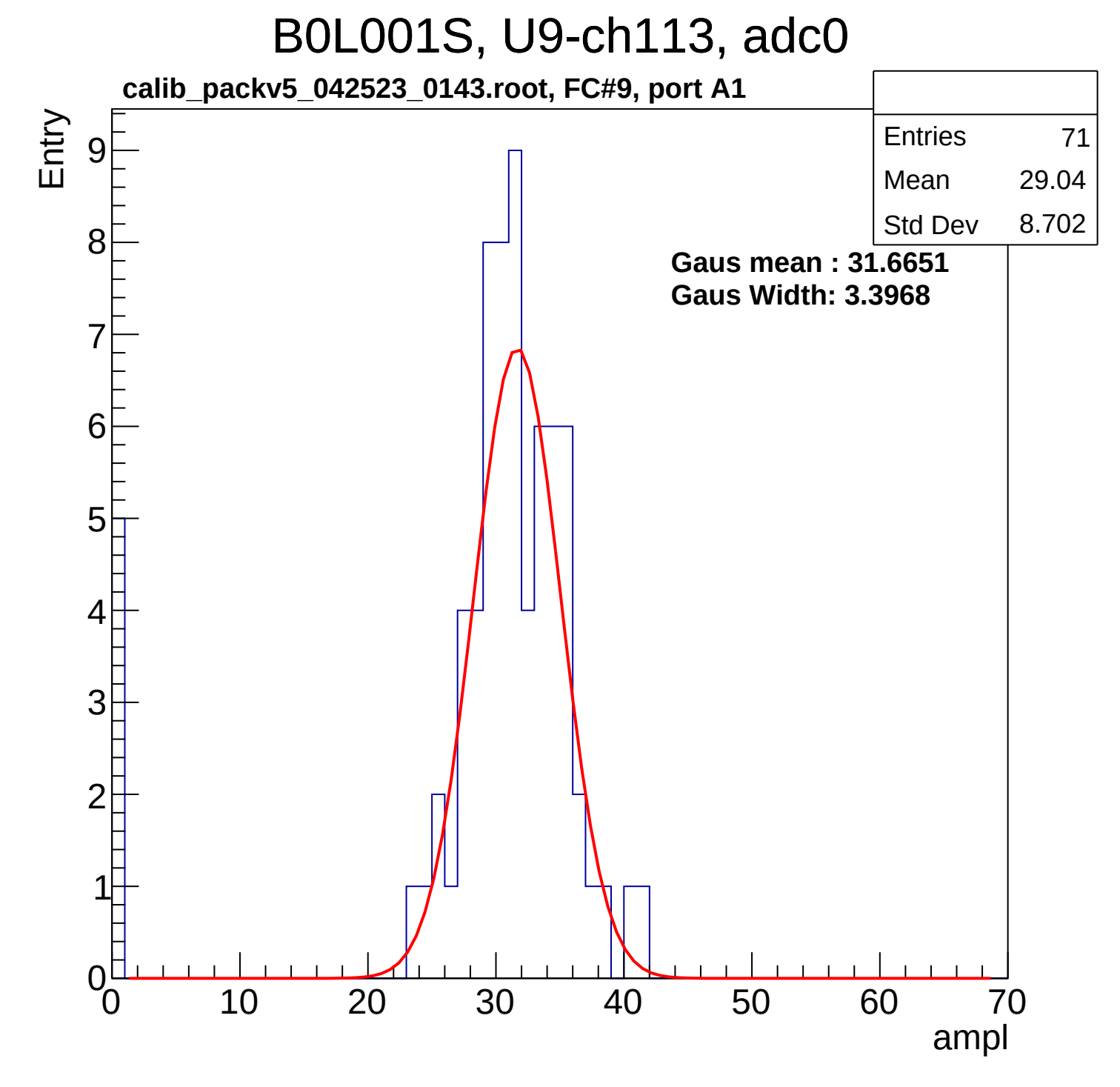

B0L001S, U9-ch112, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.93
Std Dev 3.446
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

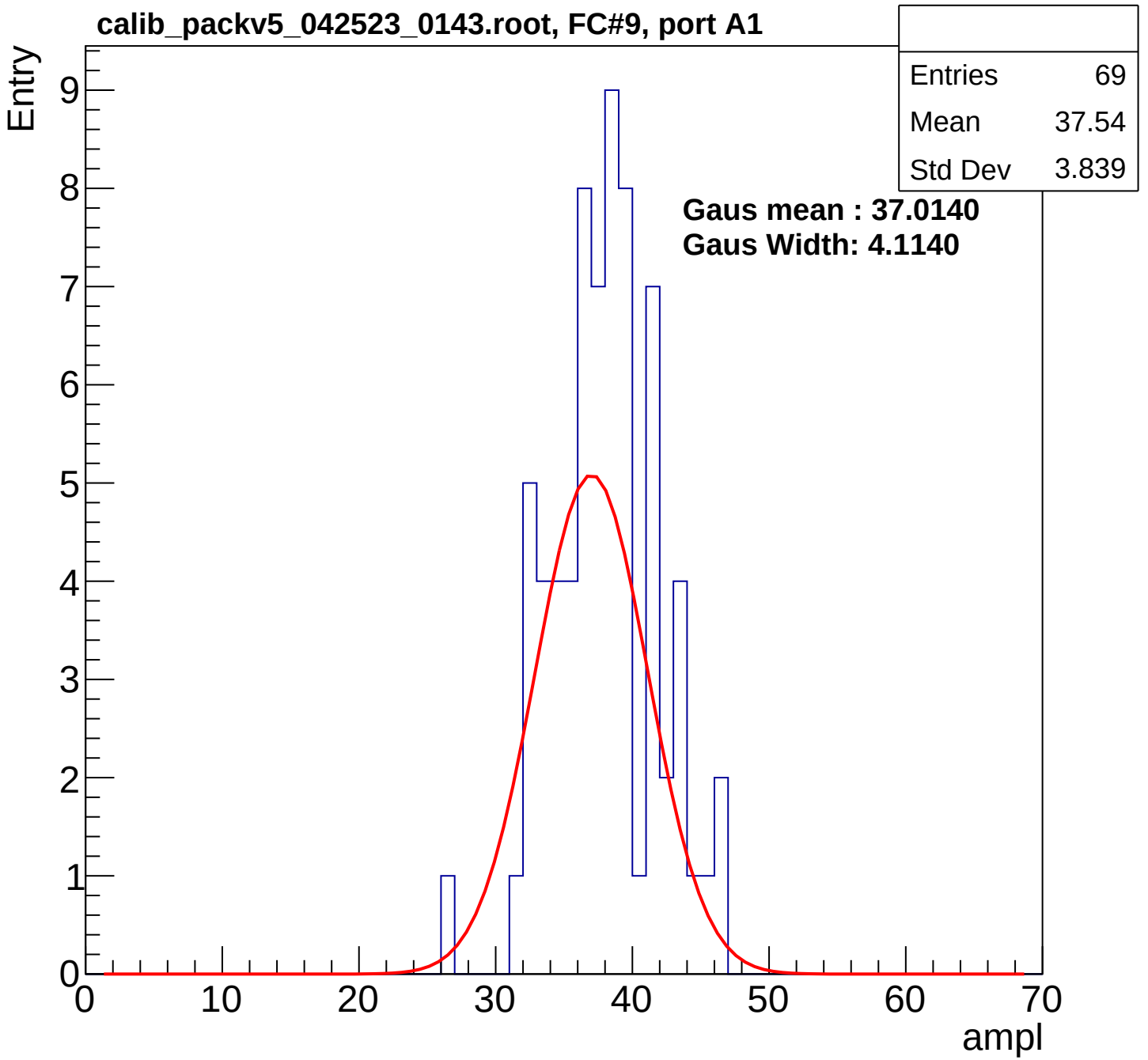

B0L001S, U9-ch112, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 56.07
Std Dev 3.313
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

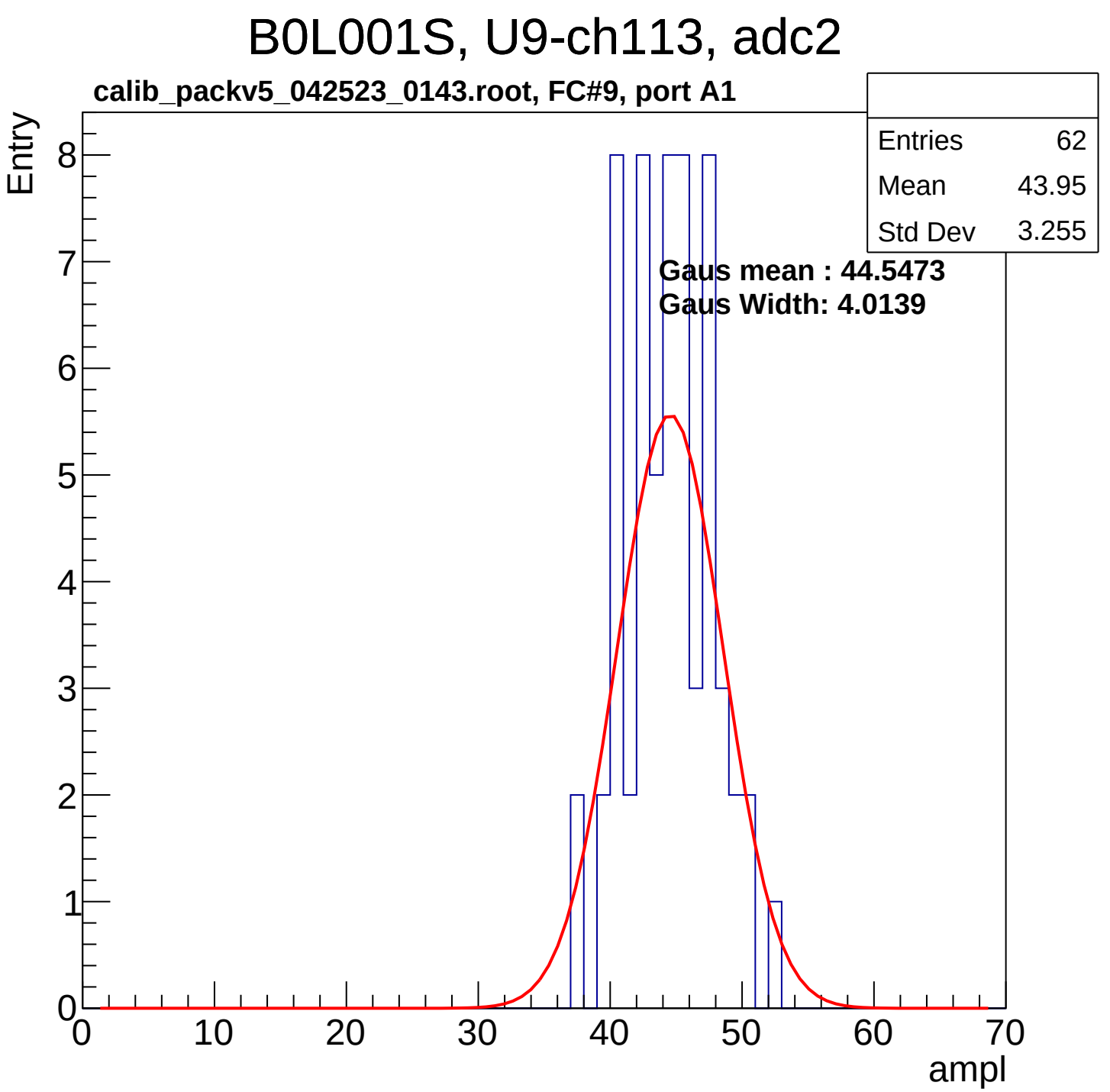

B0L001S, U9-ch112, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.85
Std Dev 9.653
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

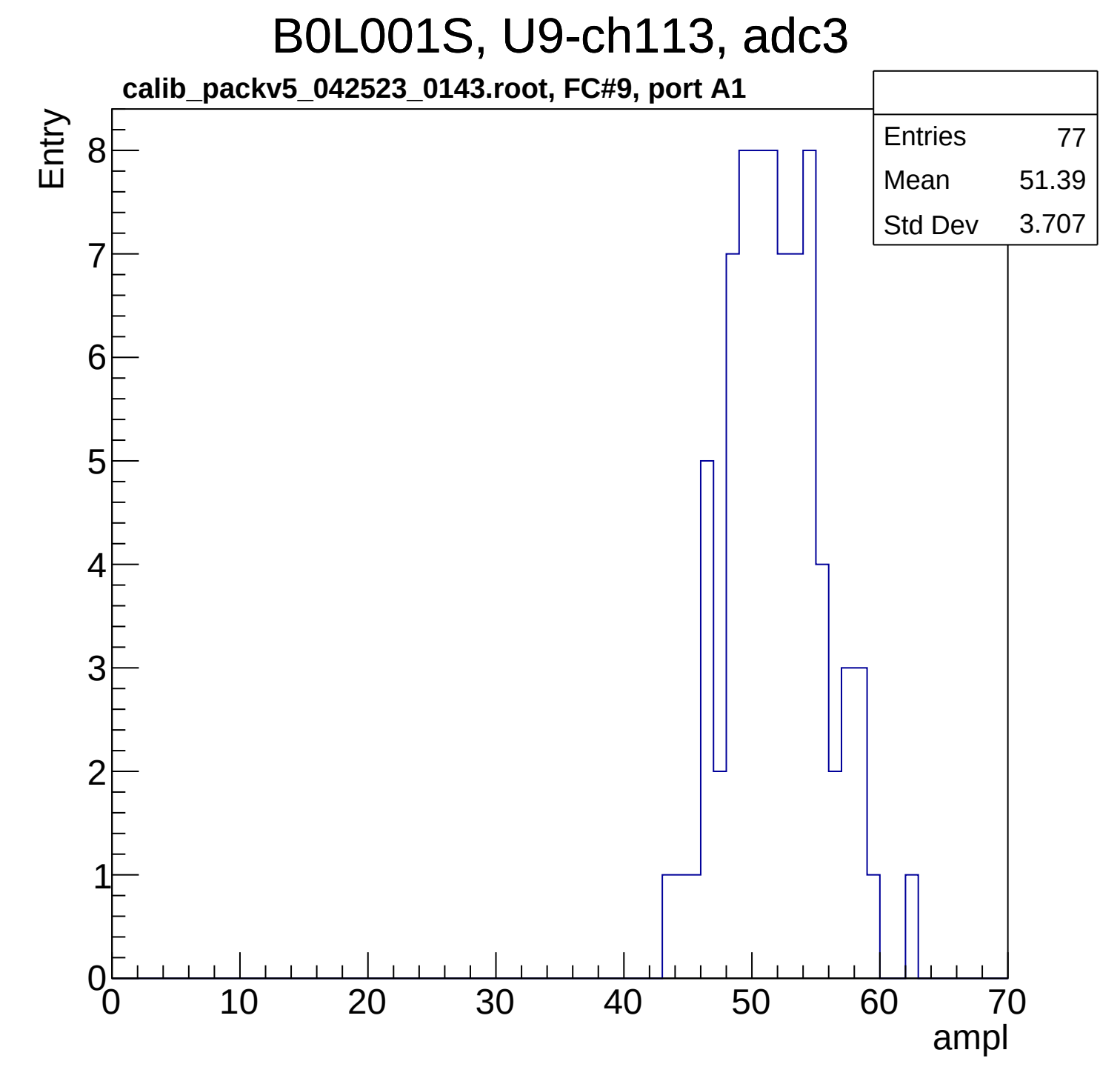

B0L001S, U9-ch112, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.43
Std Dev 1.761
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

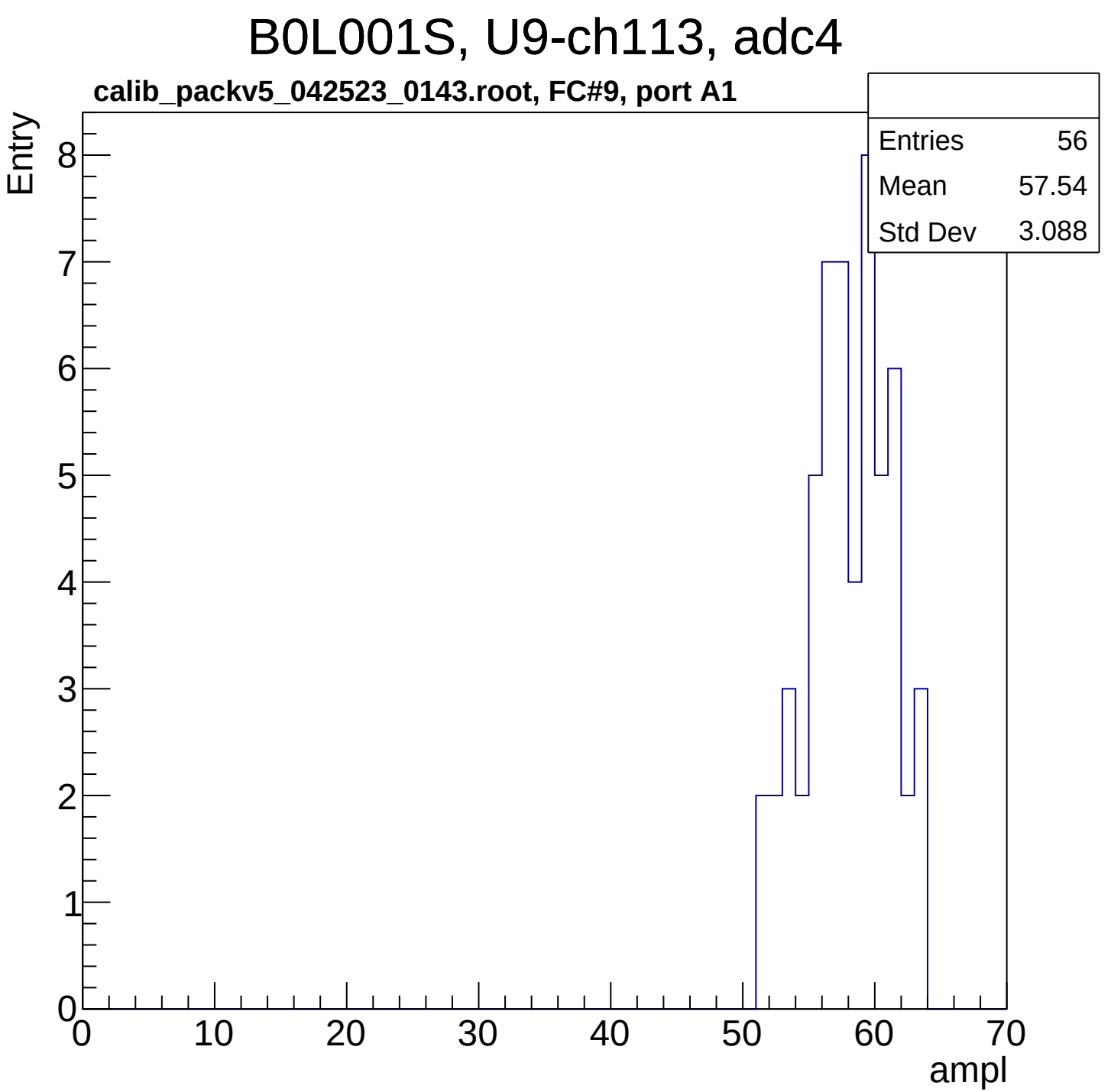

B0L001S, U9-ch112, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch113, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.04
Std Dev 8.702
Gaus mean : 31.6651
Gaus Width: 3.3968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch113, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.54
Std Dev 3.839
Gaus mean : 37.0140
Gaus Width: 4.1140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch113, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 43.95
Std Dev 3.255
Gaus mean : 44.5473
Gaus Width: 4.0139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch113, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.39
Std Dev 3.707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch113, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 57.54
Std Dev 3.088
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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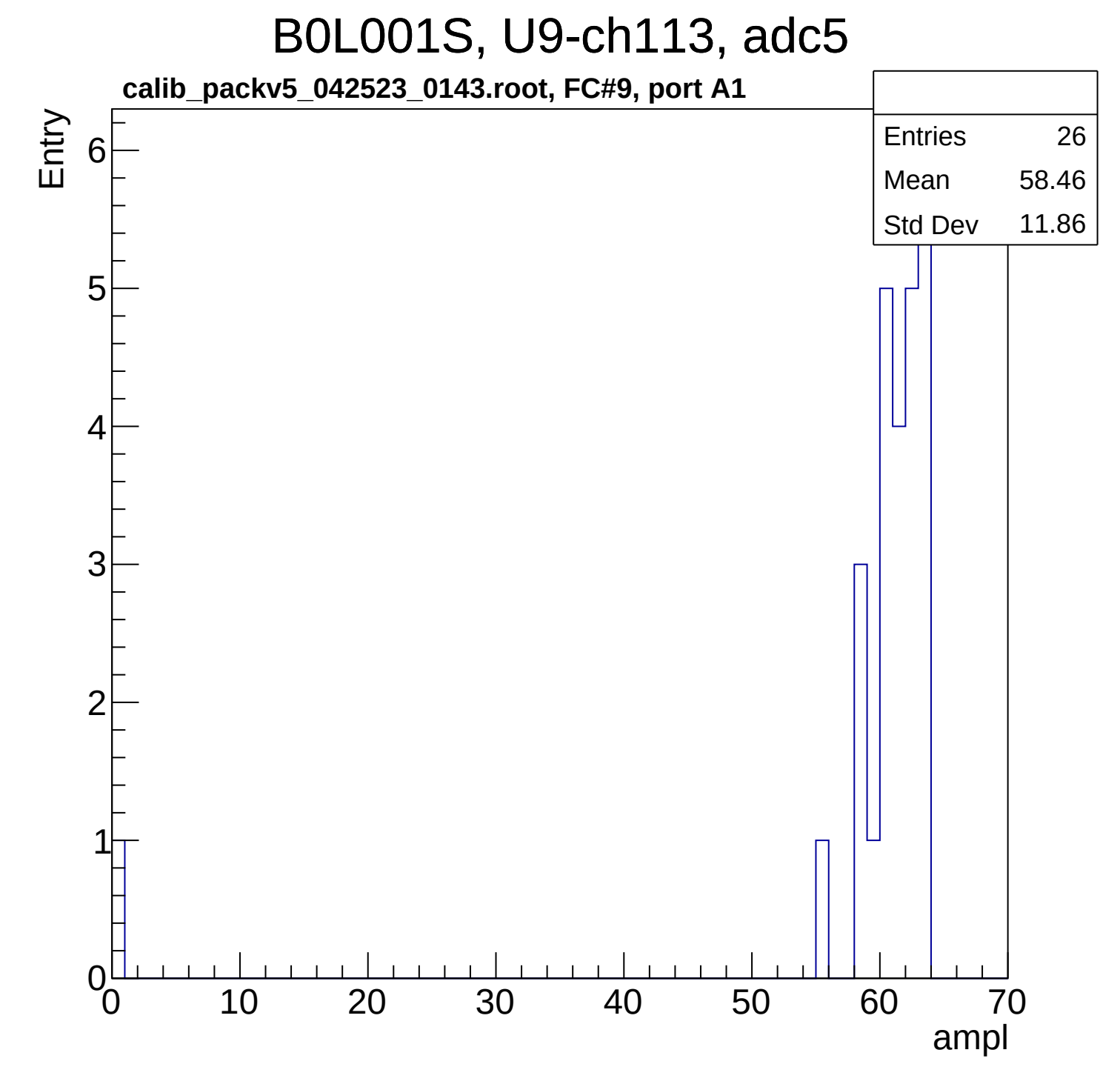
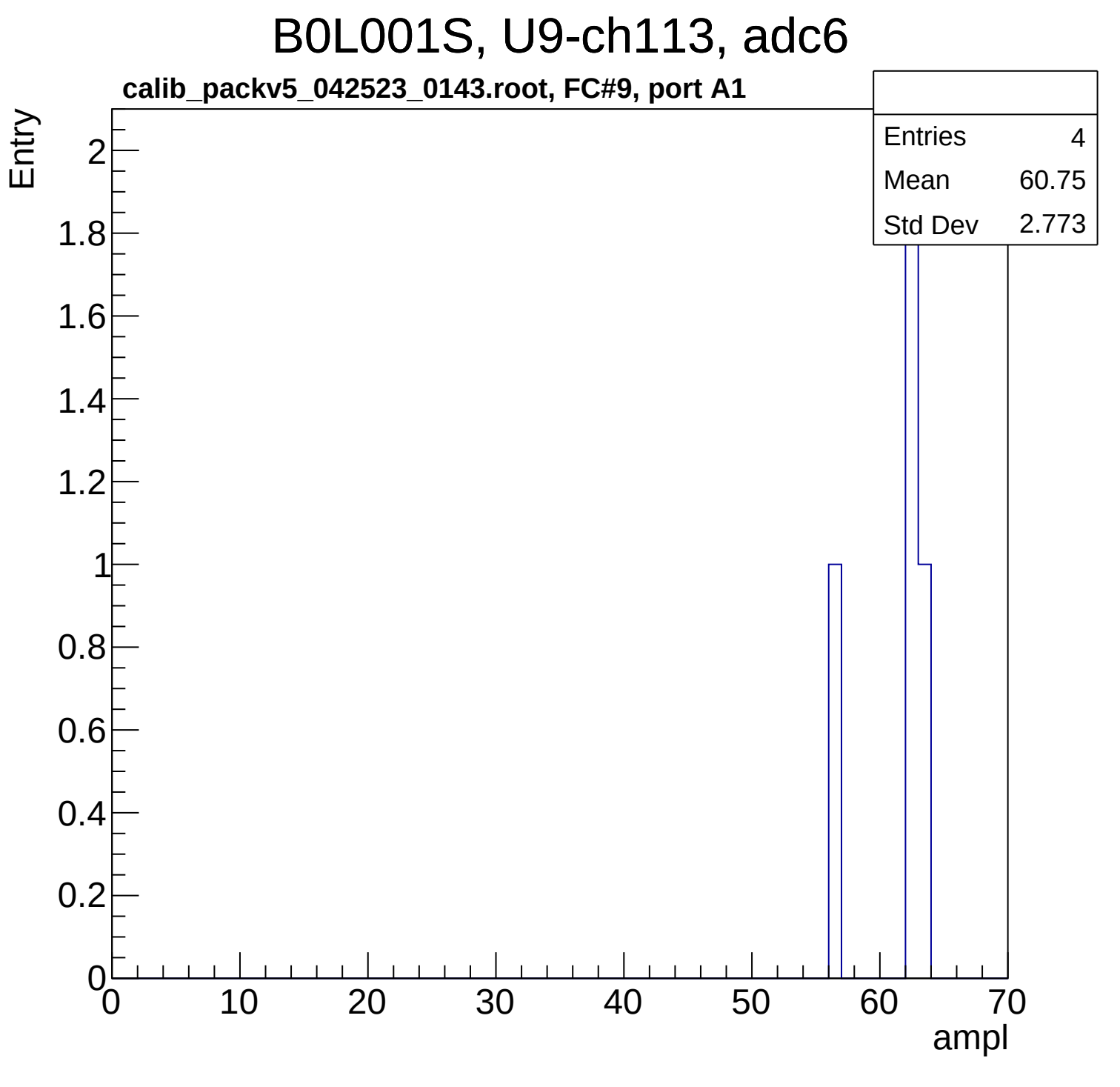
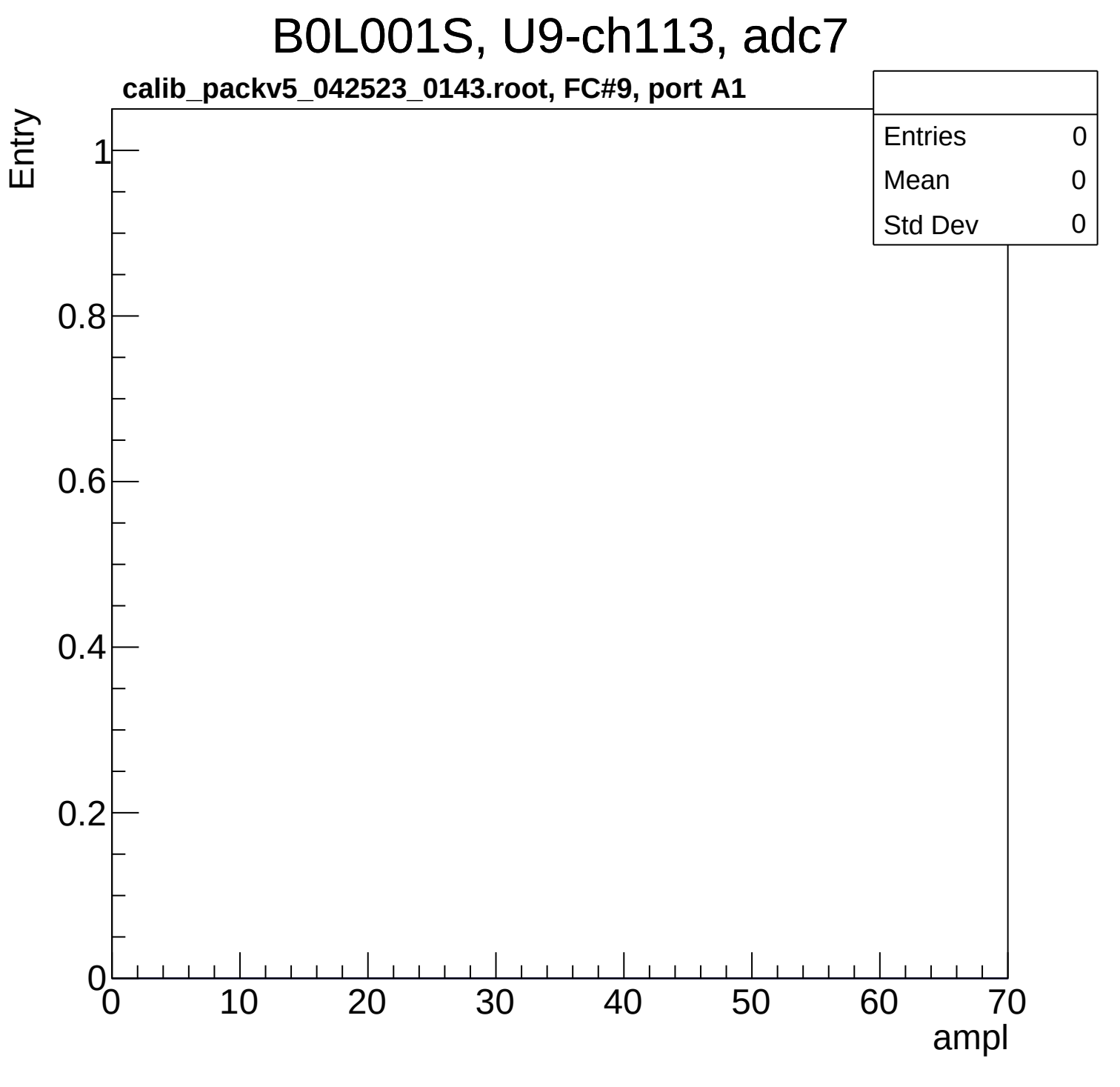
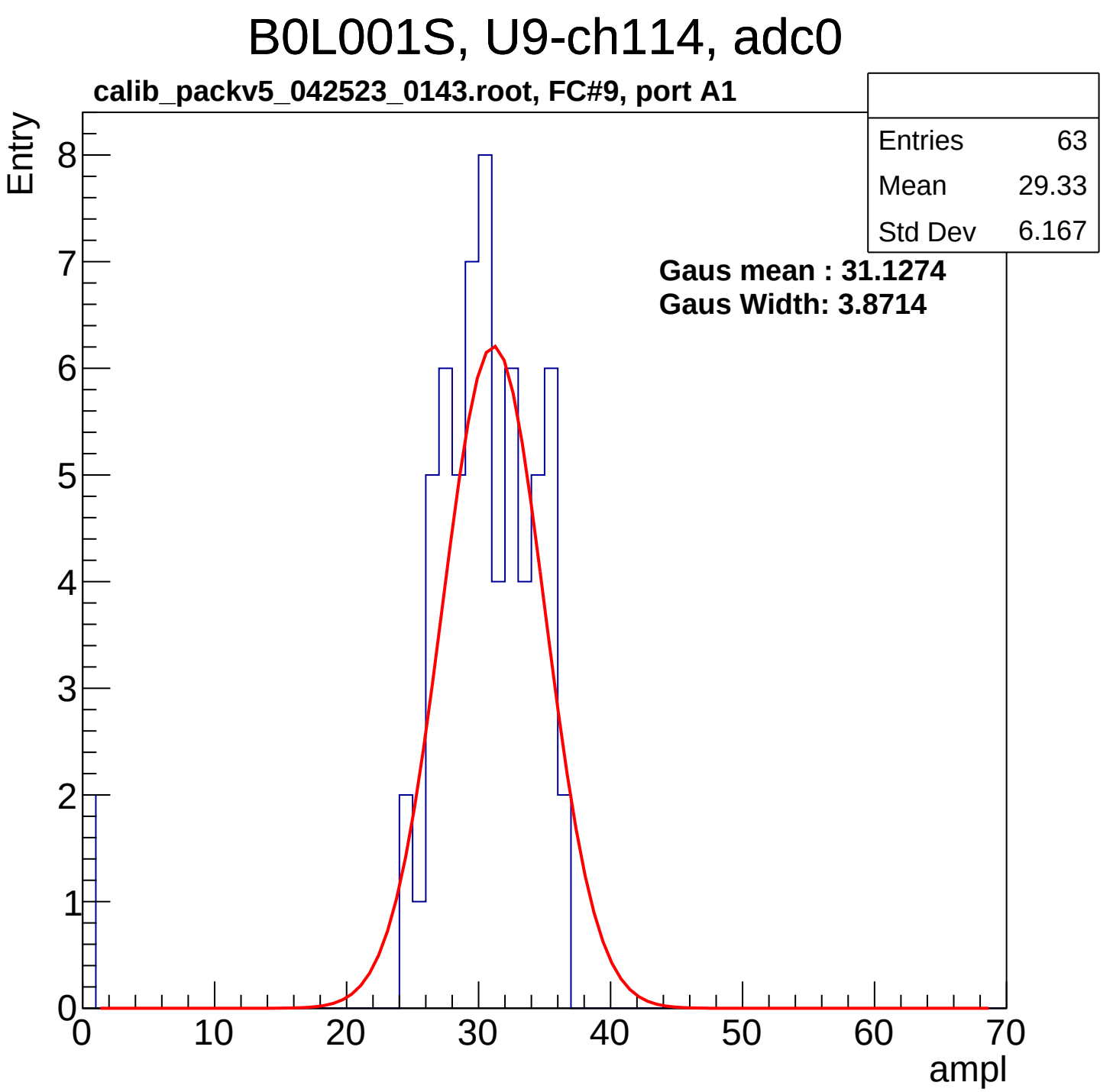

B0L001S, U9-ch113, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 58.46
Std Dev 11.86
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch113, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 60.75
Std Dev 2.773
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch113, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch114, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.33
Std Dev 6.167
Gaus mean : 31.1274
Gaus Width: 3.8714
Entry
ampl
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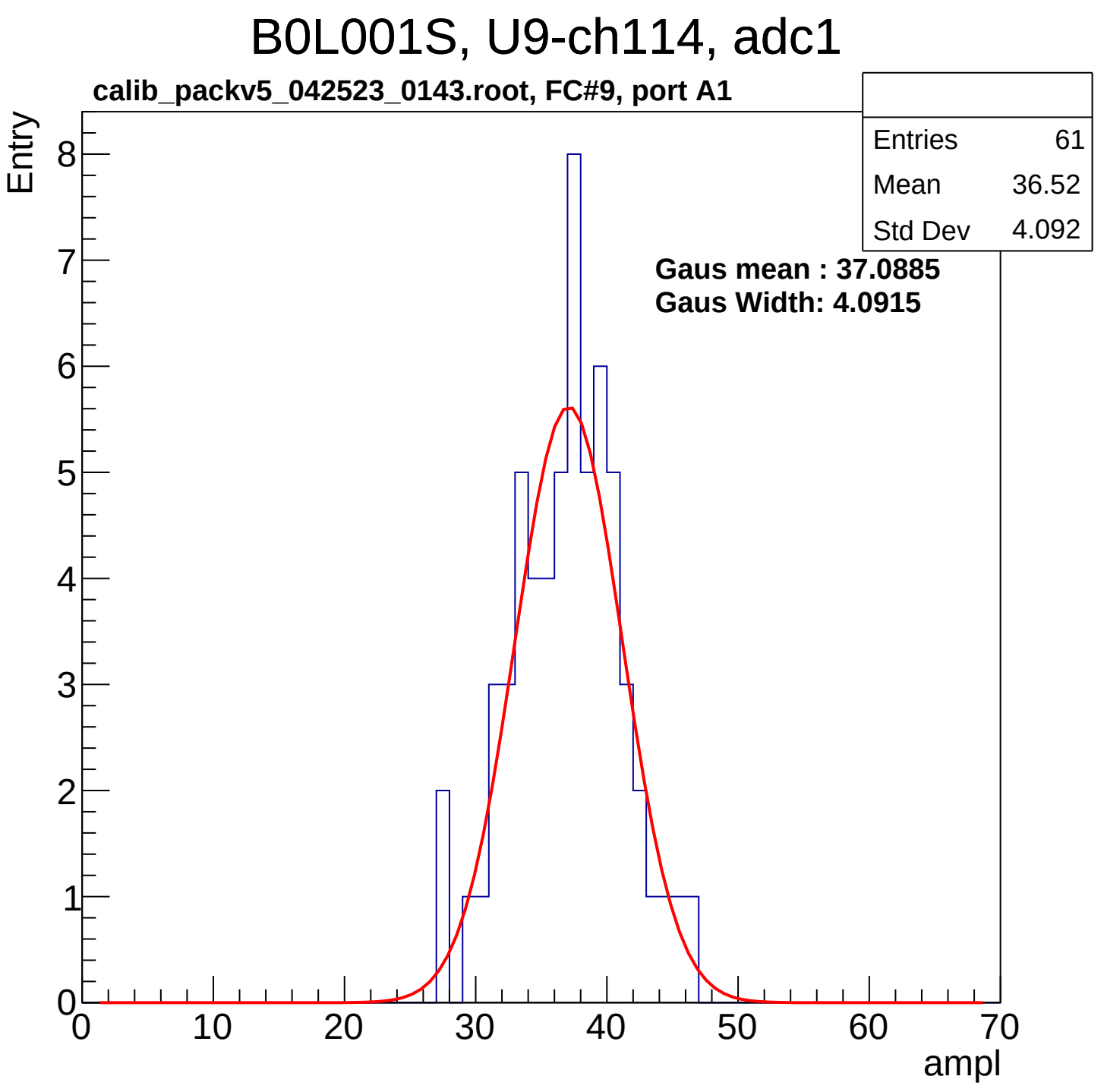
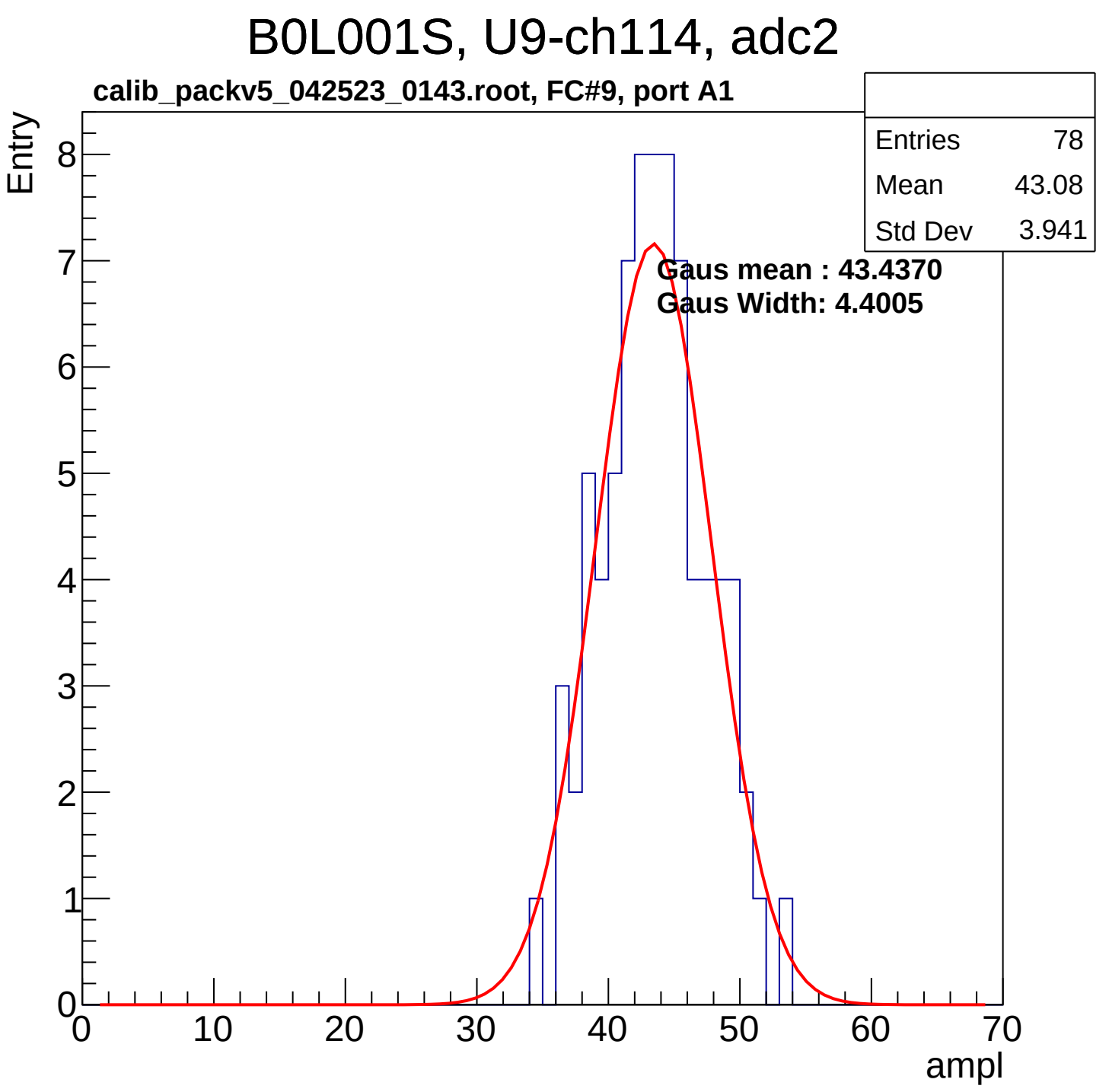
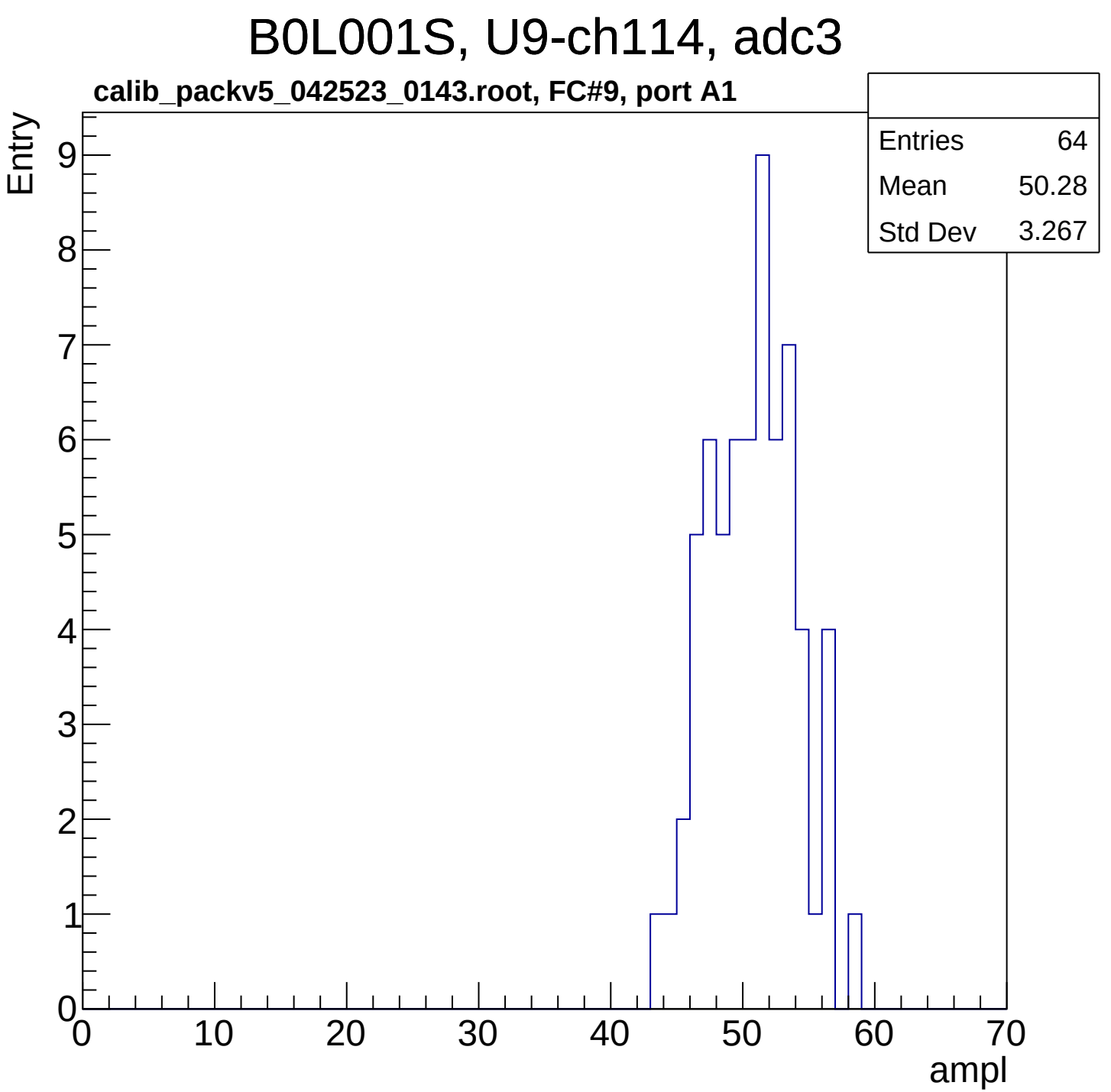
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch114, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 36.52
Std Dev 4.092
Gaus mean : 37.0885
Gaus Width: 4.0915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch114, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 43.08
Std Dev 3.941
Gaus mean : 43.4370
Gaus Width: 4.4005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch114, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.28
Std Dev 3.267
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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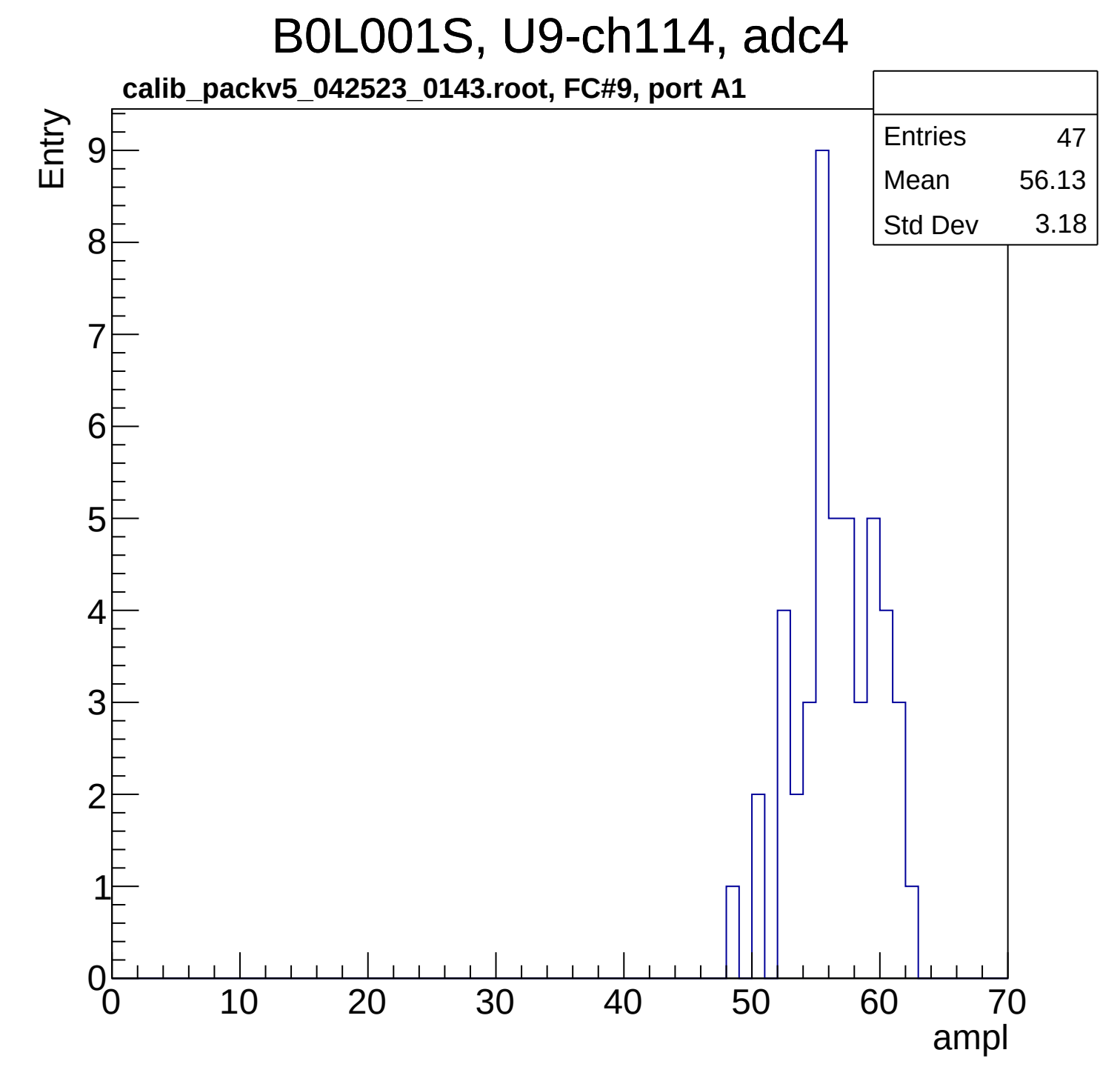
40
50
60
70


B0L001S, U9-ch114, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.13
Std Dev 3.18
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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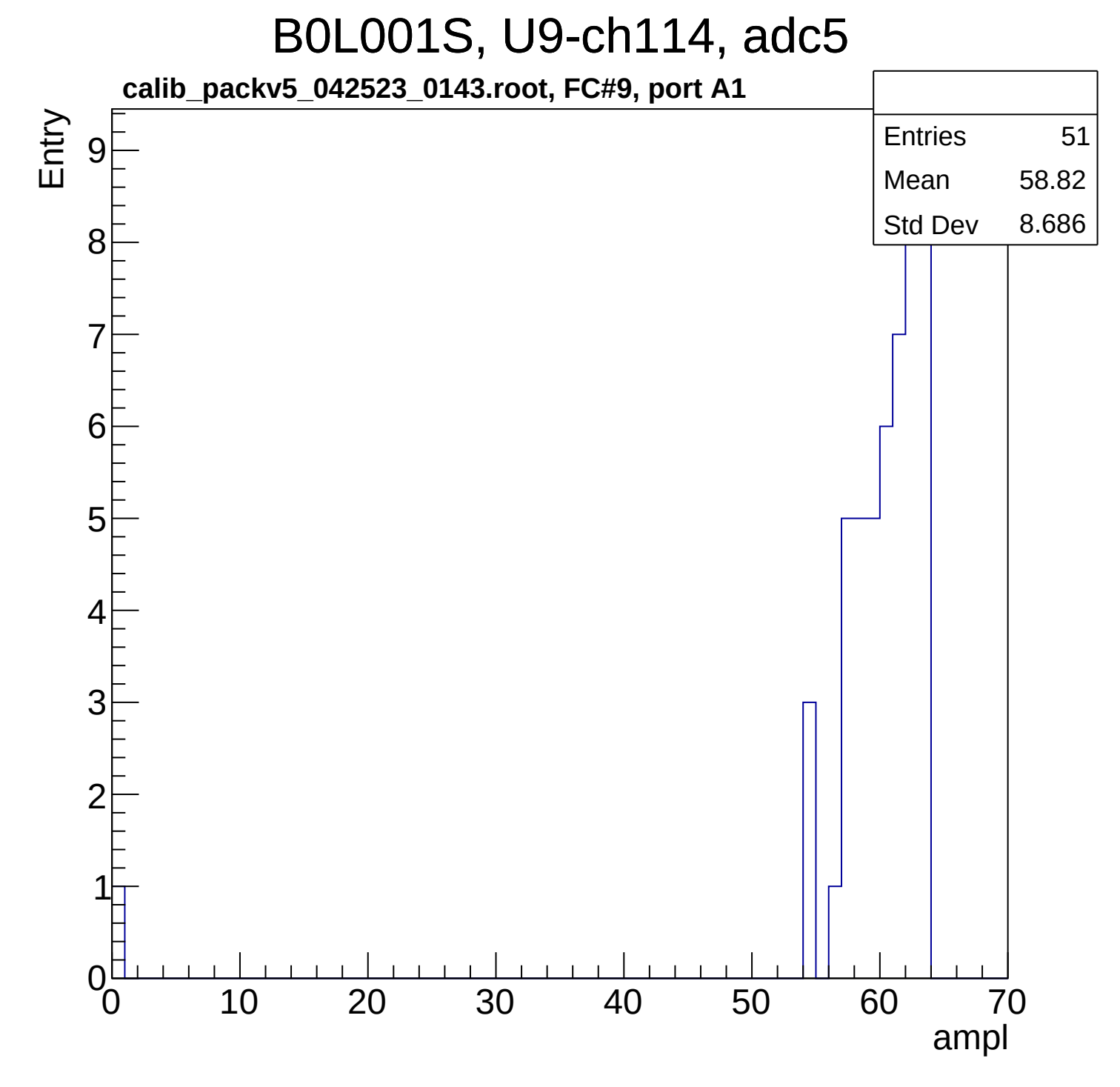
B0L001S, U9-ch114, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.82
Std Dev 8.686
Entry
ampl
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2
3
4
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6
7
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9
0
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40
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60
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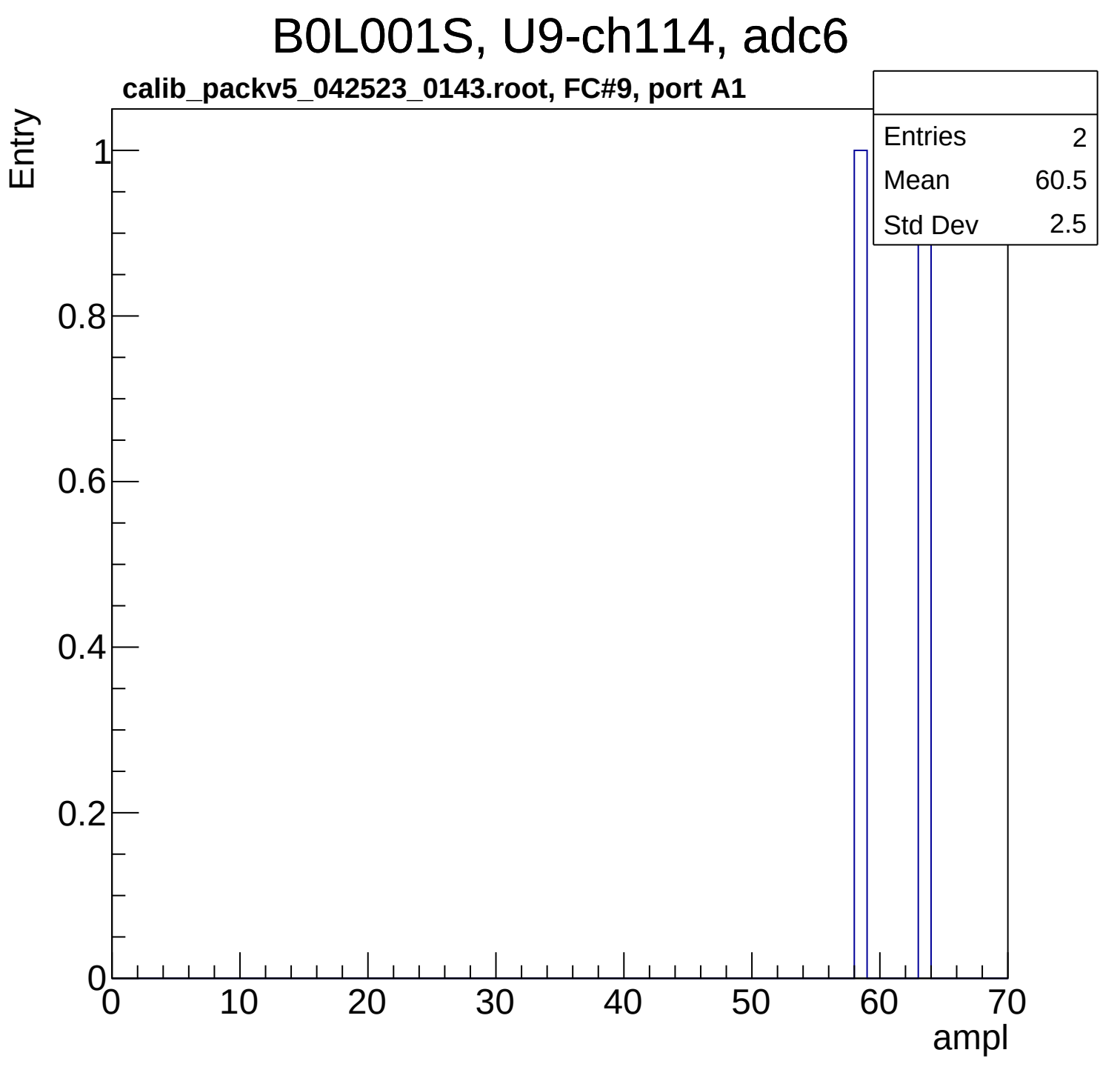
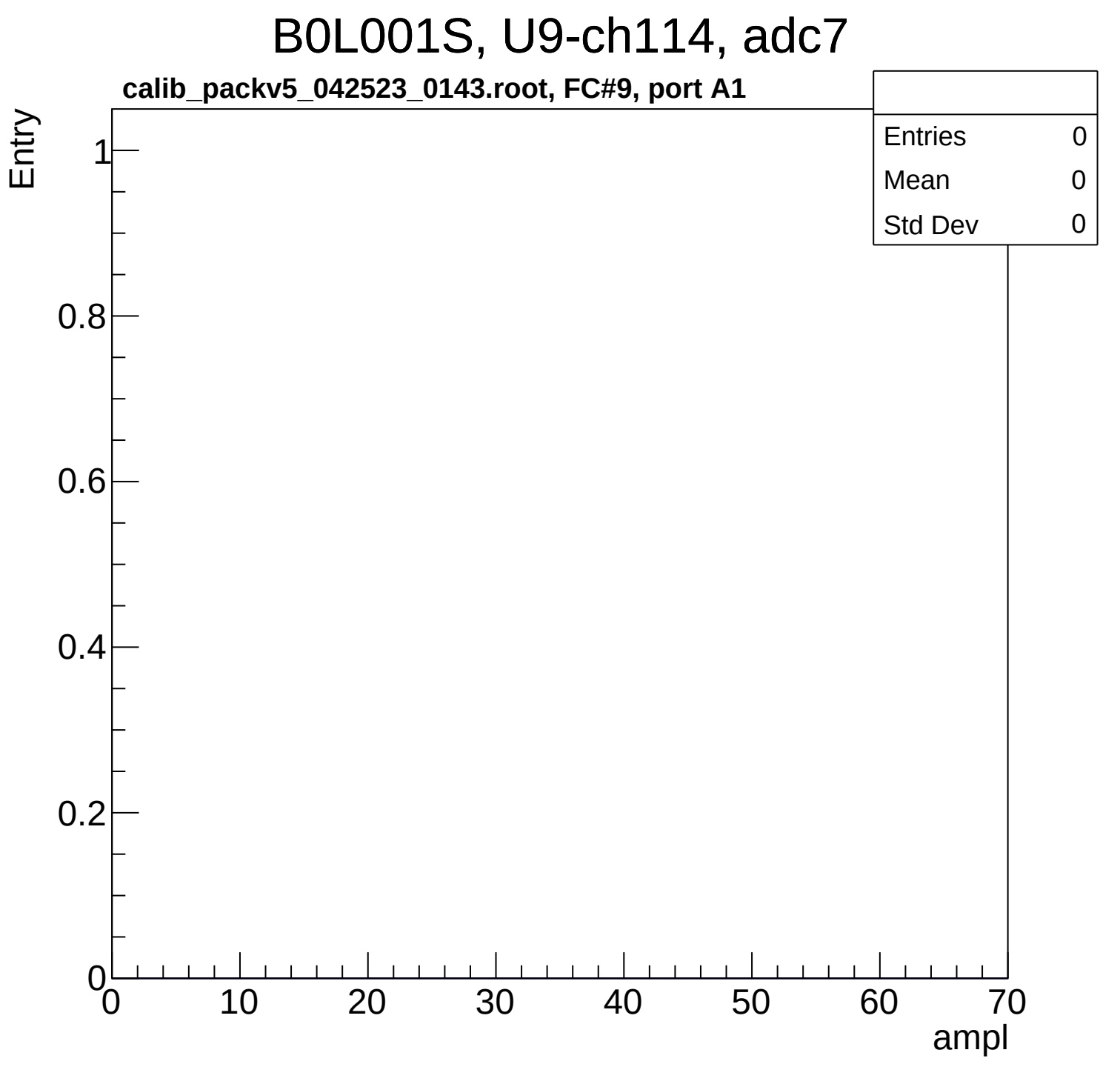

B0L001S, U9-ch114, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 60.5
Std Dev 2.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch114, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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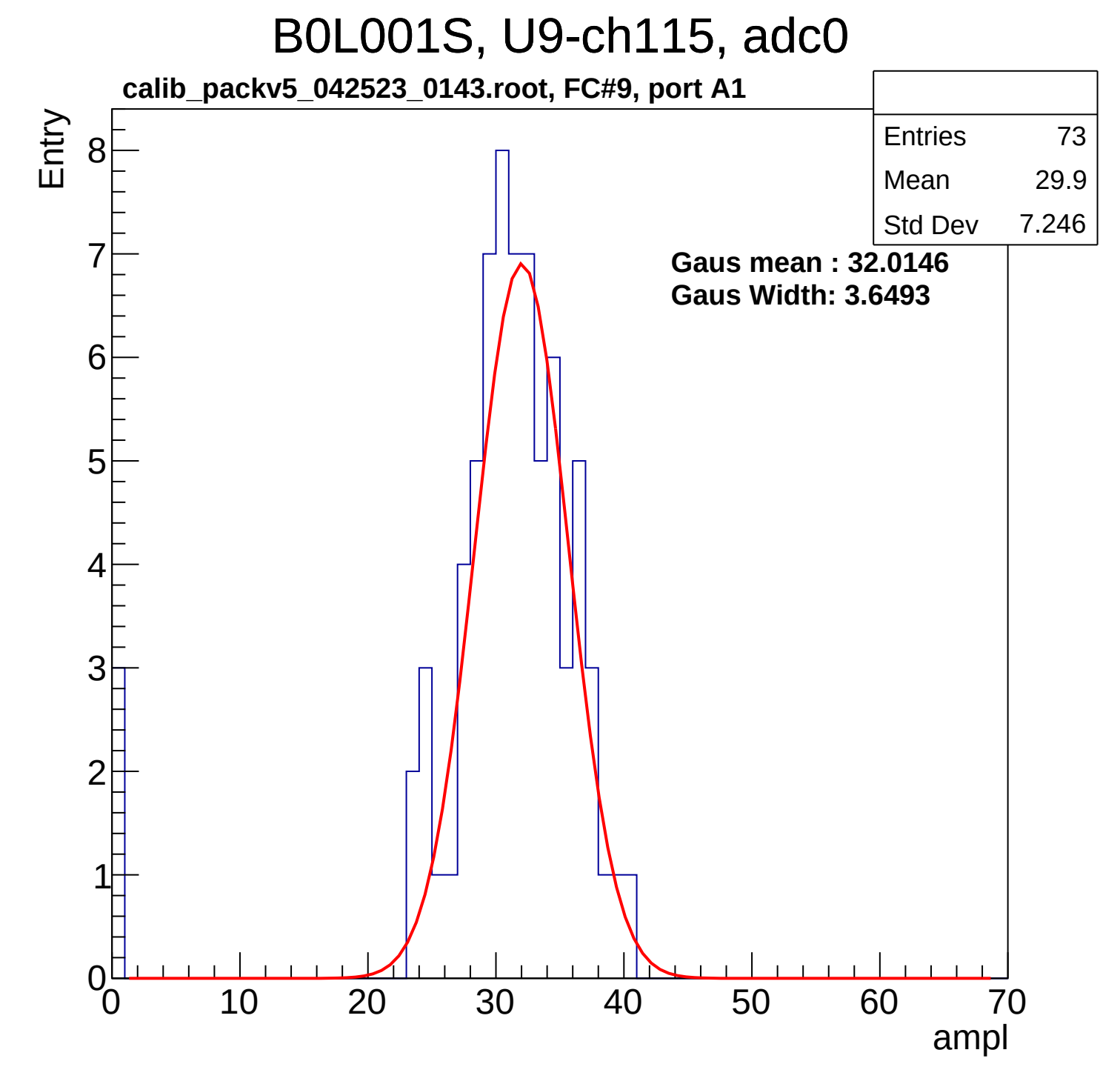
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch115, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.9
Std Dev 7.246
Gaus mean : 32.0146
Gaus Width: 3.6493
Entry
ampl
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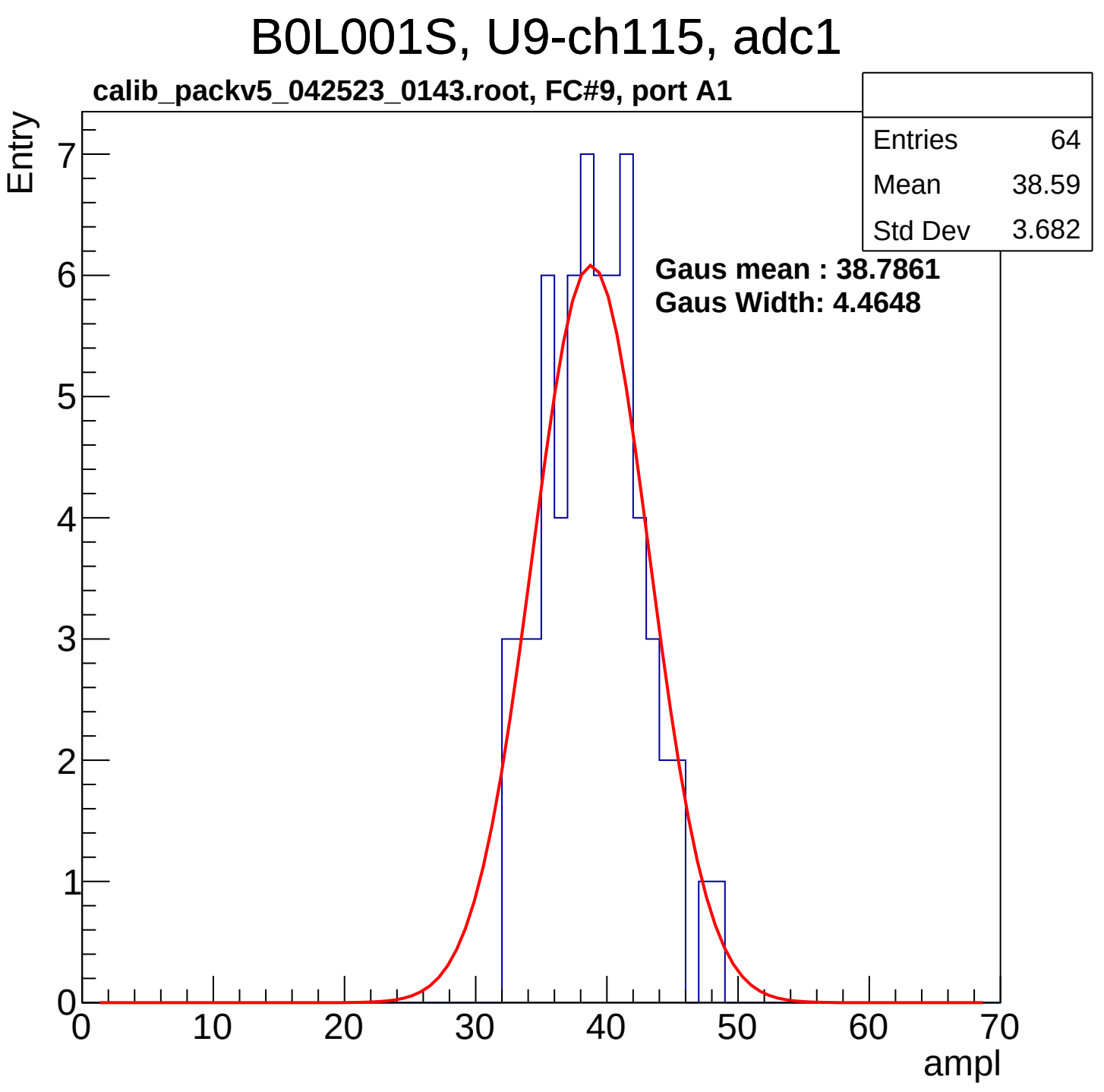
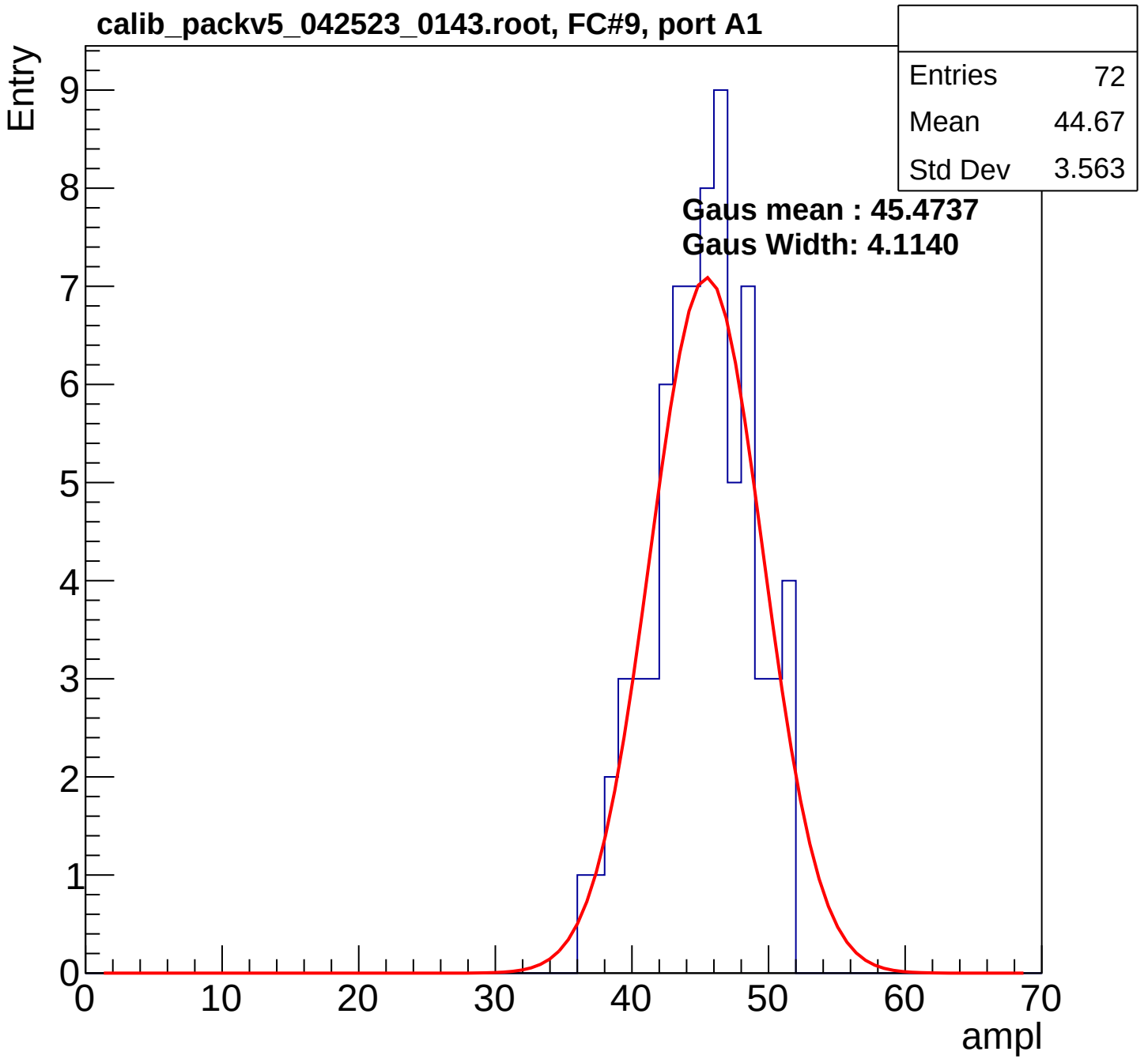
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch115, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.59
Std Dev 3.682
Gaus mean : 38.7861
Gaus Width: 4.4648
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch115, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.67
Std Dev 3.563
Gaus mean : 45.4737
Gaus Width: 4.1140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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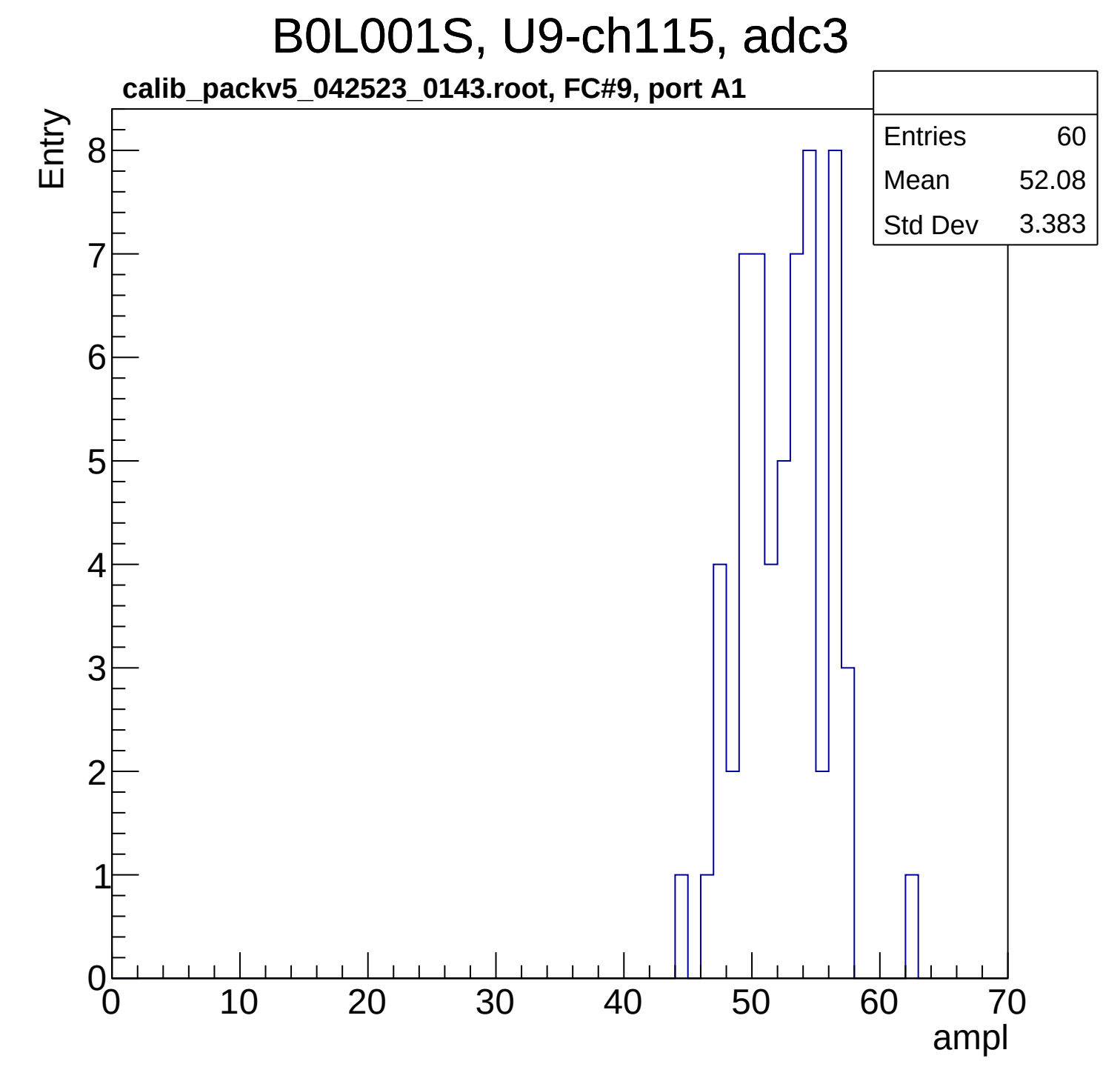
B0L001S, U9-ch115, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 52.08
Std Dev 3.383
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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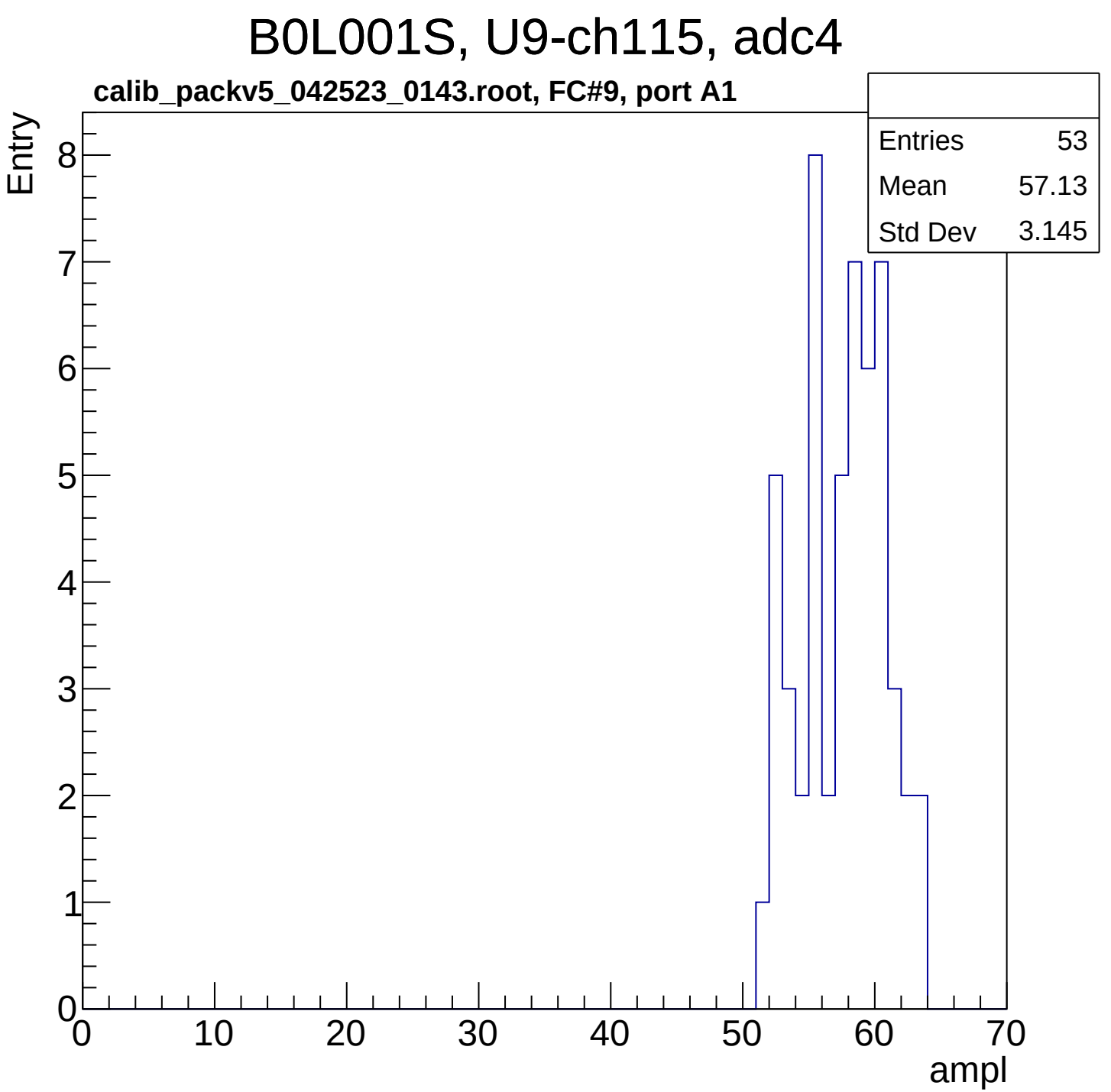
B0L001S, U9-ch115, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 57.13
Std Dev 3.145
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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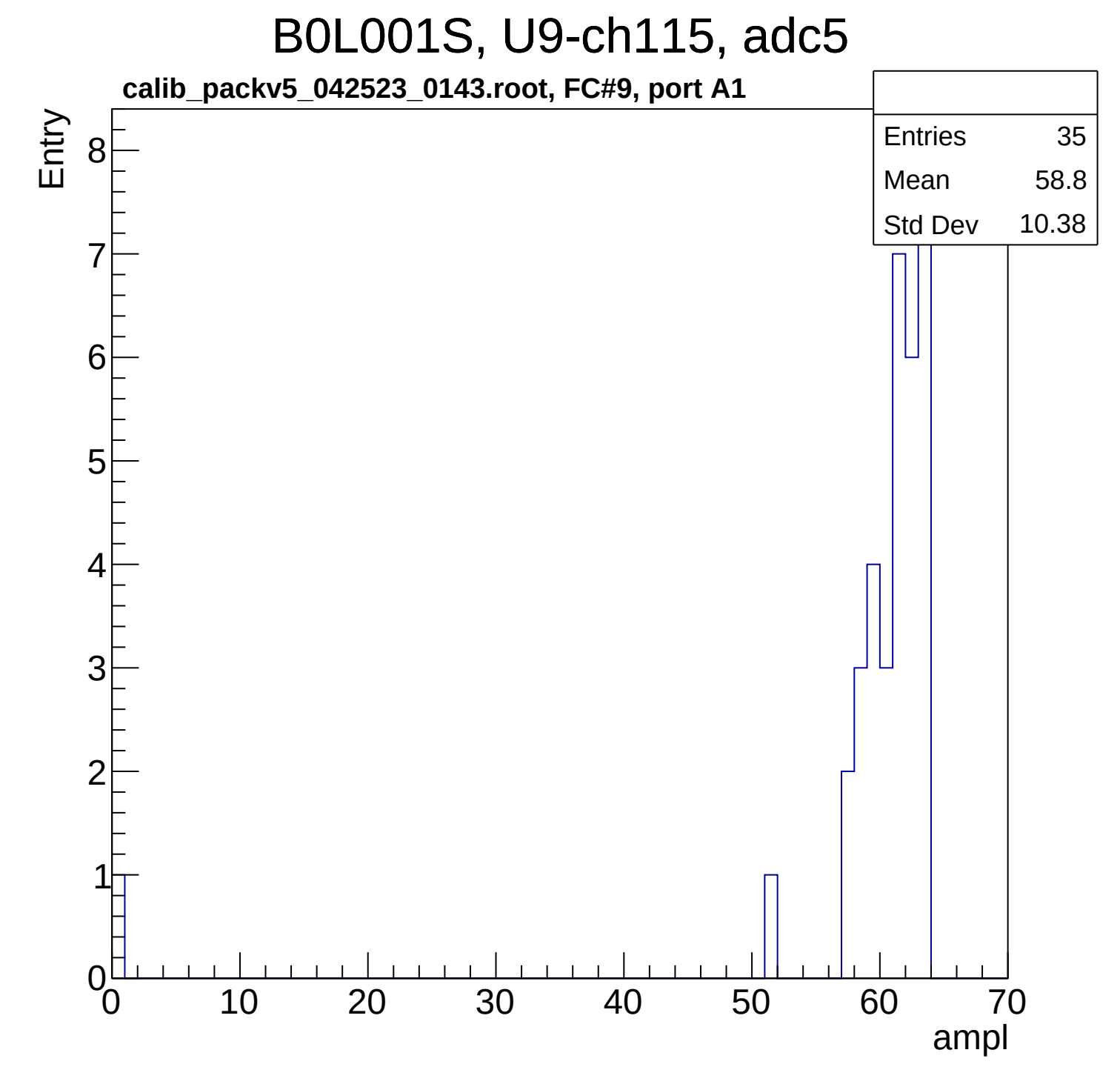
B0L001S, U9-ch115, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.8
Std Dev 10.38
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
30
40
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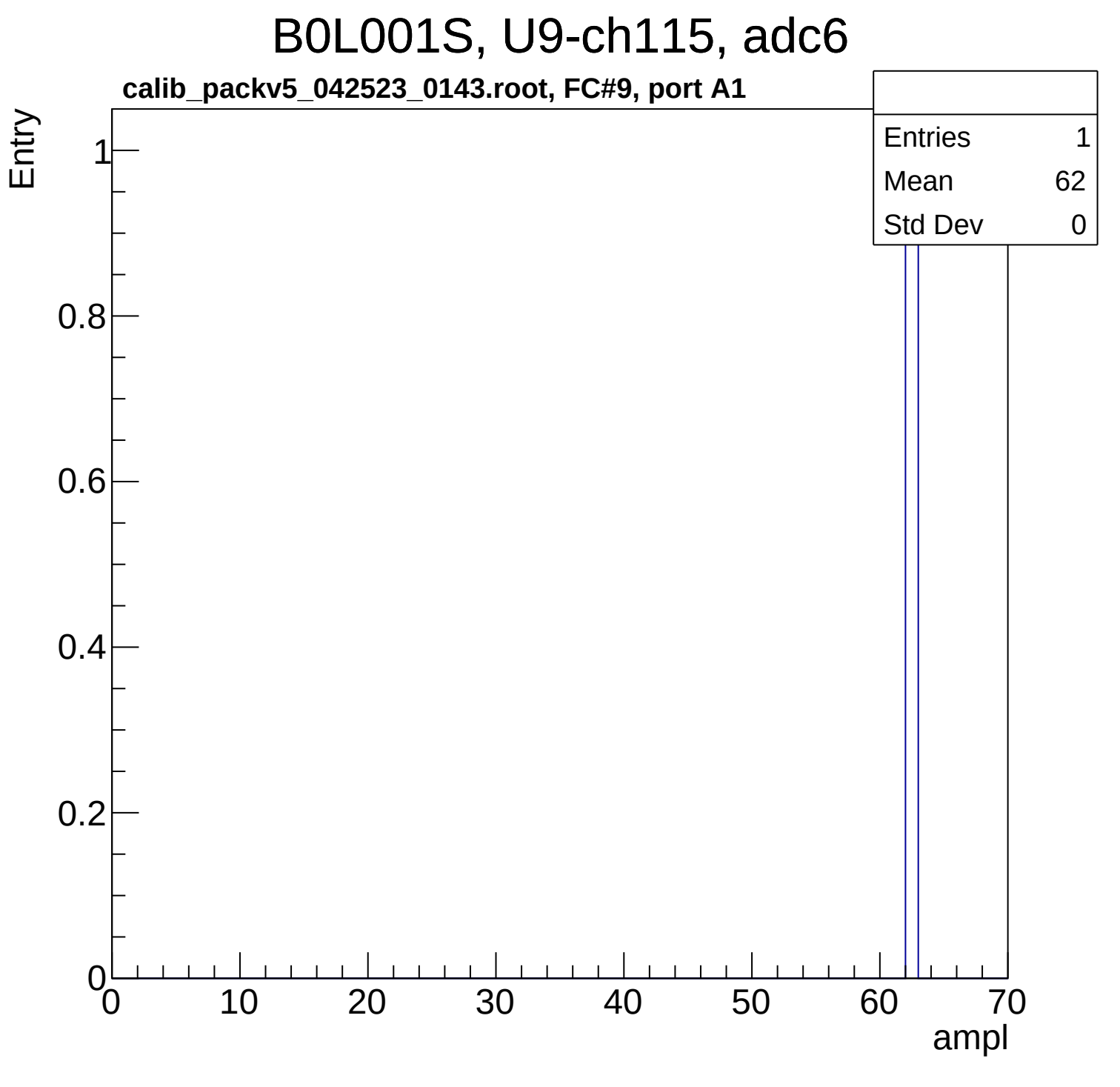
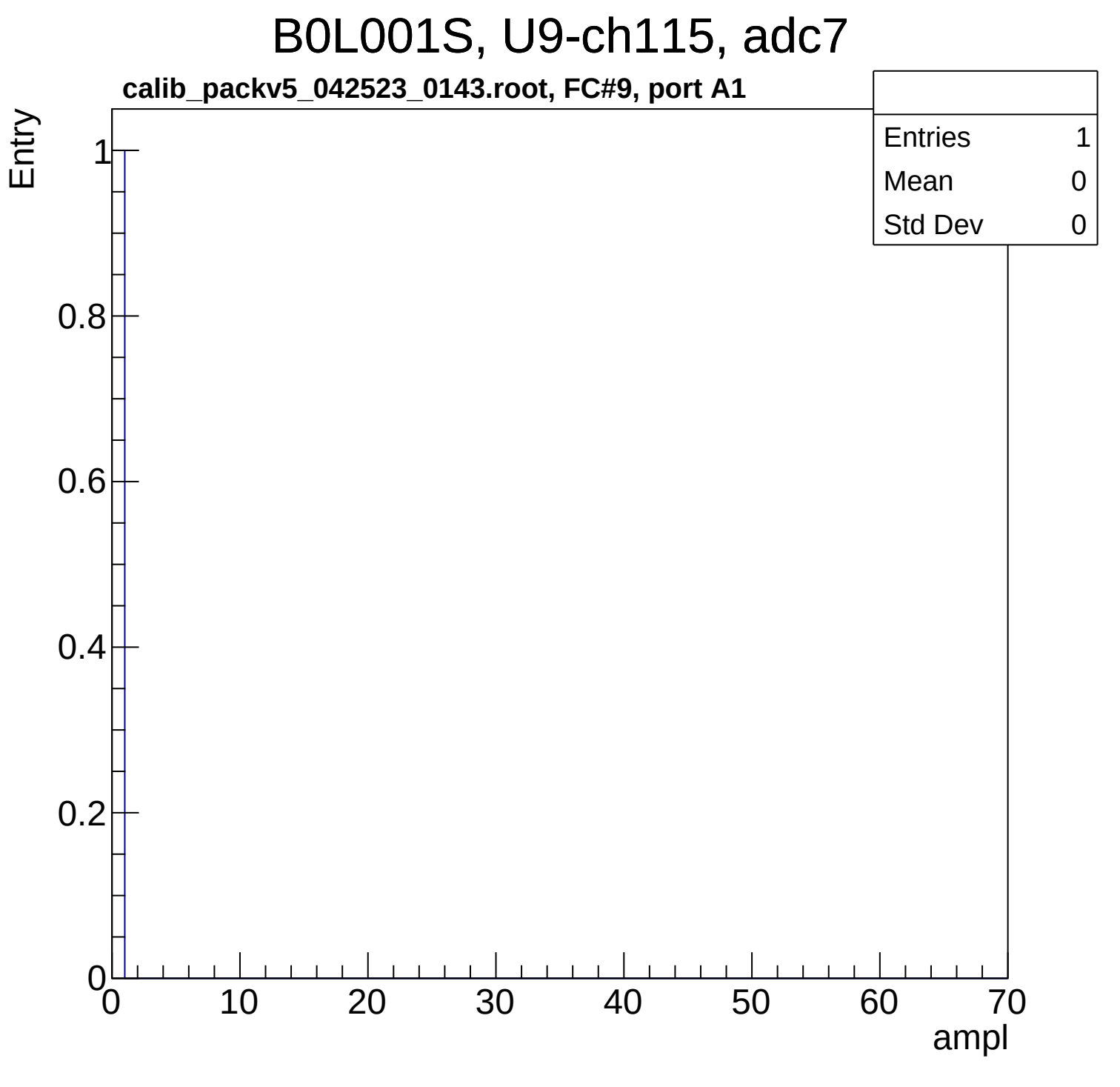
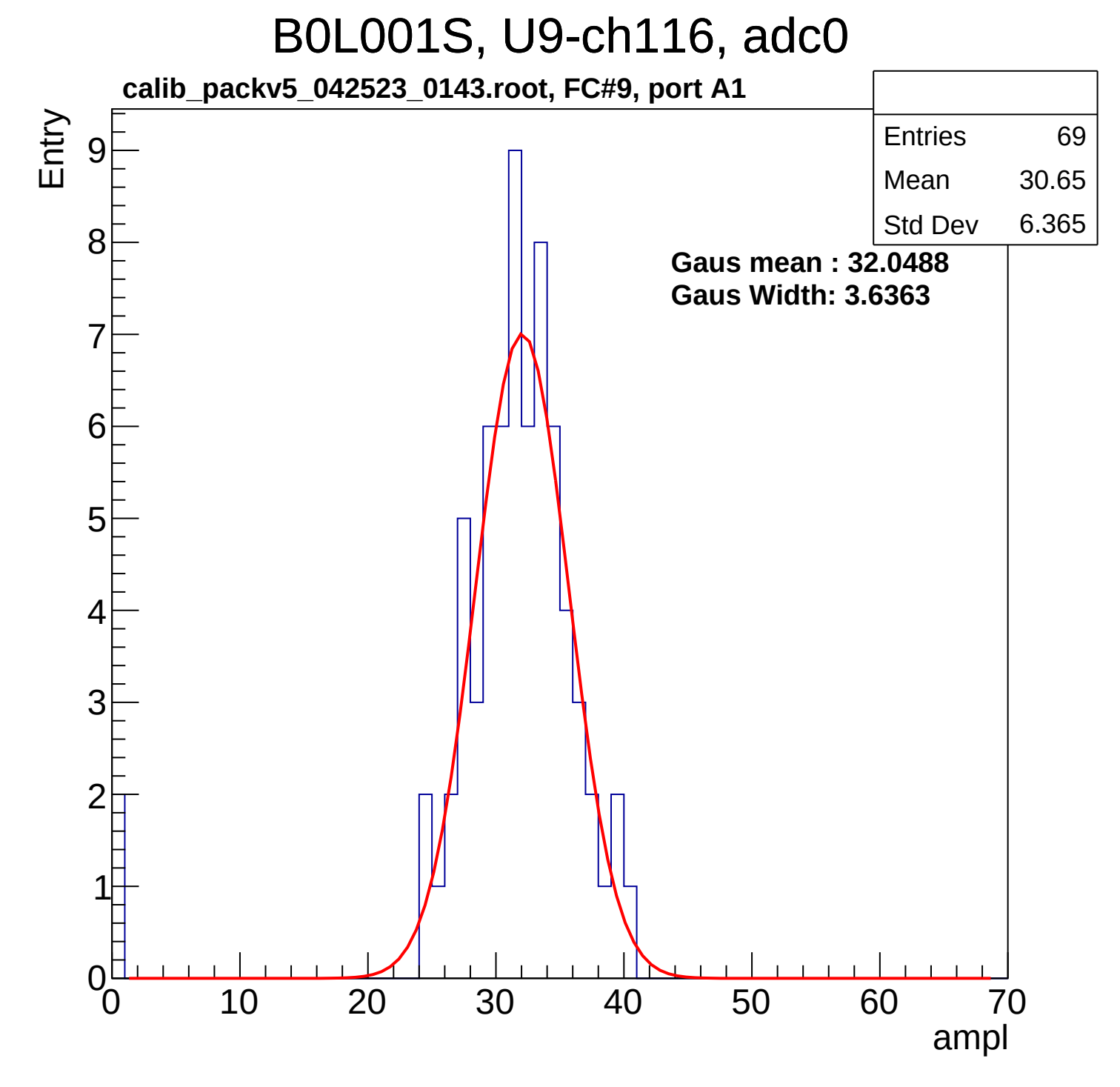

B0L001S, U9-ch115, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch115, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch116, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.65
Std Dev 6.365
Gaus mean : 32.0488
Gaus Width: 3.6363
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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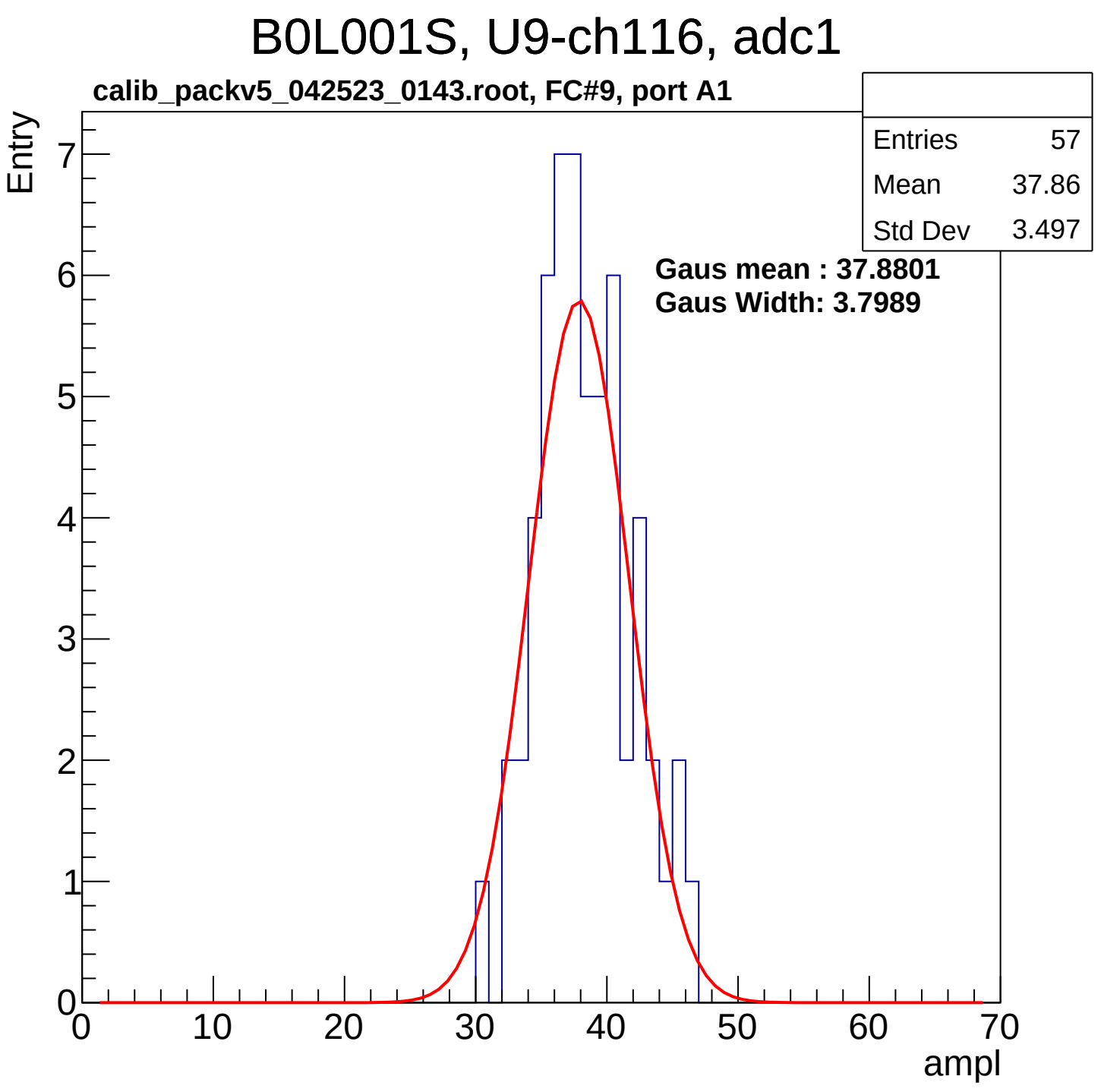
B0L001S, U9-ch116, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37.86
Std Dev 3.497
Gaus mean : 37.8801
Gaus Width: 3.7989
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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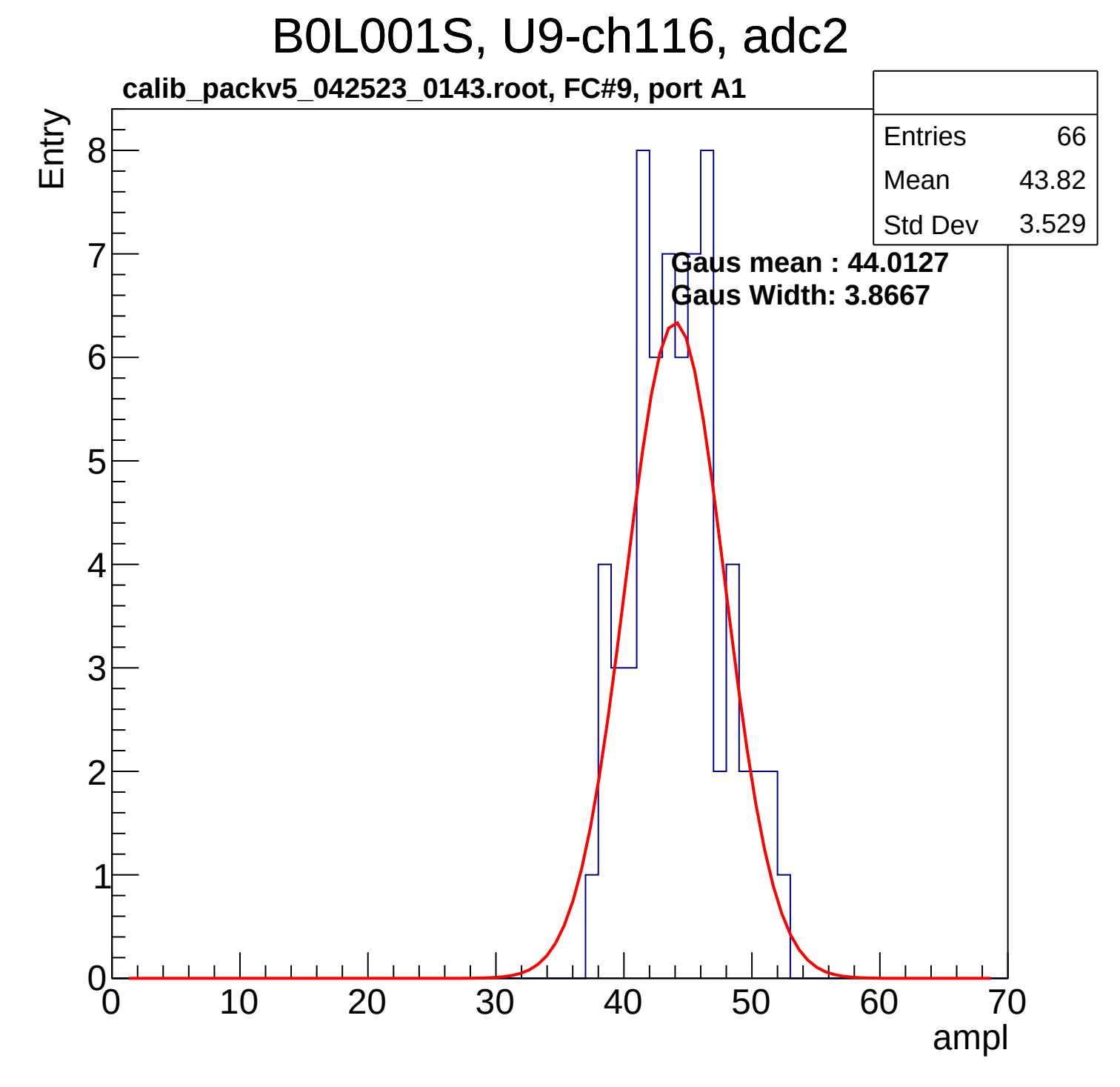
B0L001S, U9-ch116, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.82
Std Dev 3.529
Gaus mean : 44.0127
Gaus Width: 3.8667
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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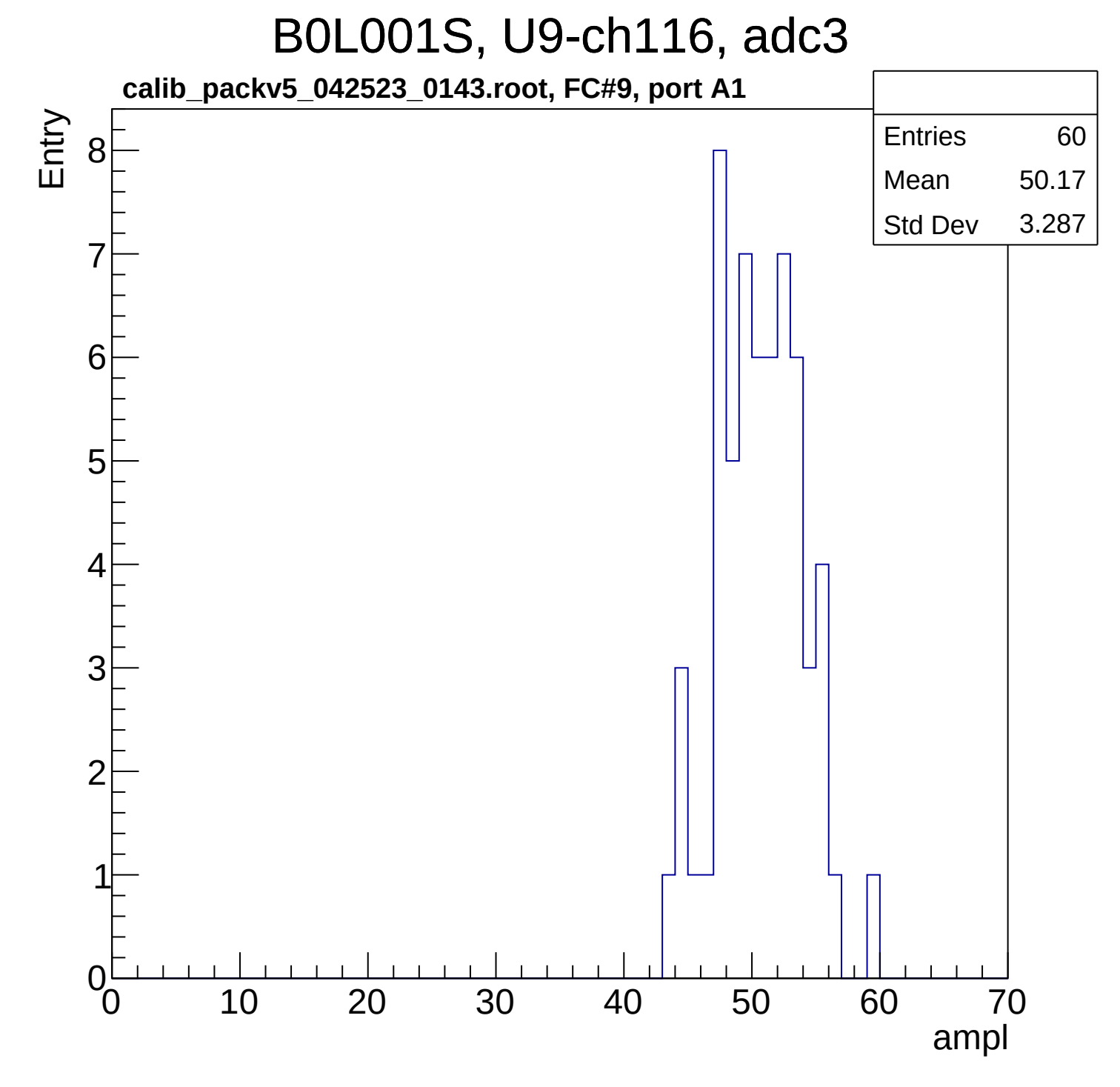
B0L001S, U9-ch116, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.17
Std Dev 3.287
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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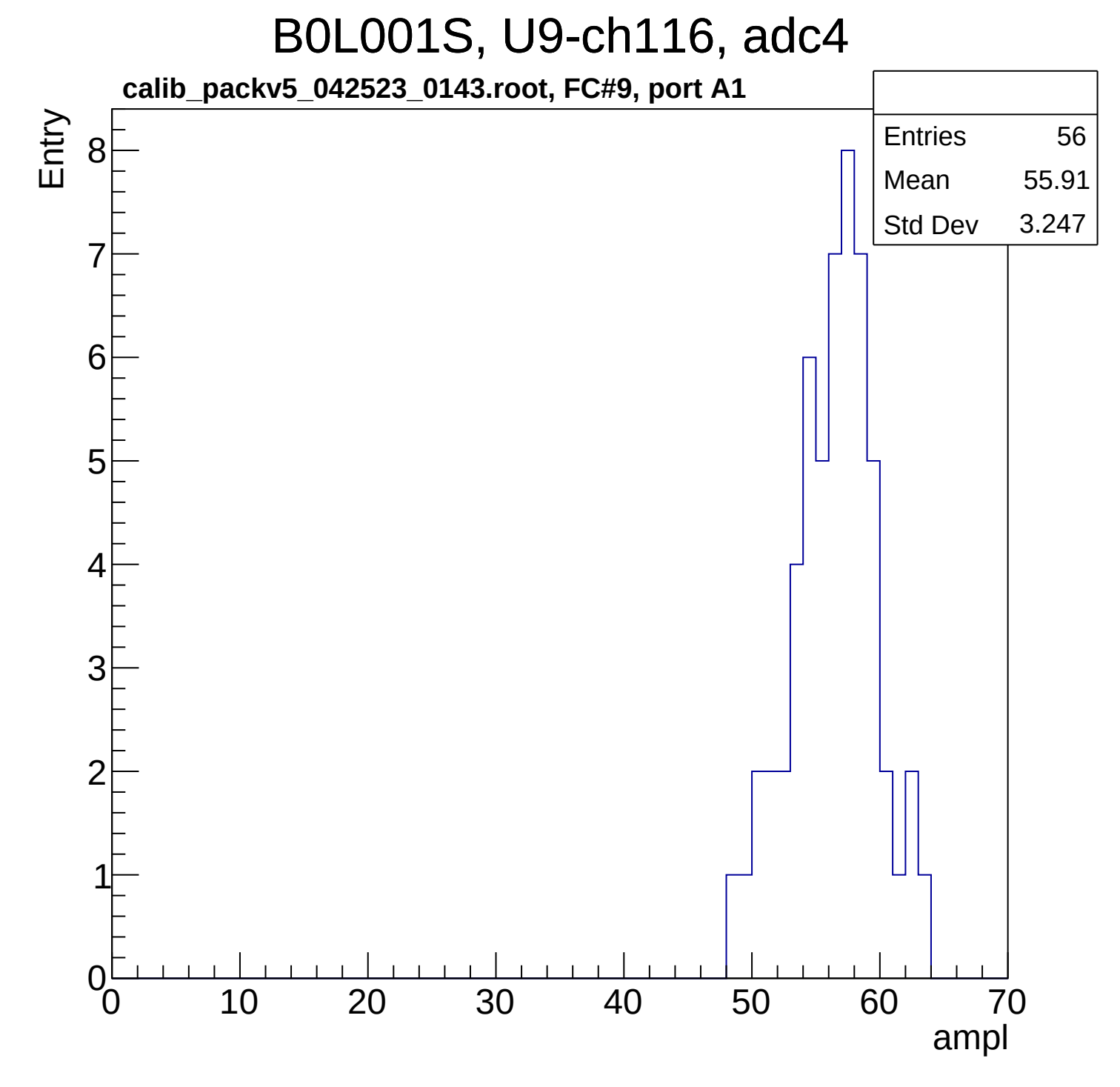
B0L001S, U9-ch116, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.91
Std Dev 3.247
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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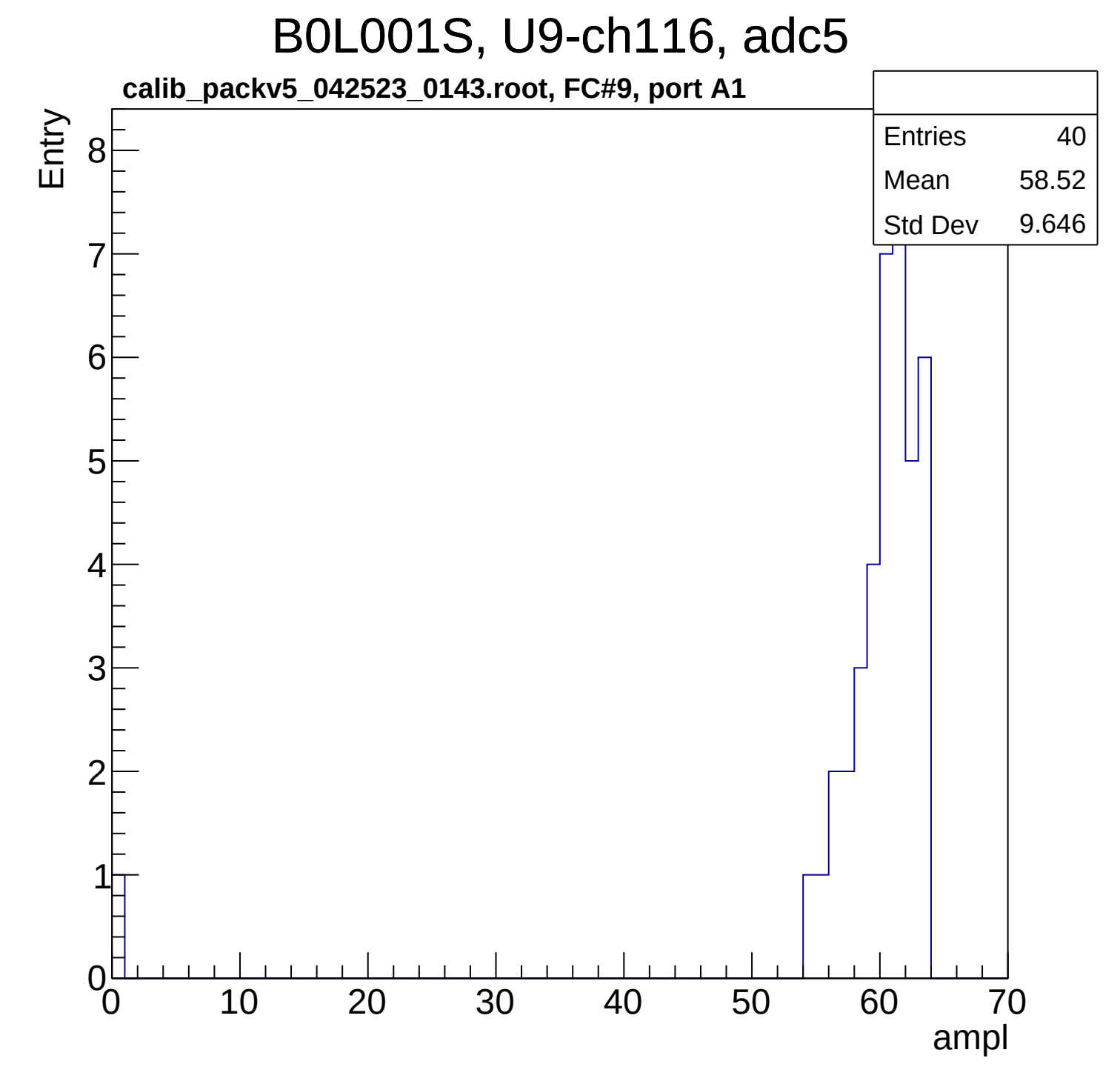
B0L001S, U9-ch116, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.52
Std Dev 9.646
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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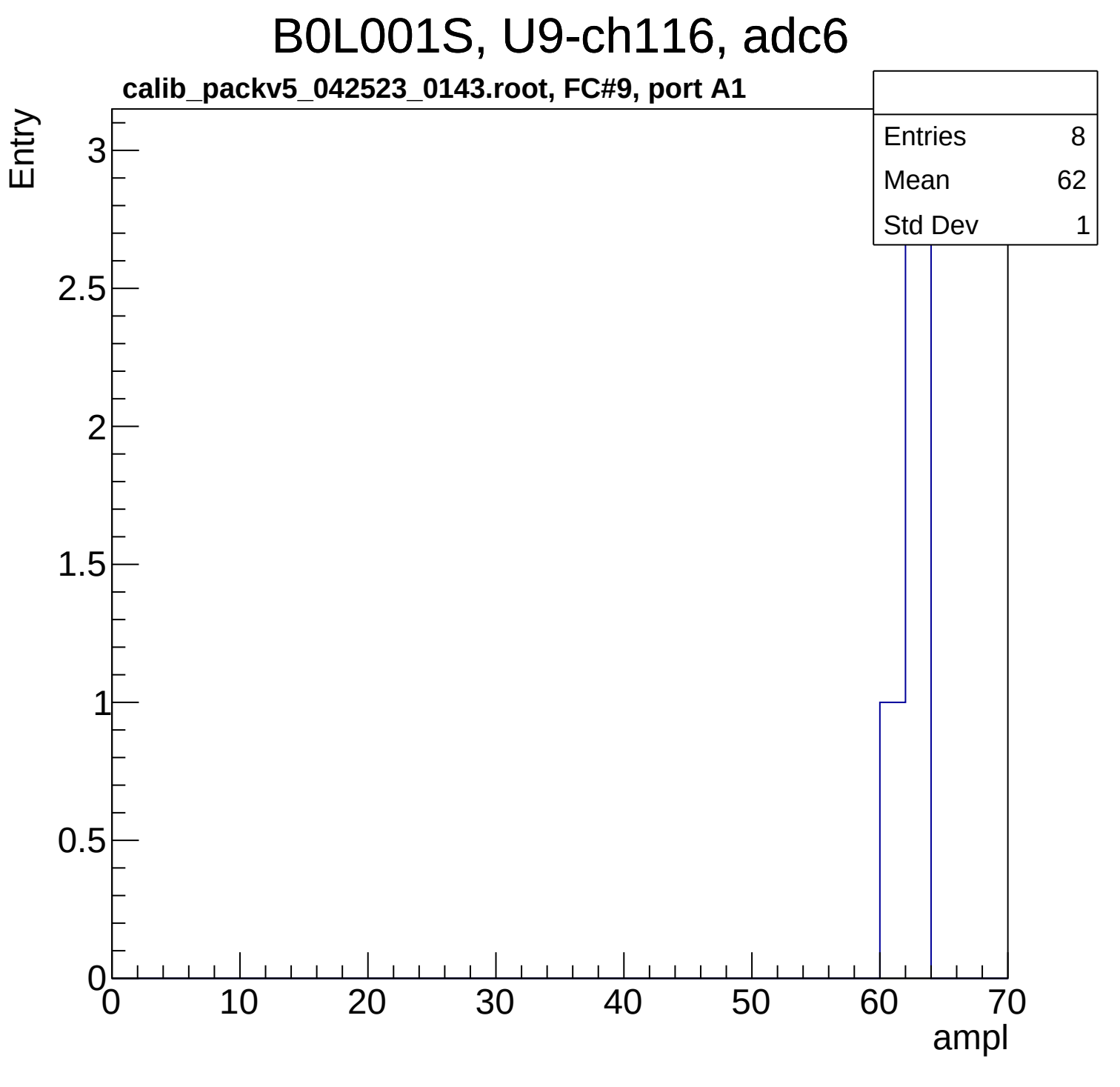
B0L001S, U9-ch116, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 62
Std Dev 1
Entry
ampl
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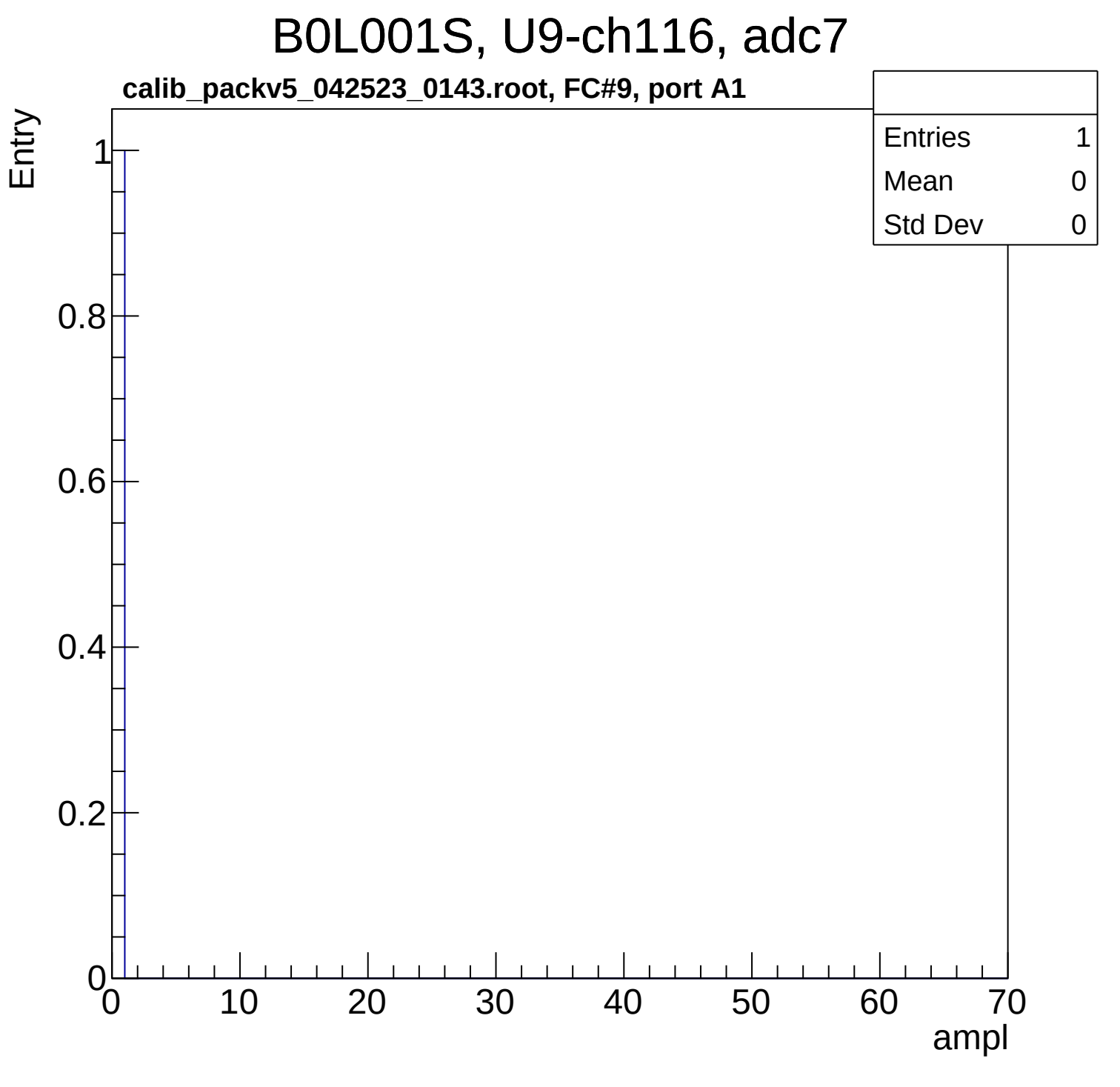
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch116, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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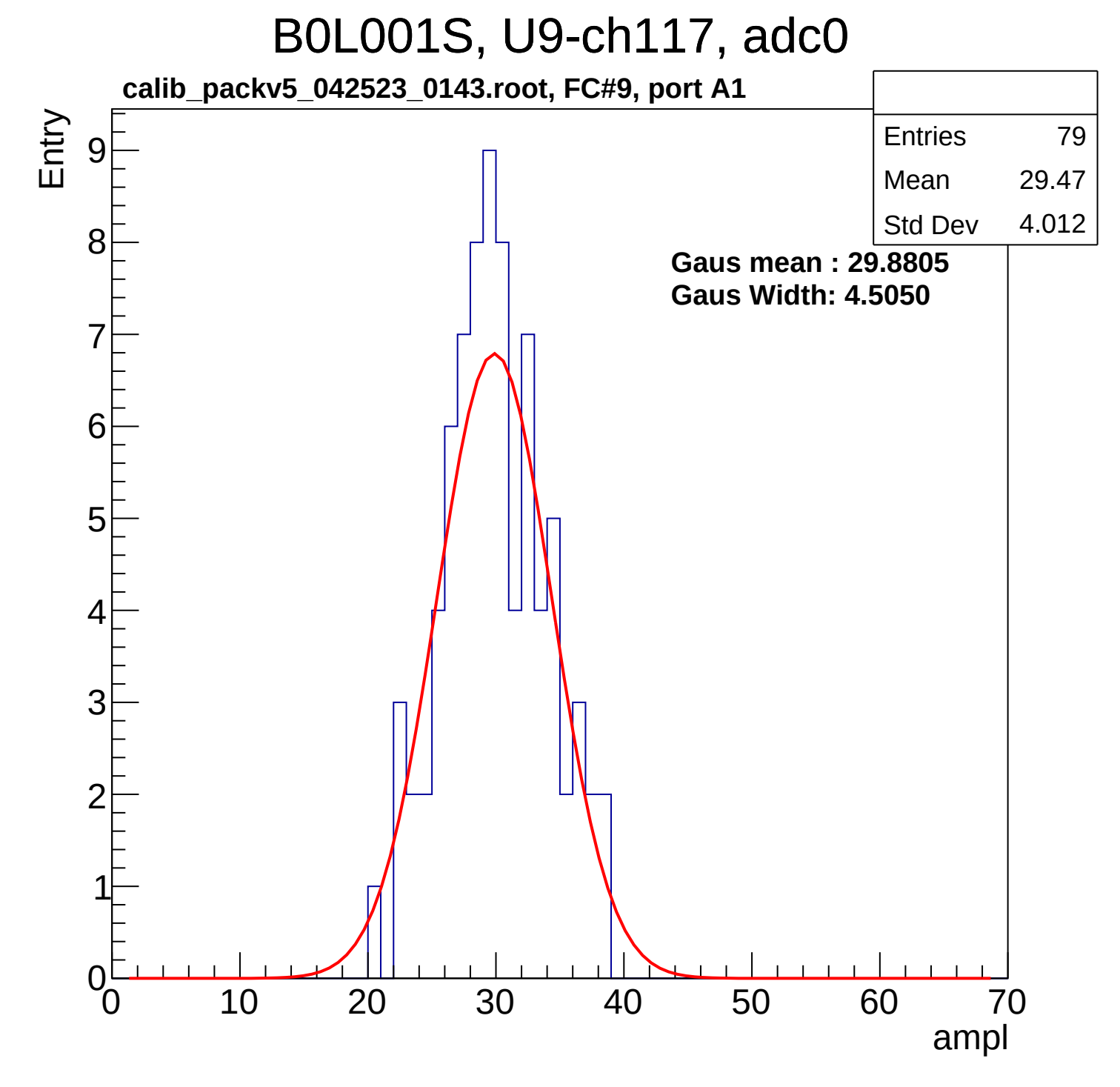
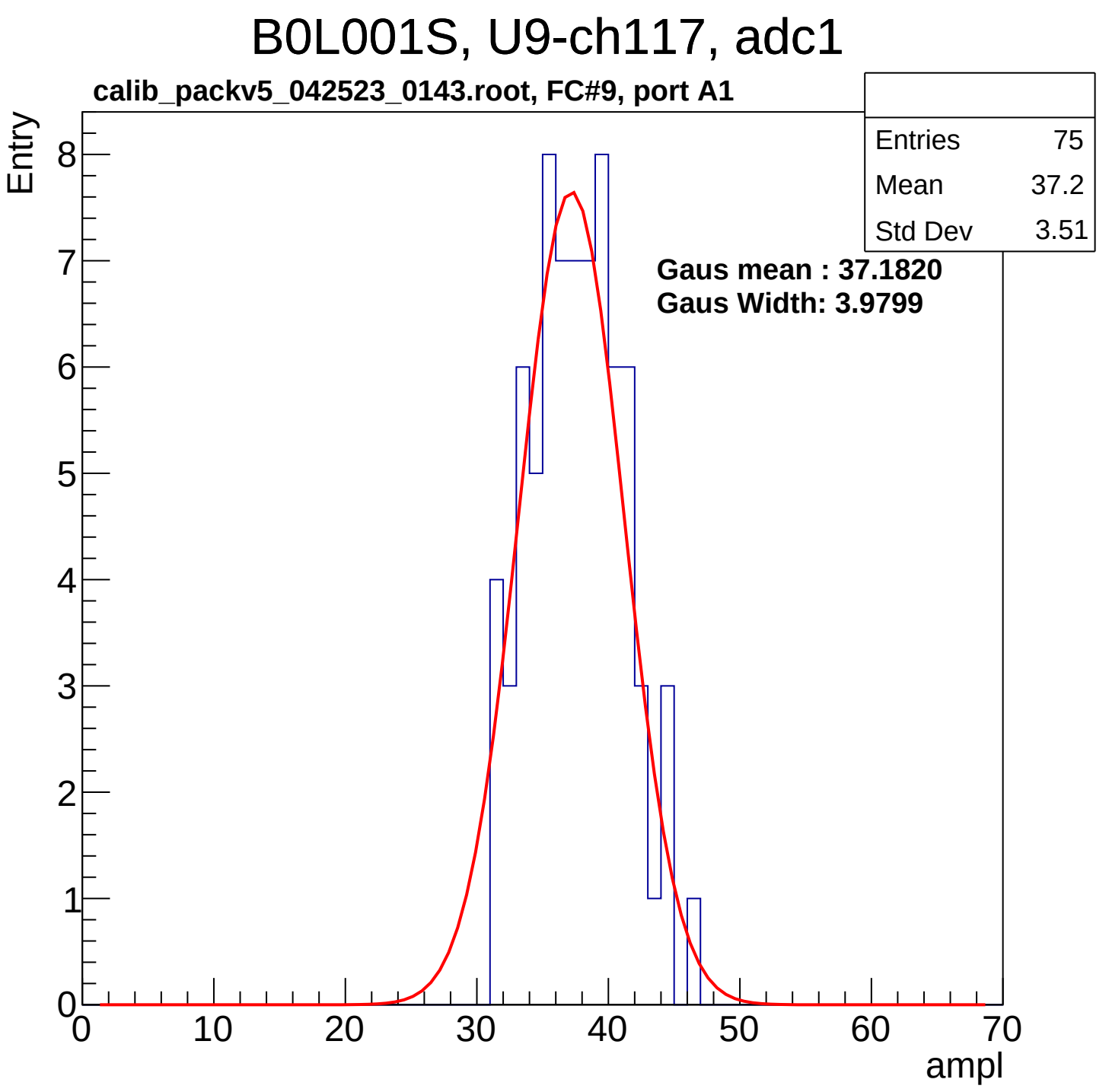
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch117, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 29.47
Std Dev 4.012
Gaus mean : 29.8805
Gaus Width: 4.5050
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch117, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.2
Std Dev 3.51
Gaus mean : 37.1820
Gaus Width: 3.9799
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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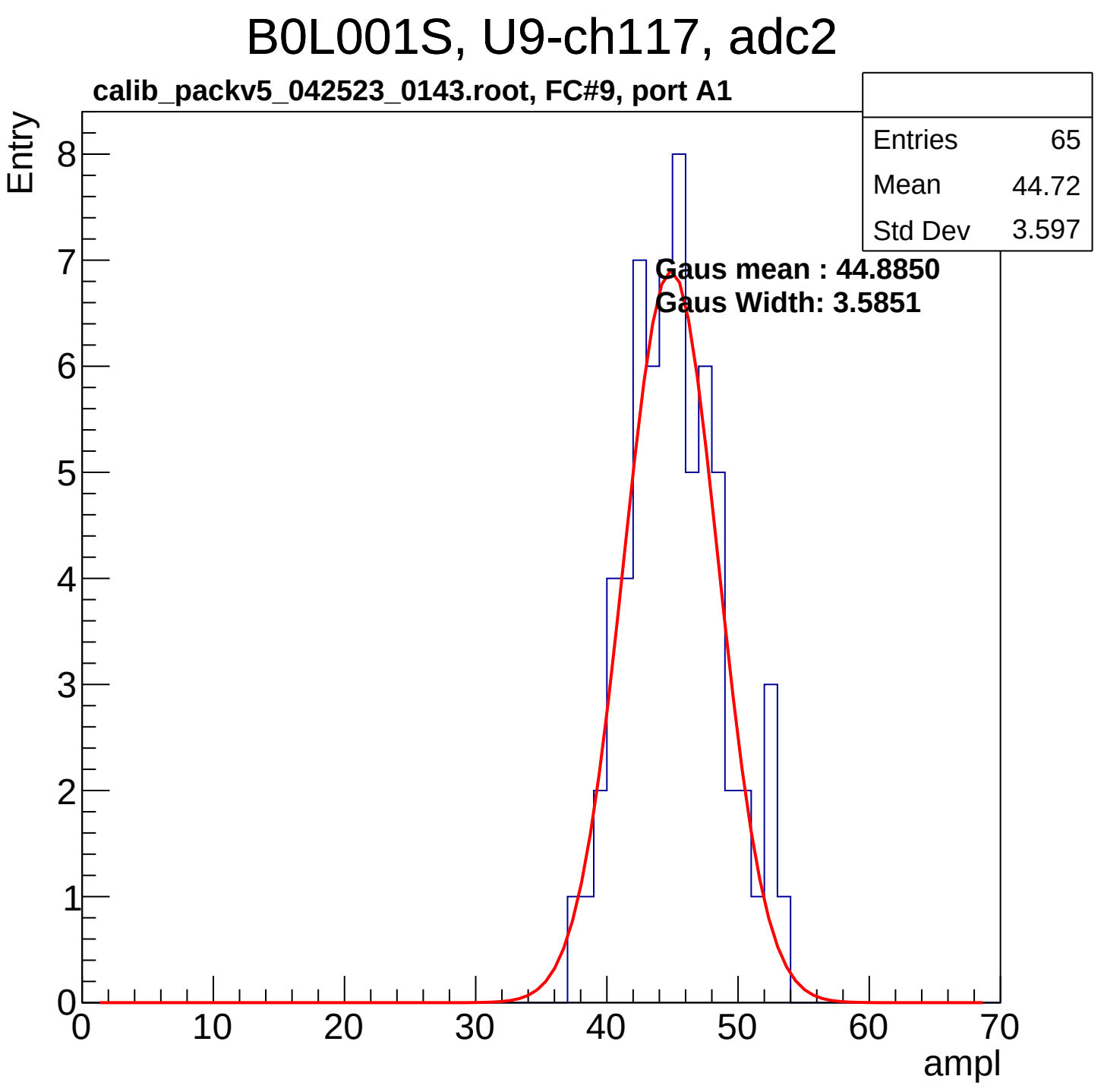
B0L001S, U9-ch117, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.72
Std Dev 3.597
Gaus mean : 44.8850
Gaus Width: 3.5851
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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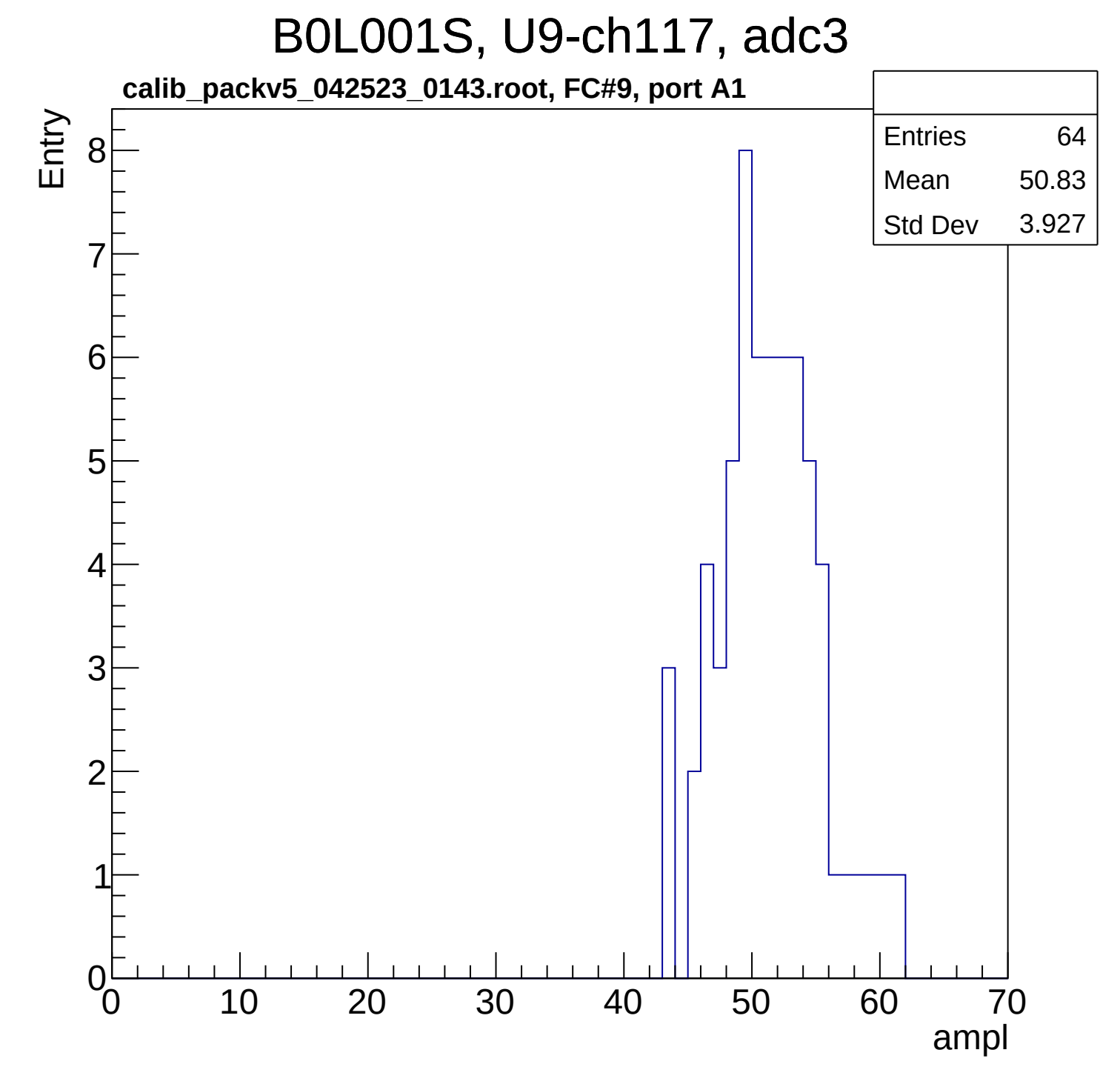
B0L001S, U9-ch117, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.83
Std Dev 3.927
Entry
ampl
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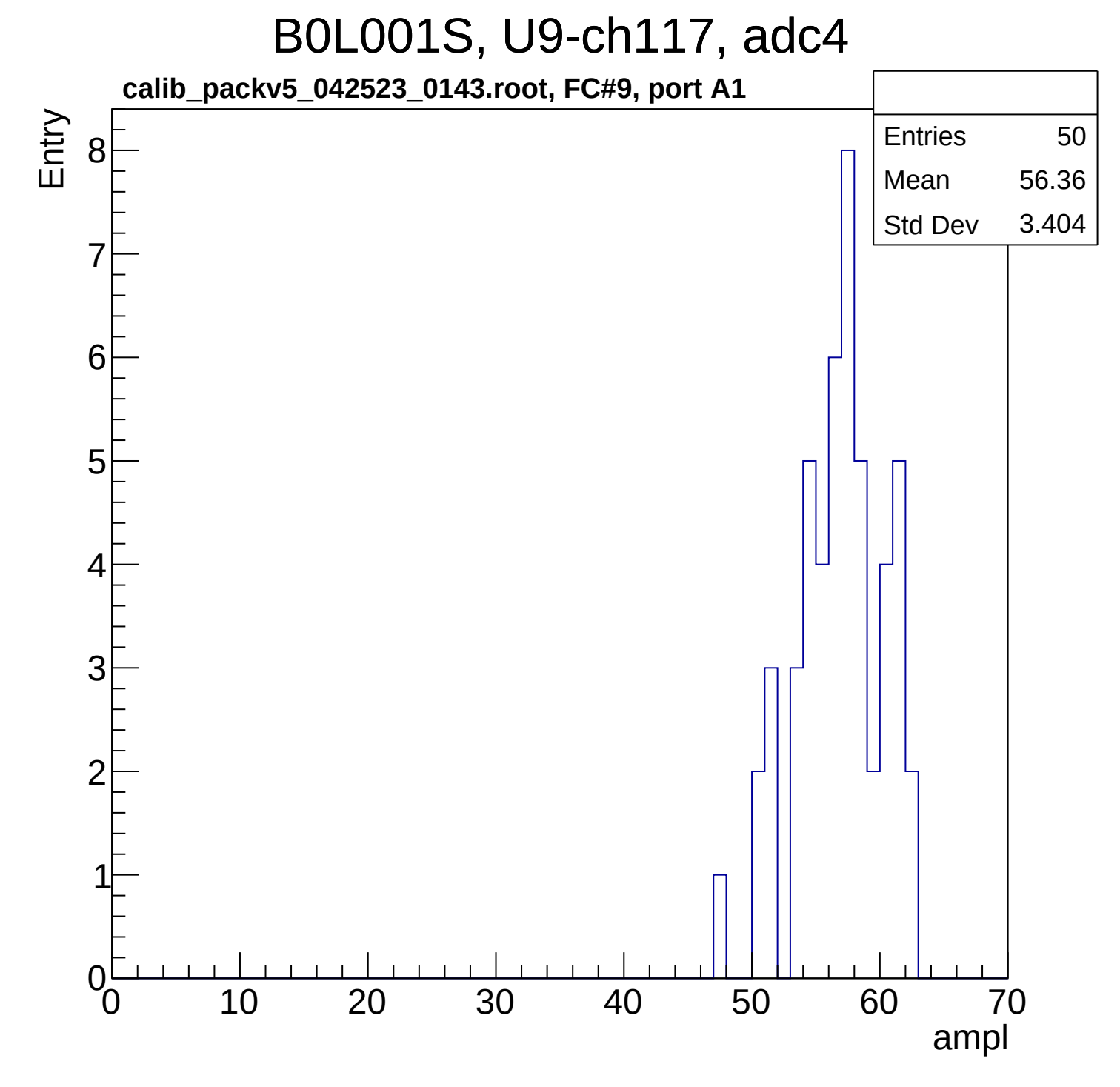
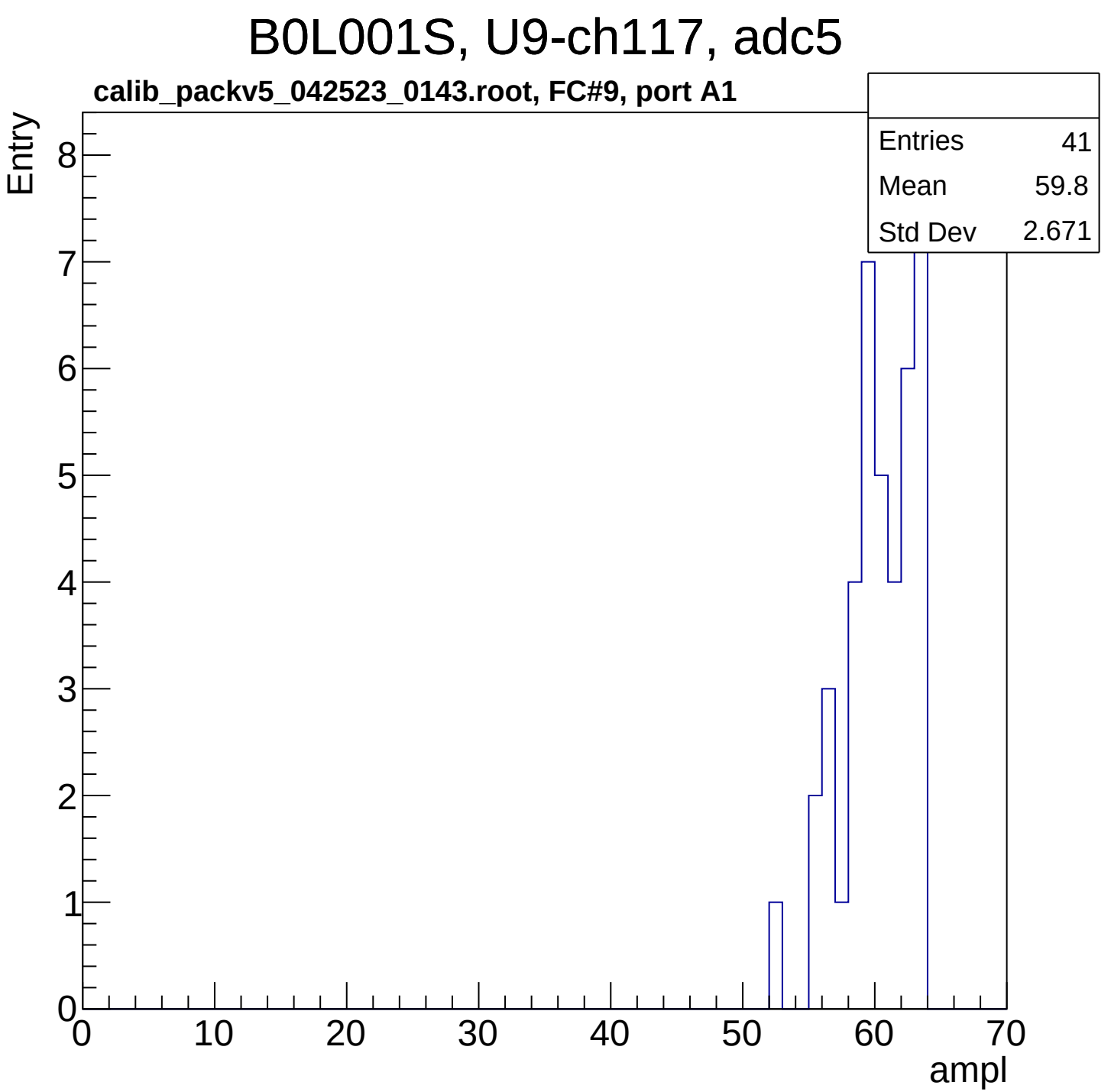
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch117, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 56.36
Std Dev 3.404
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch117, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 59.8
Std Dev 2.671
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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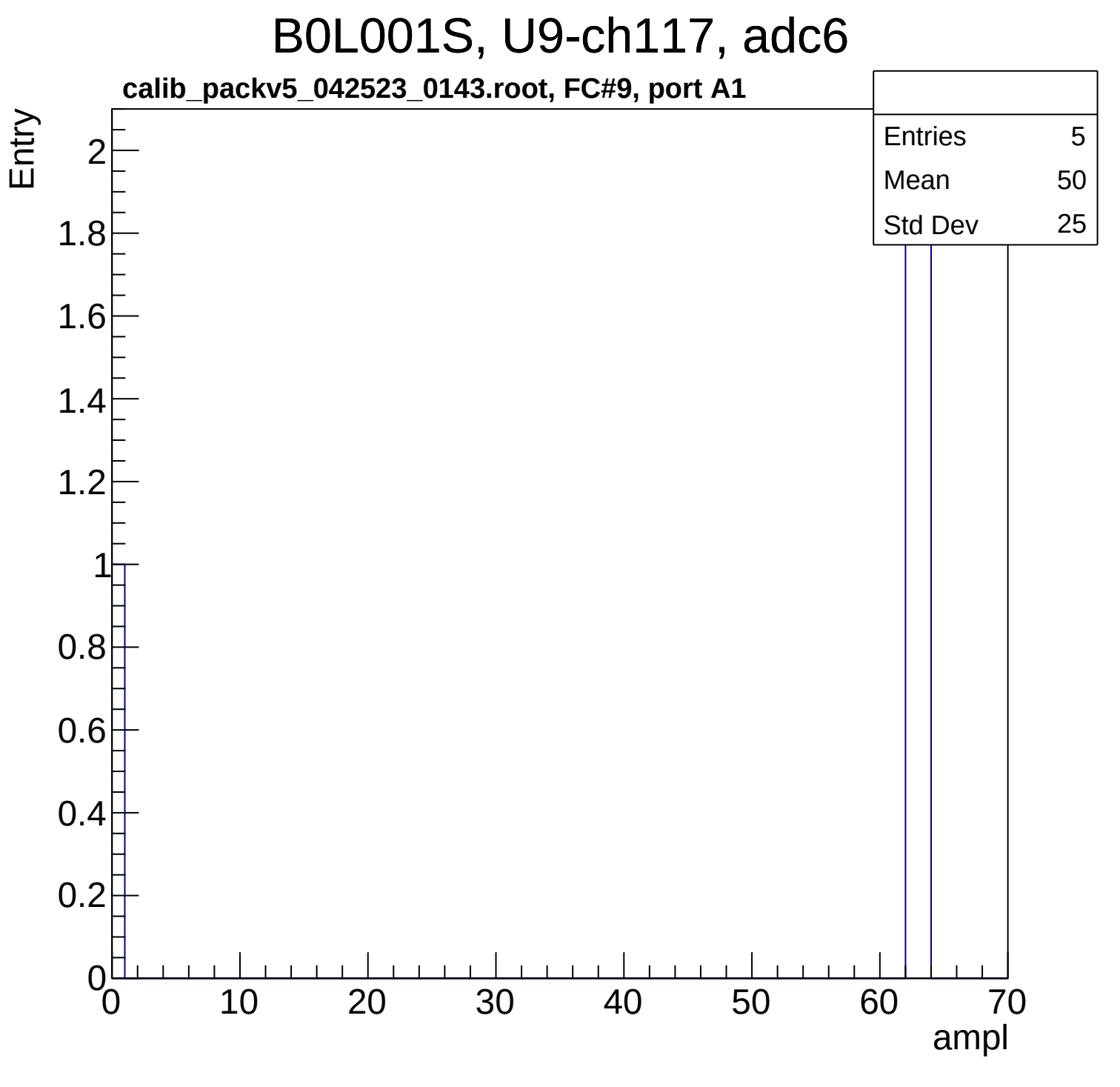
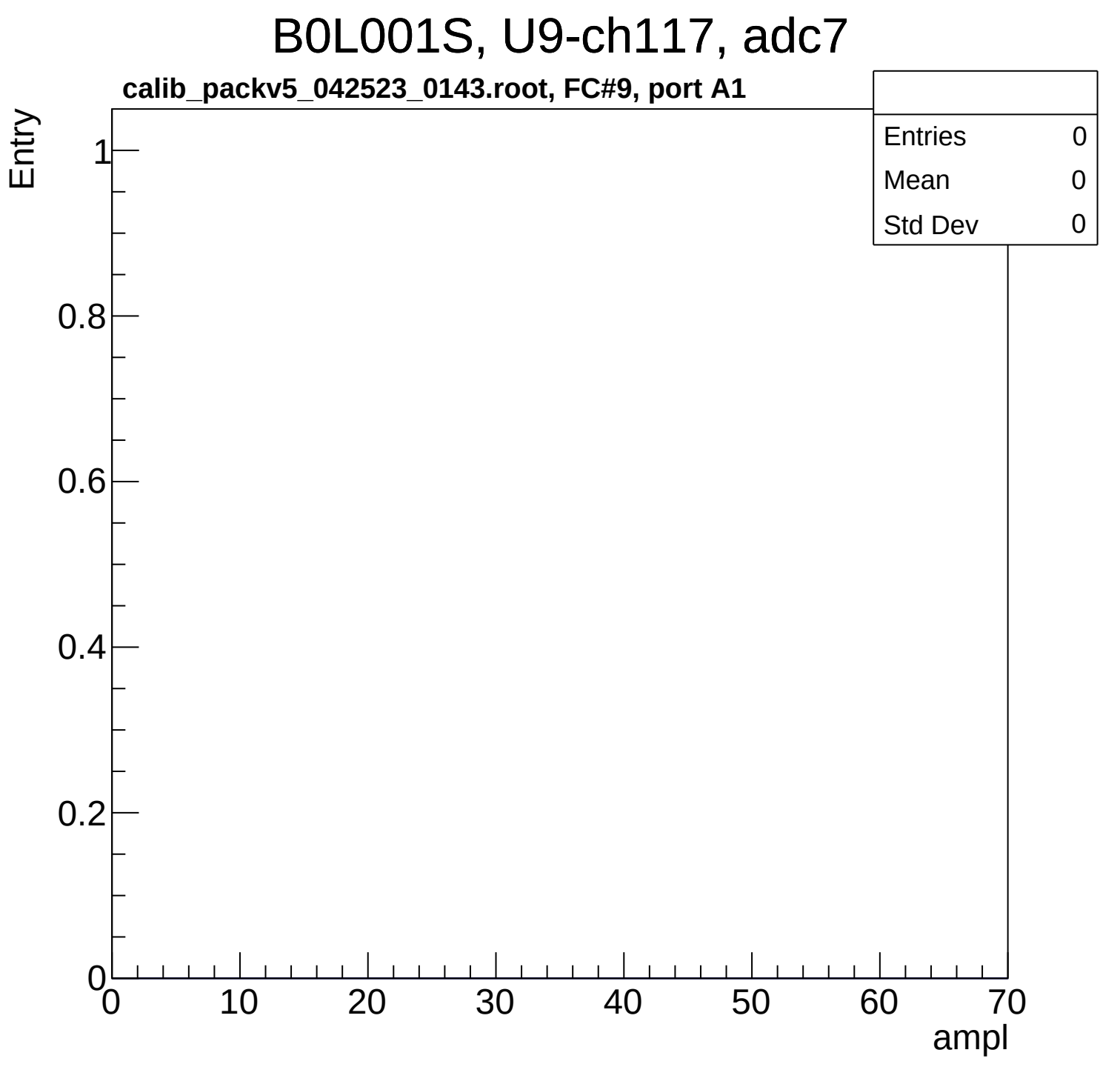

B0L001S, U9-ch117, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch117, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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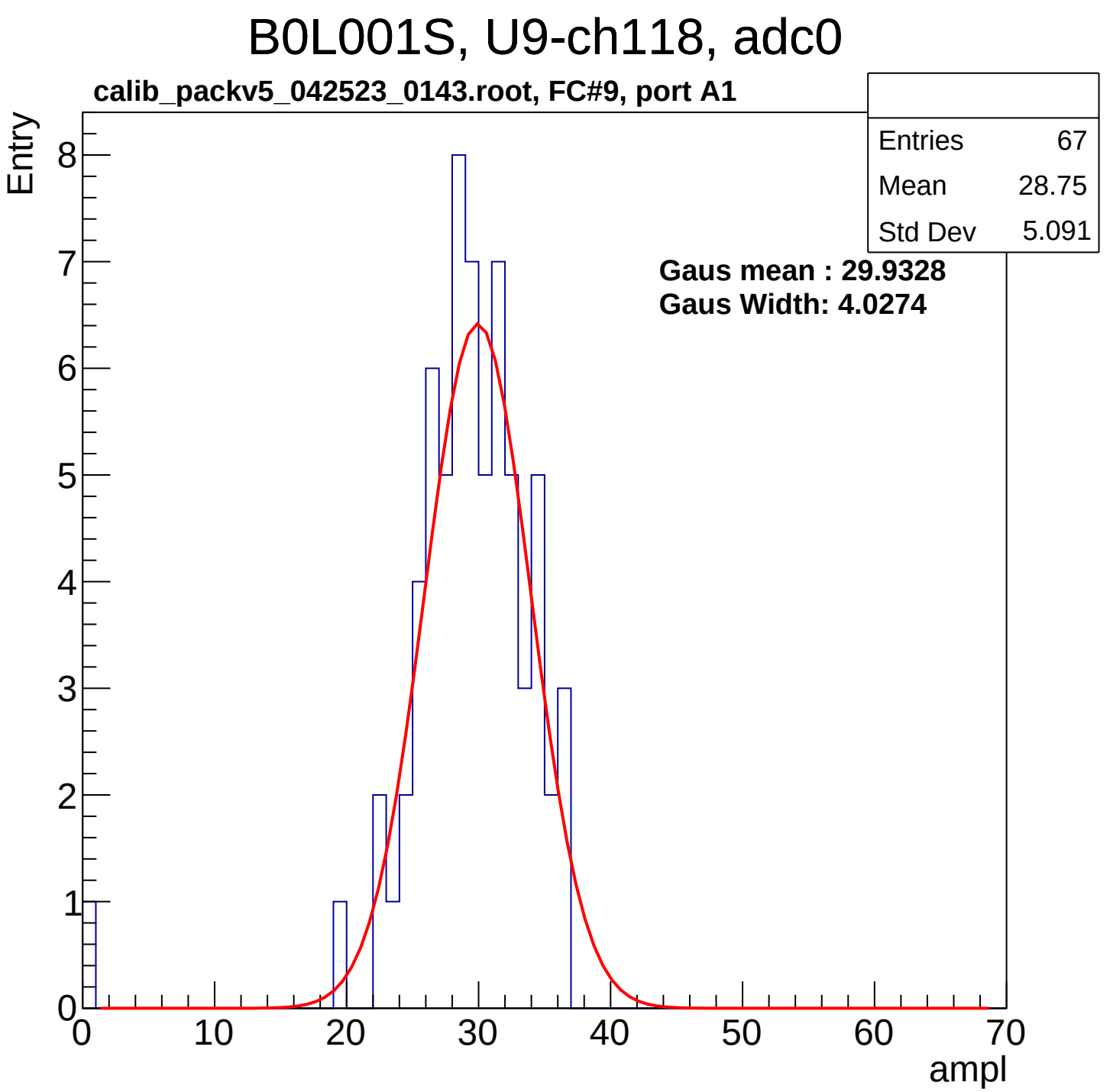
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch118, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 28.75
Std Dev 5.091
Gaus mean : 29.9328
Gaus Width: 4.0274
Entry
ampl
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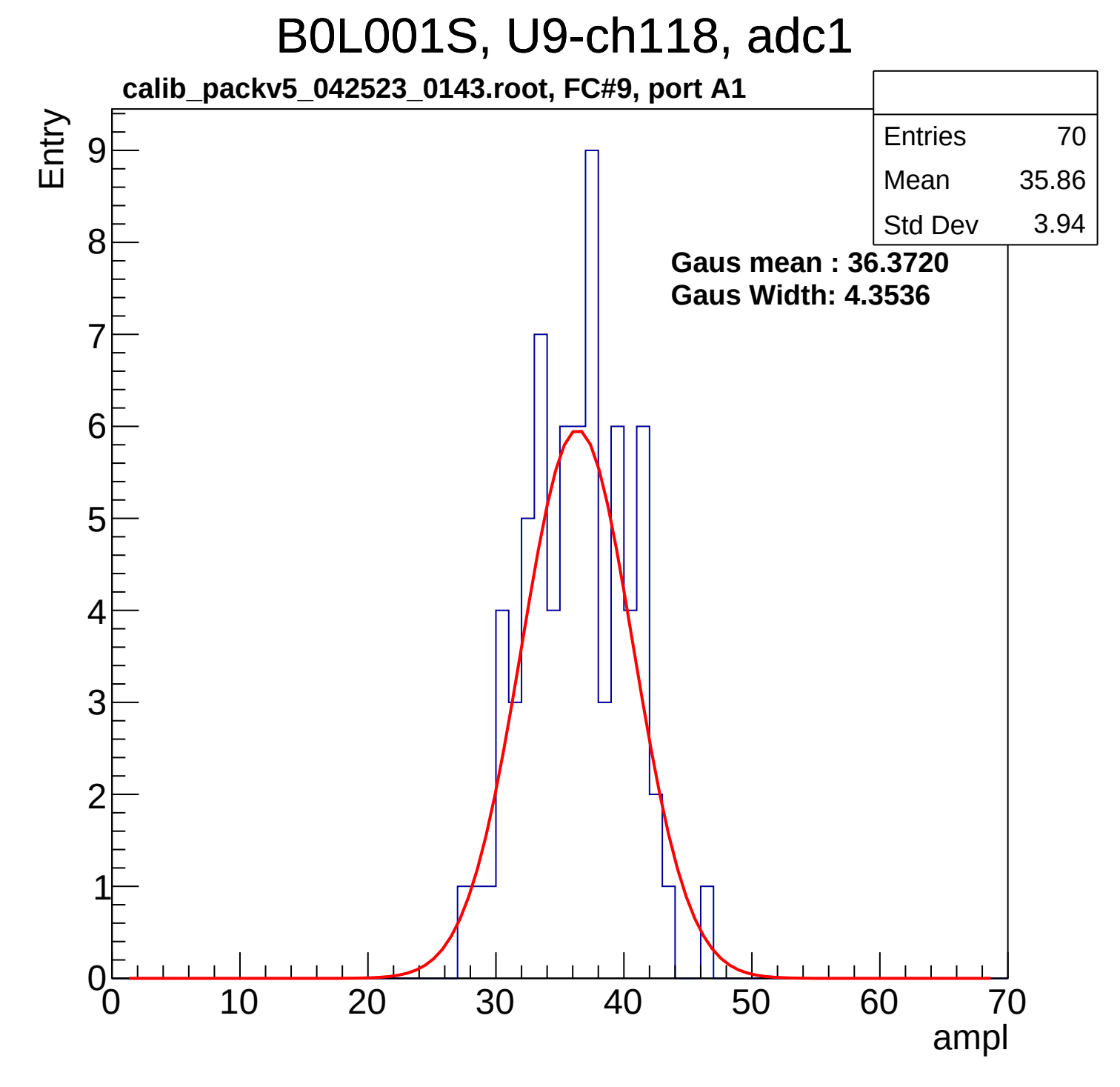
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch118, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 35.86
Std Dev 3.94
Gaus mean : 36.3720
Gaus Width: 4.3536
Entry
ampl
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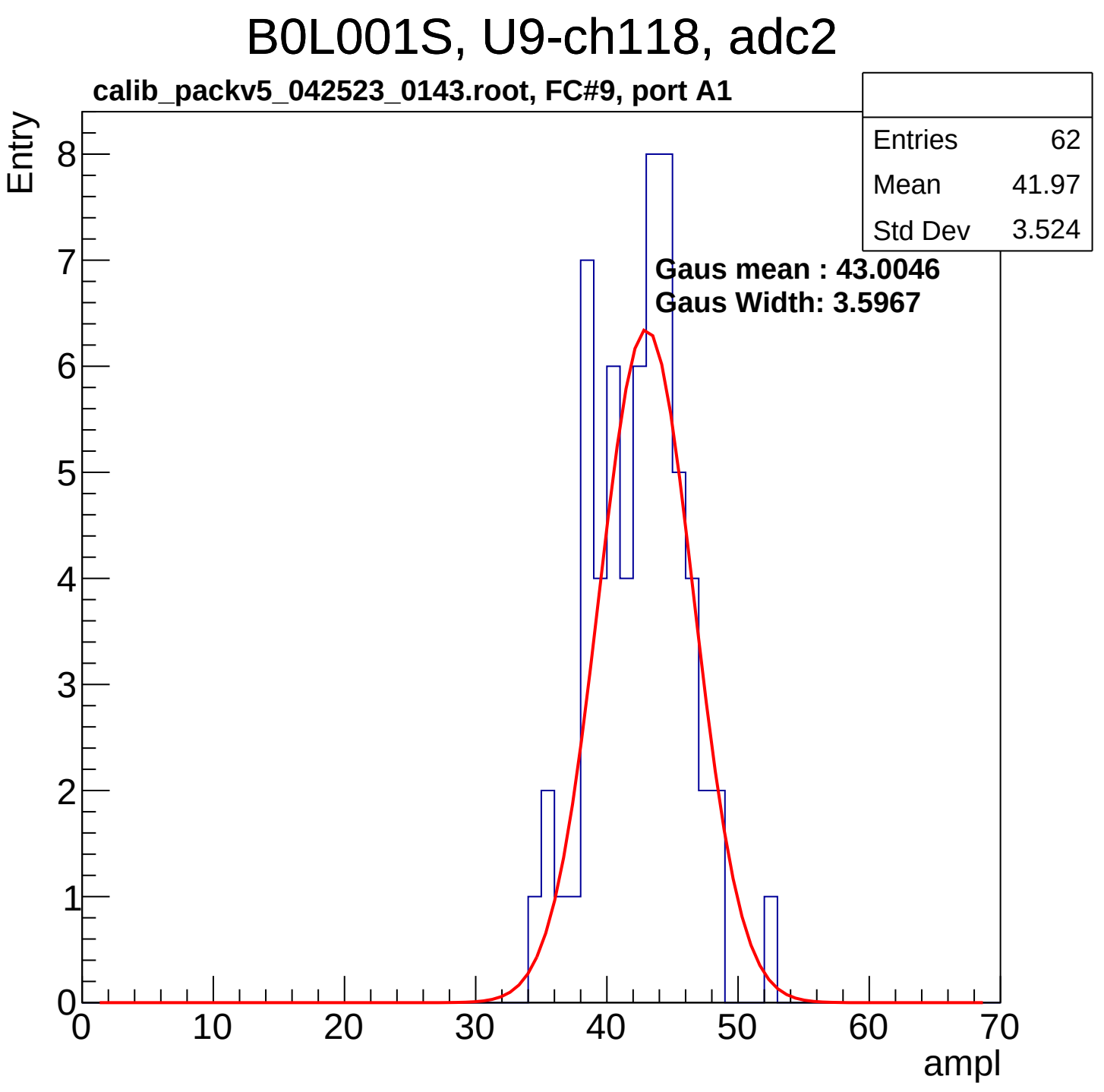
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch118, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 41.97
Std Dev 3.524
Gaus mean : 43.0046
Gaus Width: 3.5967
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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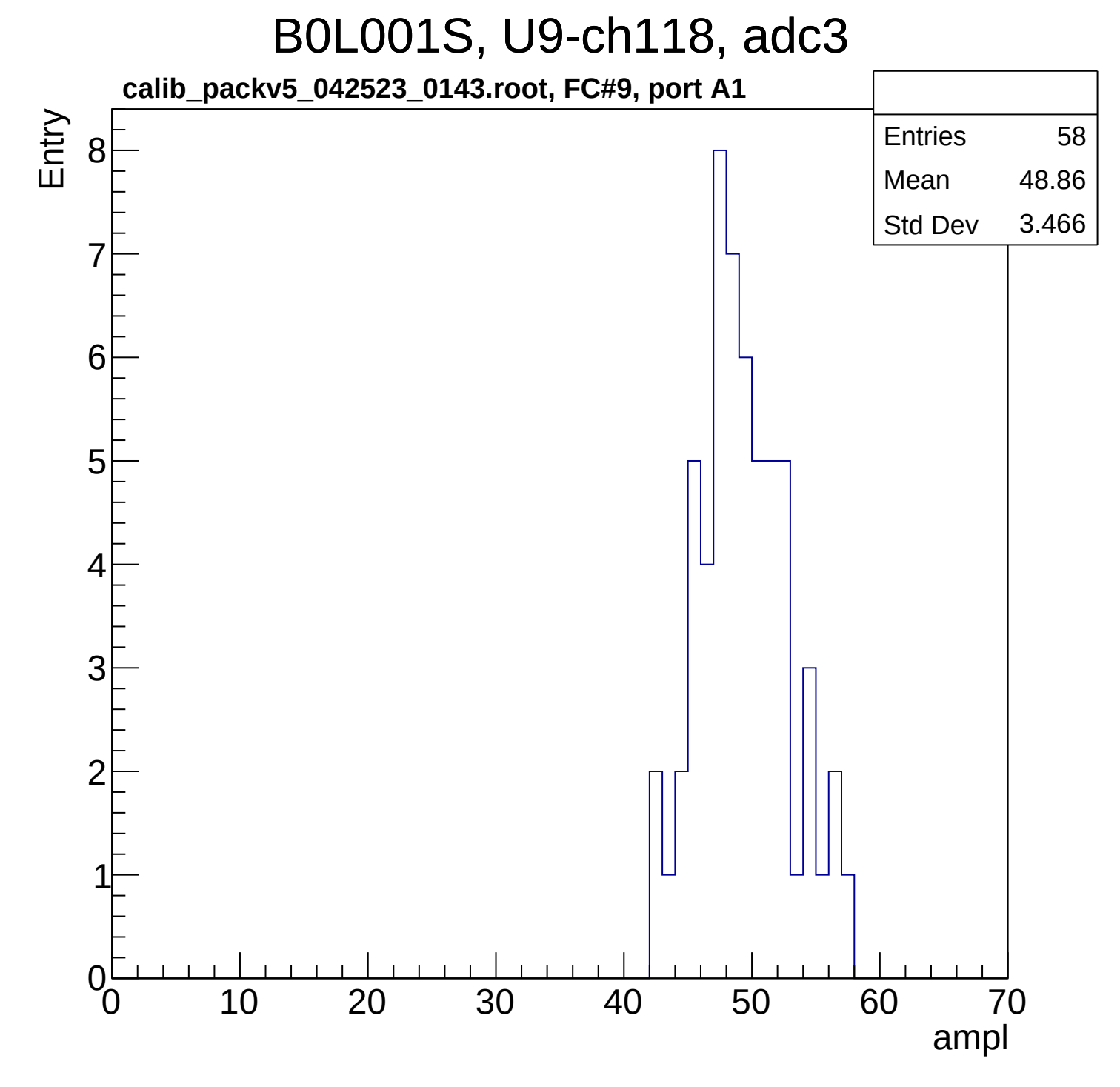
B0L001S, U9-ch118, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 48.86
Std Dev 3.466
Entry
ampl
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0
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20
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50
60
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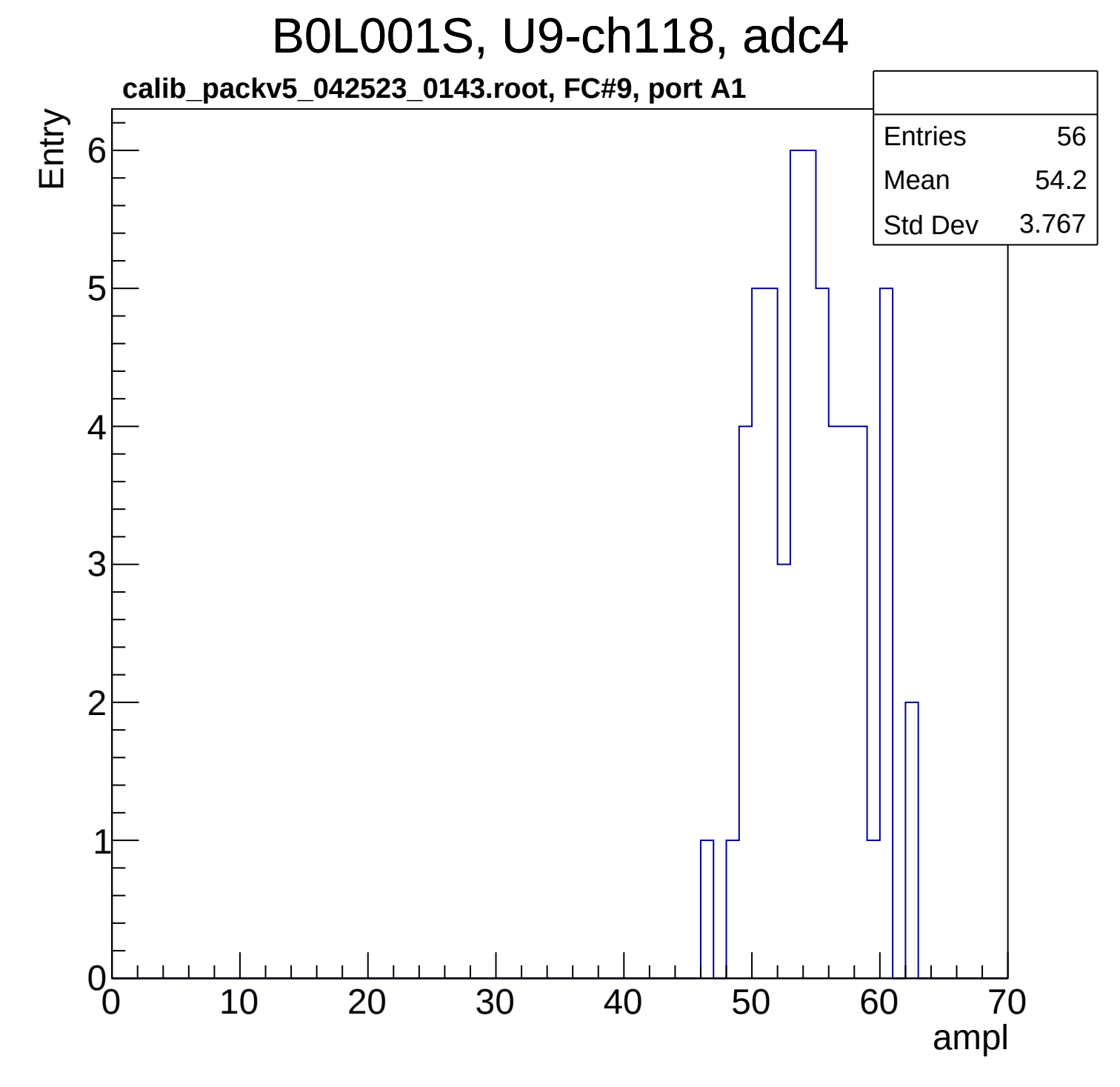
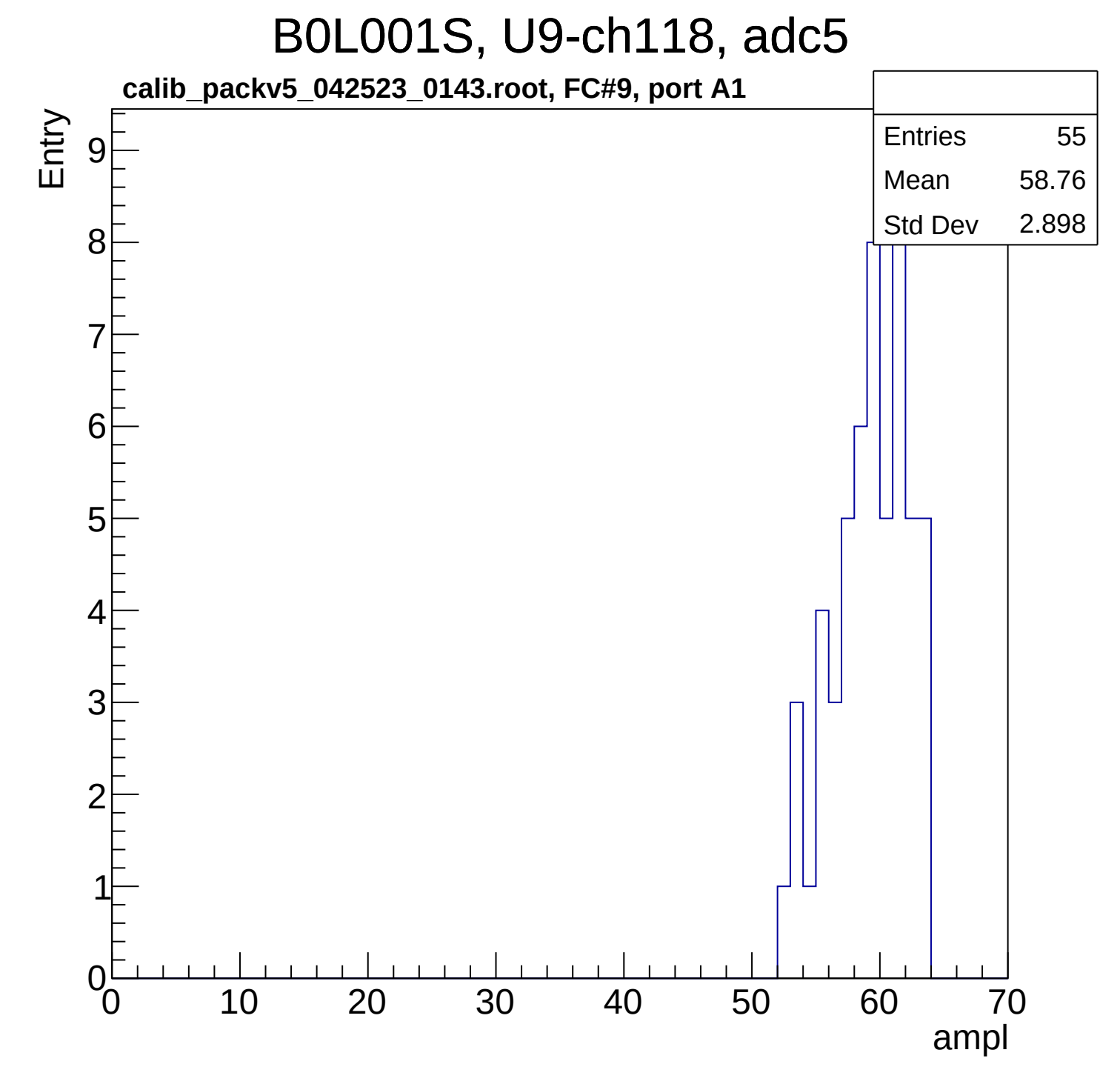

B0L001S, U9-ch118, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 54.2
Std Dev 3.767
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch118, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 58.76
Std Dev 2.898
Entry
ampl
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3
4
5
6
7
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0
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60
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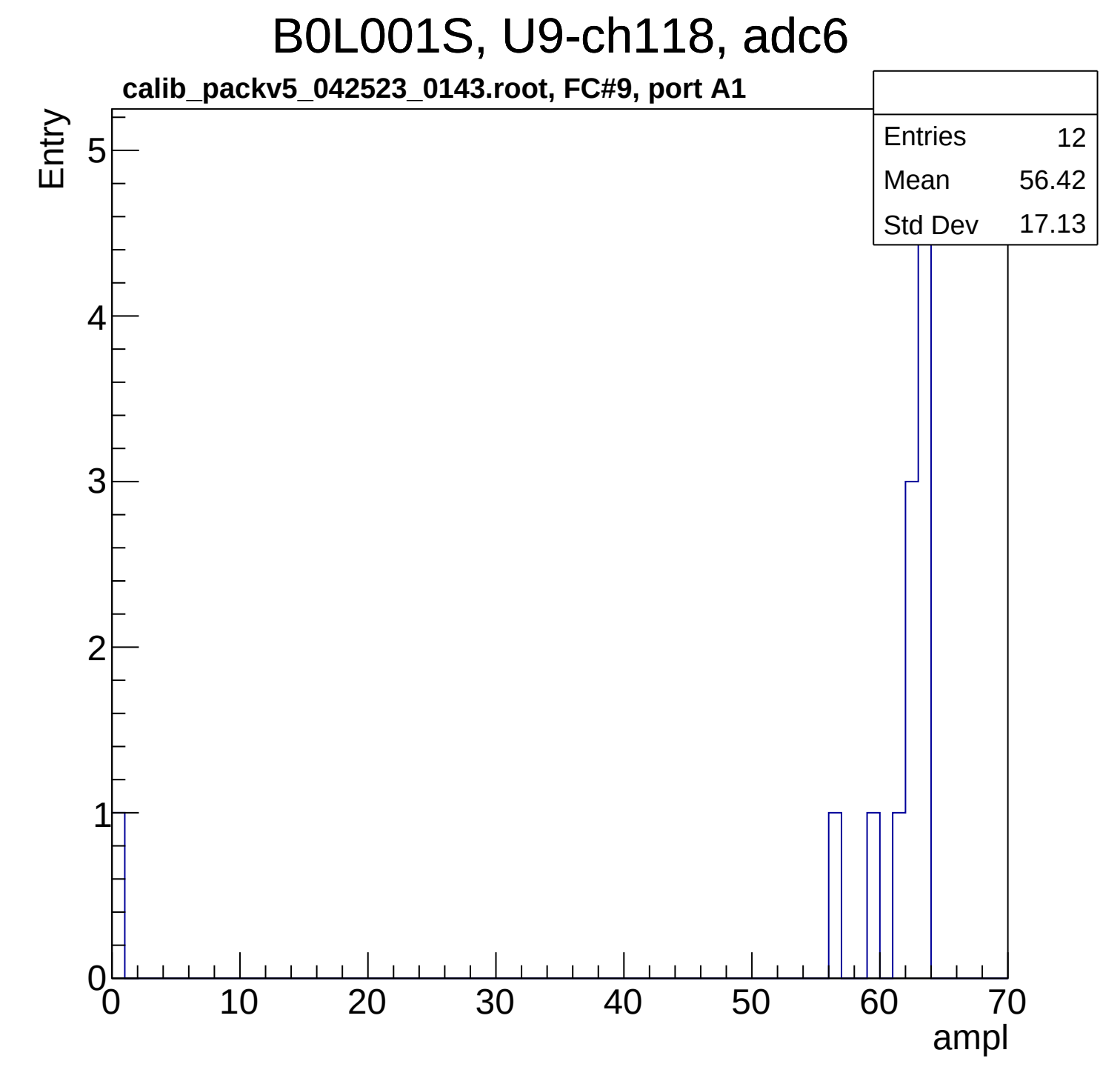
B0L001S, U9-ch118, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 56.42
Std Dev 17.13
Entry
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3
4
5
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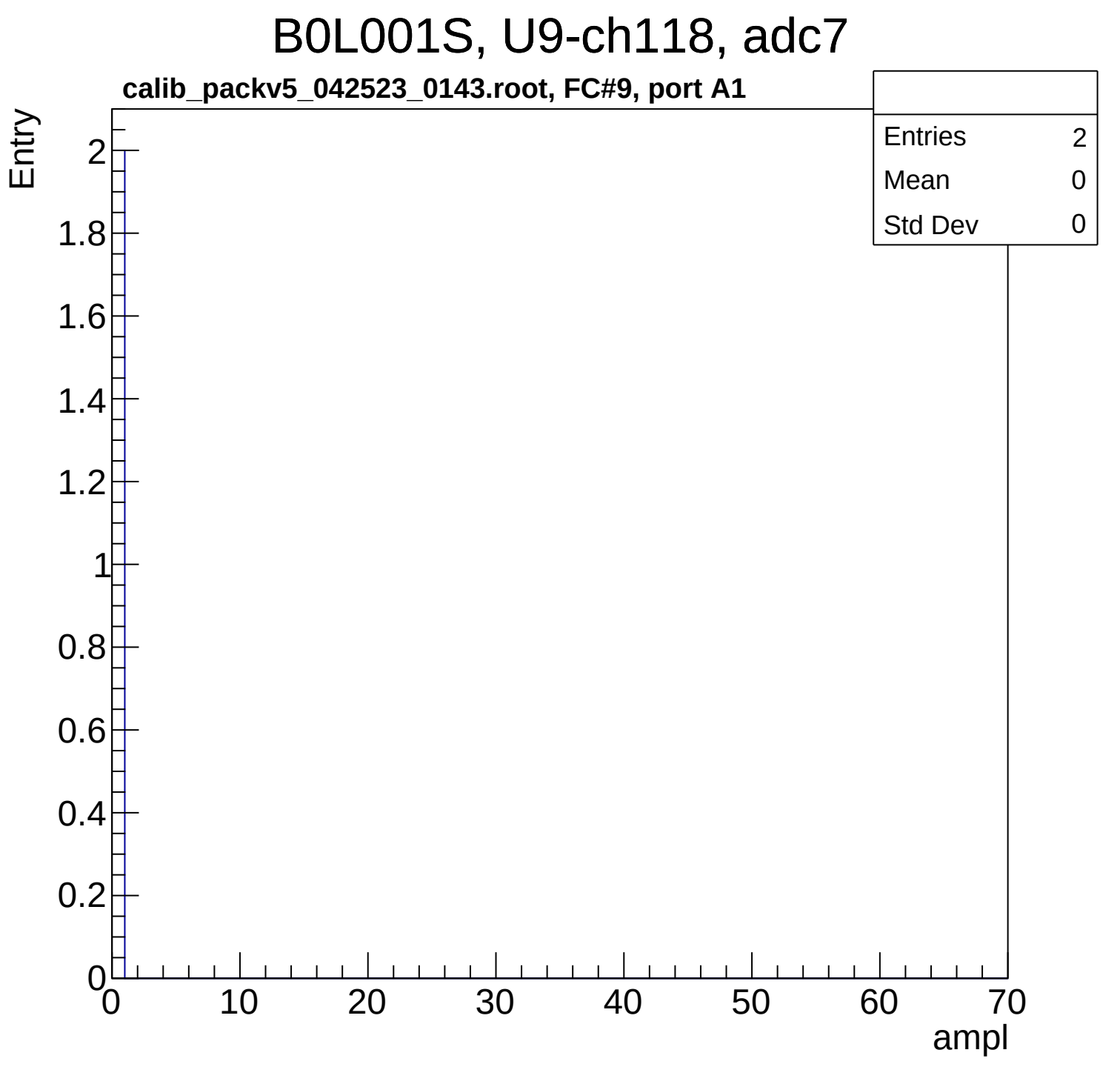
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch118, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
30
40
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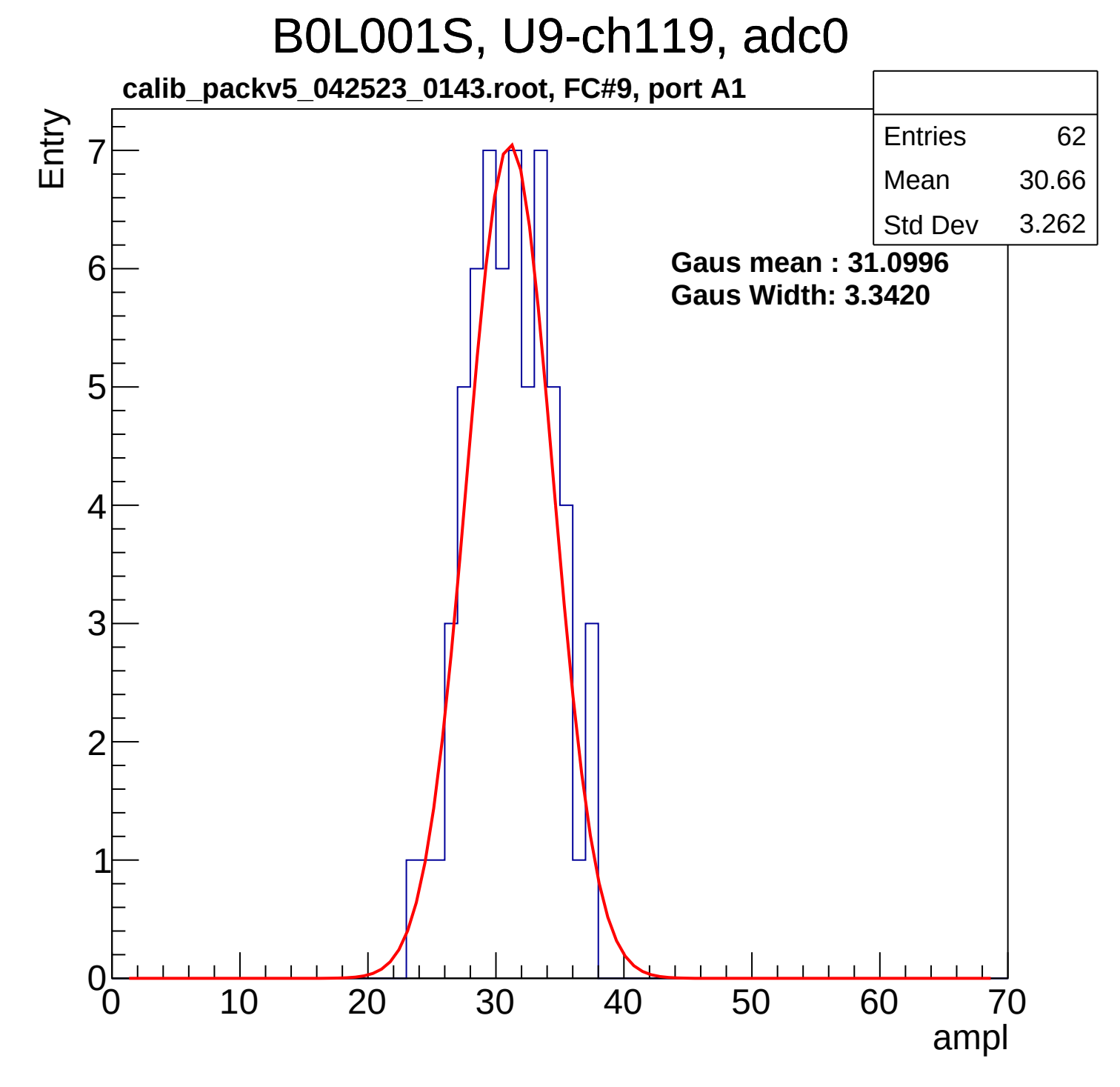
60
70


B0L001S, U9-ch119, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 30.66
Std Dev 3.262
Gaus mean : 31.0996
Gaus Width: 3.3420
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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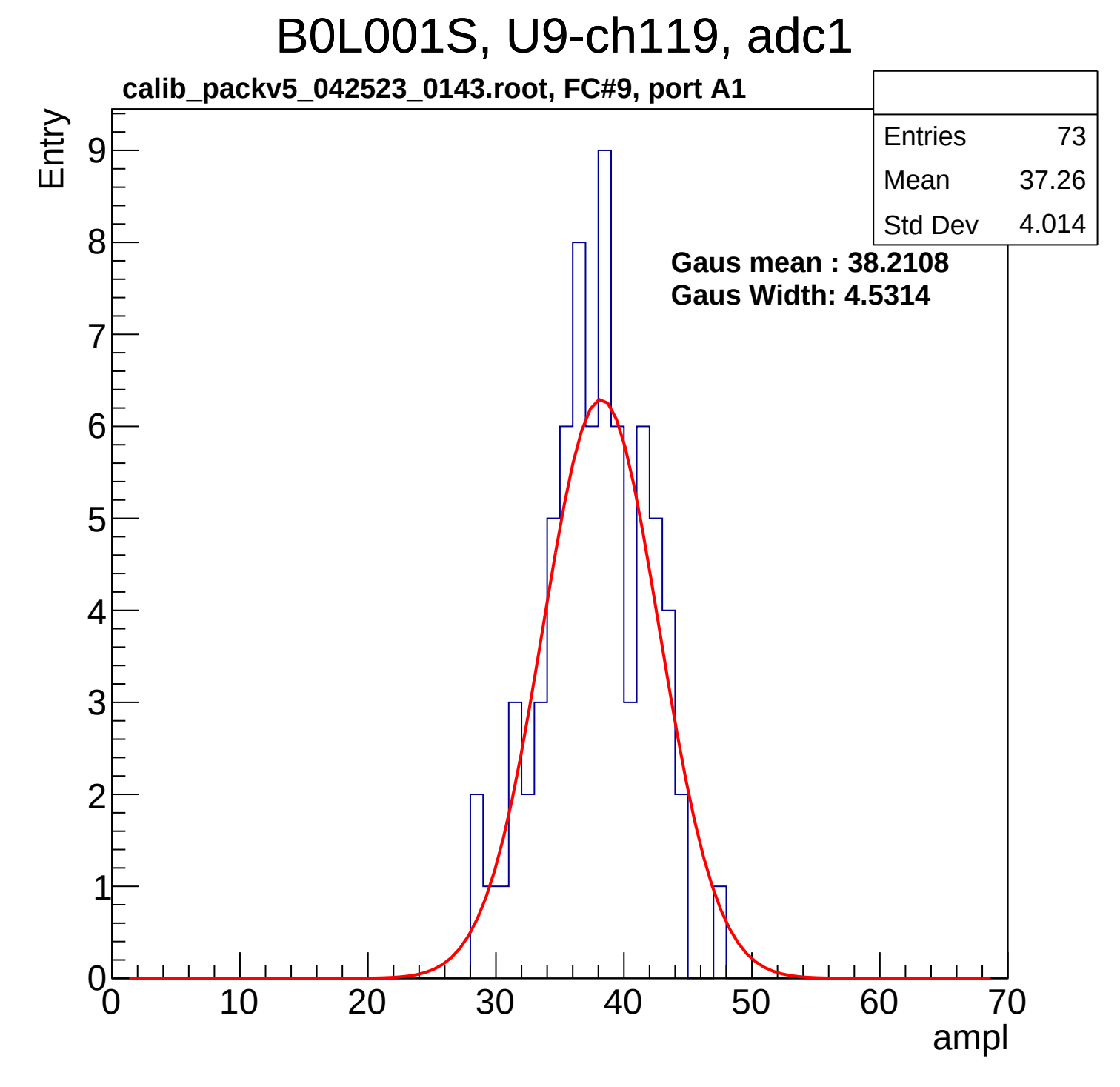
40
50
60
70


B0L001S, U9-ch119, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 37.26
Std Dev 4.014
Gaus mean : 38.2108
Gaus Width: 4.5314
Entry
ampl
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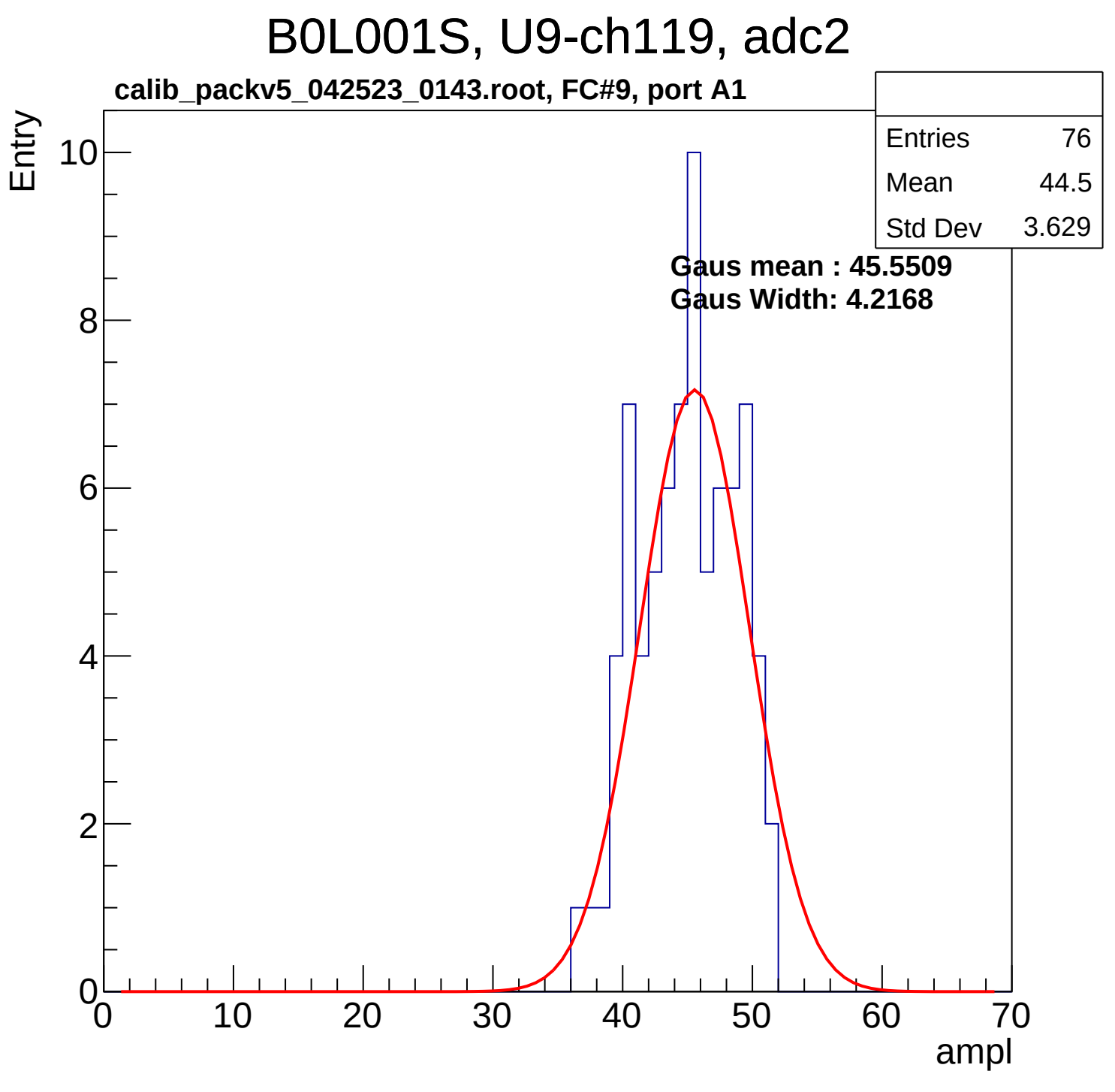
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch119, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 44.5
Std Dev 3.629
Gaus mean : 45.5509
Gaus Width: 4.2168
Entry
ampl
0
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4
6
8
10
0
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20
30
40
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60
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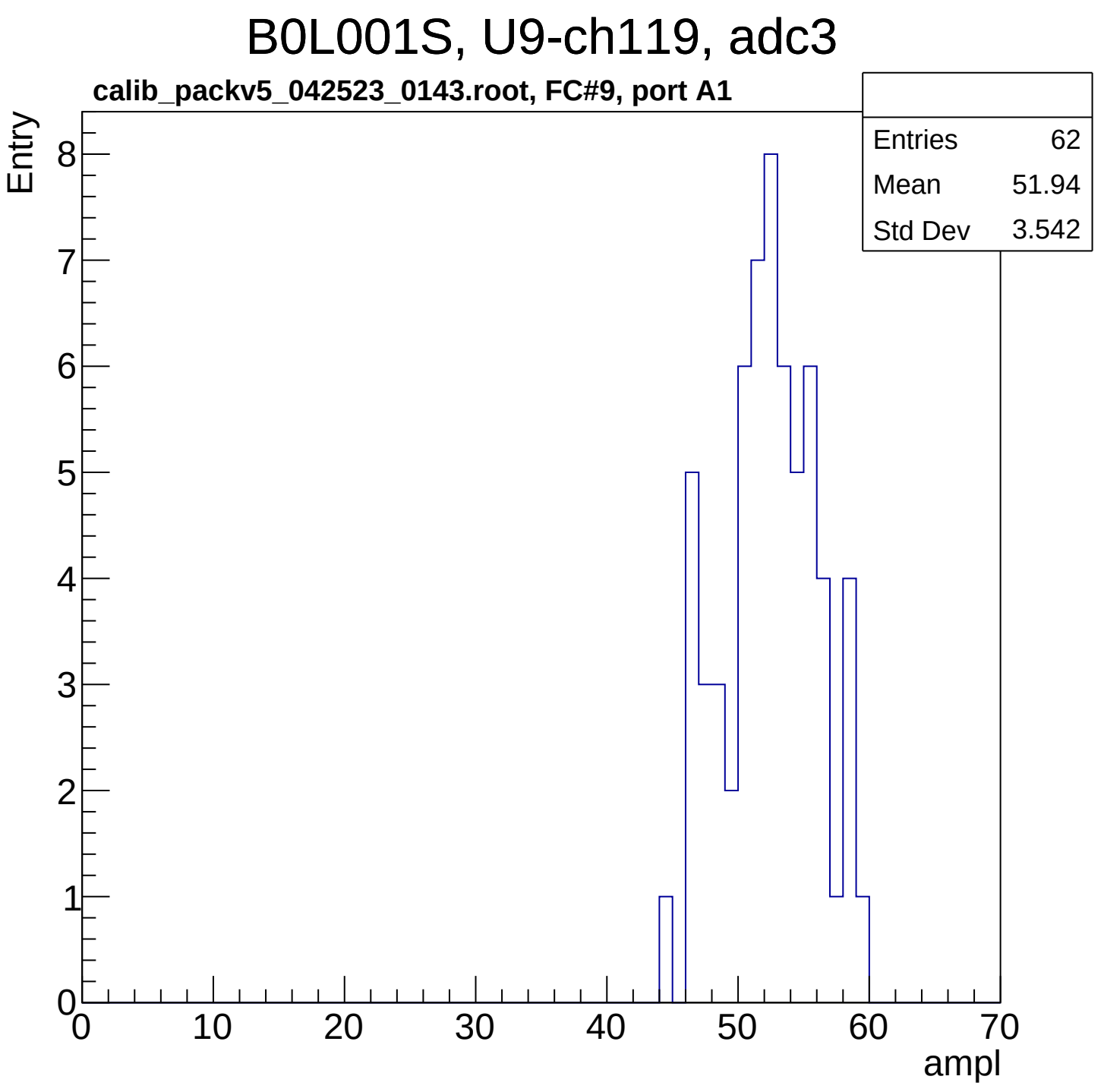
B0L001S, U9-ch119, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 51.94
Std Dev 3.542
Entry
ampl
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2
3
4
5
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7
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60
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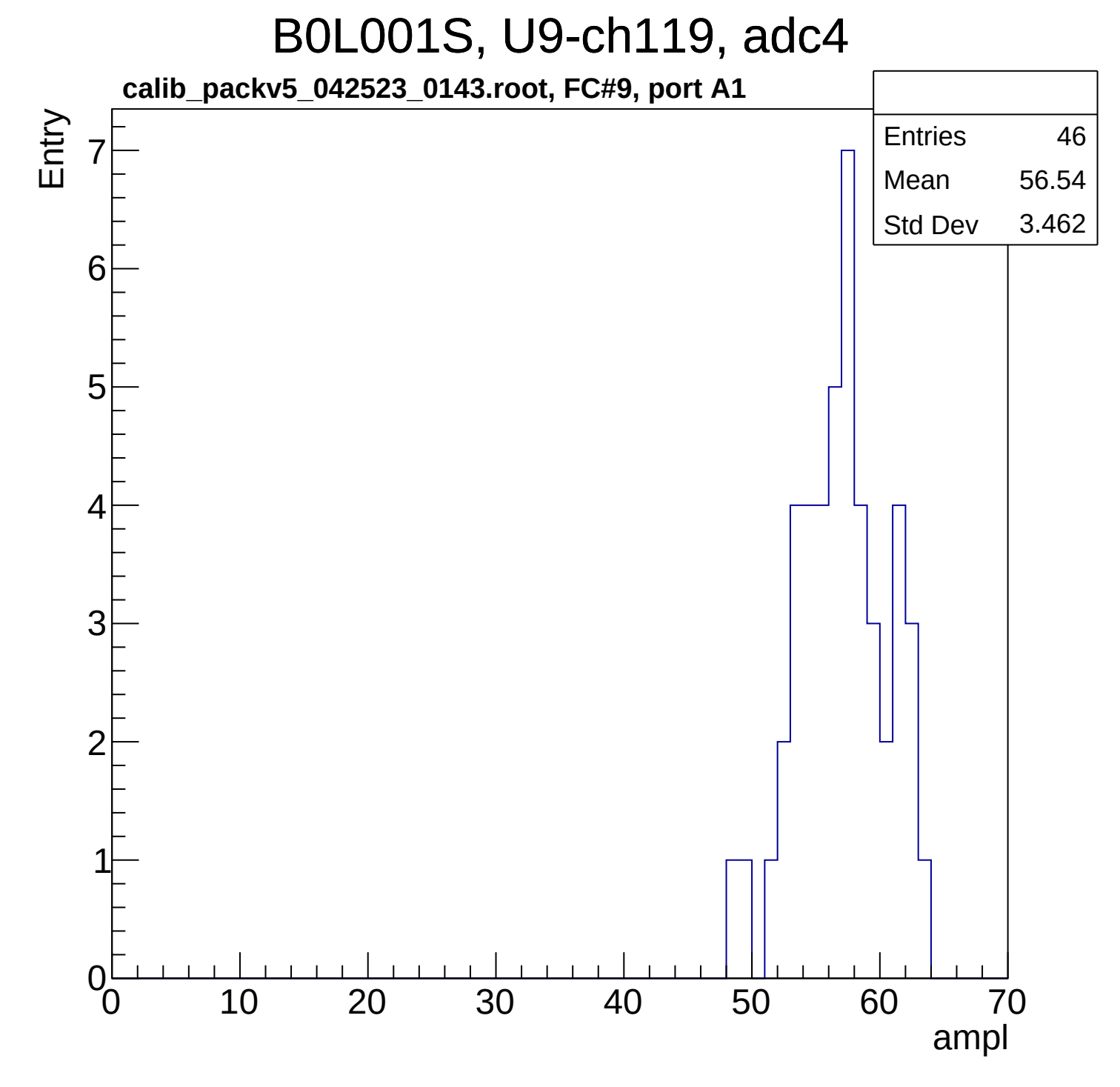
B0L001S, U9-ch119, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 56.54
Std Dev 3.462
Entry
ampl
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3
4
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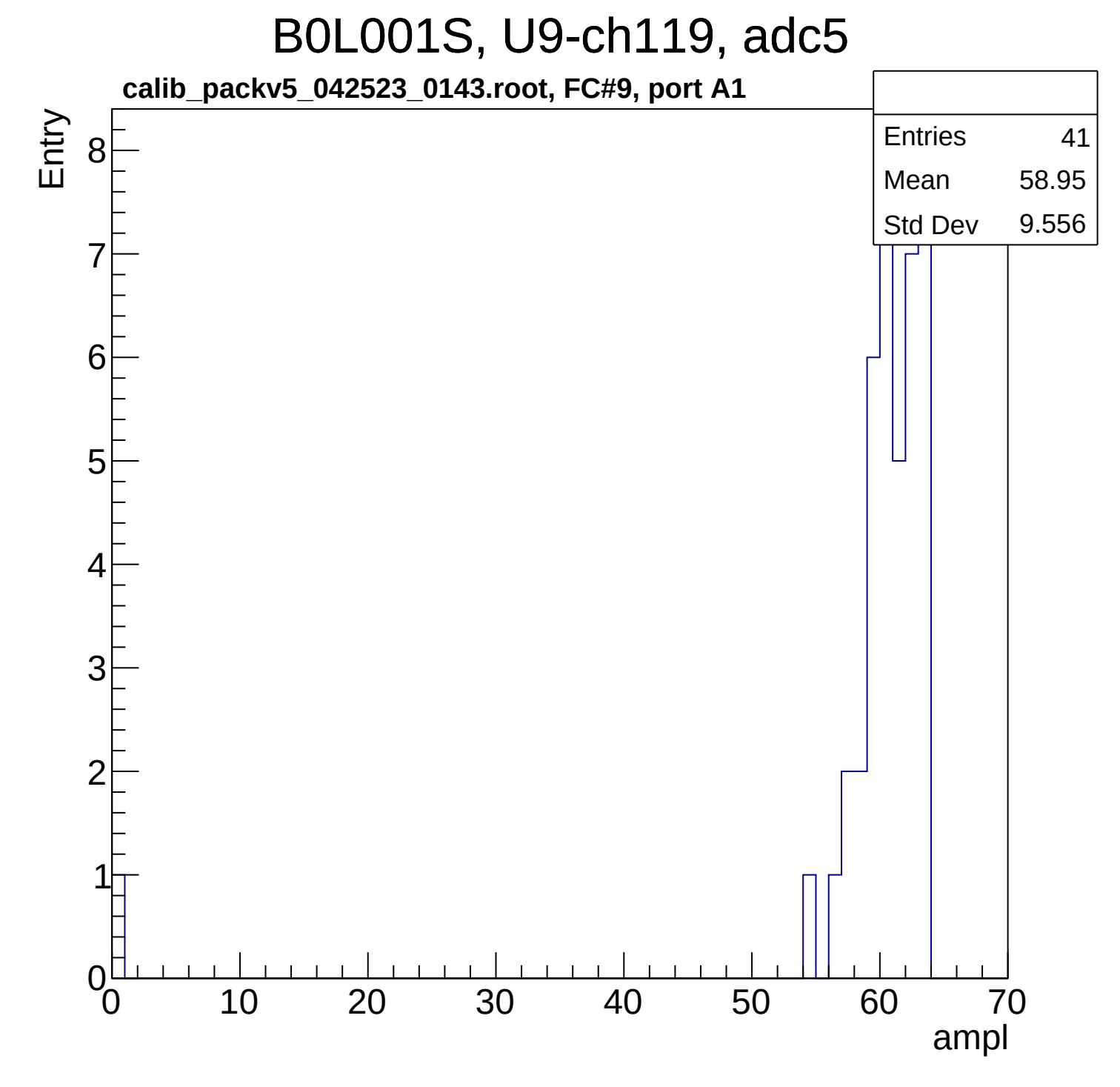
B0L001S, U9-ch119, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.95
Std Dev 9.556
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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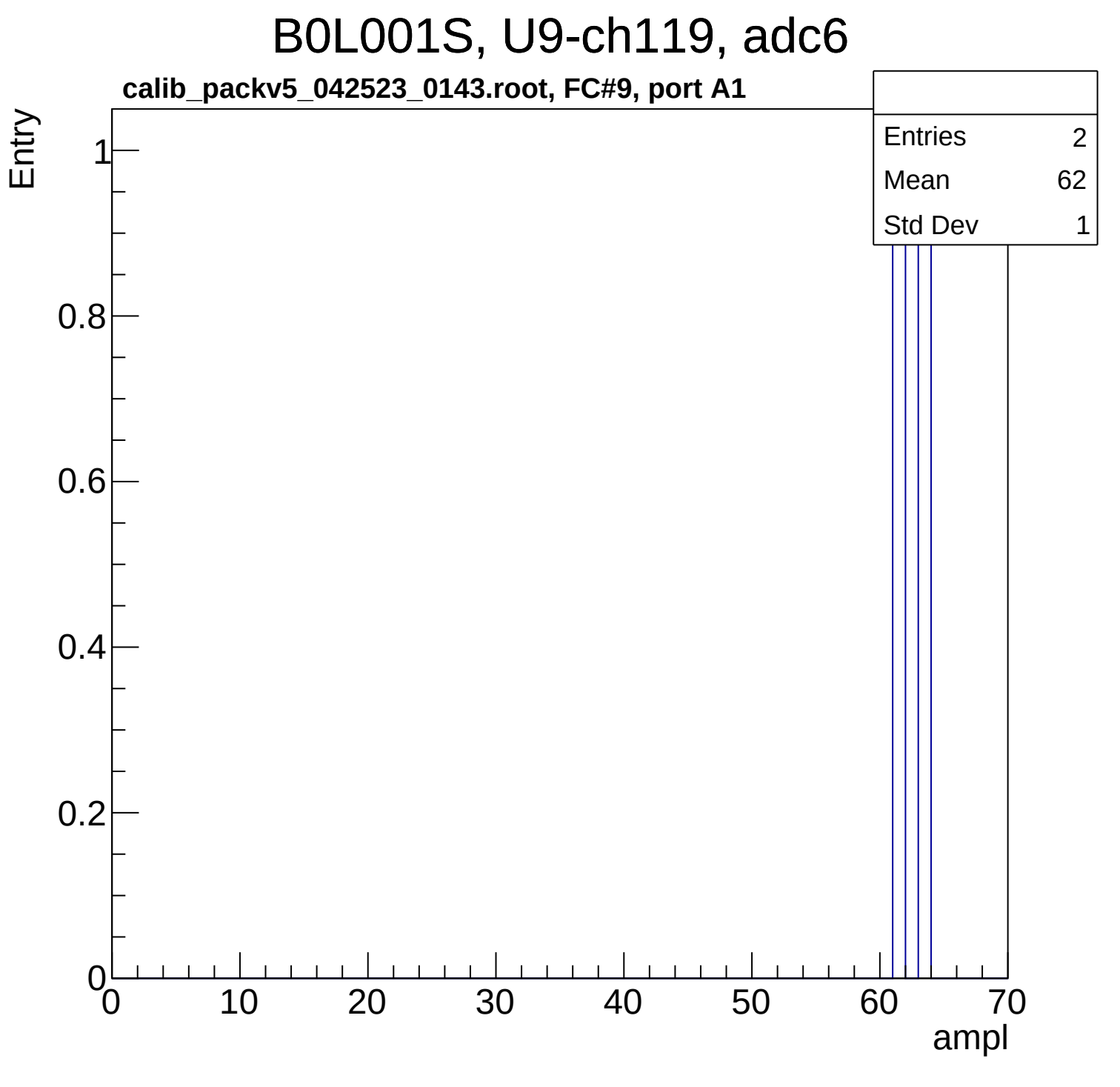
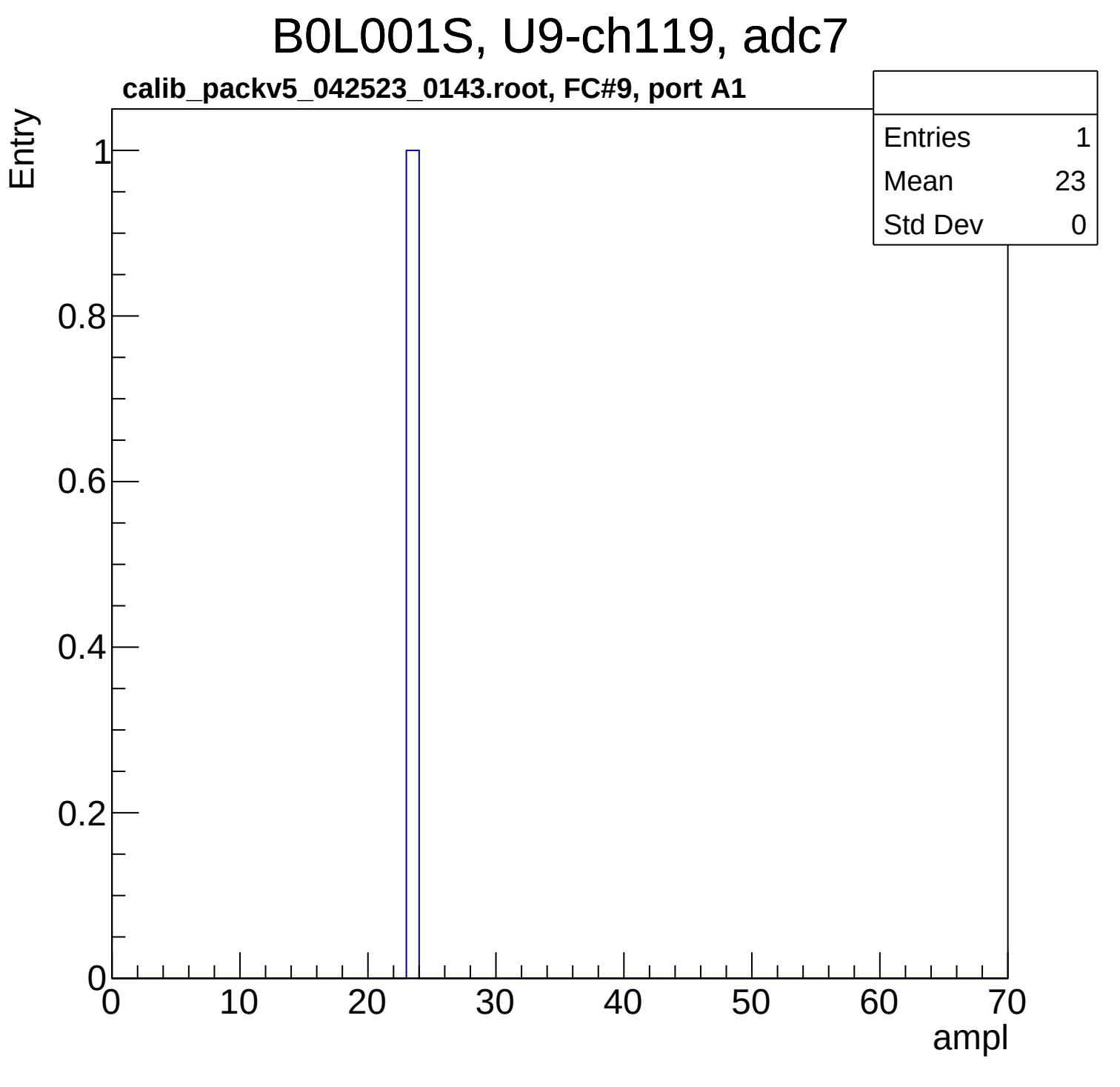
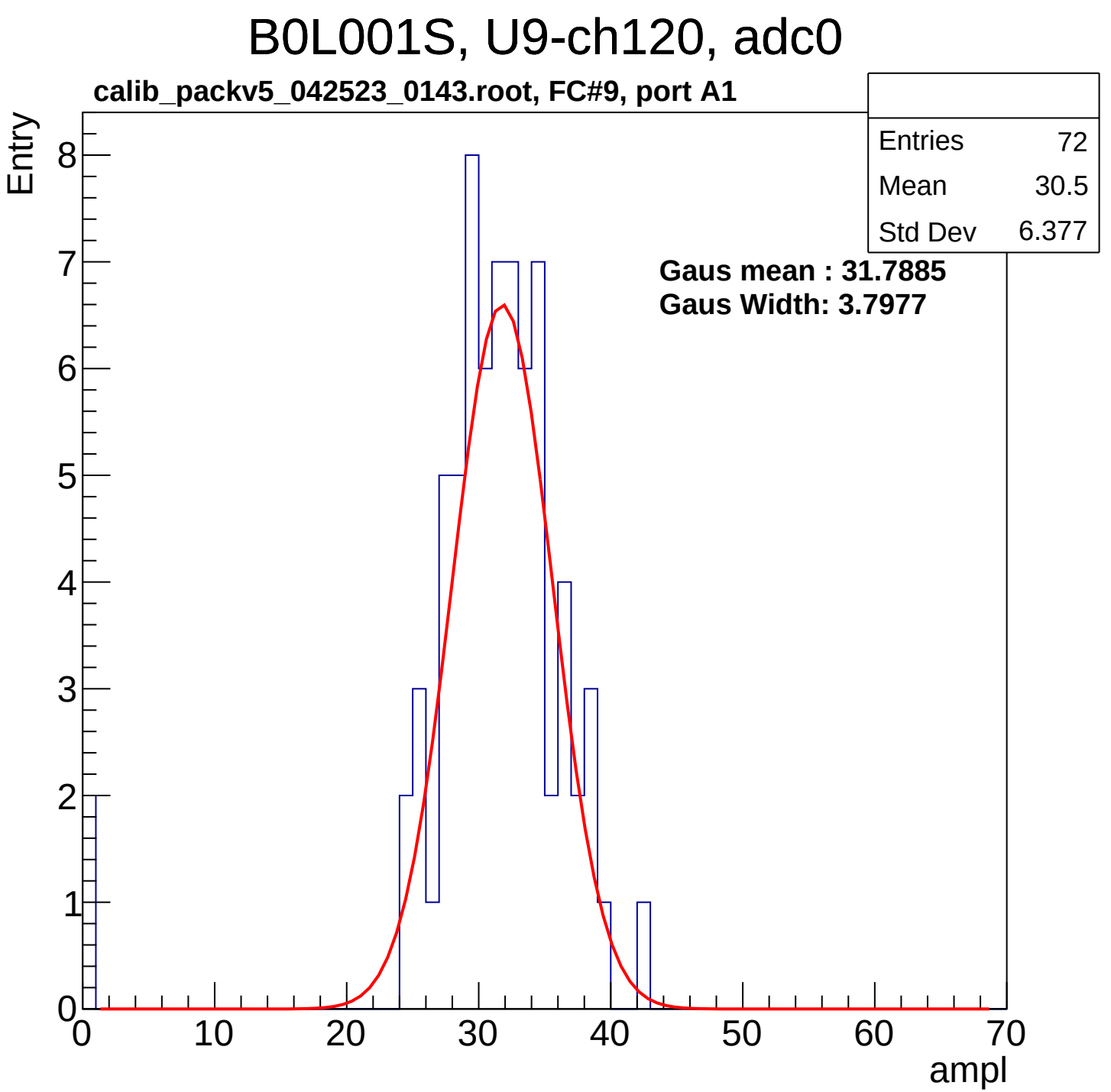
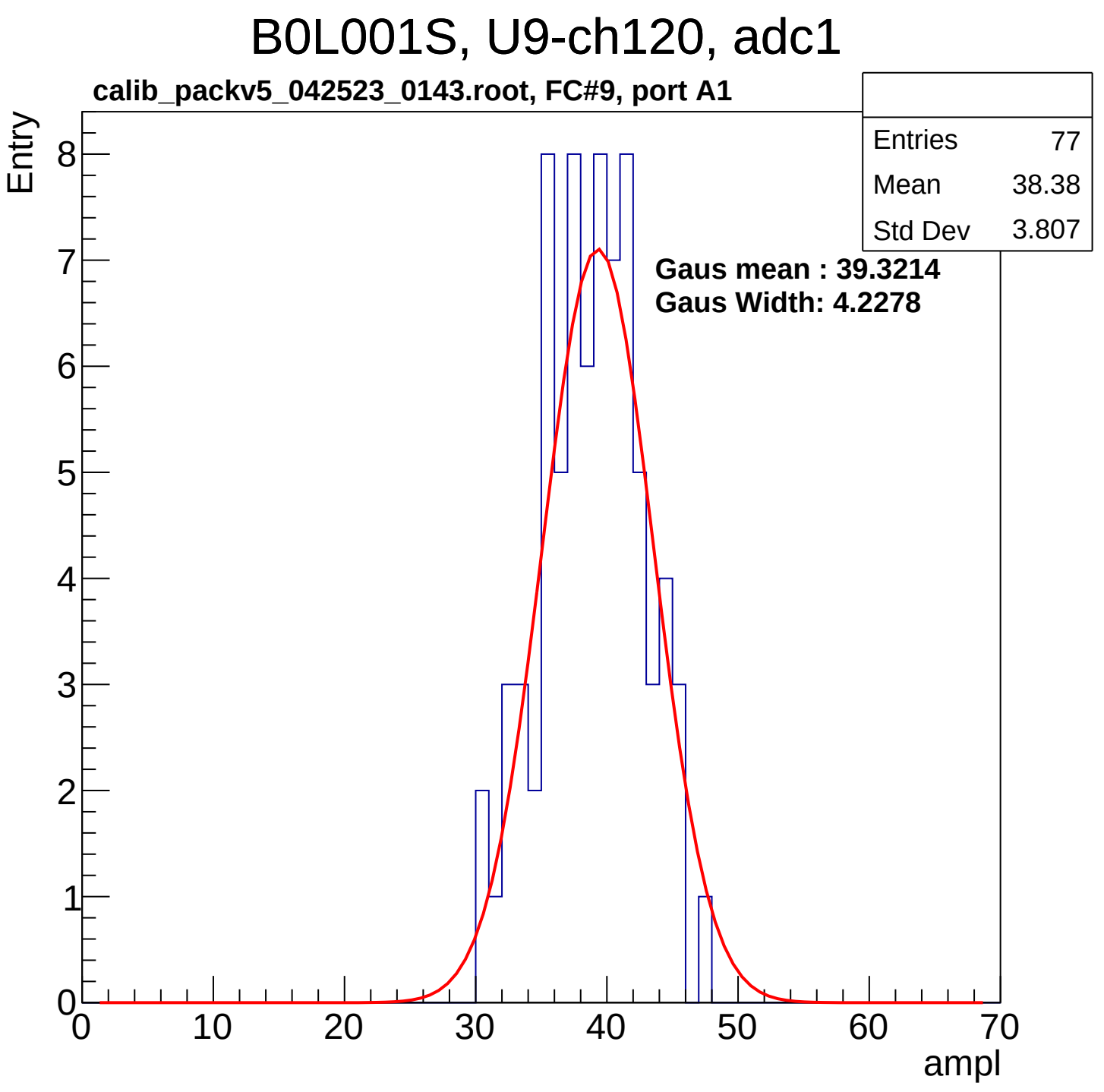
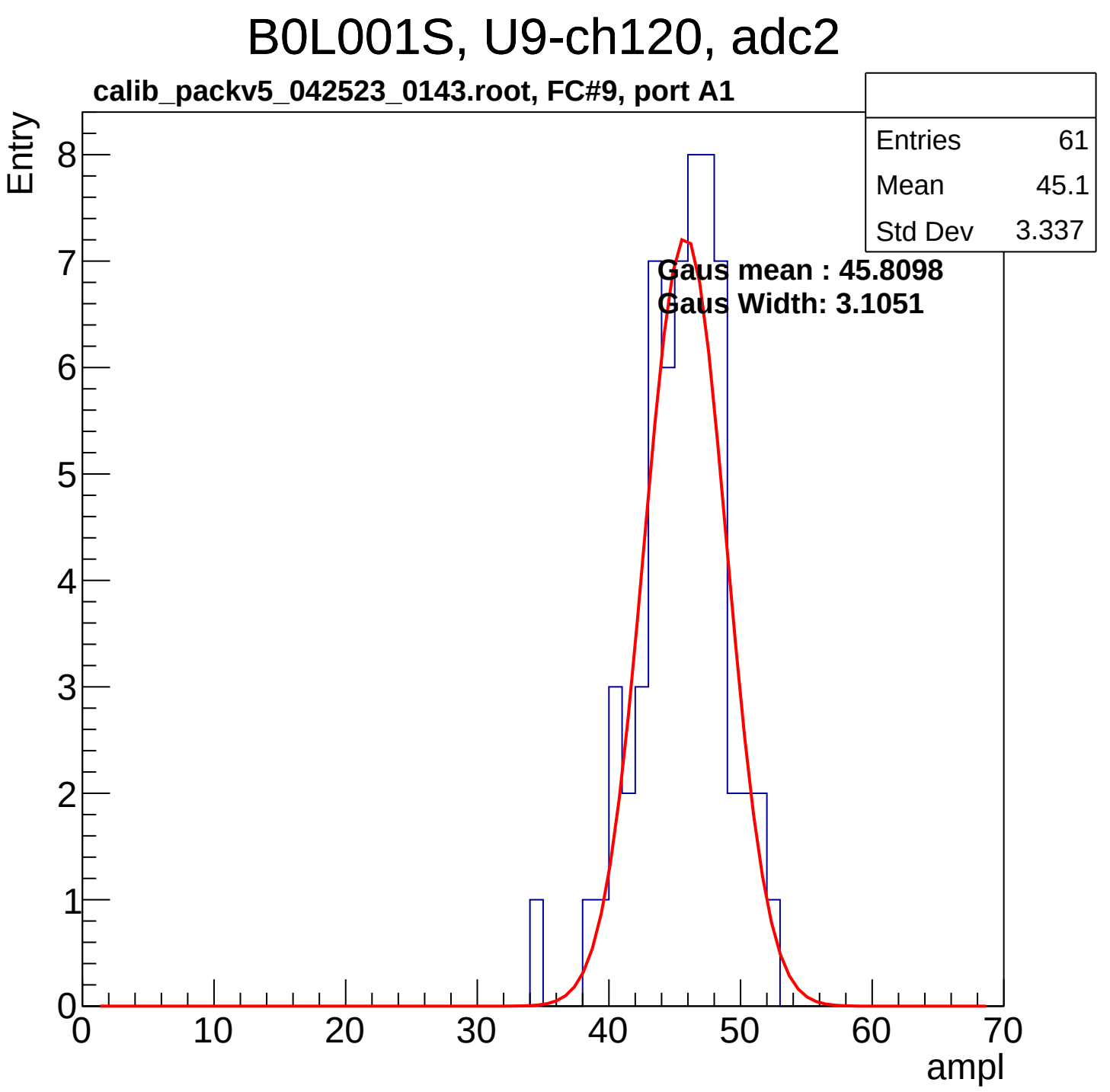
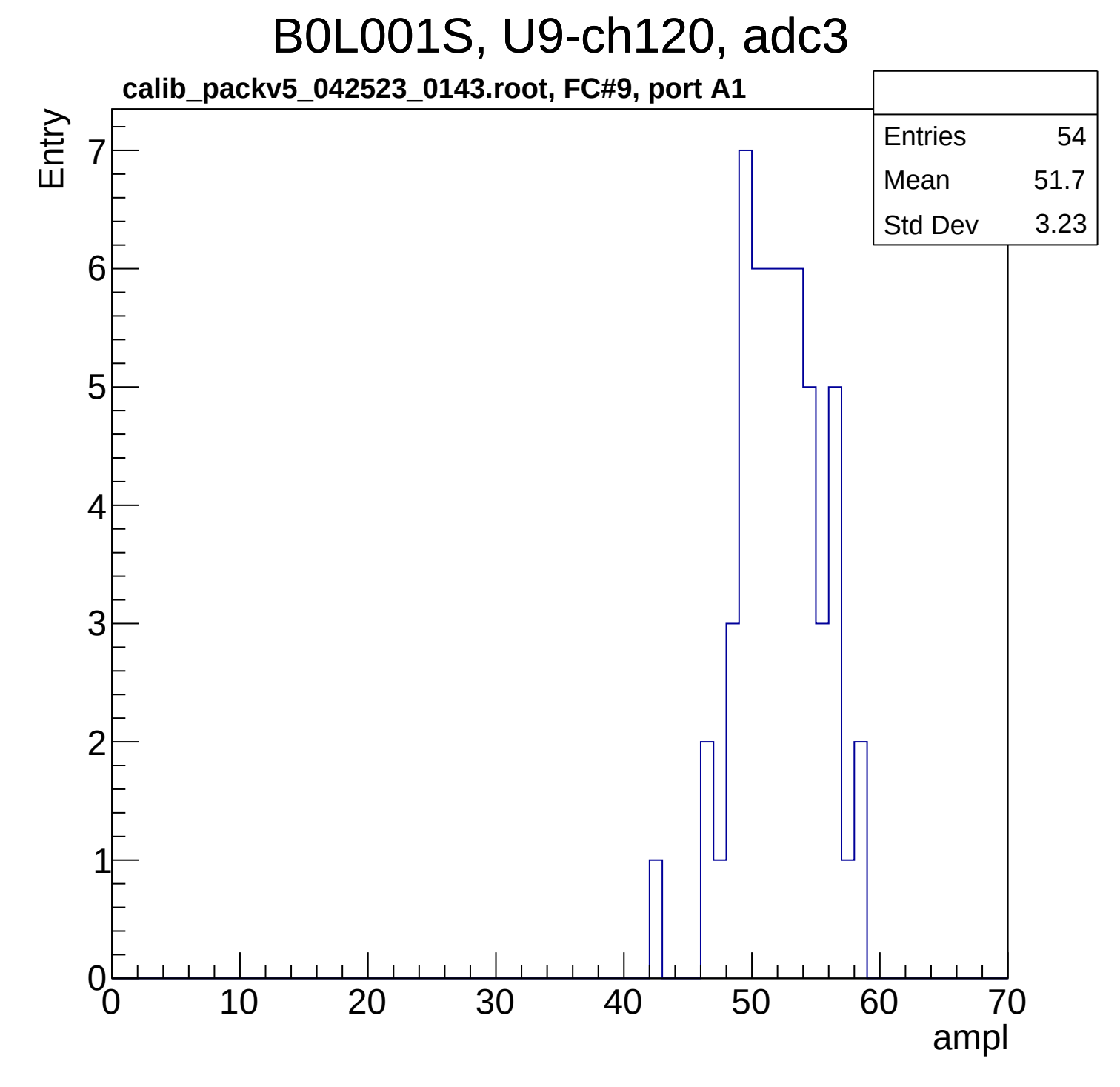

B0L001S, U9-ch119, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

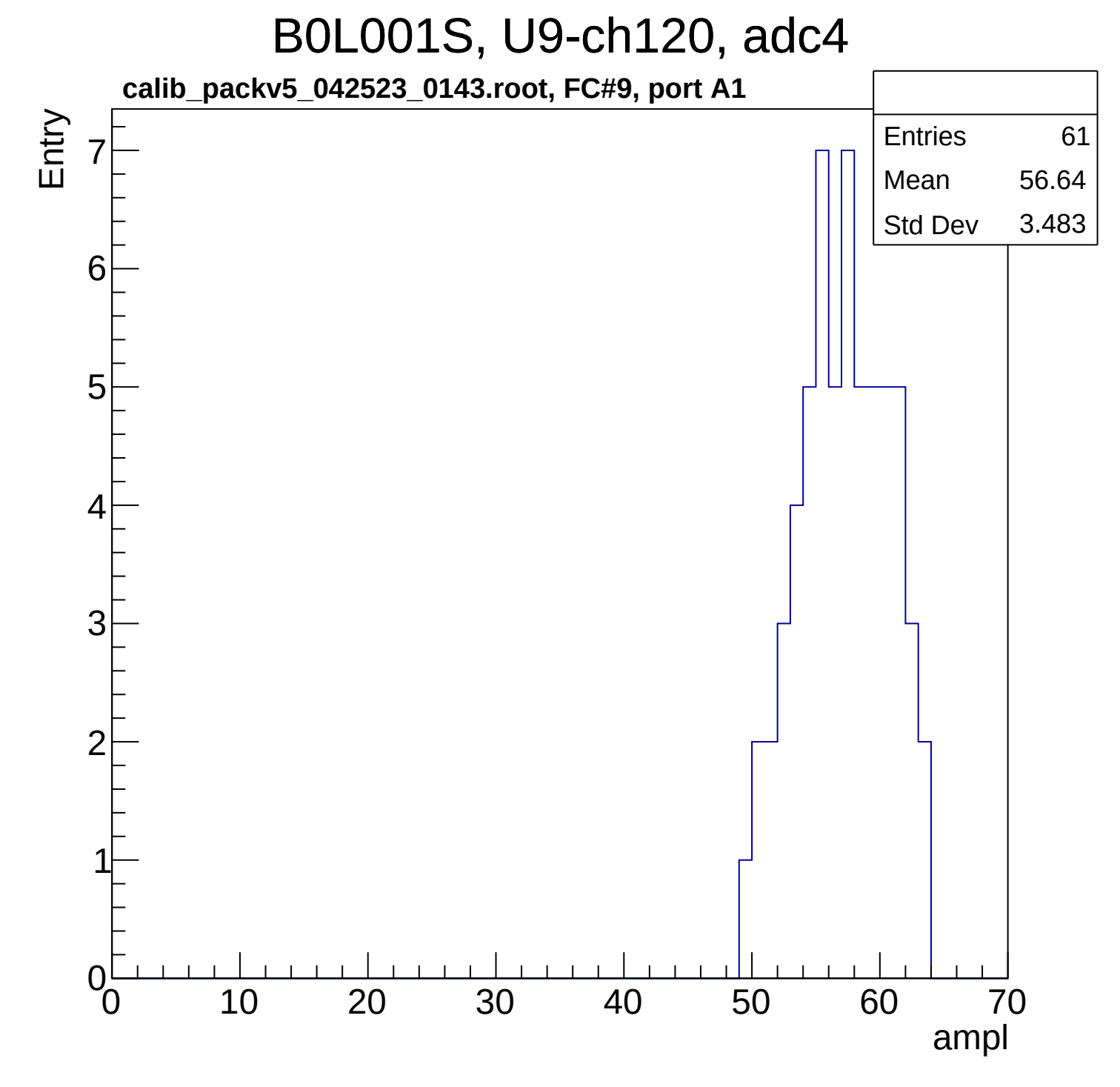

B0L001S, U9-ch119, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

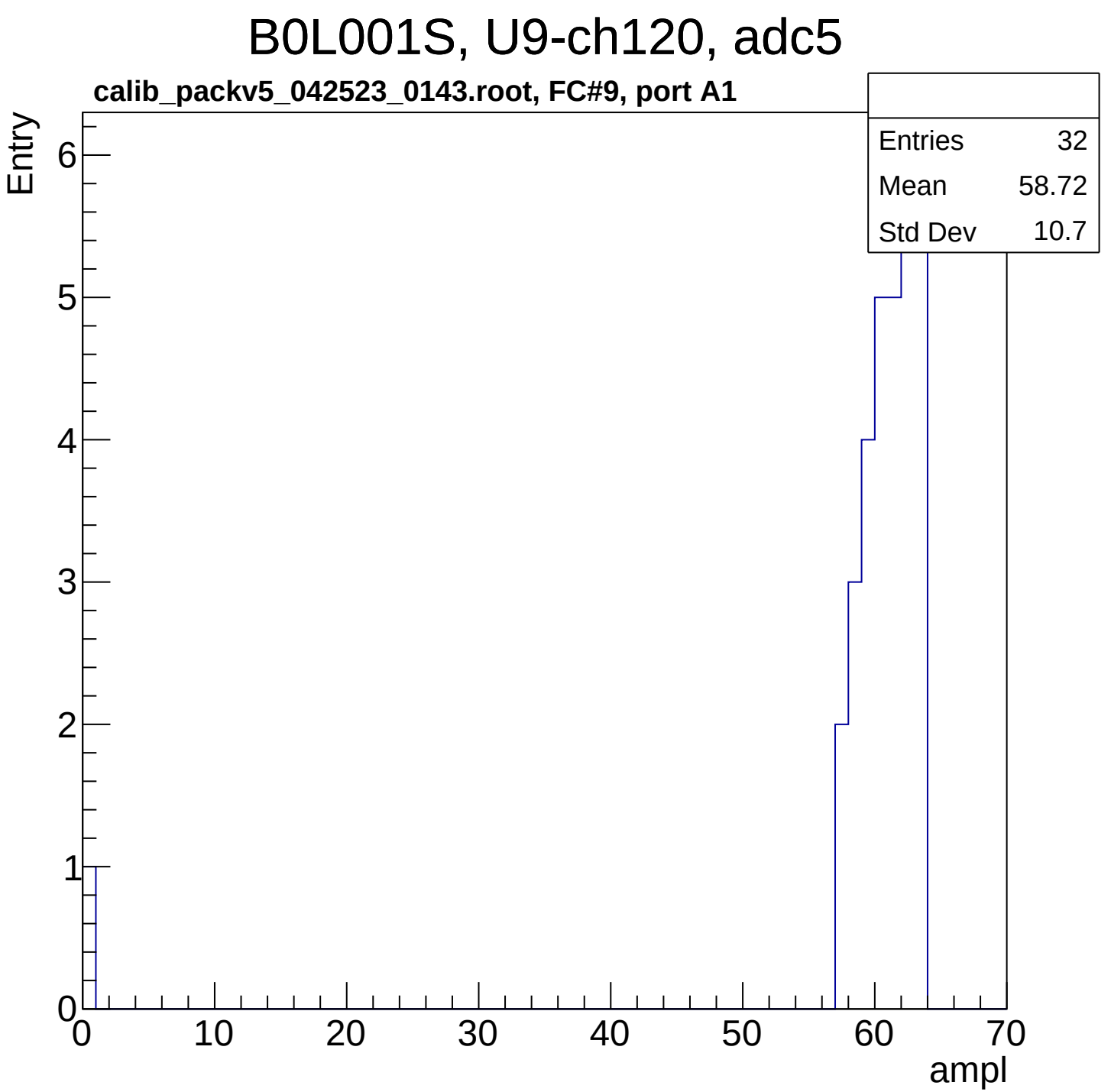

B0L001S, U9-ch120, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.5
Std Dev 6.377
Gaus mean : 31.7885
Gaus Width: 3.7977
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

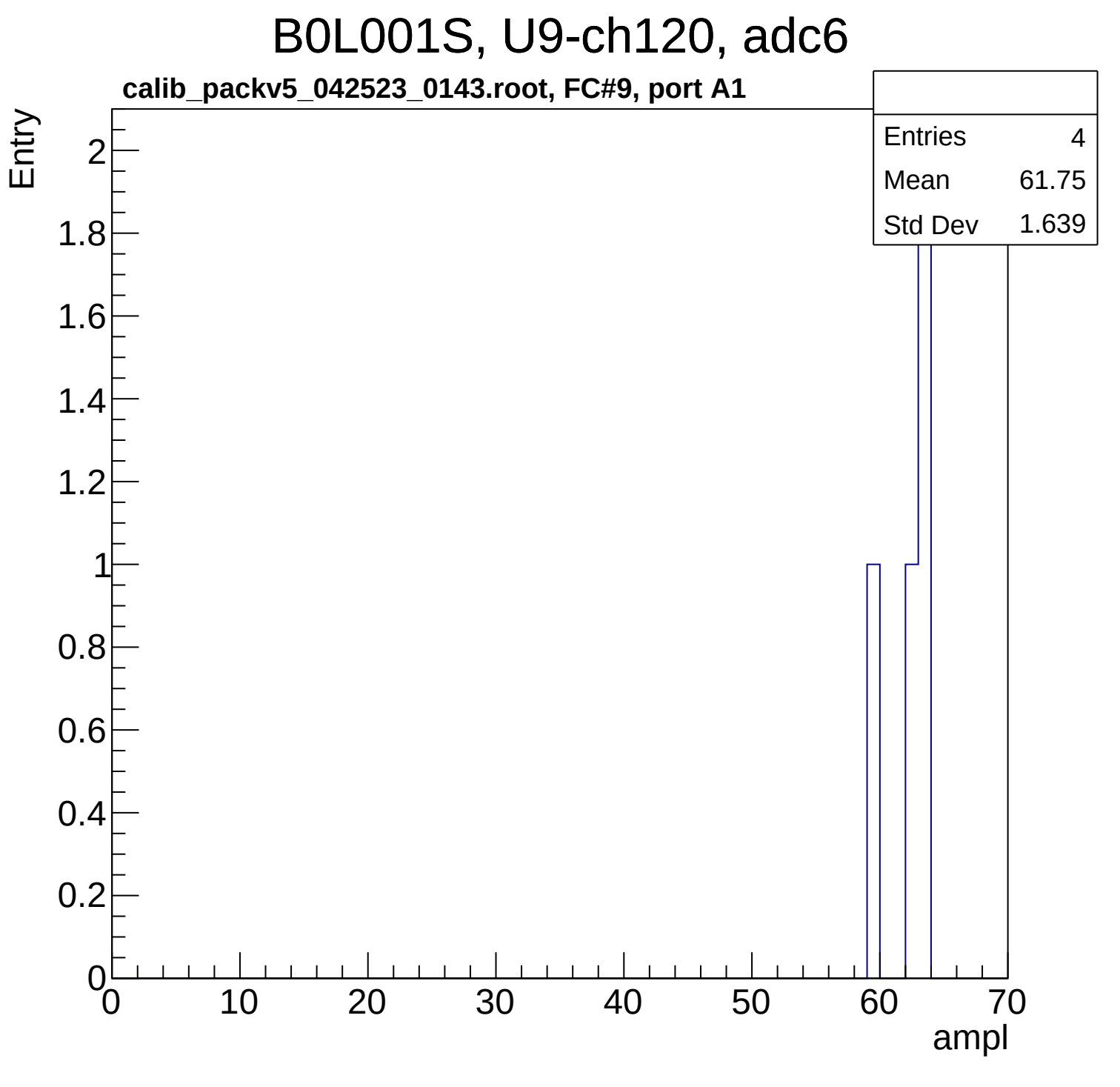

B0L001S, U9-ch120, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 38.38
Std Dev 3.807
Gaus mean : 39.3214
Gaus Width: 4.2278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

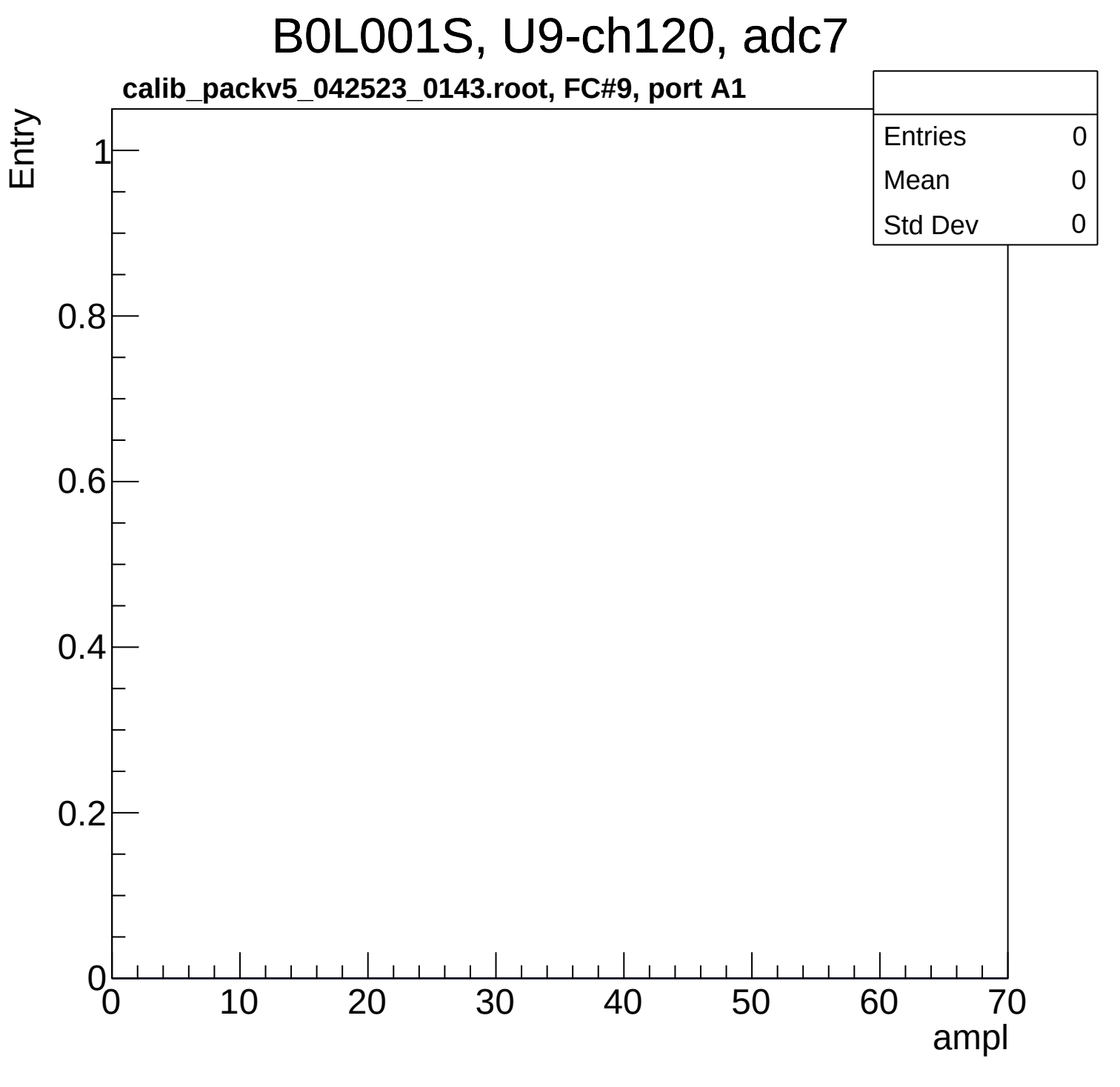

B0L001S, U9-ch120, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 45.1
Std Dev 3.337
Gaus mean : 45.8098
Gaus Width: 3.1051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

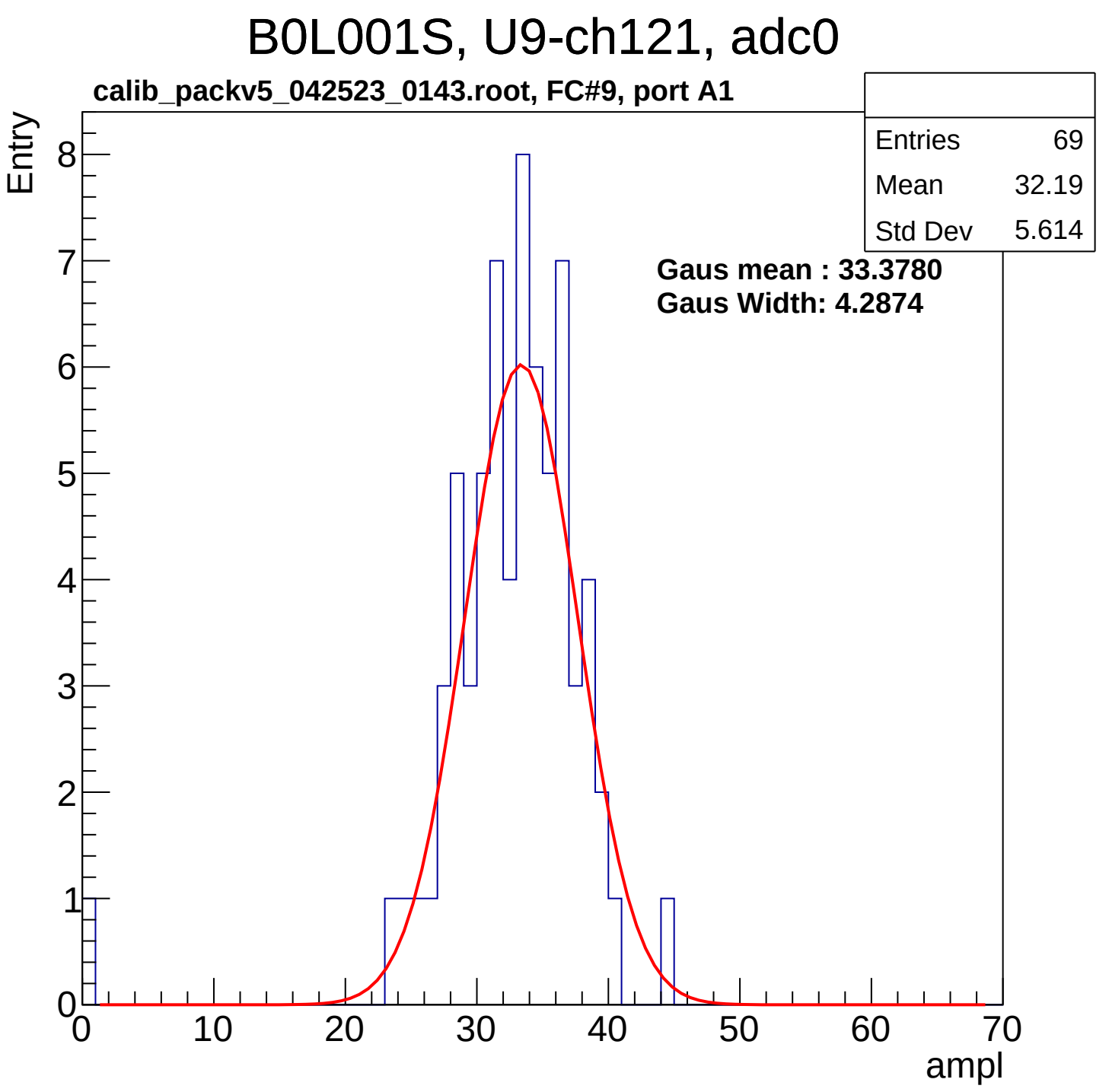

B0L001S, U9-ch120, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 51.7
Std Dev 3.23
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

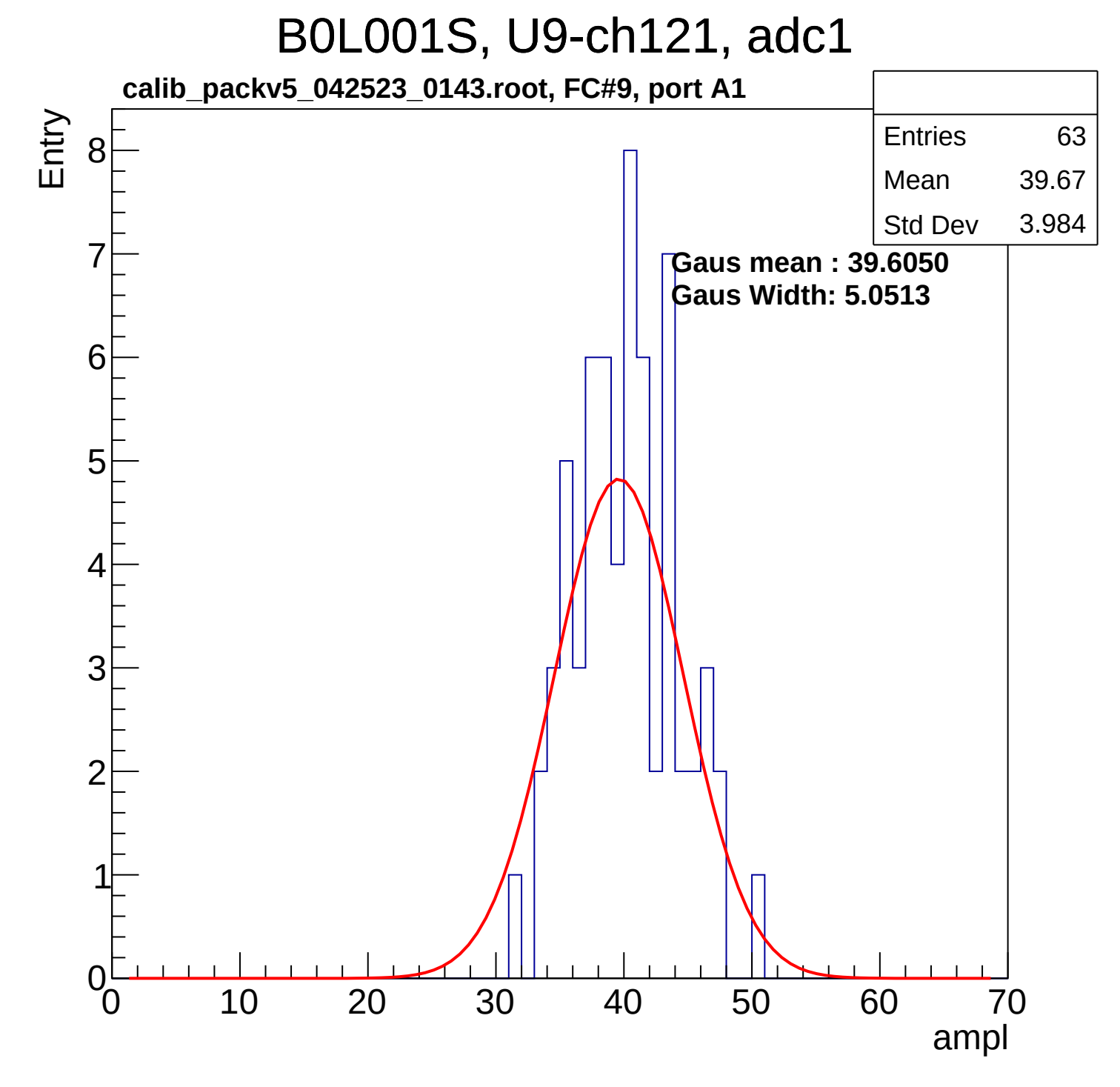

B0L001S, U9-ch120, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.64
Std Dev 3.483
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

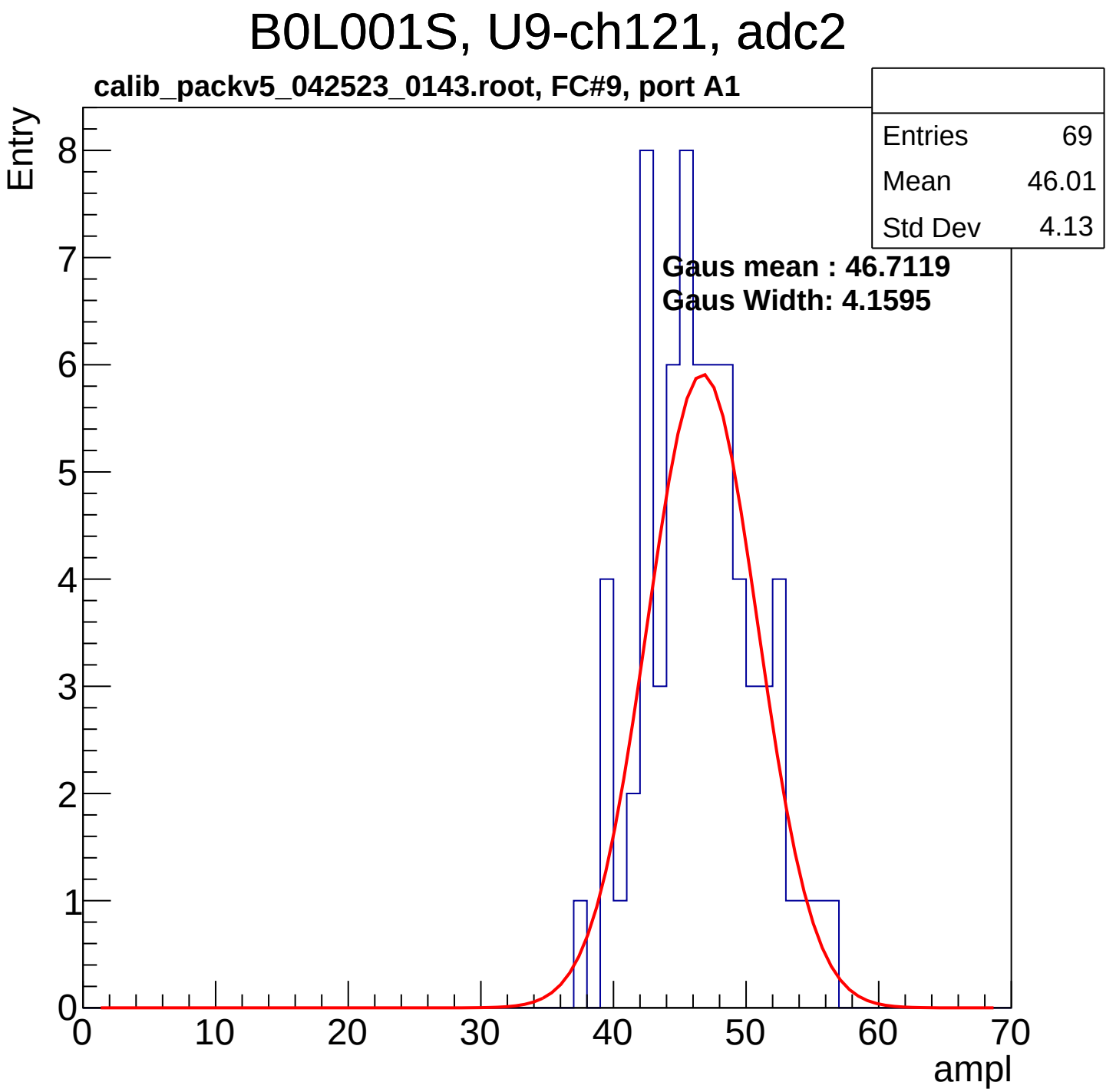

B0L001S, U9-ch120, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 32
Mean 58.72
Std Dev 10.7
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch120, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch120, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch121, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 32.19
Std Dev 5.614
Gaus mean : 33.3780
Gaus Width: 4.2874
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch121, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 39.67
Std Dev 3.984
Gaus mean : 39.6050
Gaus Width: 5.0513
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch121, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 46.01
Std Dev 4.13
Gaus mean : 46.7119
Gaus Width: 4.1595
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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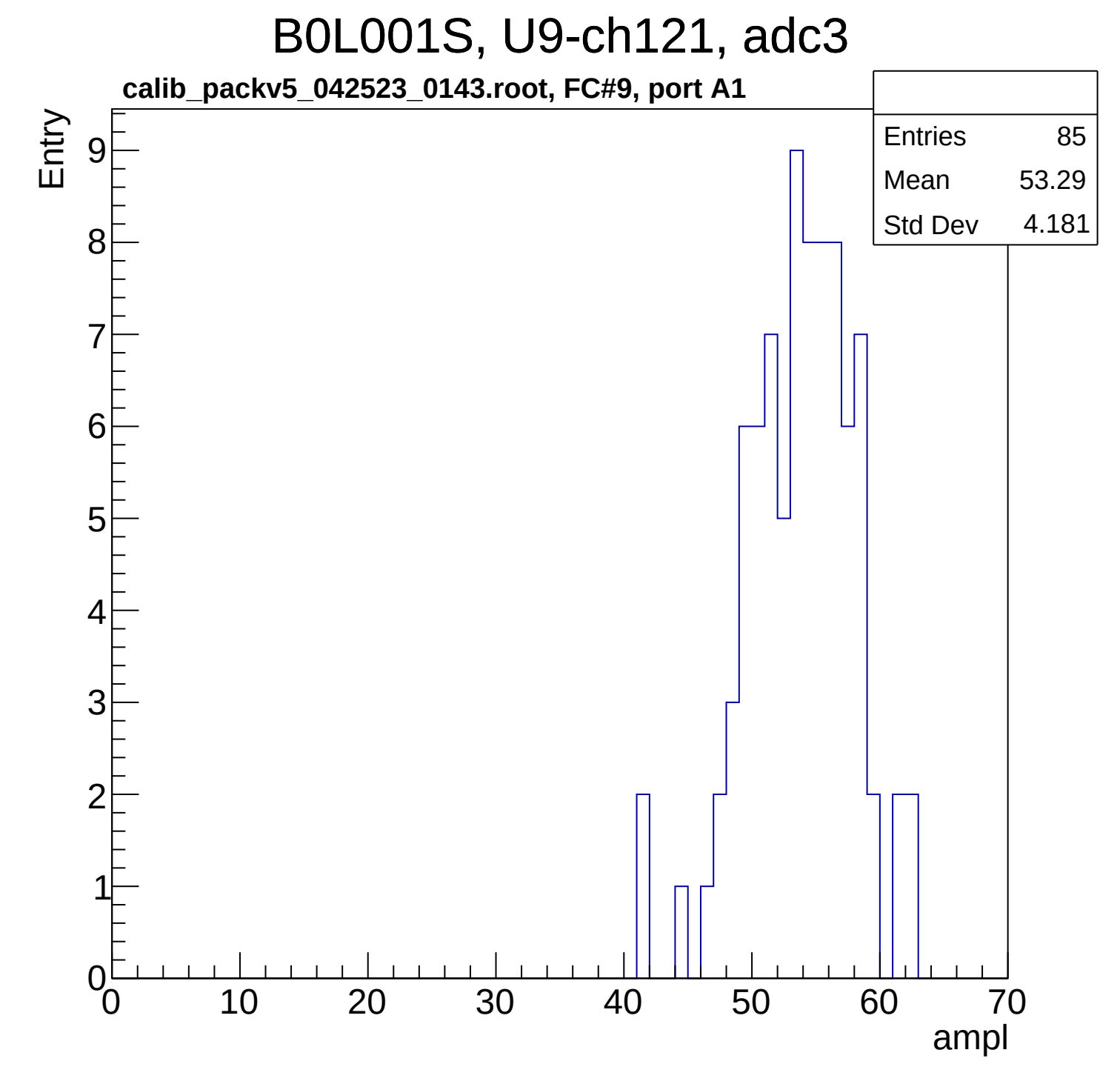
B0L001S, U9-ch121, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 53.29
Std Dev 4.181
Entry
ampl
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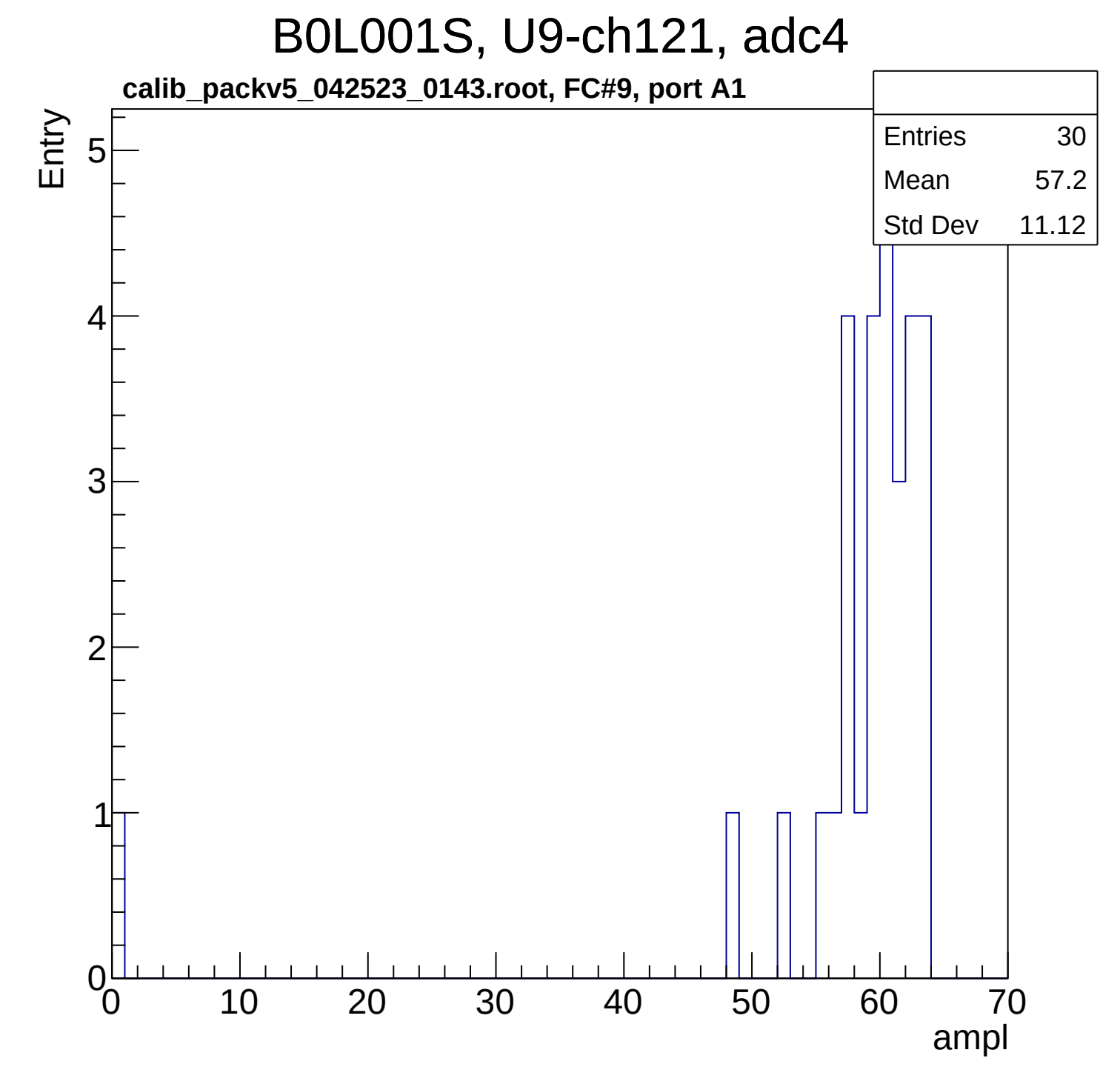
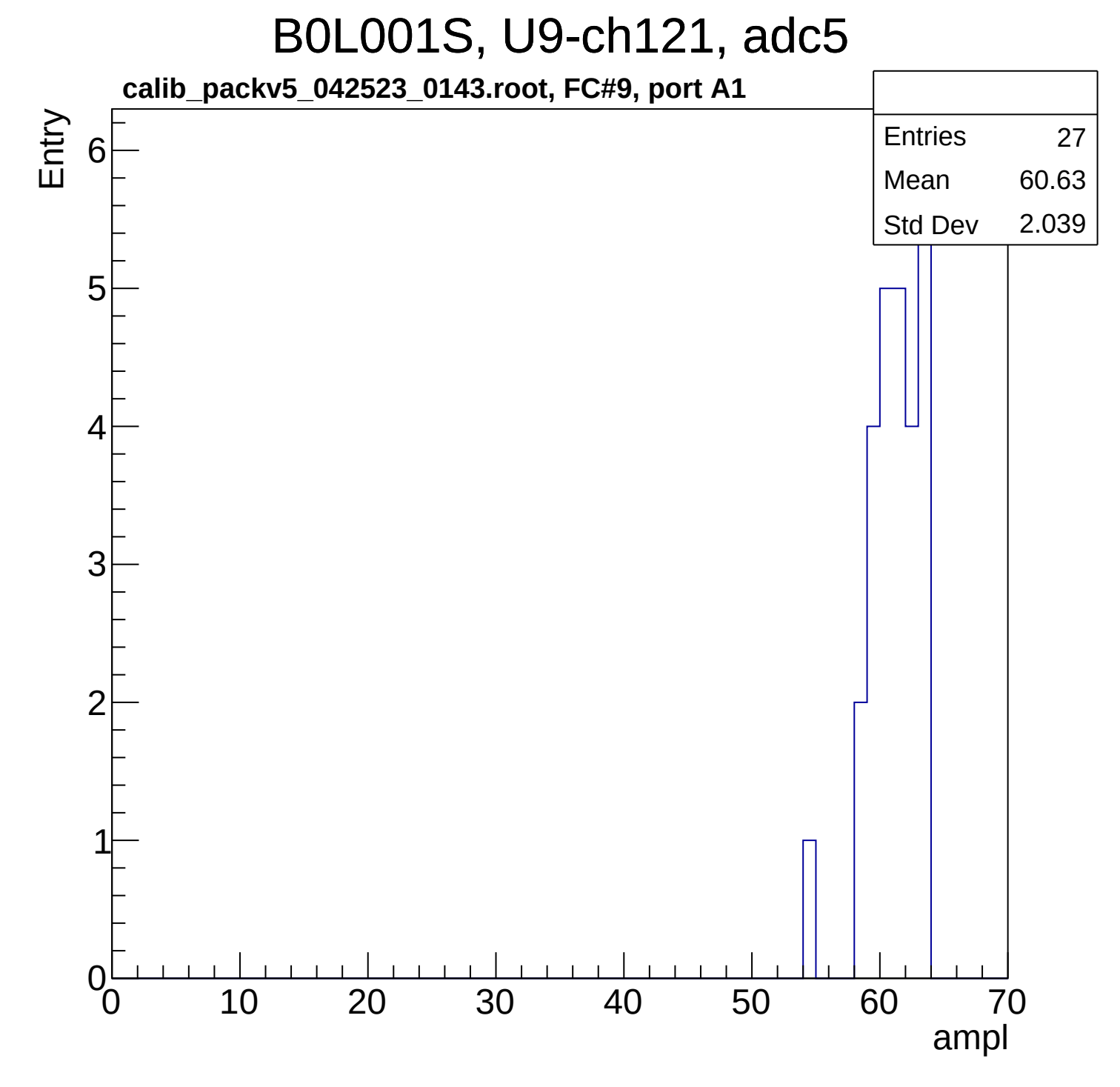
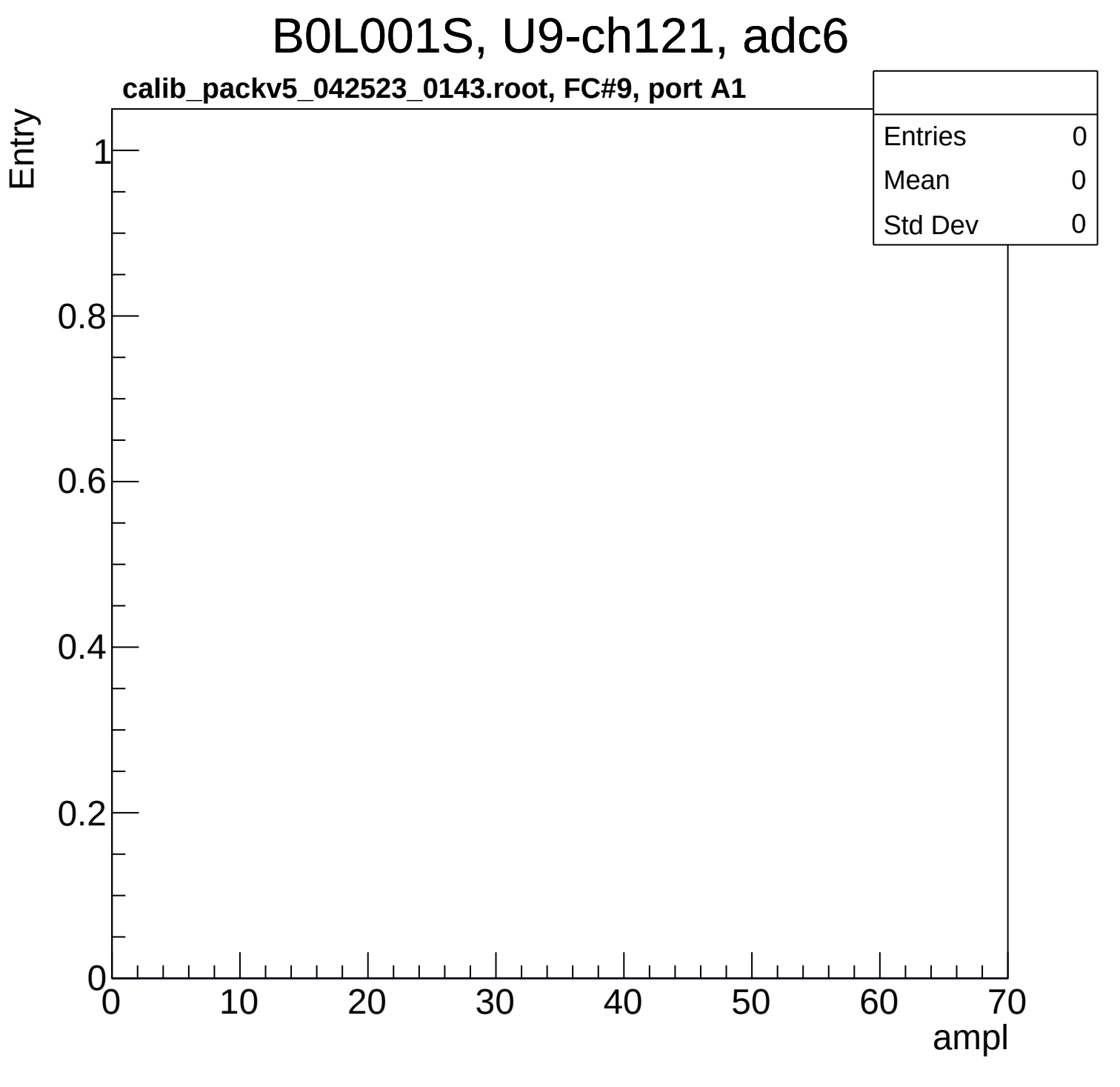
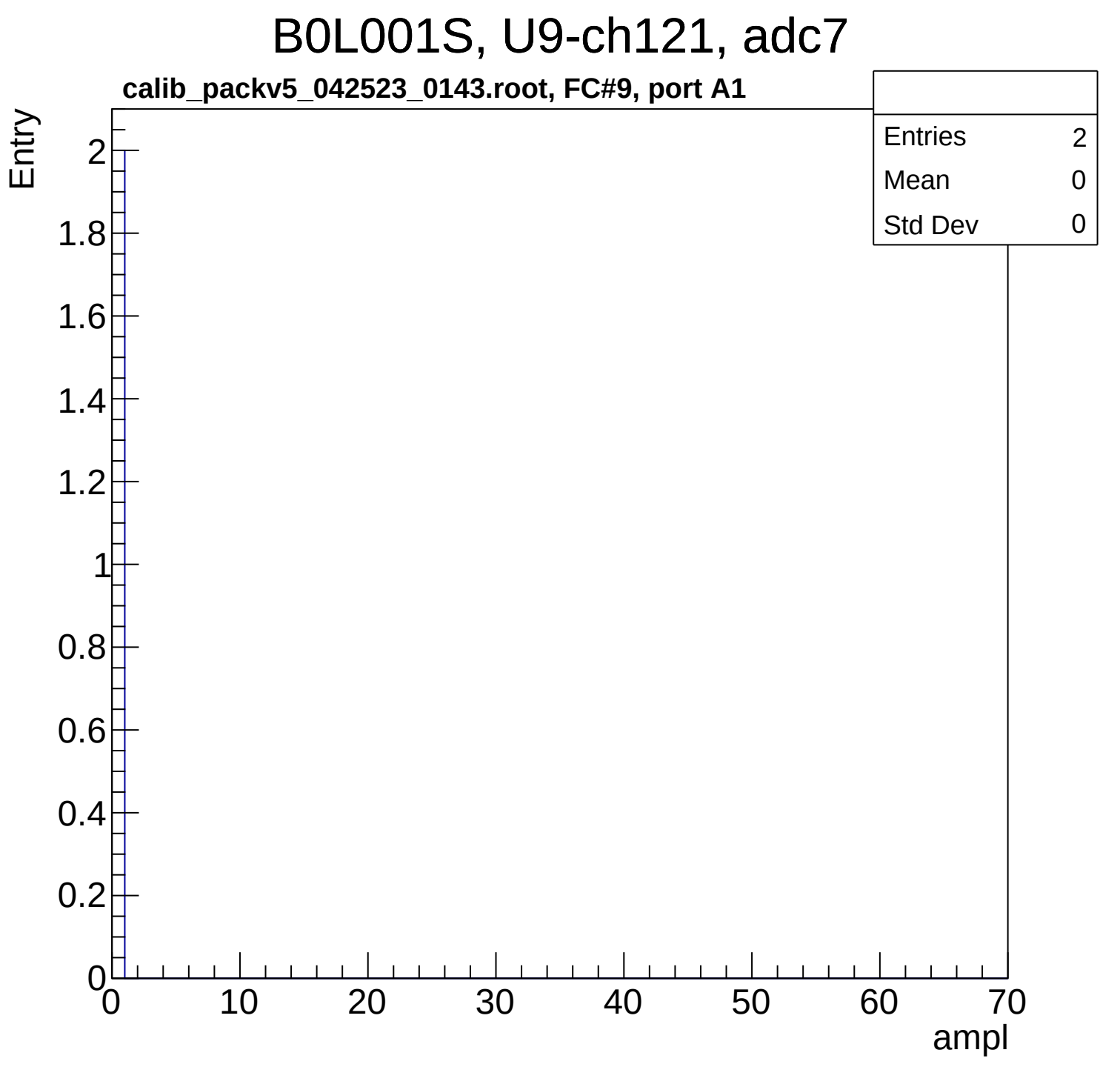
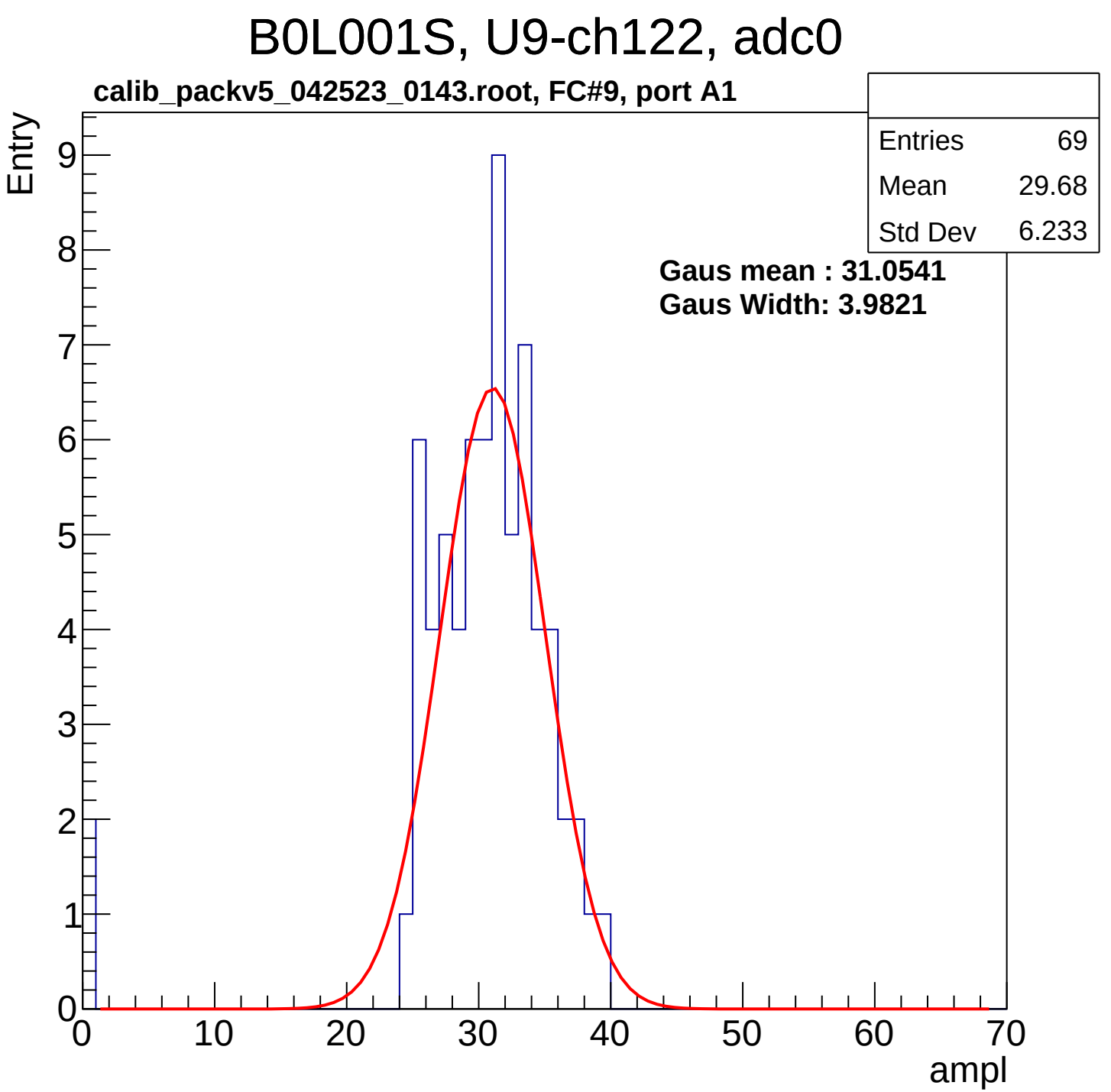
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

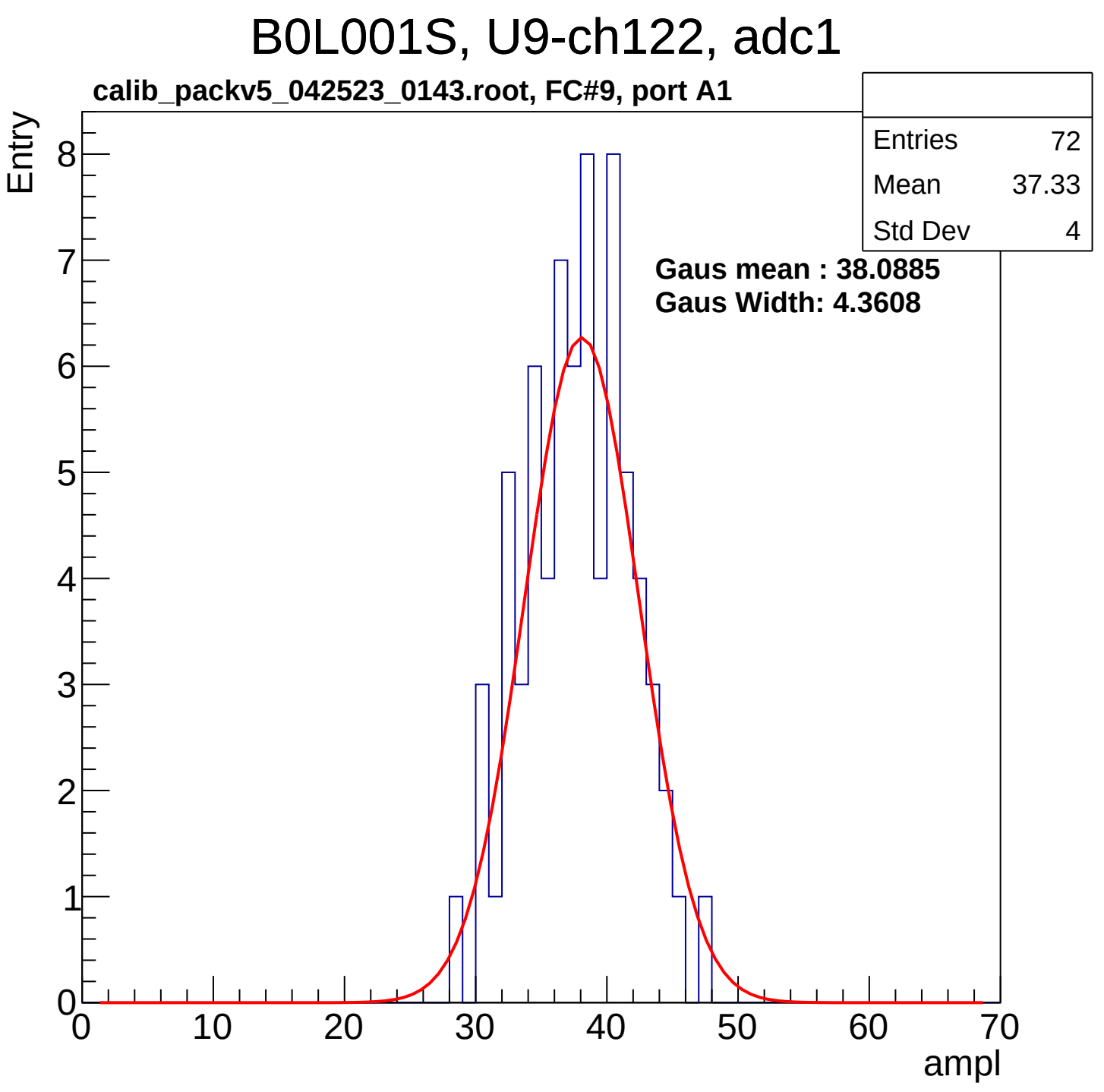

B0L001S, U9-ch121, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 57.2
Std Dev 11.12
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

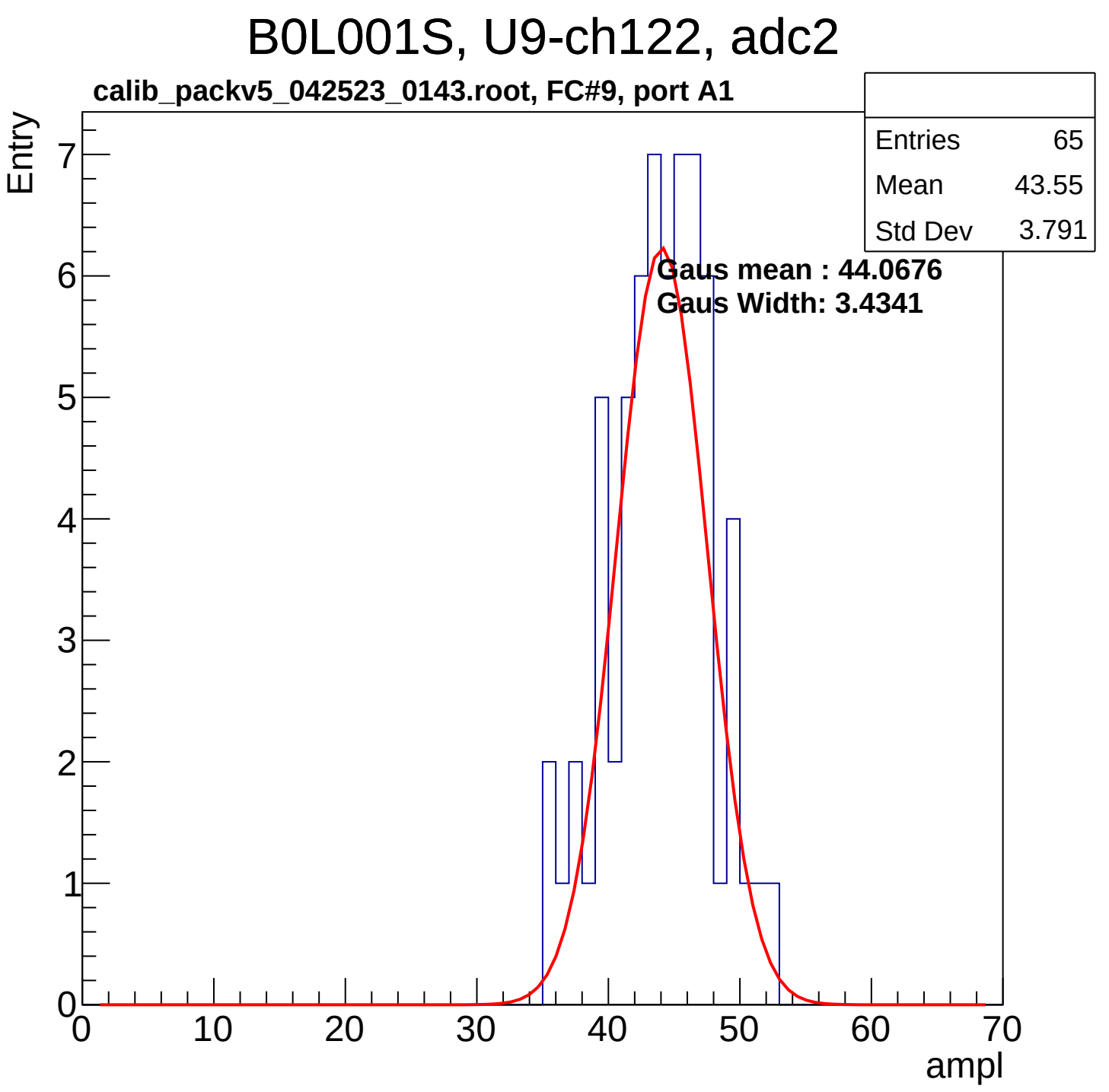

B0L001S, U9-ch121, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 60.63
Std Dev 2.039
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

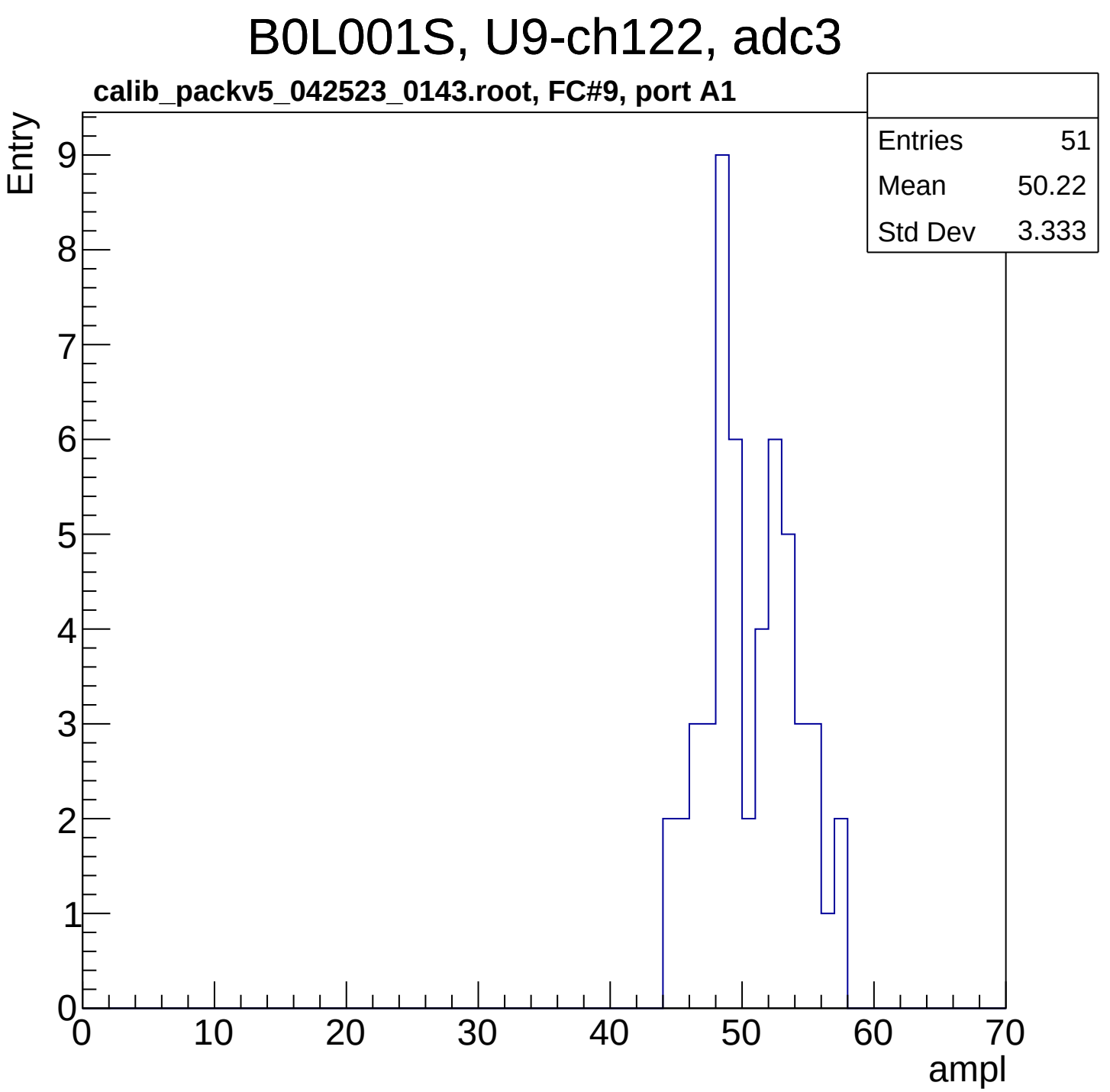

B0L001S, U9-ch121, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch121, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch122, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.68
Std Dev 6.233
Gaus mean : 31.0541
Gaus Width: 3.9821
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch122, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 37.33
Std Dev 4
Gaus mean : 38.0885
Gaus Width: 4.3608
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch122, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.55
Std Dev 3.791
Gaus mean : 44.0676
Gaus Width: 3.4341
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch122, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 50.22
Std Dev 3.333
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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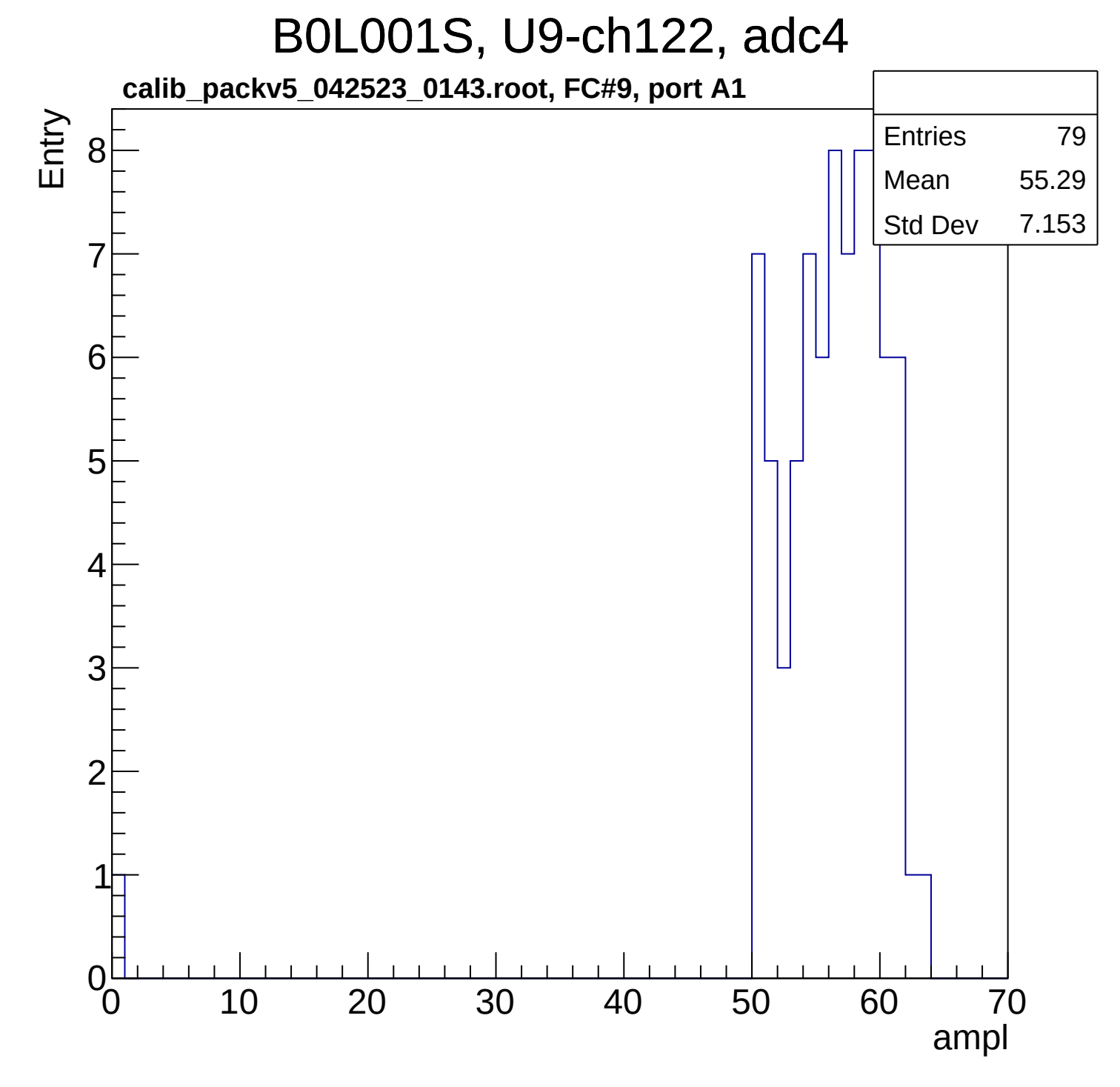
40
50
60
70


B0L001S, U9-ch122, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 55.29
Std Dev 7.153
Entry
ampl
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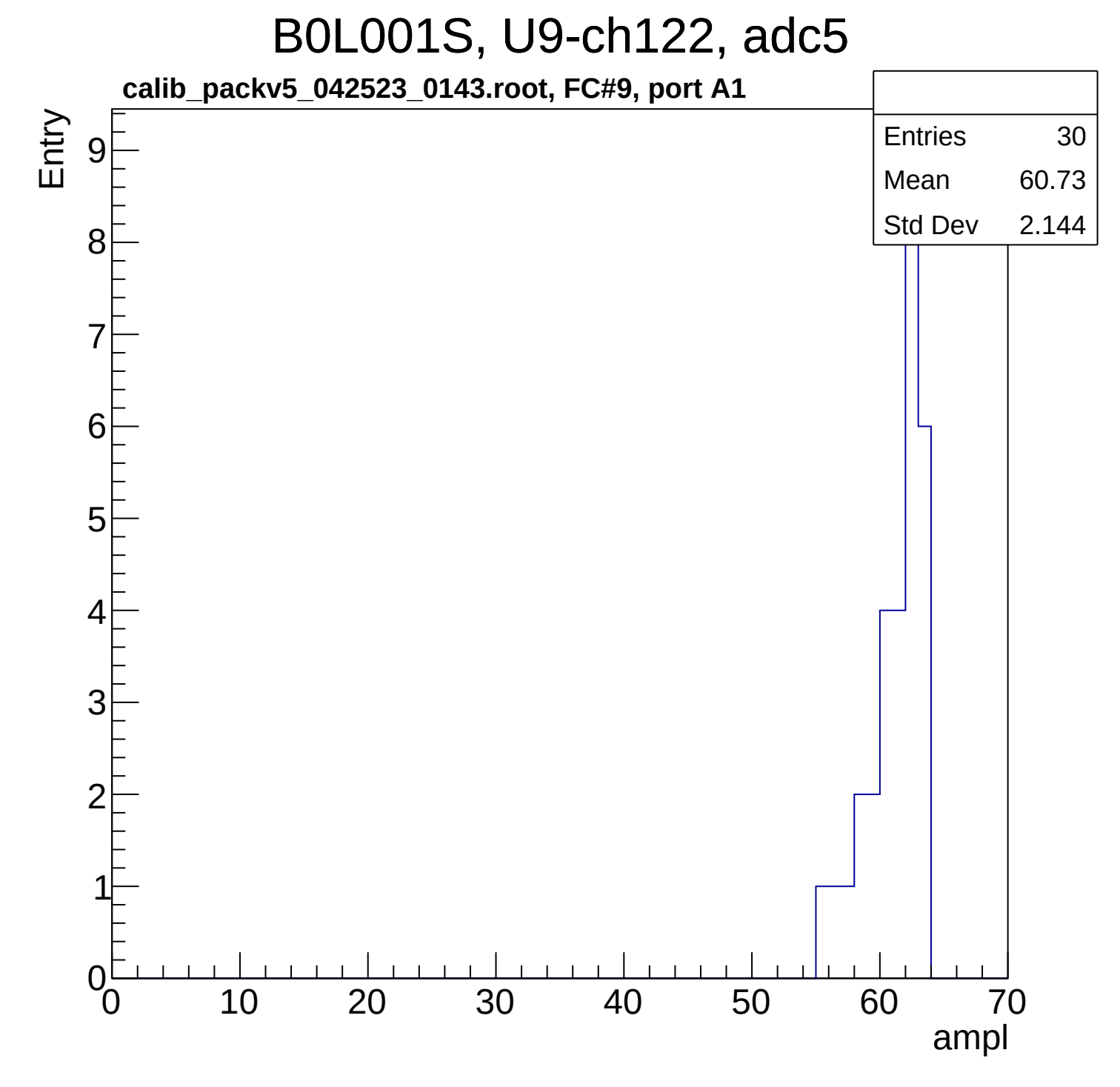
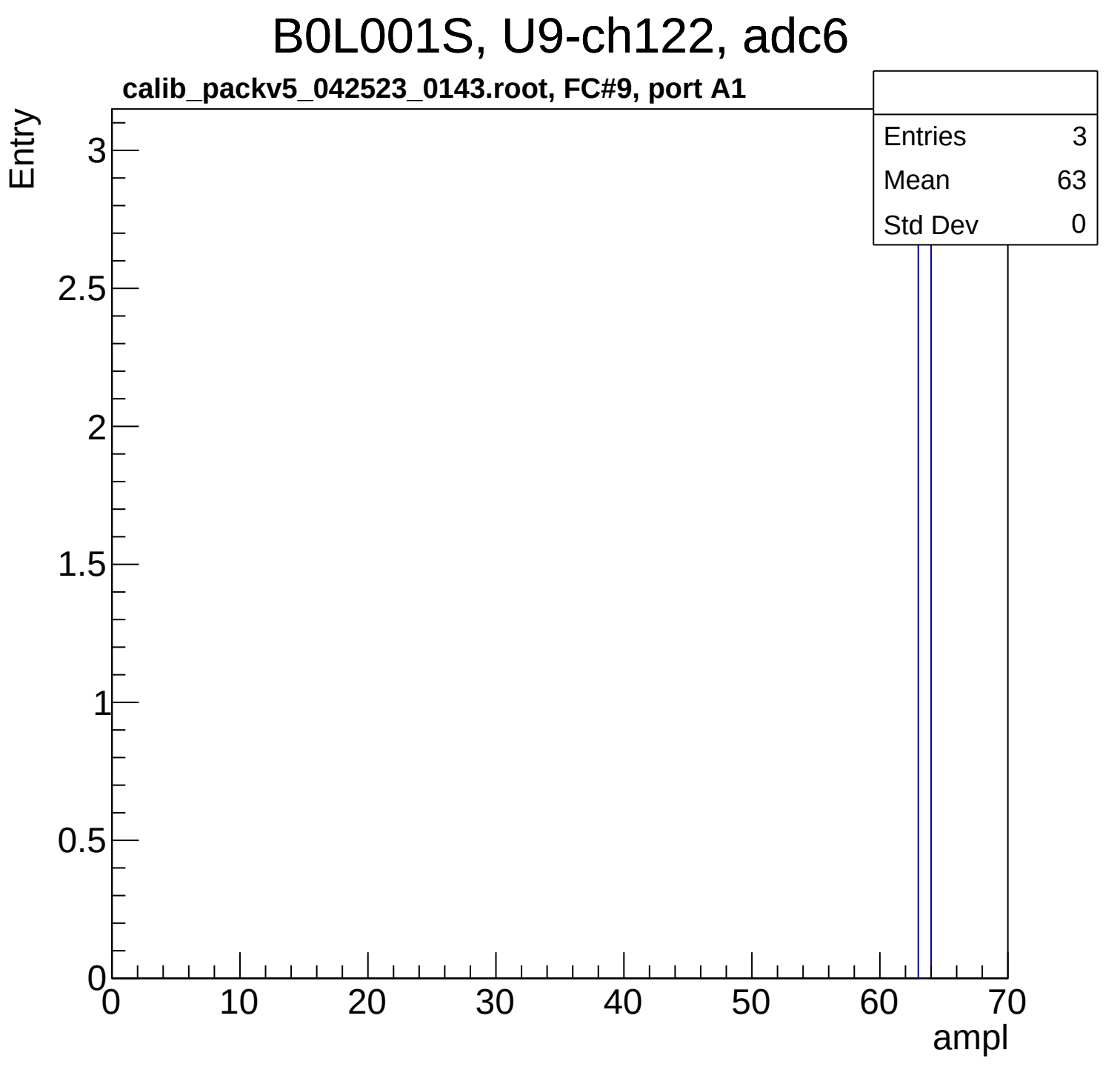
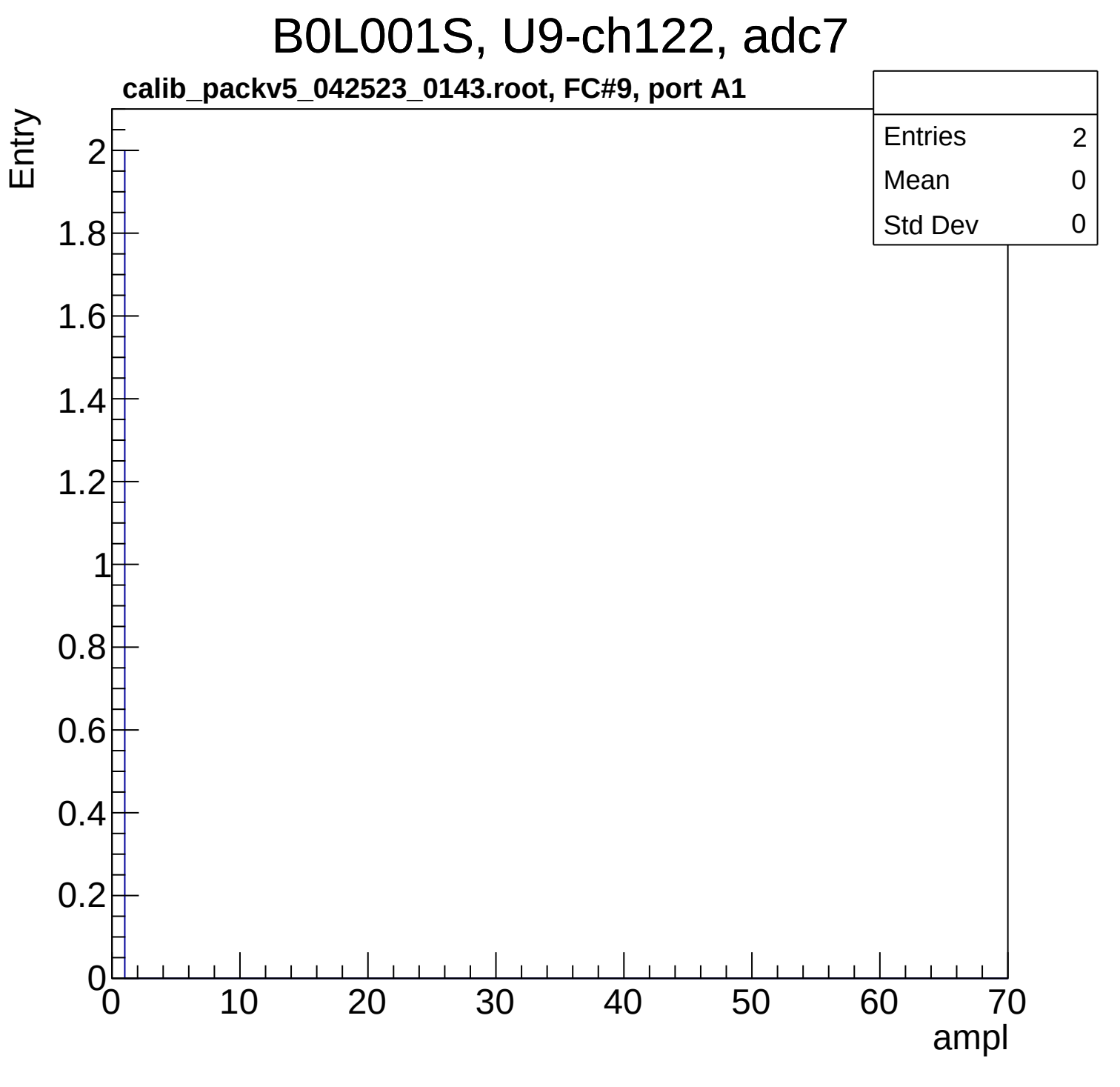
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch122, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 60.73
Std Dev 2.144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch122, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch122, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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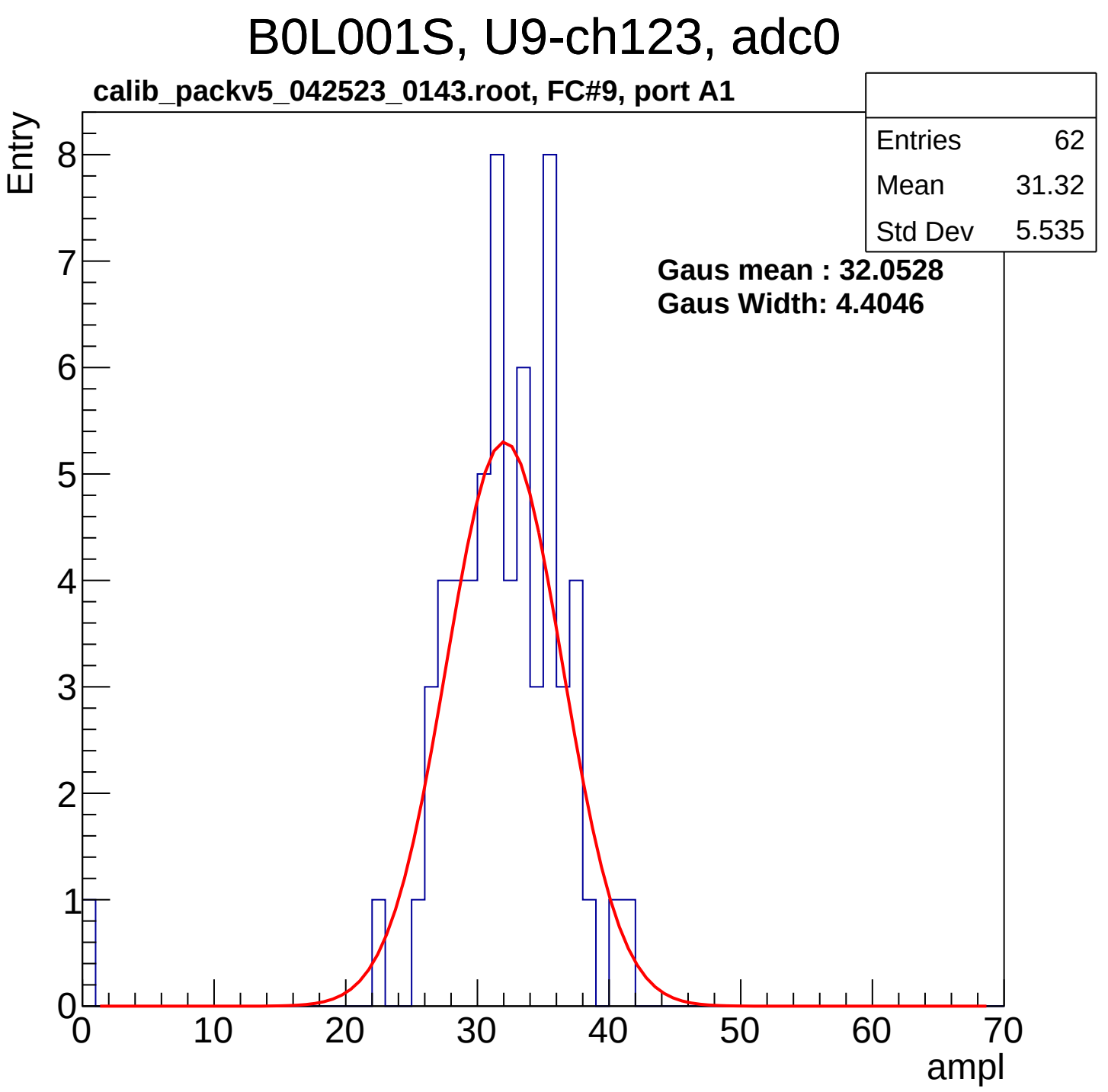
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch123, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 31.32
Std Dev 5.535
Gaus mean : 32.0528
Gaus Width: 4.4046
Entry
ampl
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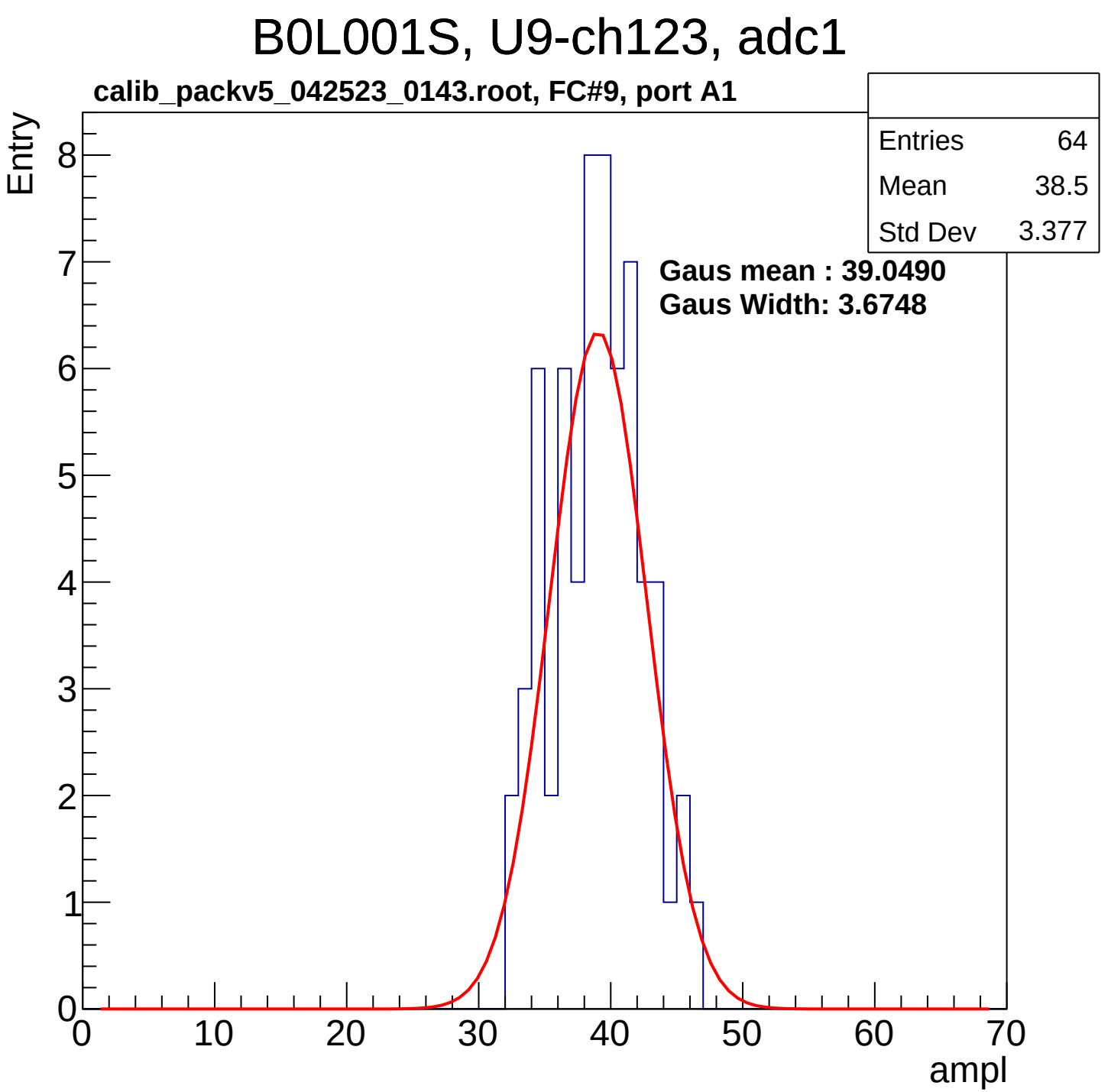
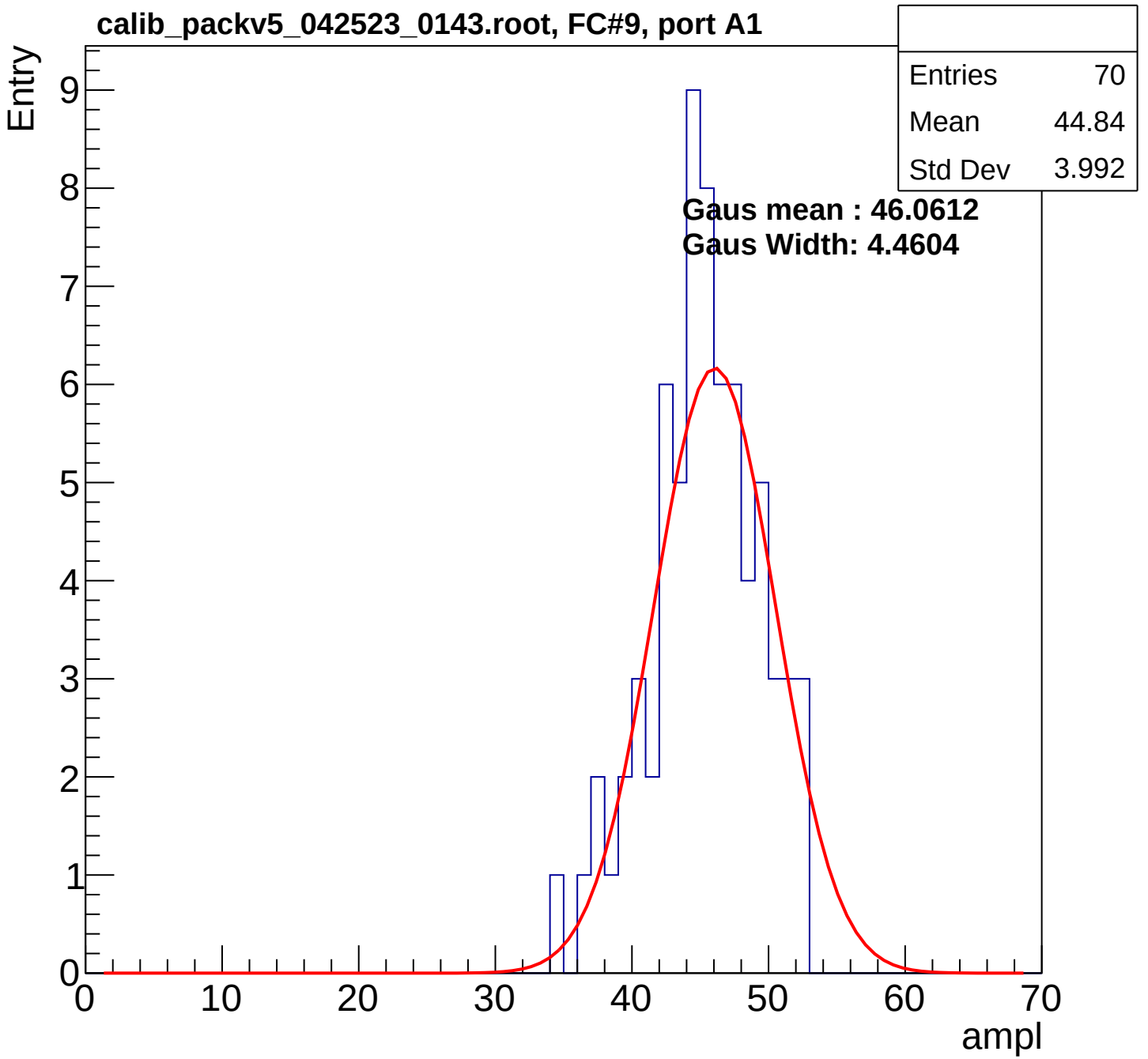
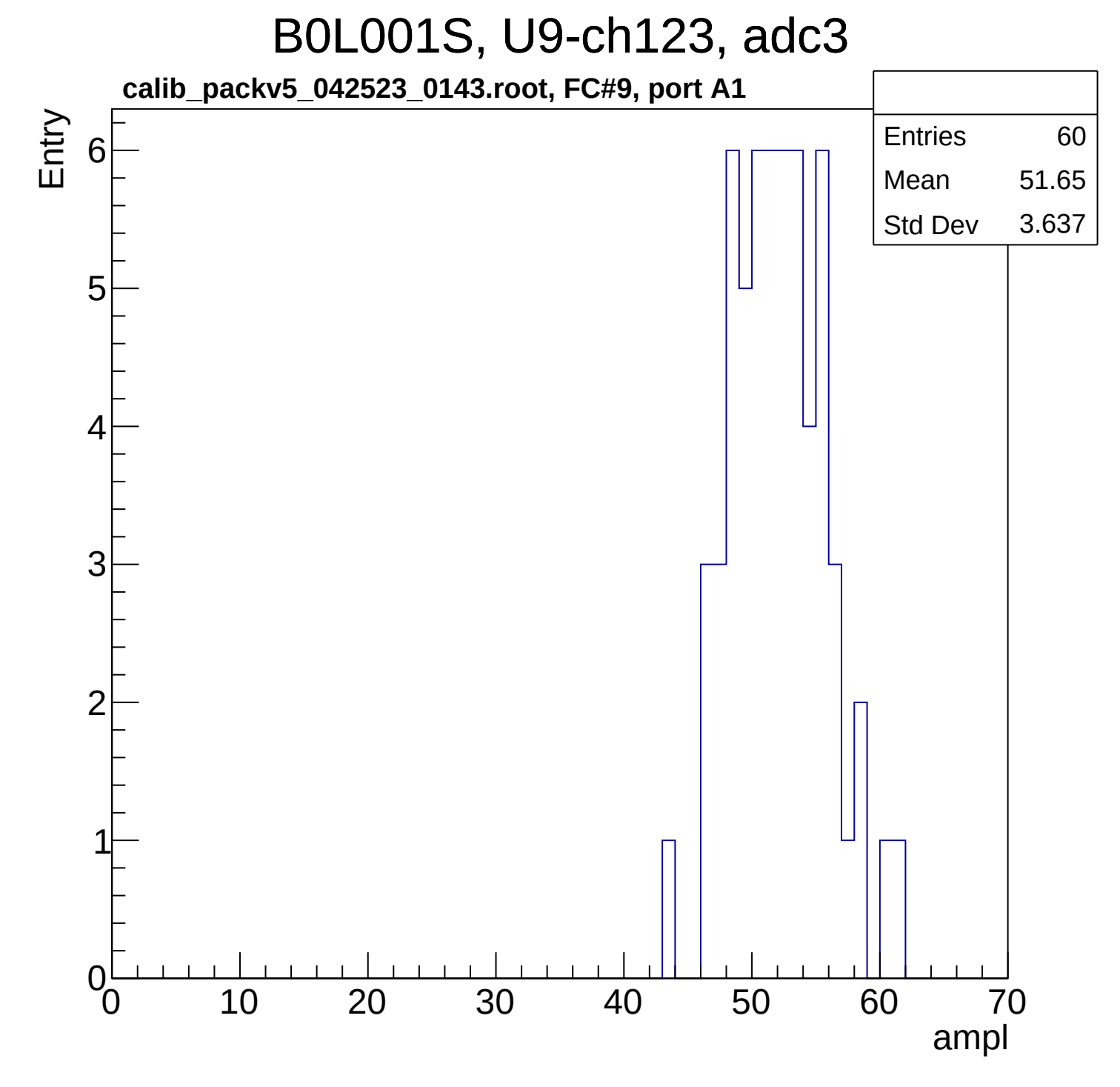
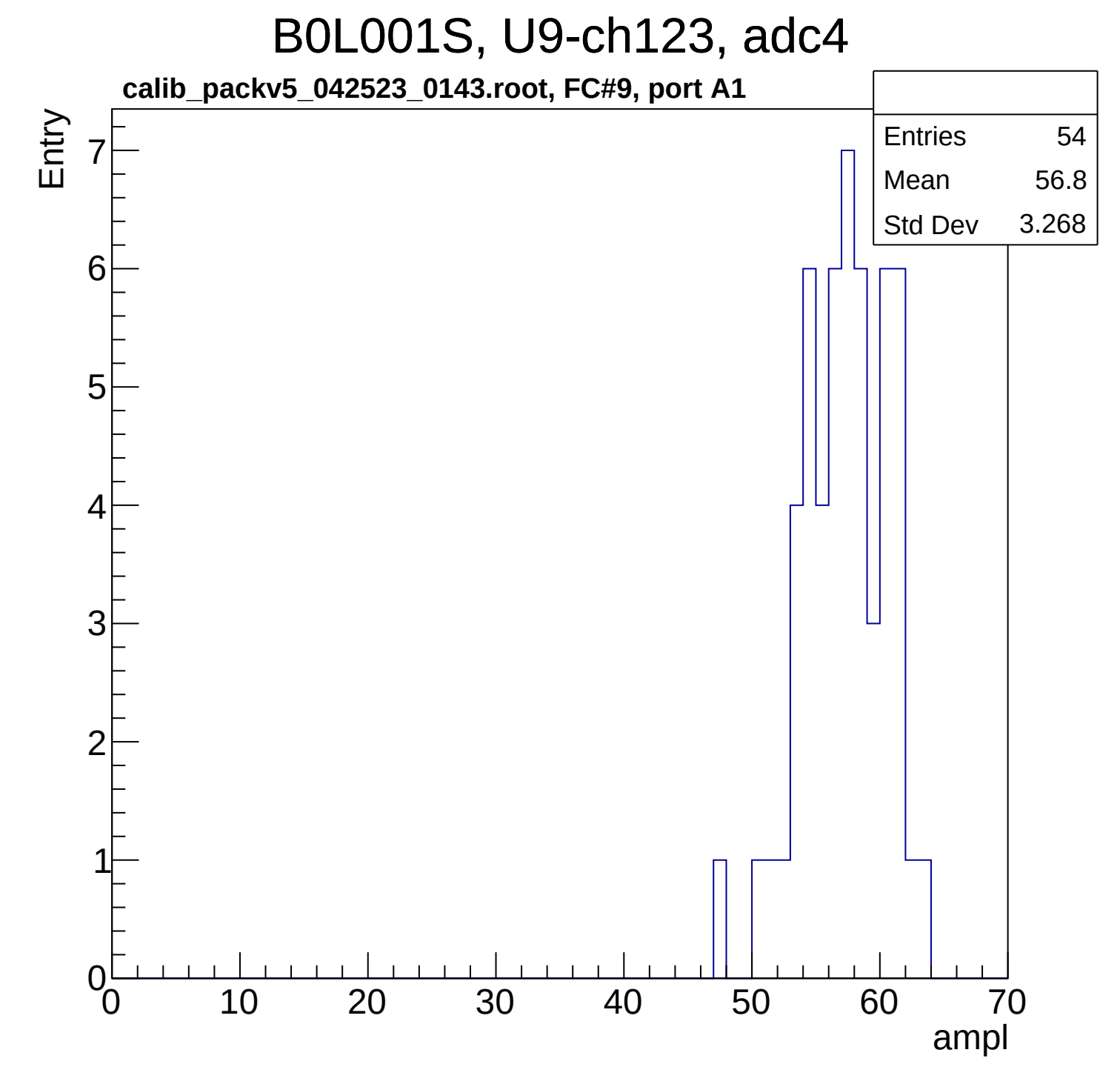
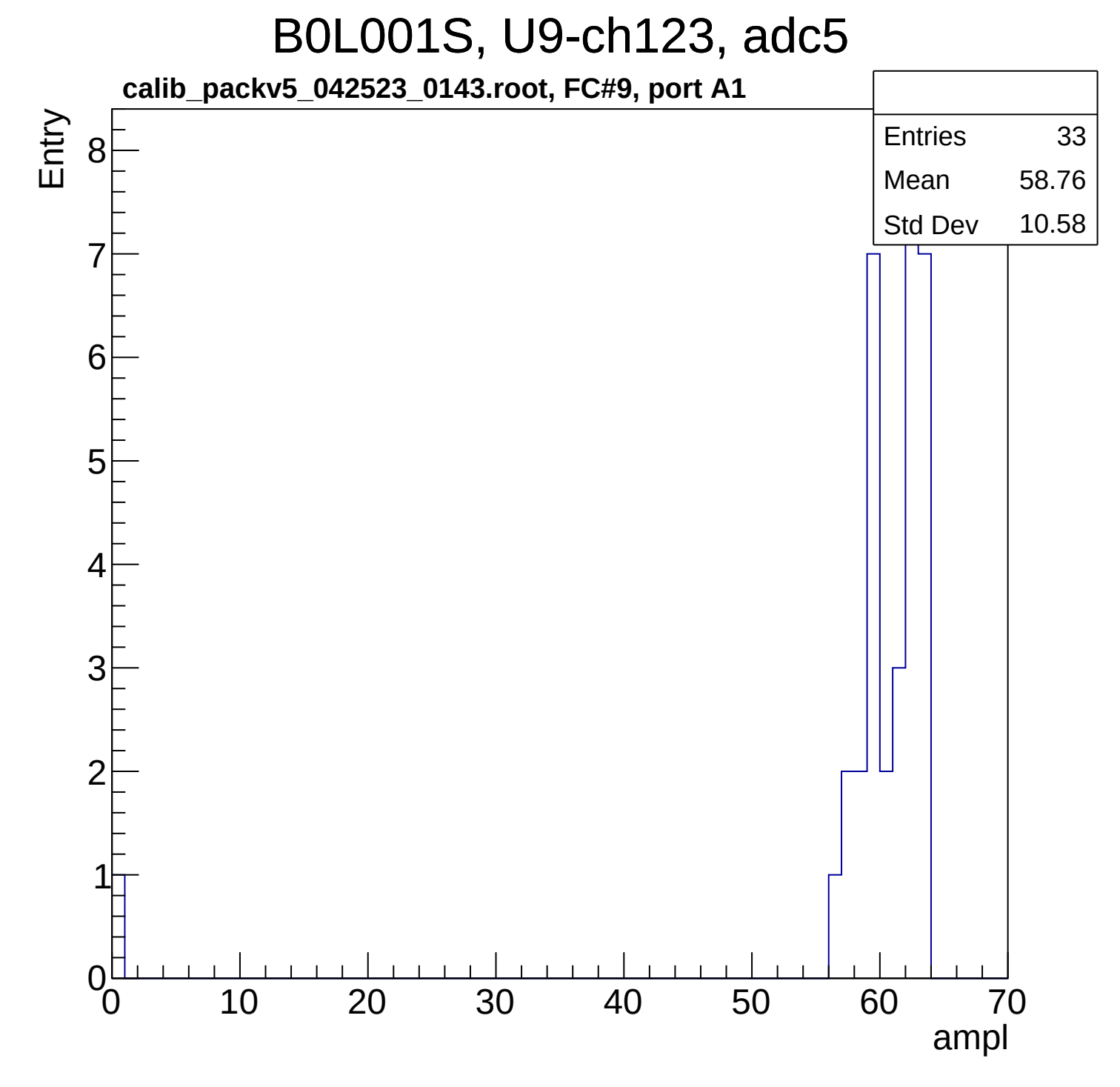
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch123, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.5
Std Dev 3.377
Gaus mean : 39.0490
Gaus Width: 3.6748
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch123, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.84
Std Dev 3.992
Gaus mean : 46.0612
Gaus Width: 4.4604
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch123, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 51.65
Std Dev 3.637
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch123, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.8
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch123, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 58.76
Std Dev 10.58
Entry
ampl
0
1
2
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4
5
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7
8
0
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60
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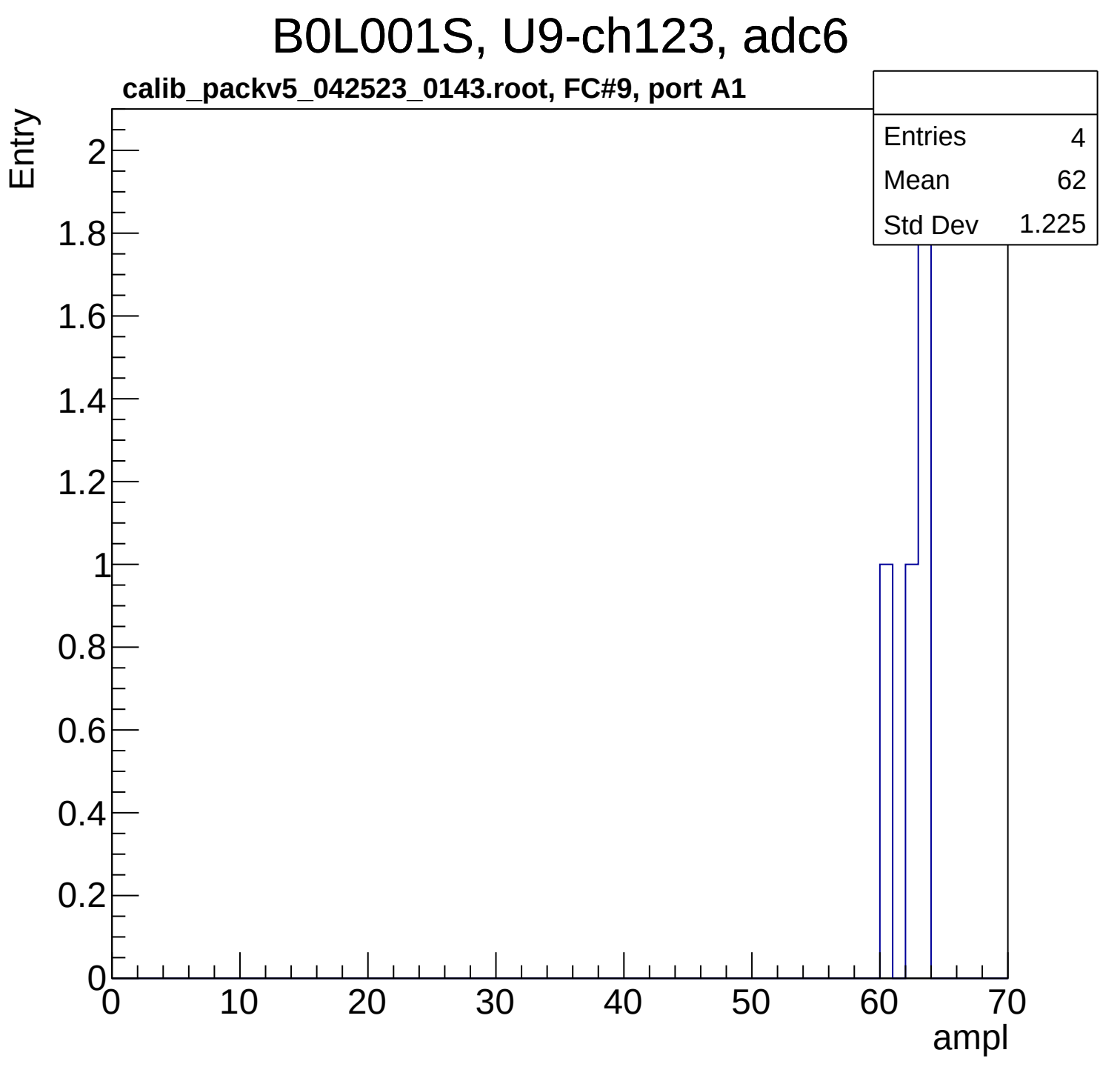
B0L001S, U9-ch123, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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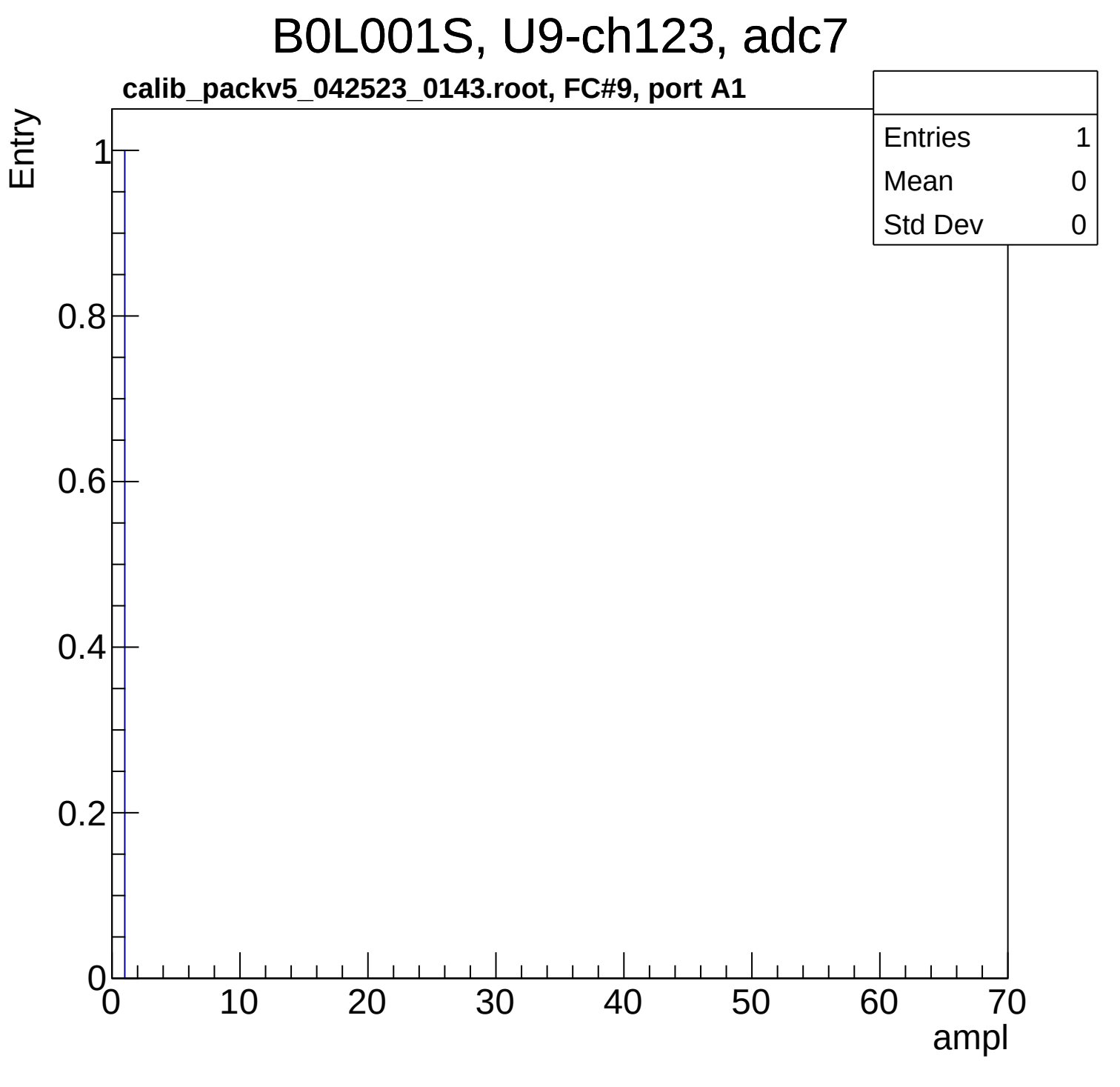
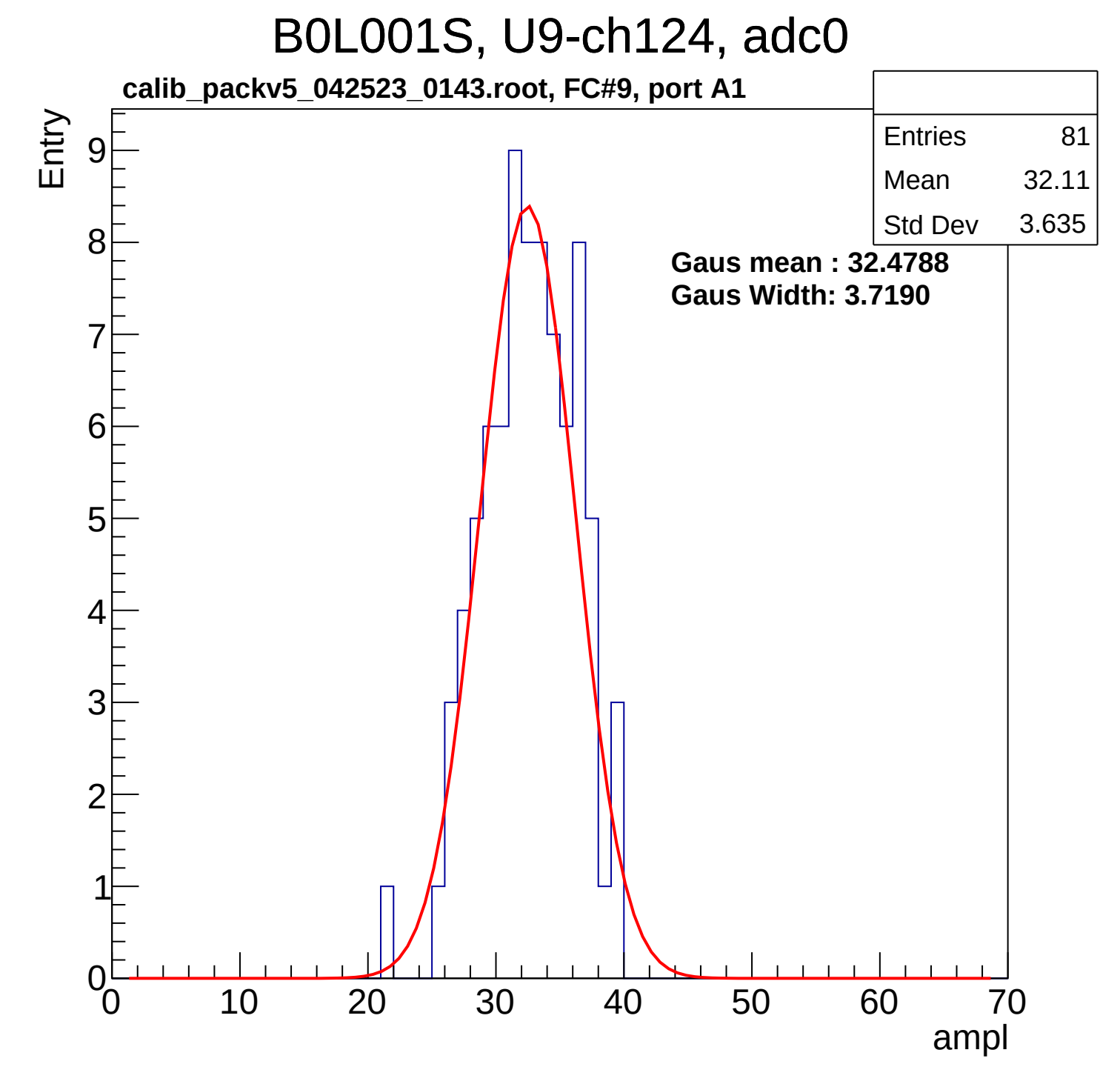

B0L001S, U9-ch123, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch124, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 32.11
Std Dev 3.635
Gaus mean : 32.4788
Gaus Width: 3.7190
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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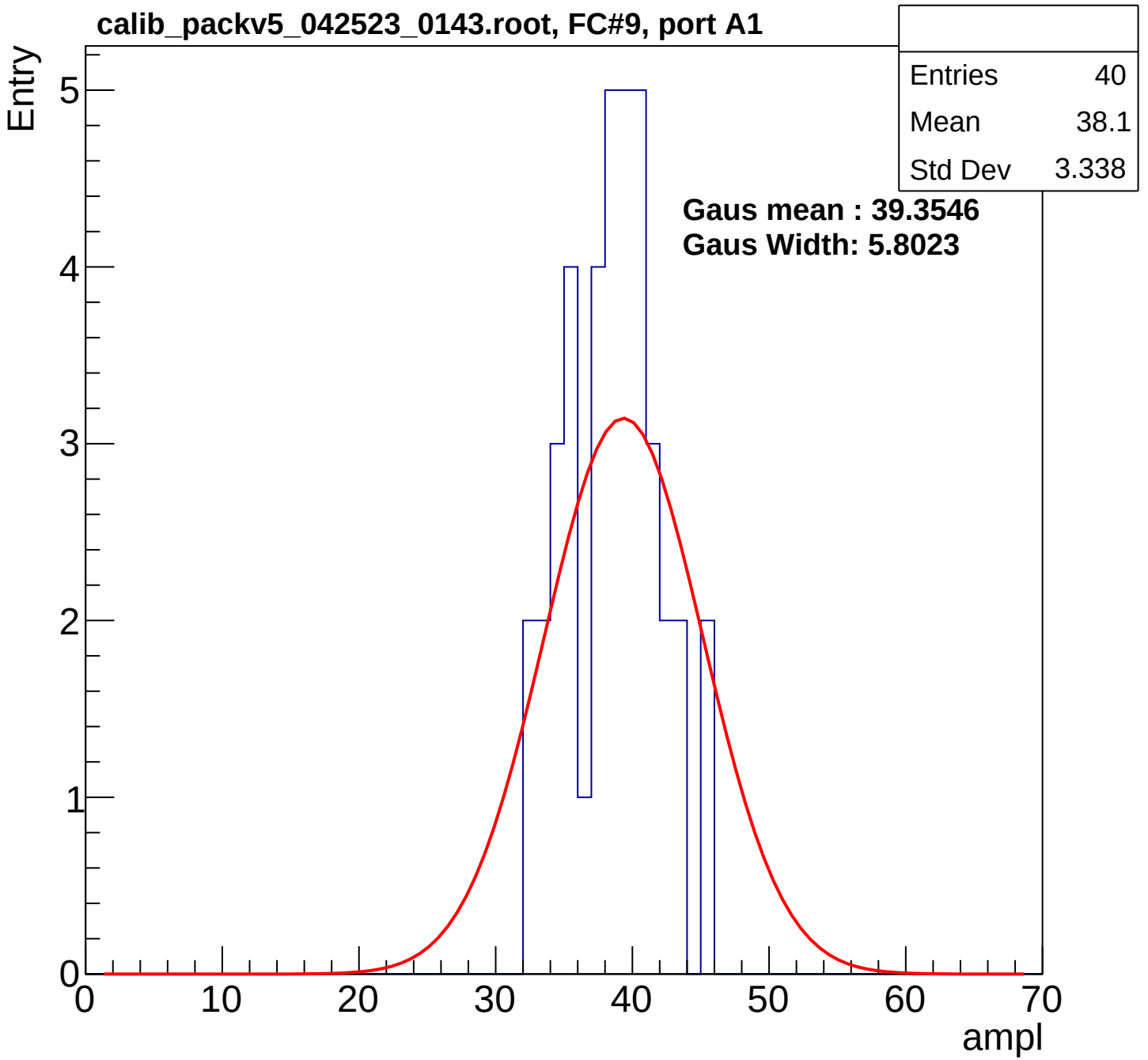
B0L001S, U9-ch124, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 38.1
Std Dev 3.338
Gaus mean : 39.3546
Gaus Width: 5.8023
Entry
ampl
0
1
2
3
4
5
0
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20
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40
50
60
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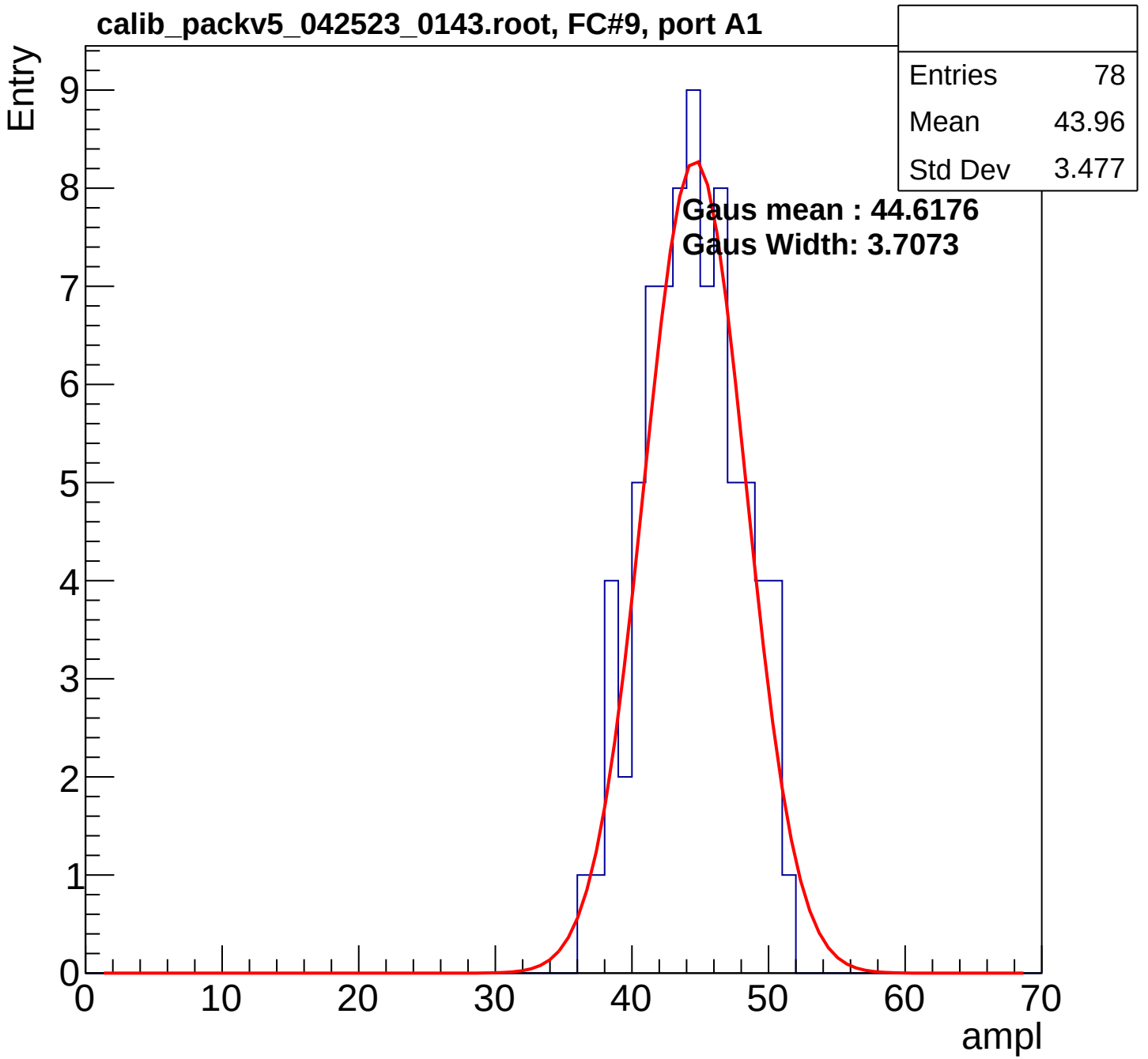
B0L001S, U9-ch124, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 43.96
Std Dev 3.477
Gaus mean : 44.6176
Gaus Width: 3.7073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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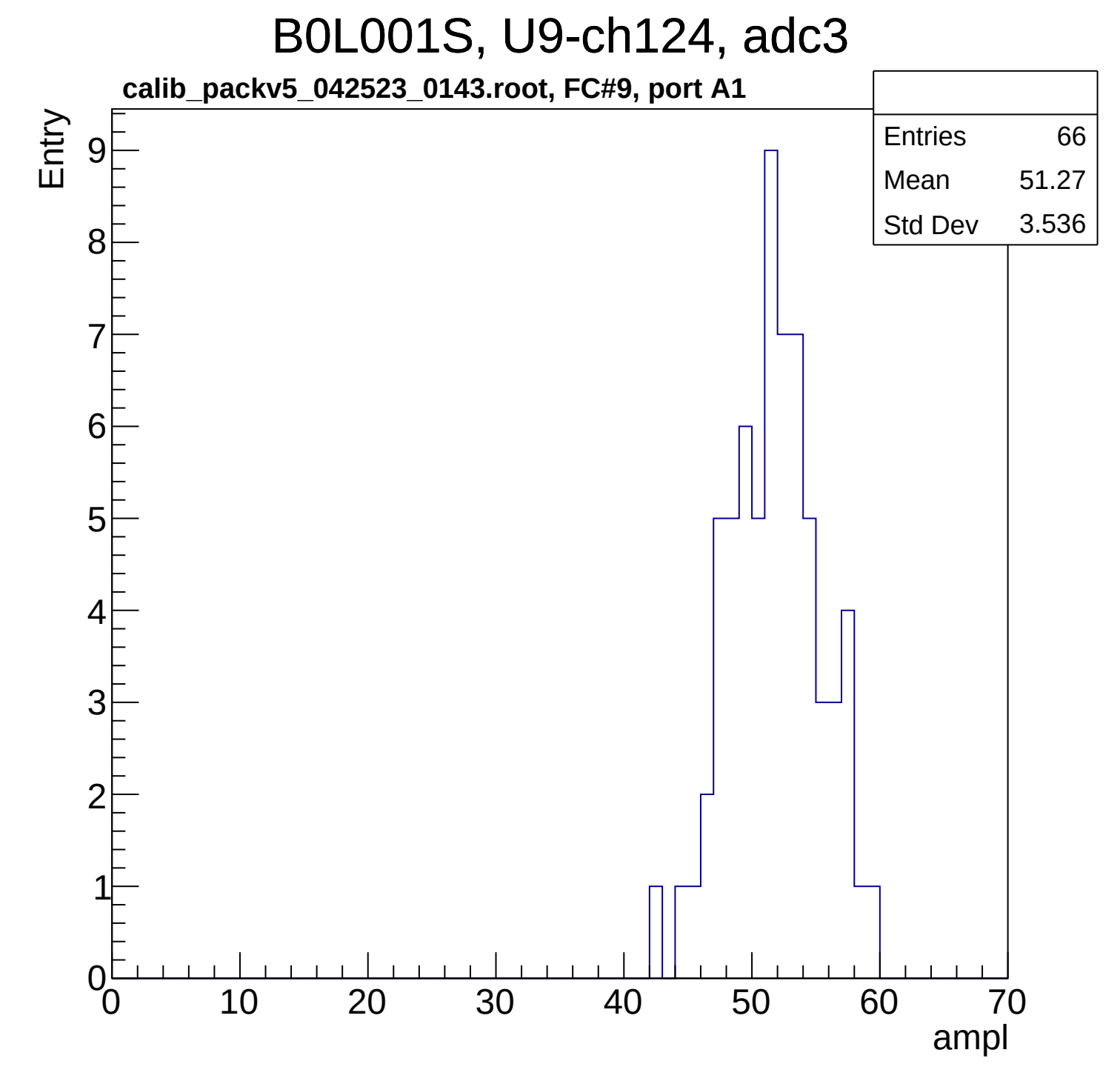
B0L001S, U9-ch124, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.27
Std Dev 3.536
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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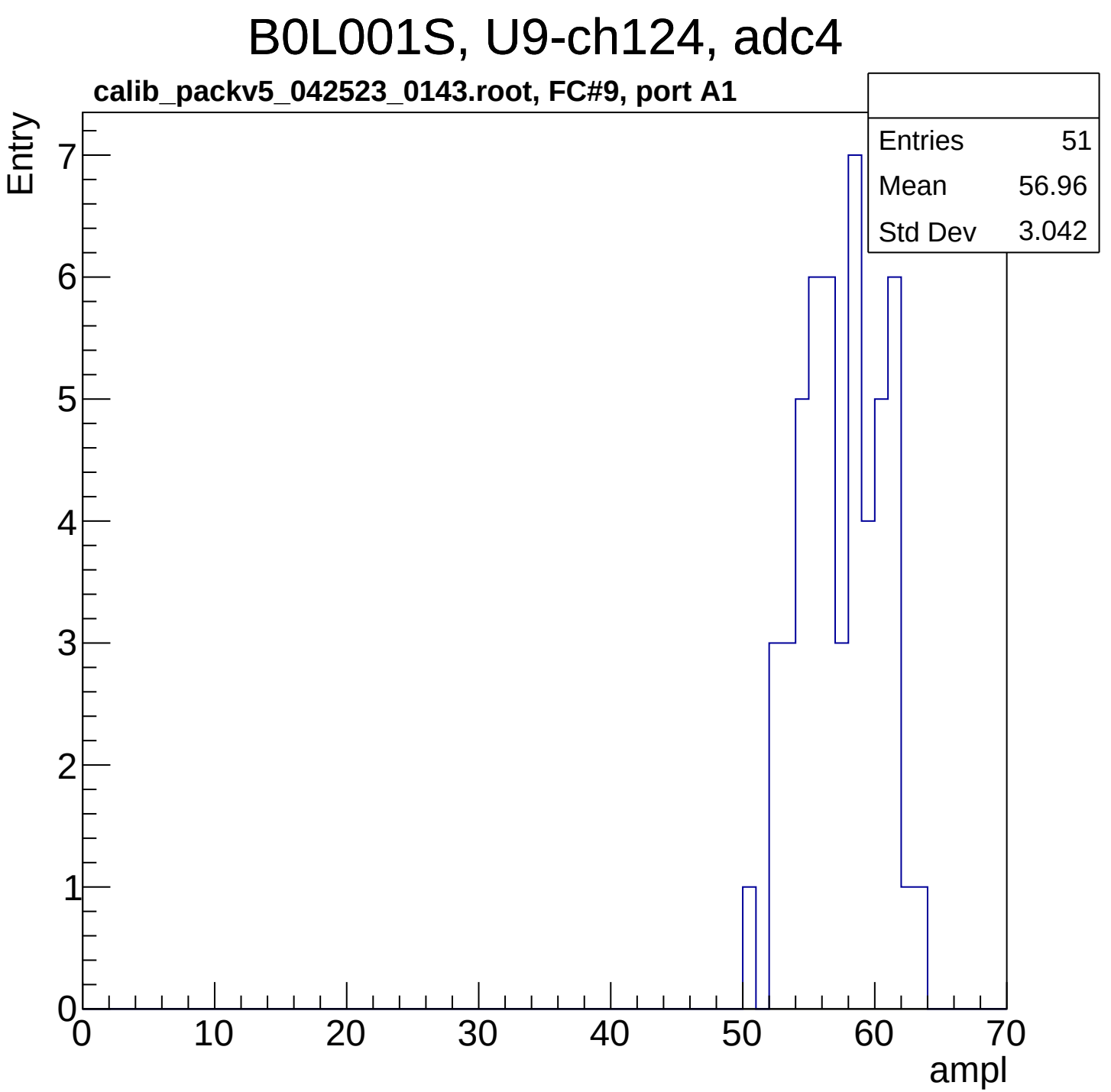
B0L001S, U9-ch124, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 56.96
Std Dev 3.042
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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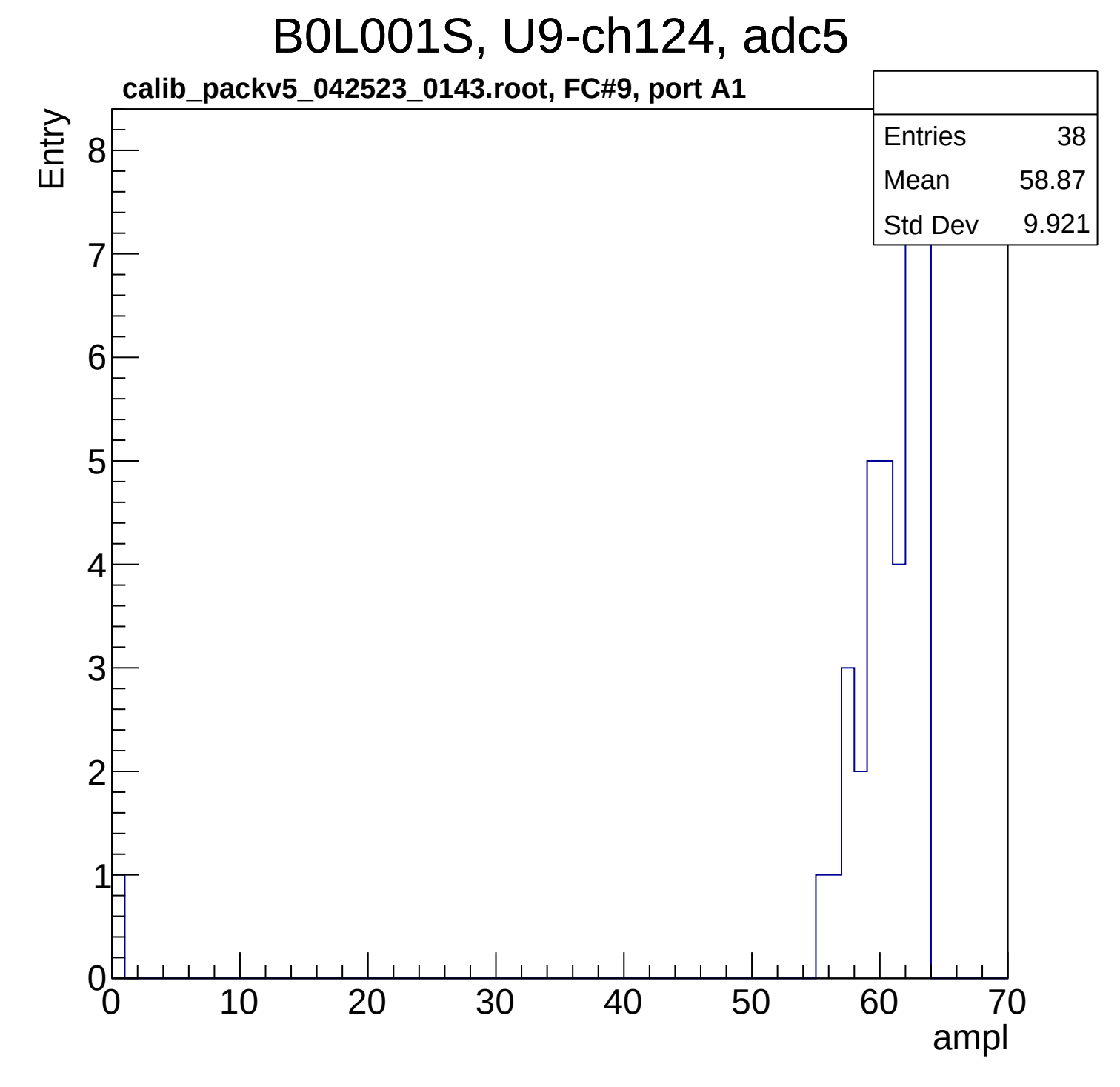
B0L001S, U9-ch124, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.87
Std Dev 9.921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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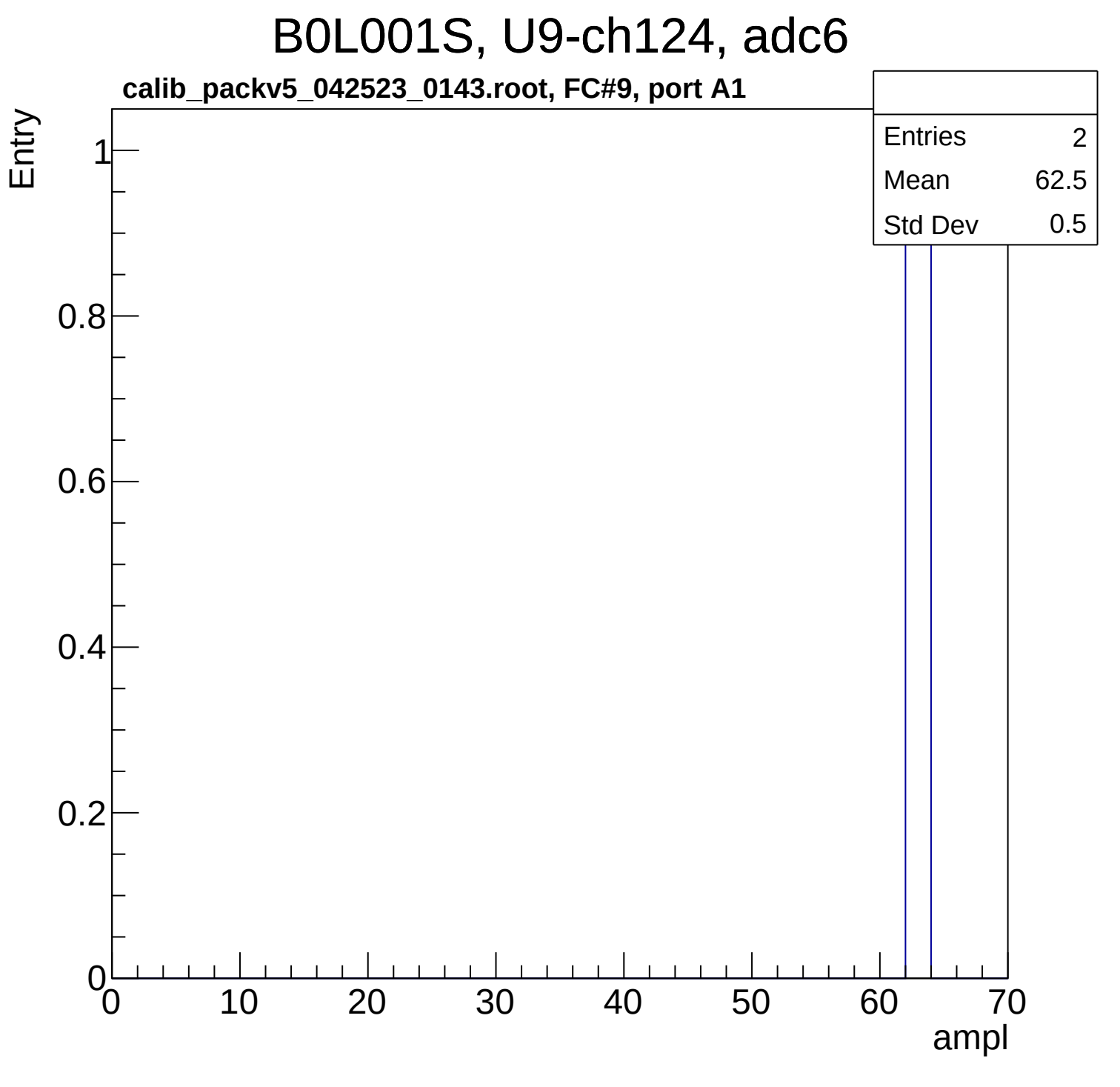
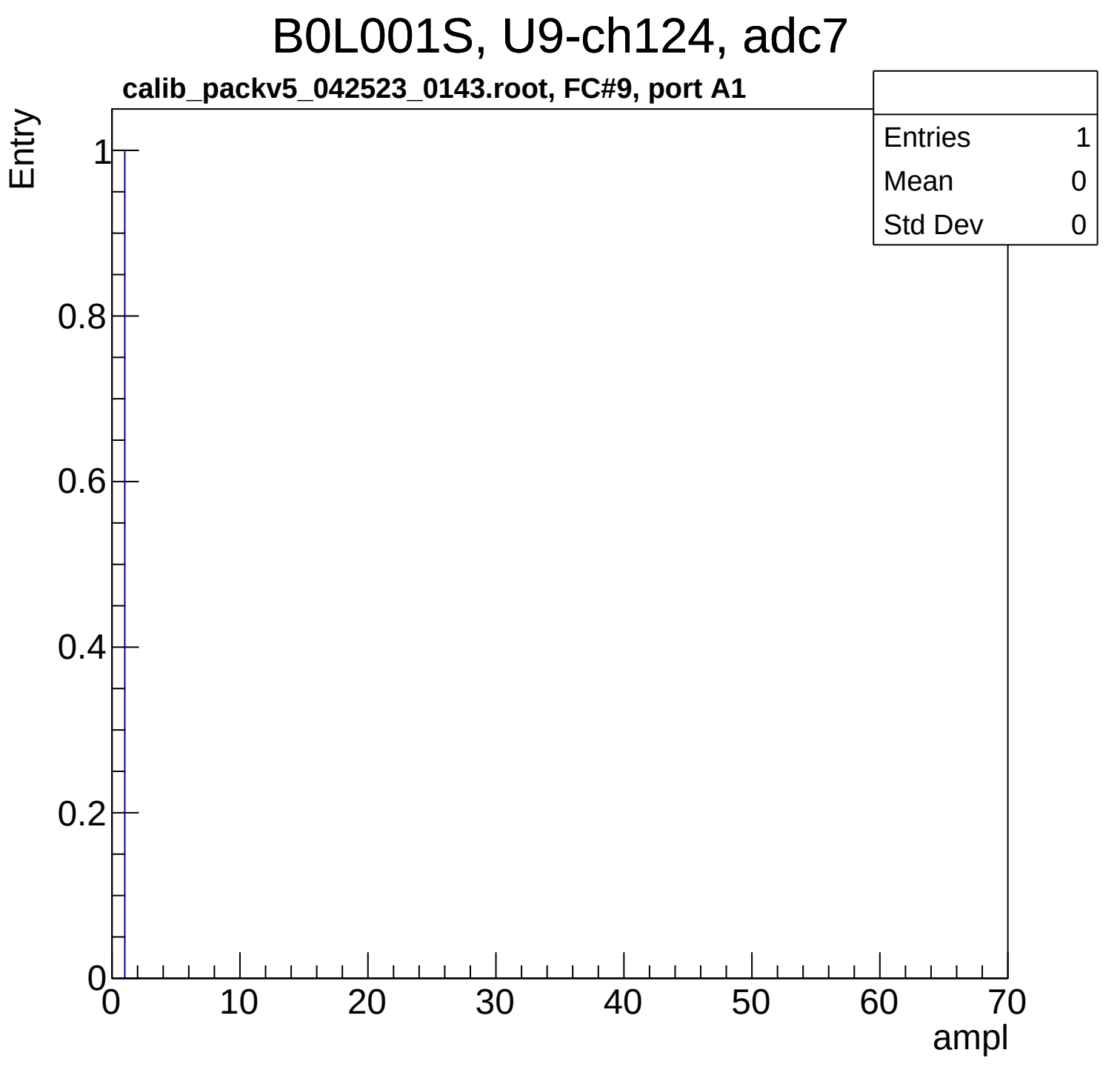
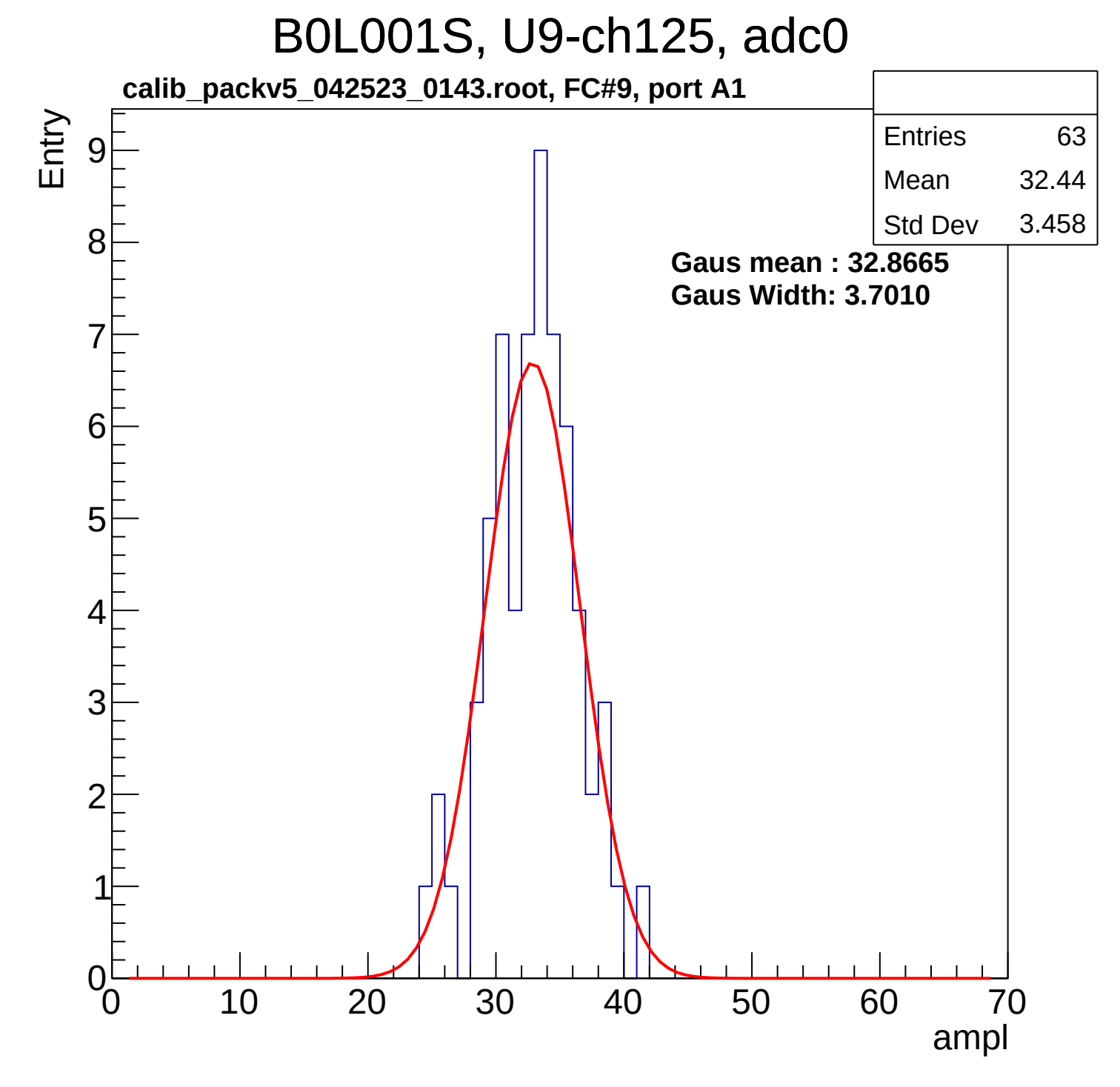
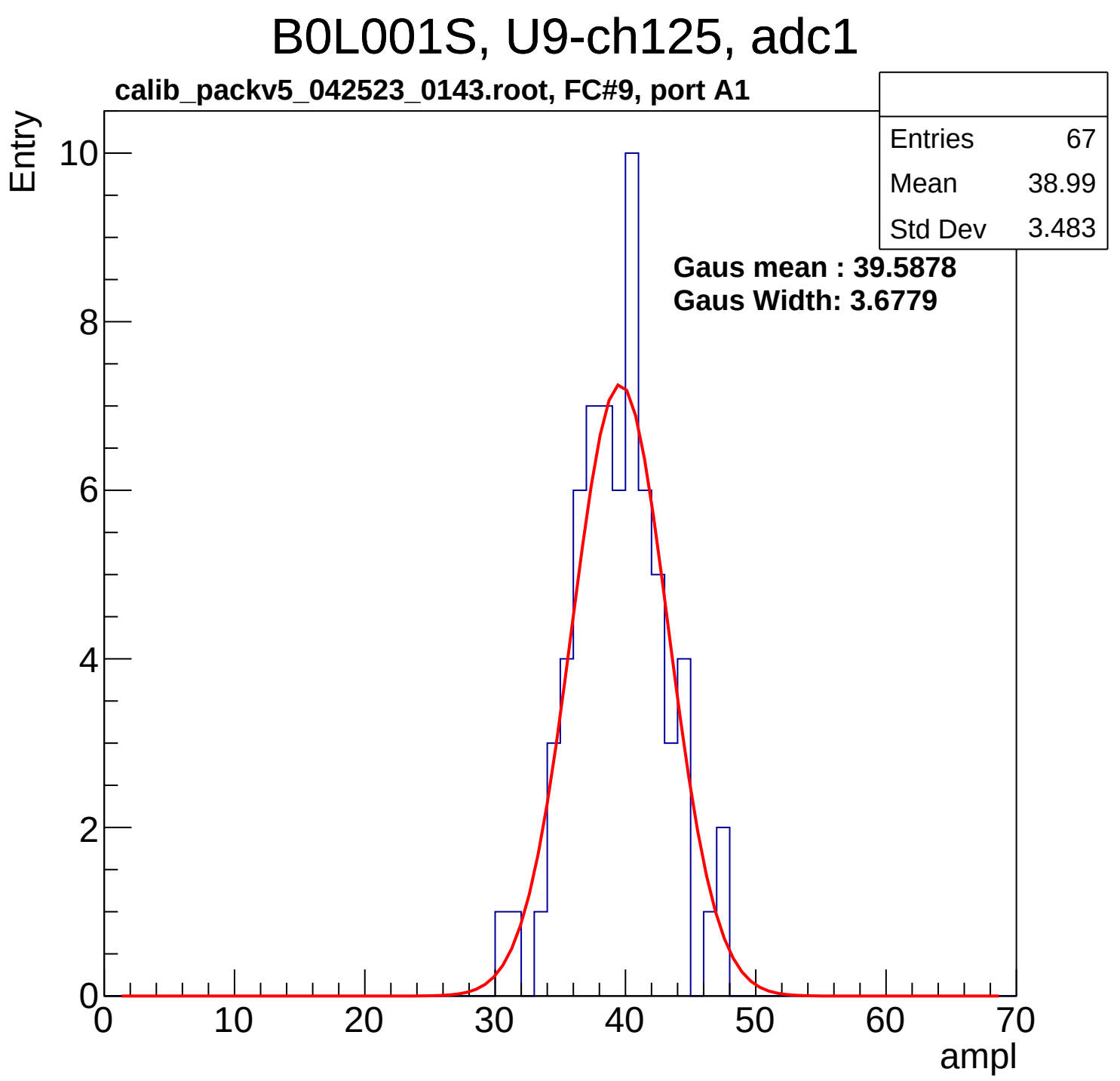

B0L001S, U9-ch124, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch124, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch125, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 32.44
Std Dev 3.458
Gaus mean : 32.8665
Gaus Width: 3.7010
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U9-ch125, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.99
Std Dev 3.483
Gaus mean : 39.5878
Gaus Width: 3.6779
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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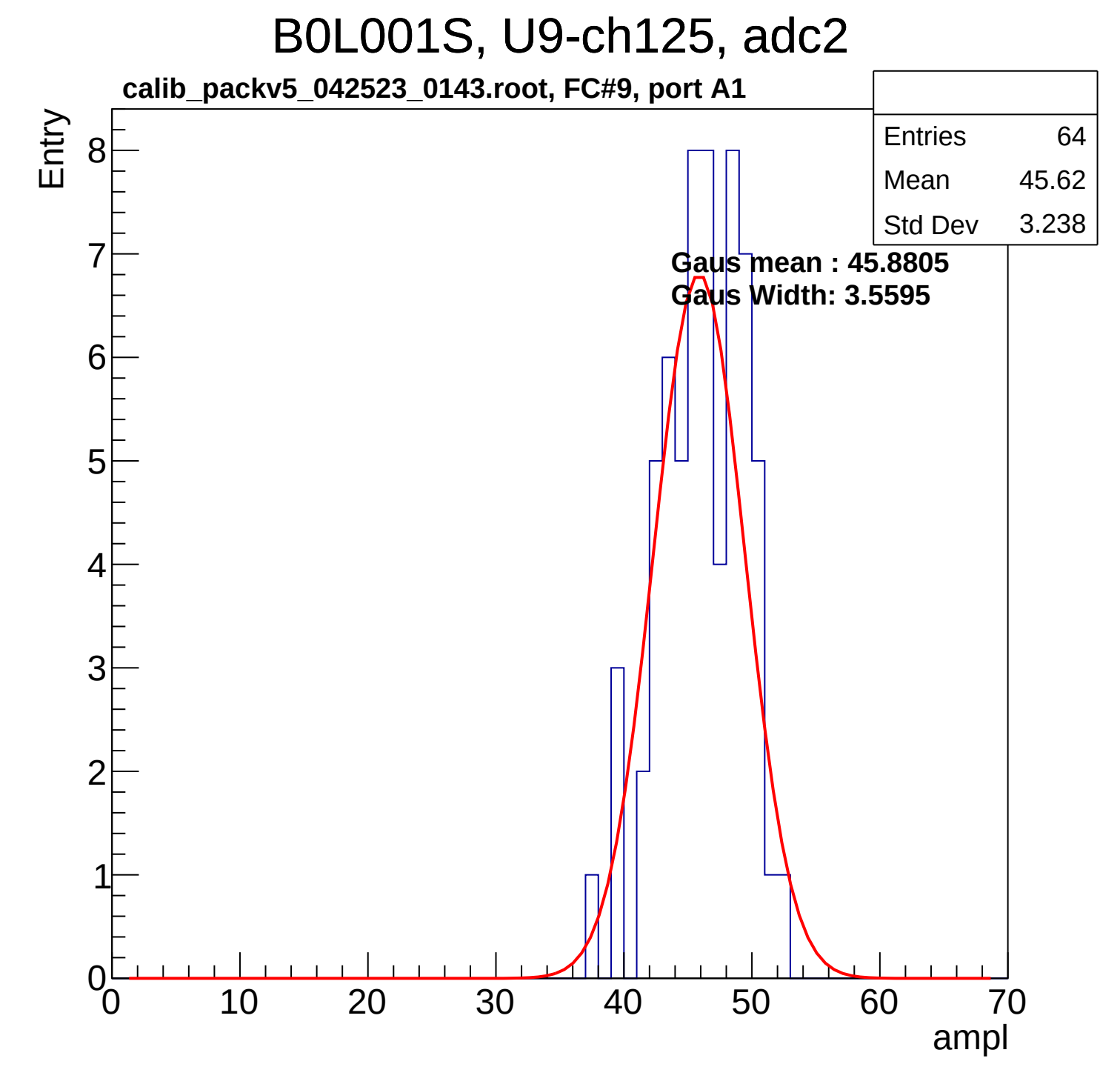
B0L001S, U9-ch125, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 45.62
Std Dev 3.238
Gaus mean : 45.8805
Gaus Width: 3.5595
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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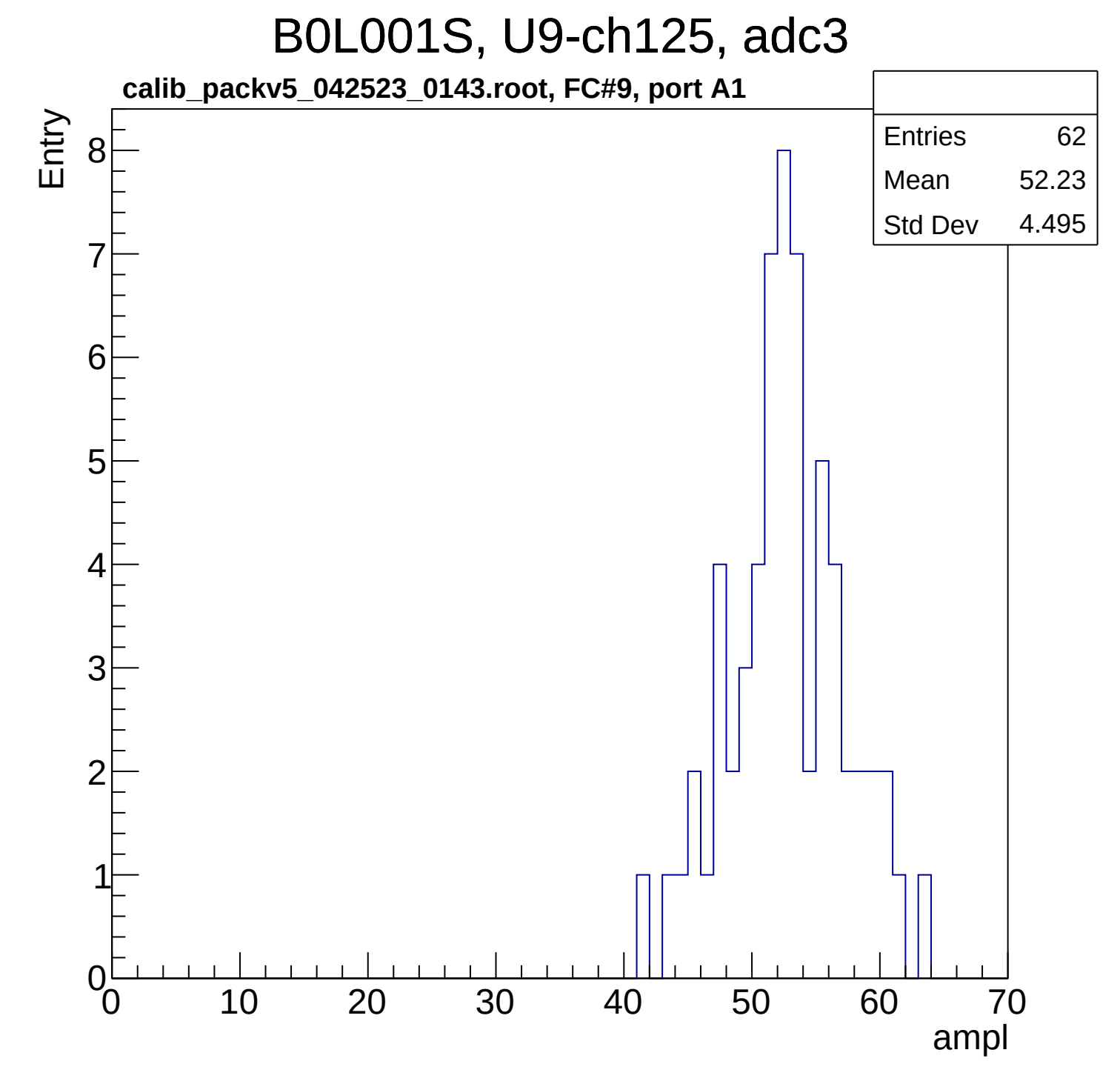
B0L001S, U9-ch125, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 52.23
Std Dev 4.495
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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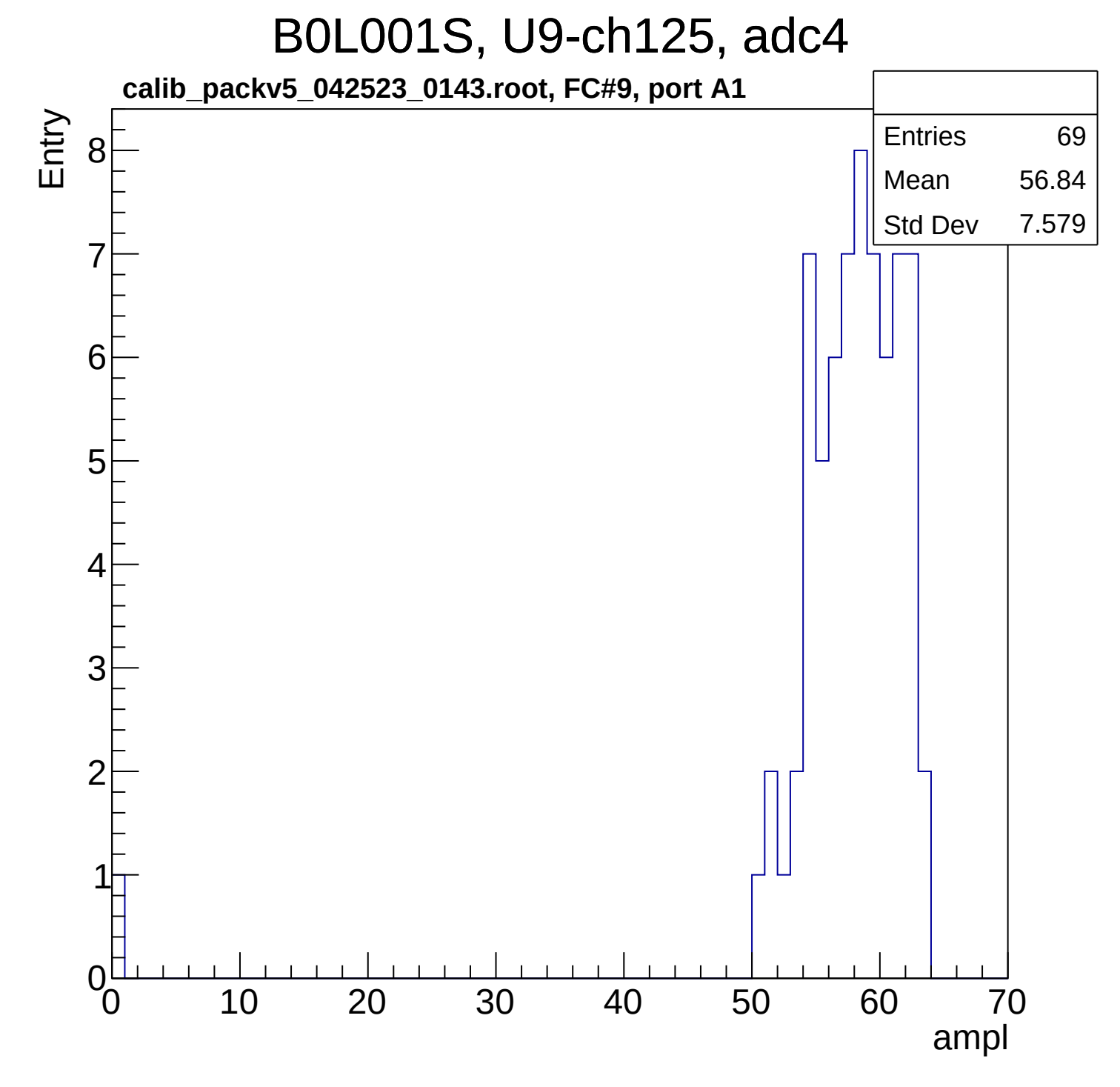
B0L001S, U9-ch125, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 56.84
Std Dev 7.579
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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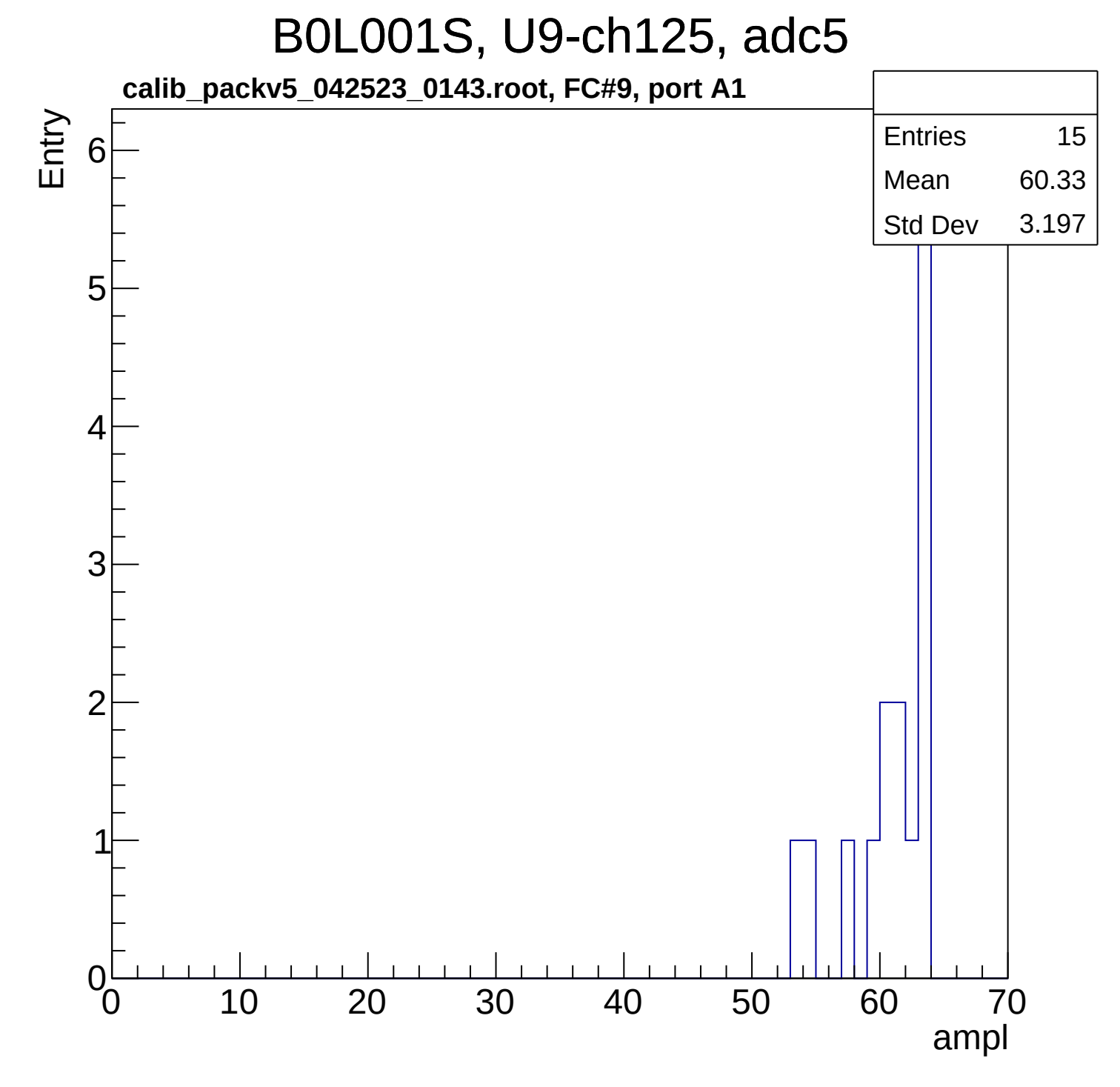
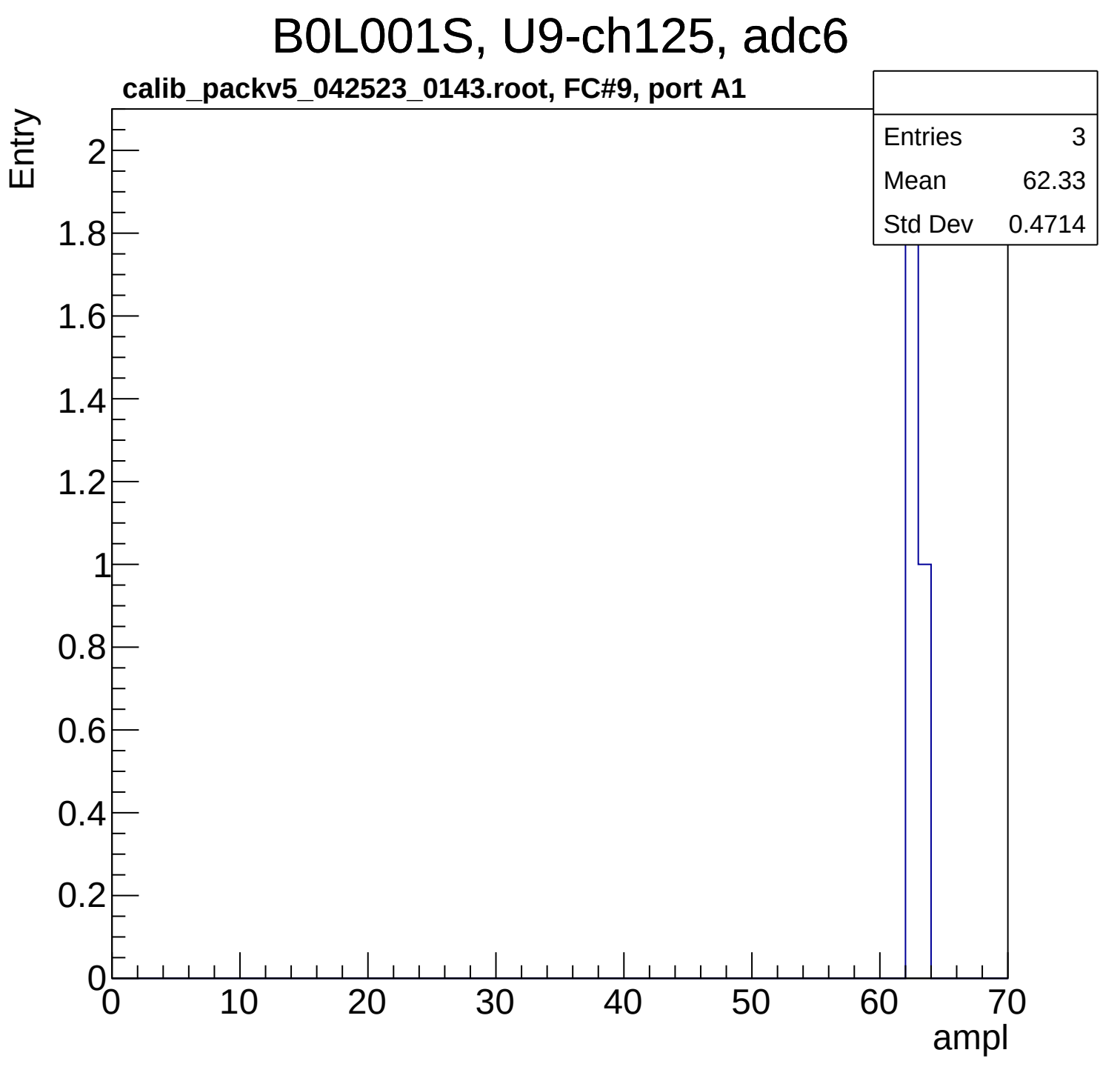
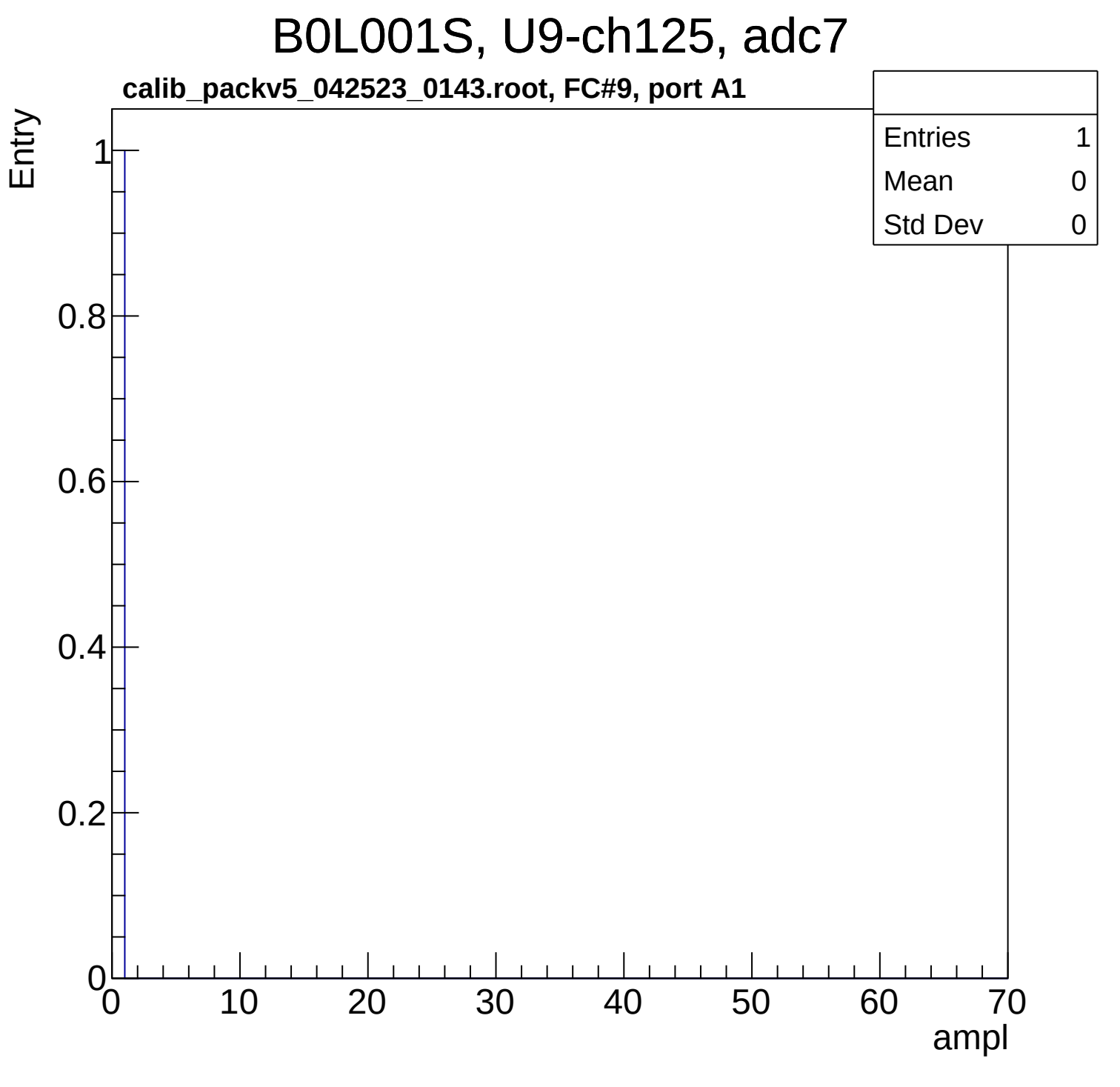
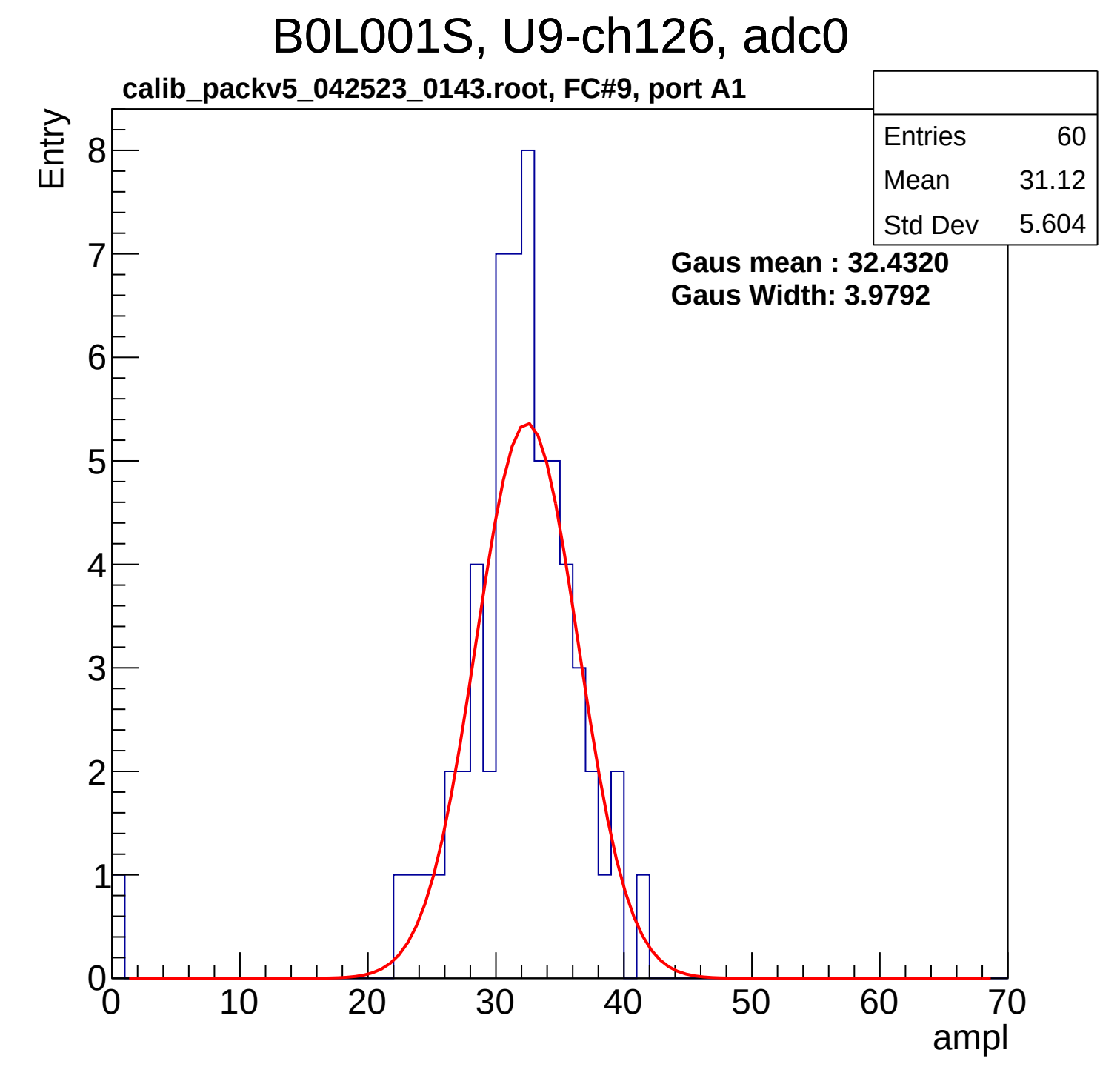

B0L001S, U9-ch125, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 15
Mean 60.33
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U9-ch125, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch125, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch126, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 31.12
Std Dev 5.604
Gaus mean : 32.4320
Gaus Width: 3.9792
Entry
ampl
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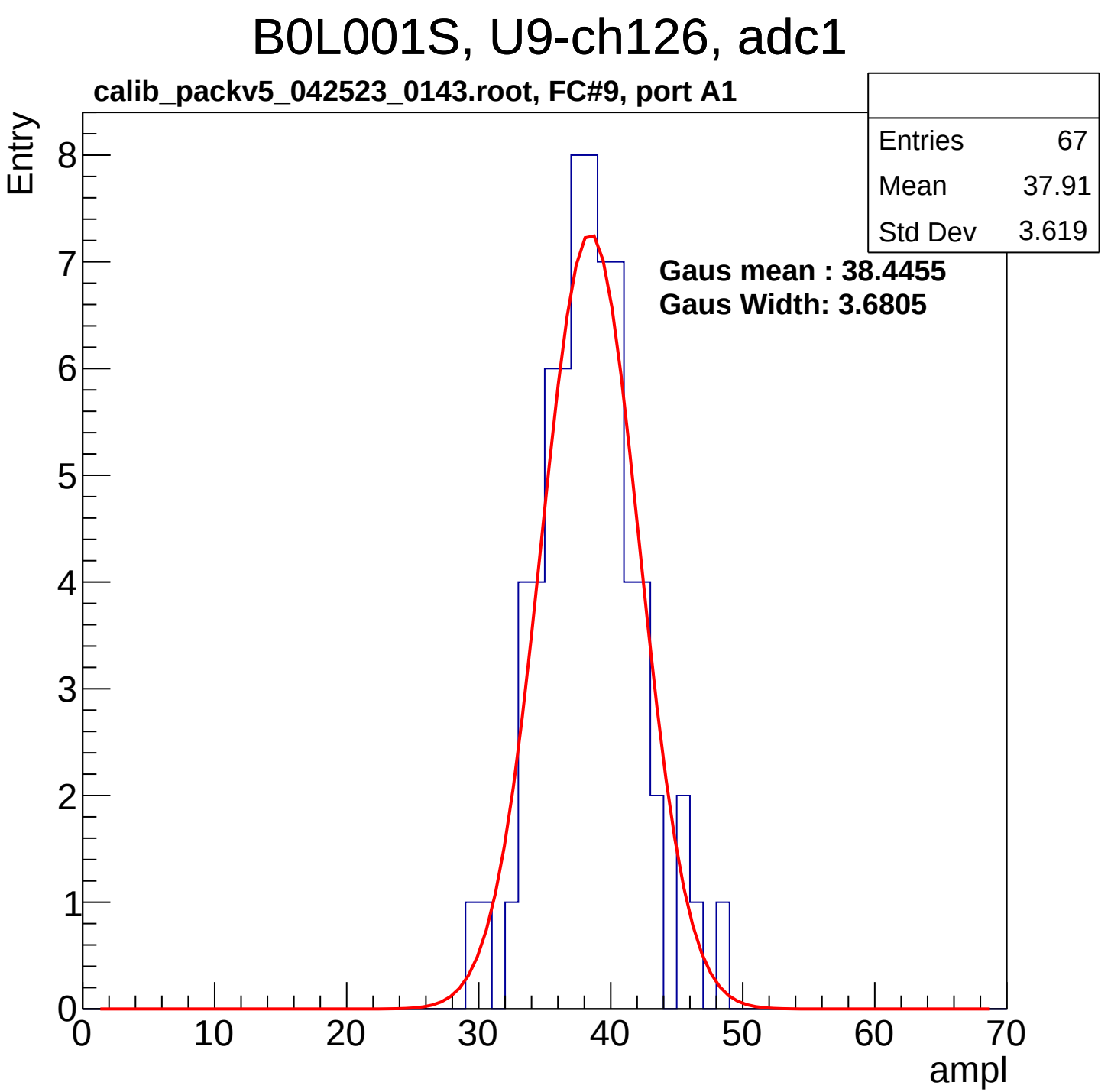
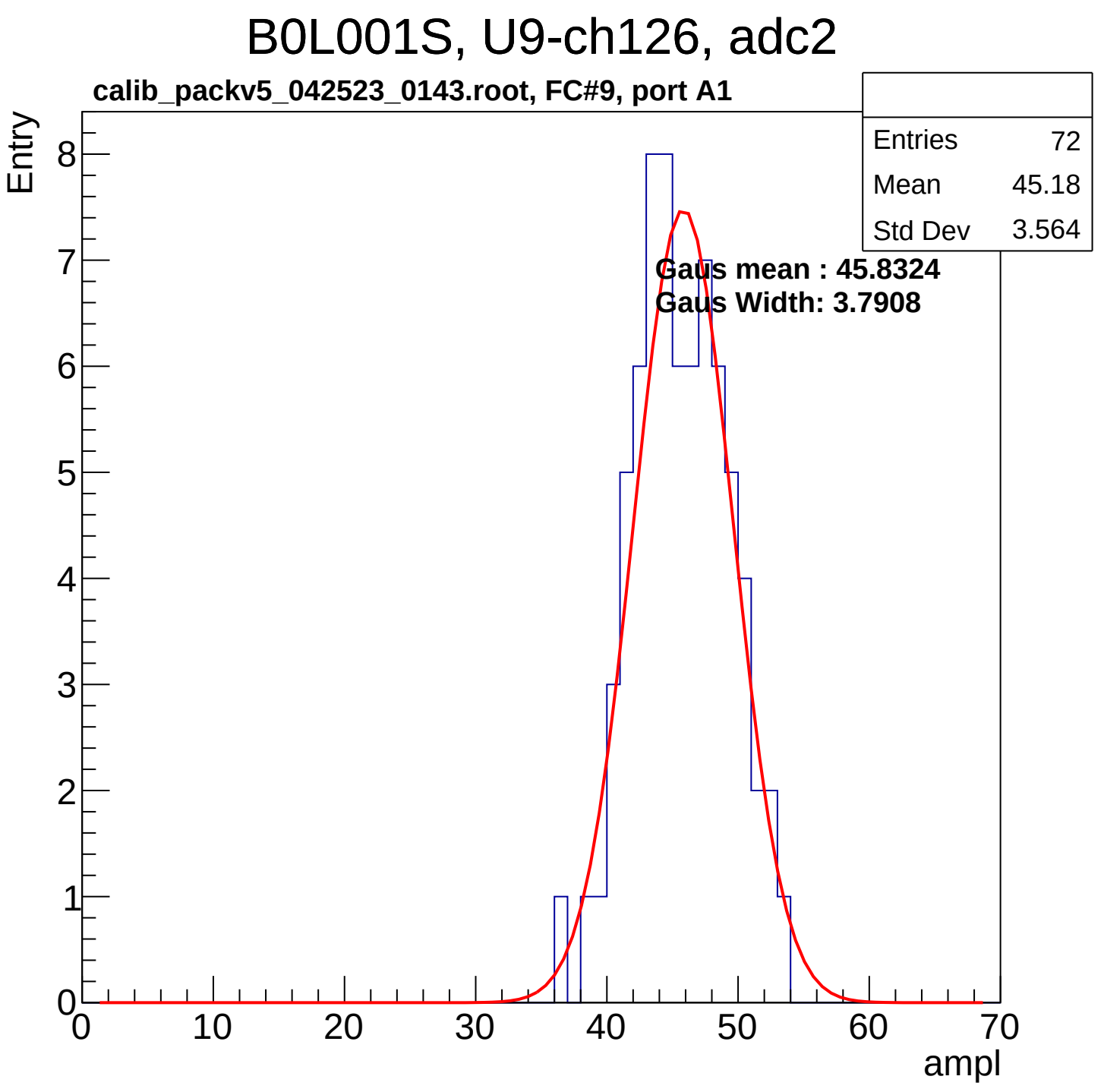
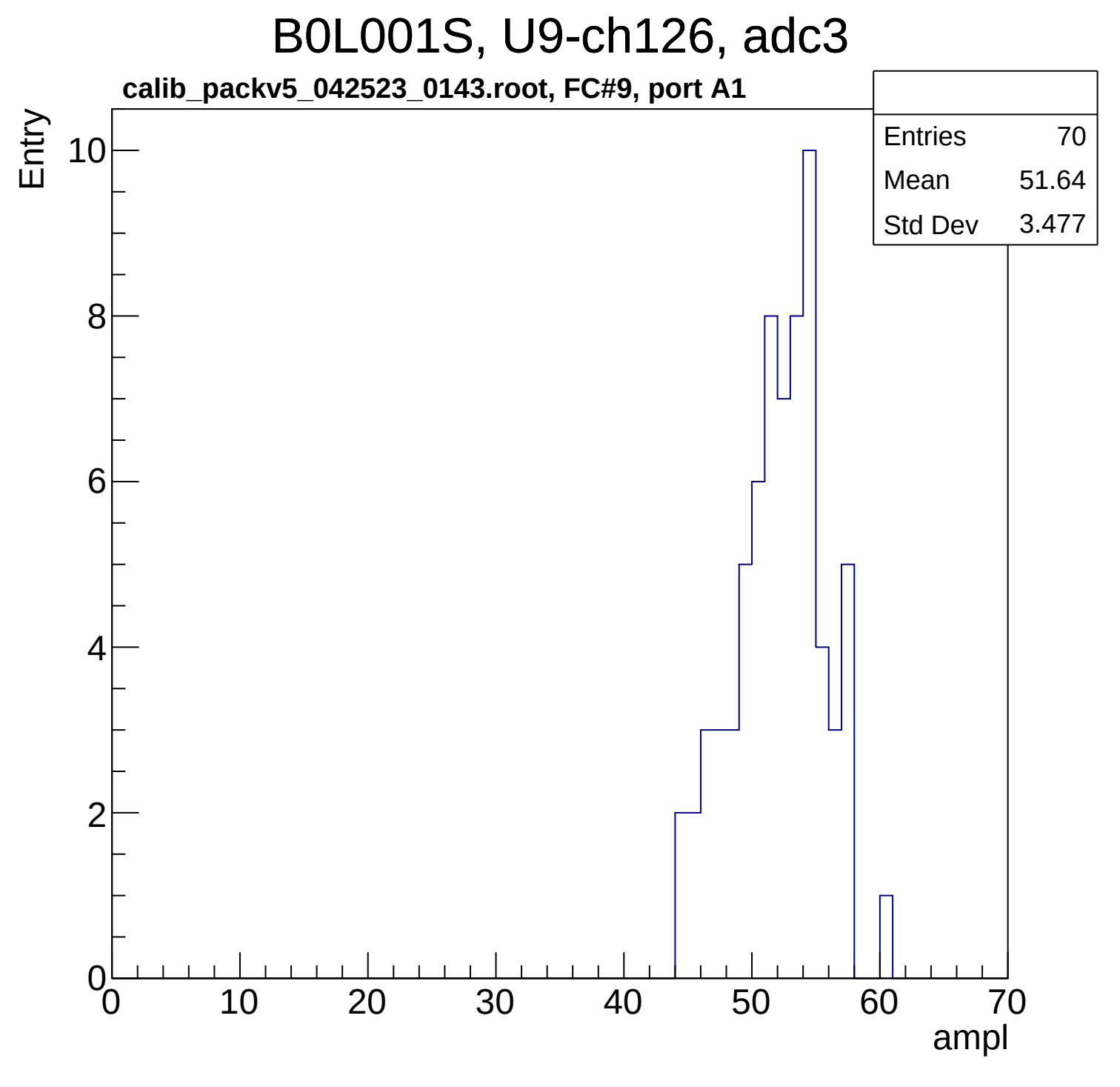
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch126, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.91
Std Dev 3.619
Gaus mean : 38.4455
Gaus Width: 3.6805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch126, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 45.18
Std Dev 3.564
Gaus mean : 45.8324
Gaus Width: 3.7908
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch126, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 51.64
Std Dev 3.477
Entry
0
2
4
6
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10
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10
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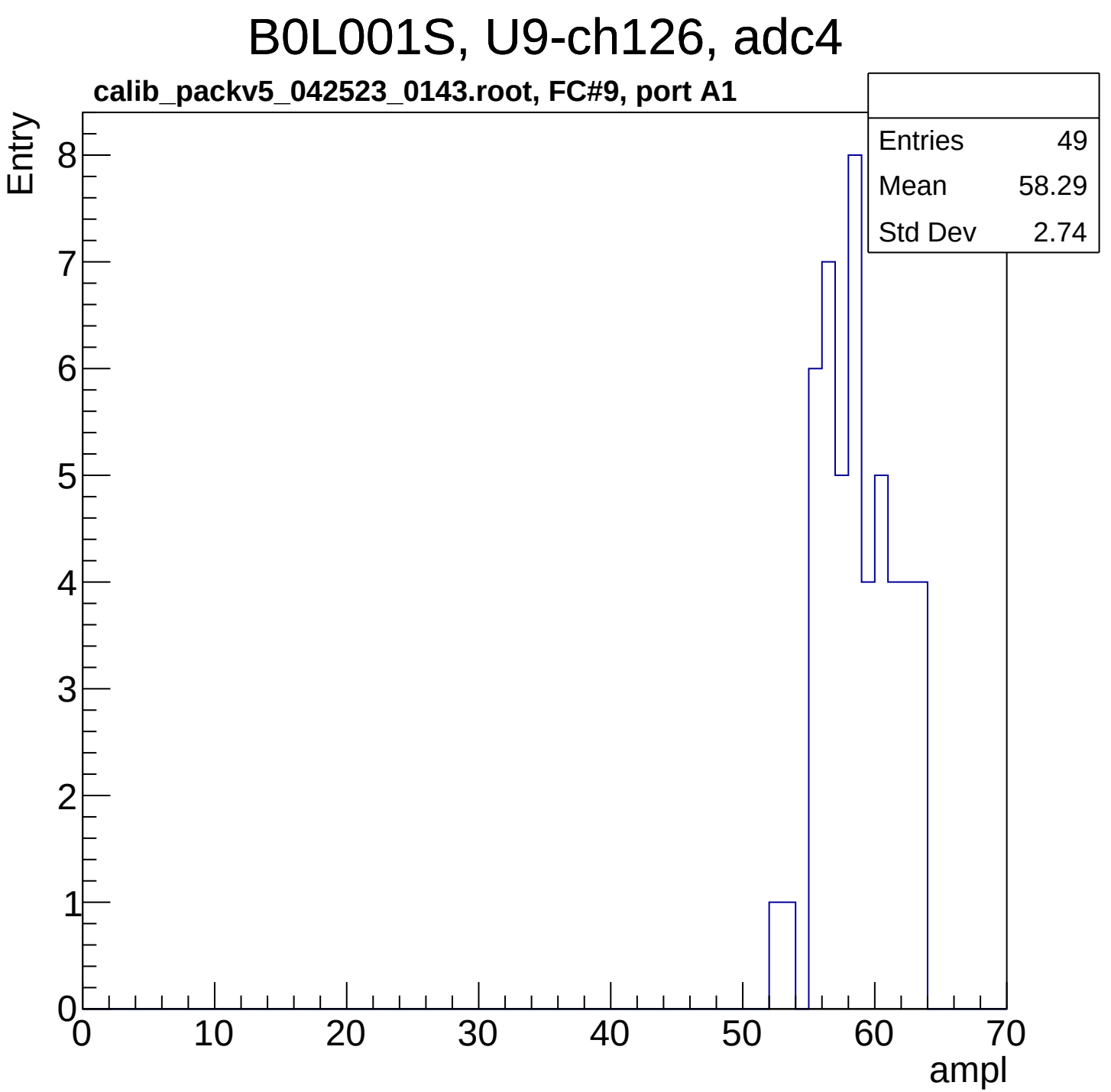
50
60
70
ampl


B0L001S, U9-ch126, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.29
Std Dev 2.74
Entry
ampl
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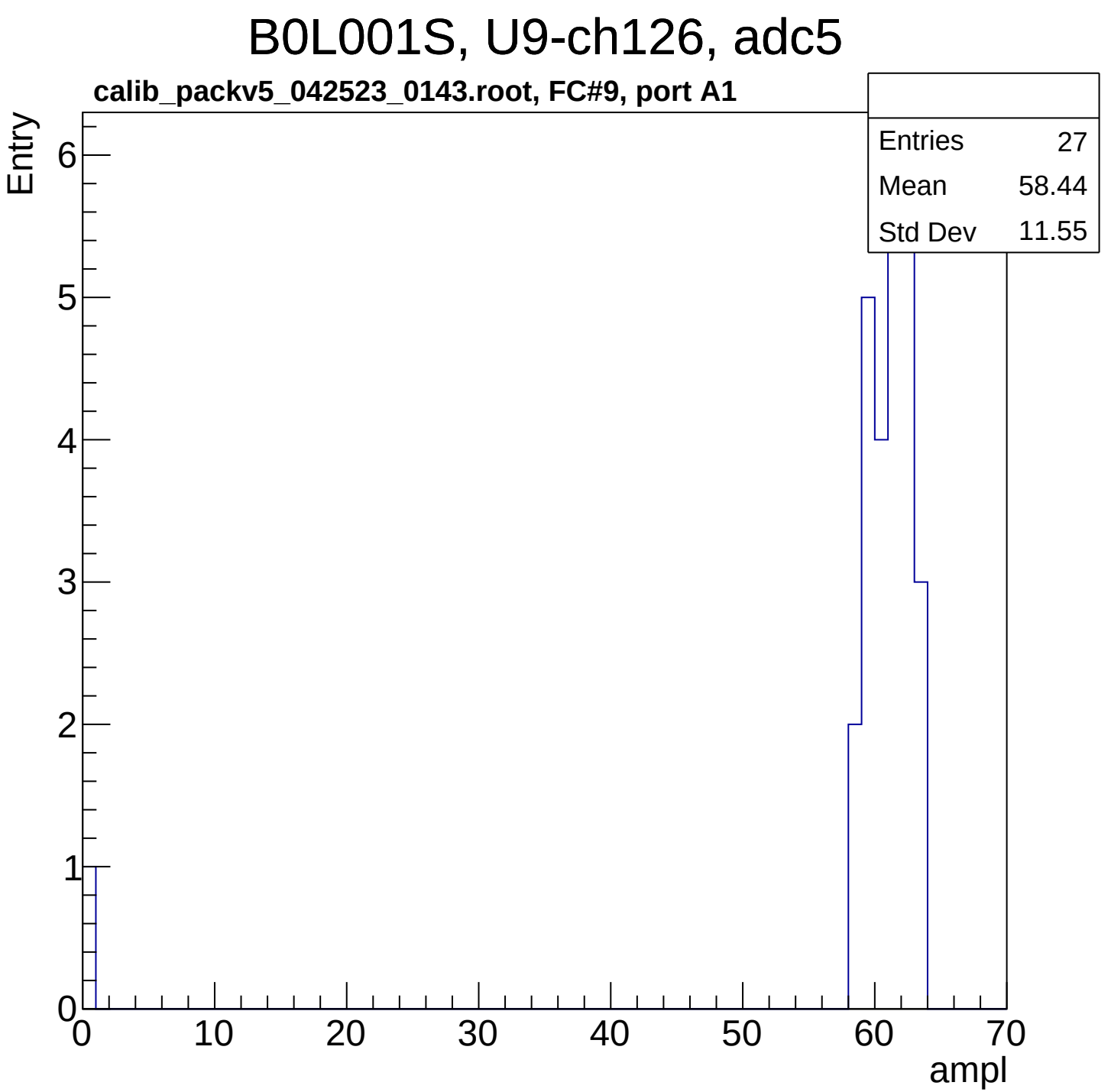
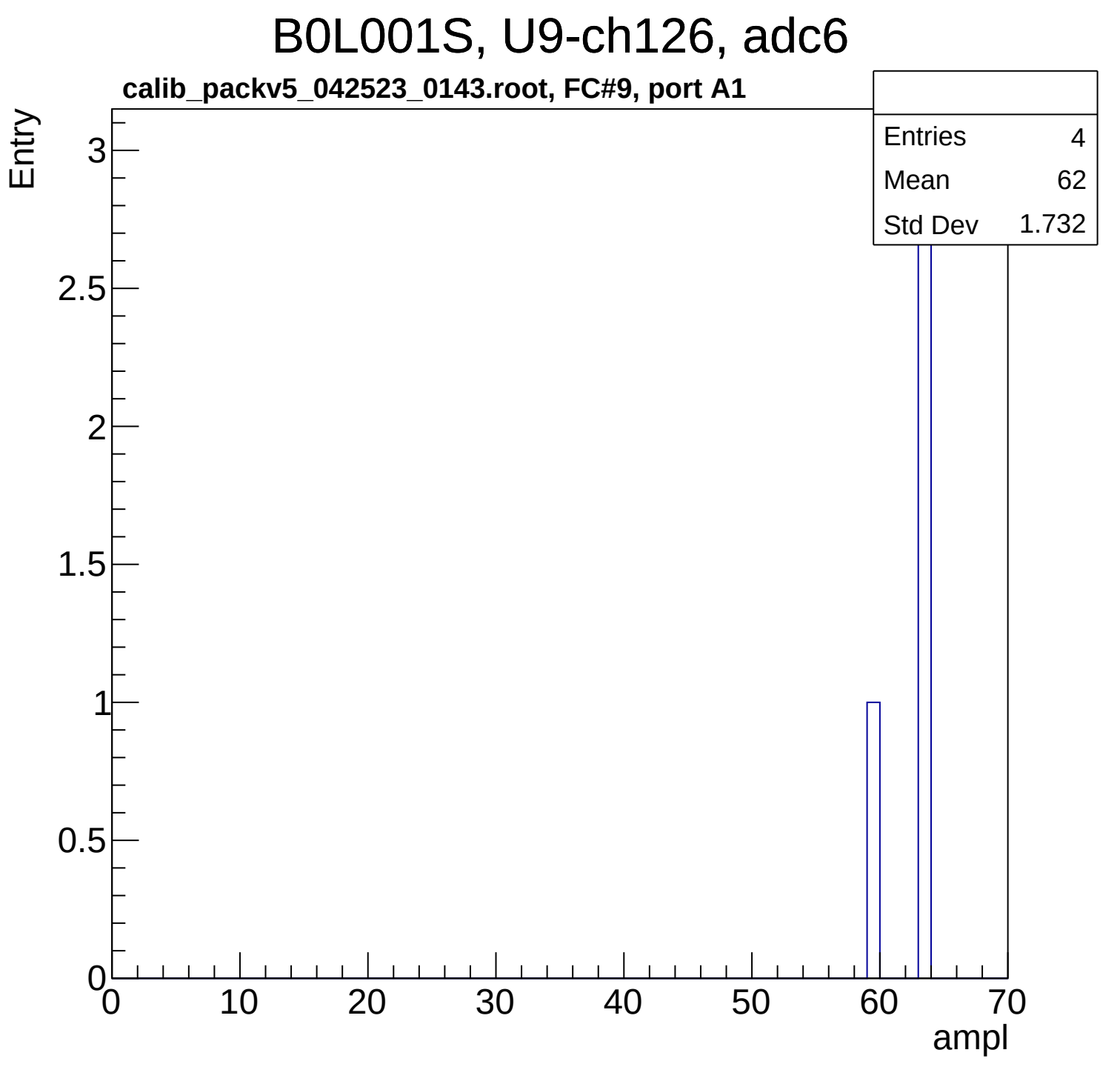
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U9-ch126, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.44
Std Dev 11.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch126, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1.732
Entry
ampl
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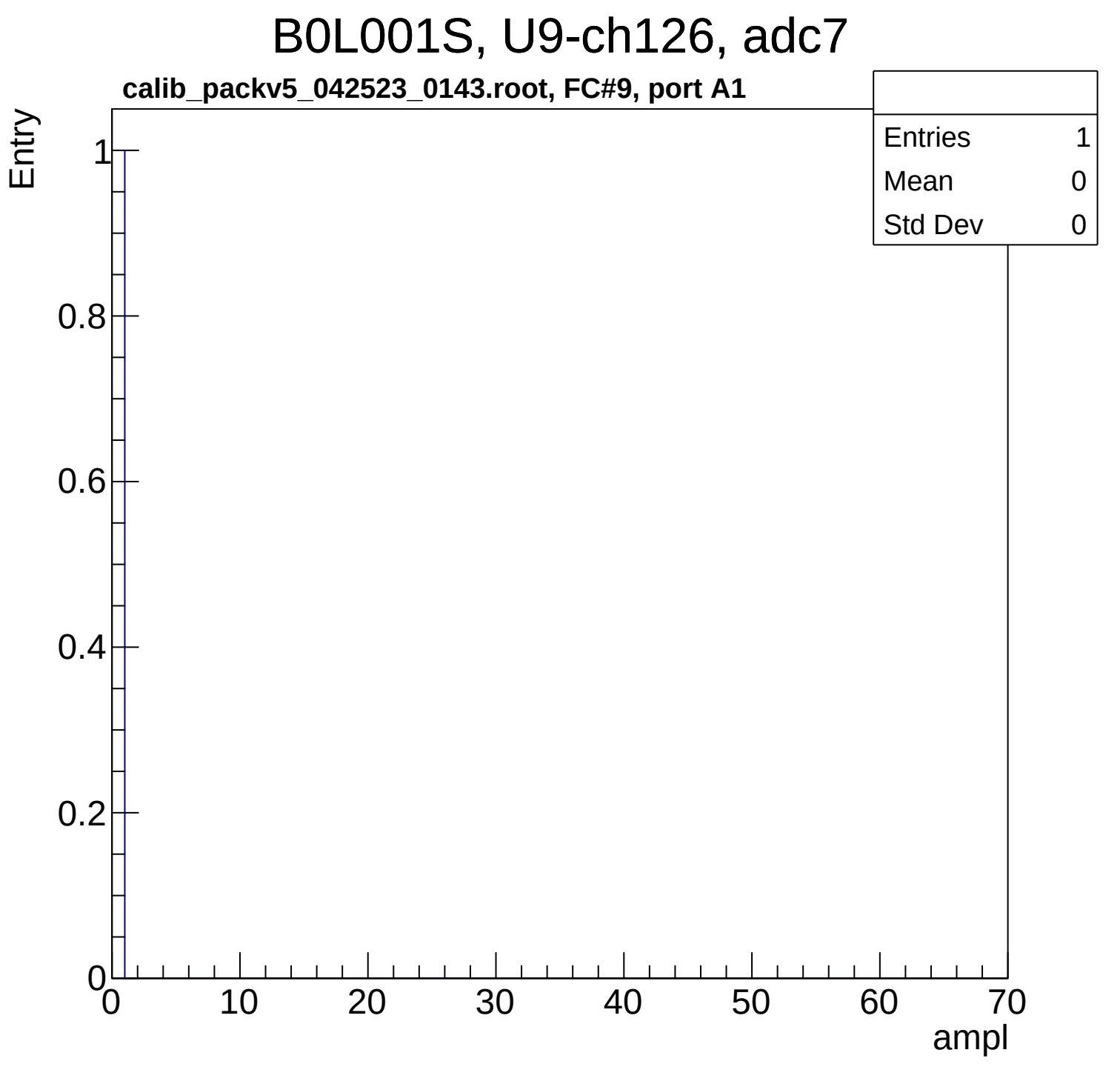
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U9-ch126, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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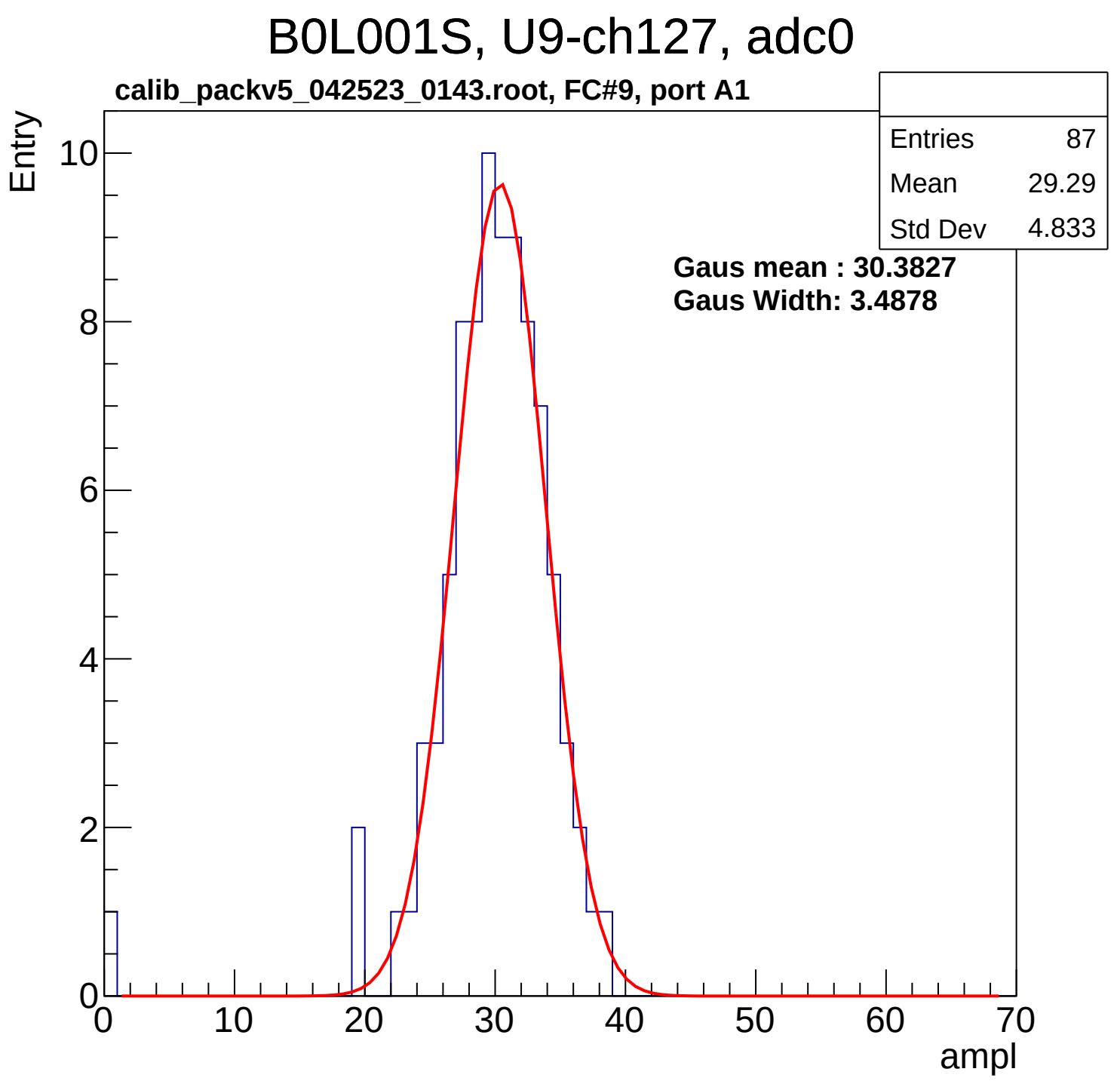
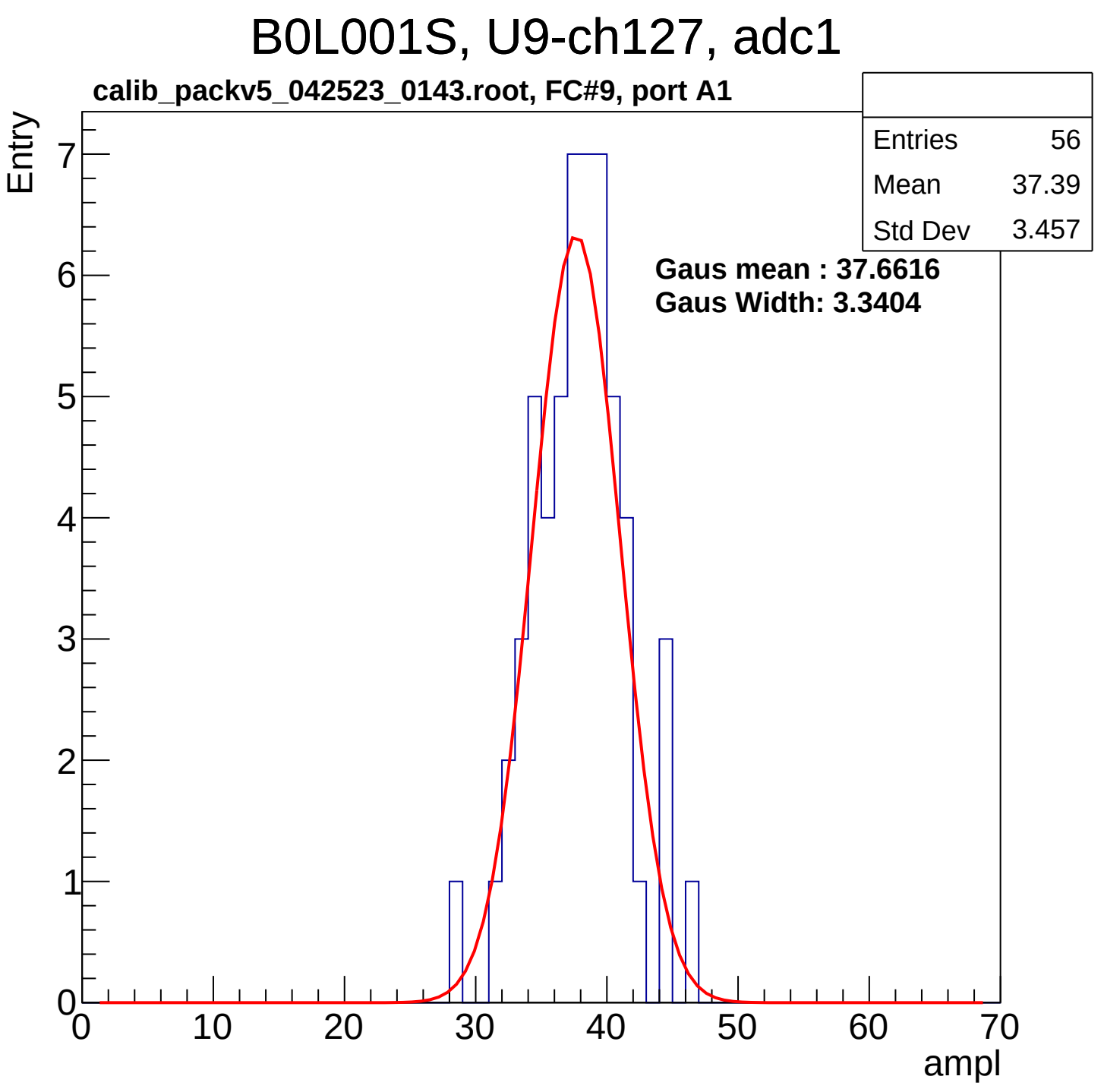
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch127, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 29.29
Std Dev 4.833
Gaus mean : 30.3827
Gaus Width: 3.4878
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U9-ch127, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 37.39
Std Dev 3.457
Gaus mean : 37.6616
Gaus Width: 3.3404
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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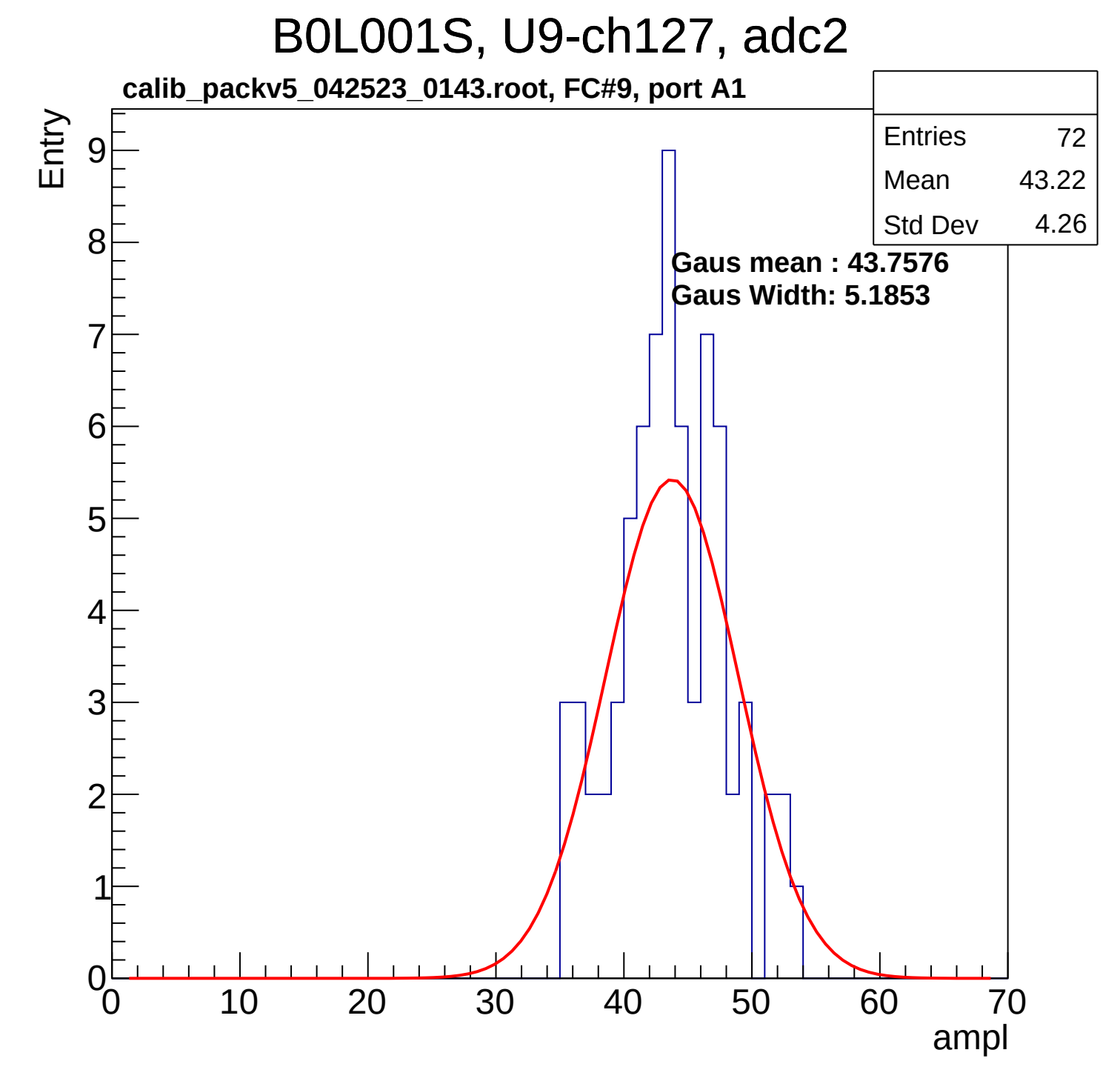
B0L001S, U9-ch127, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 43.22
Std Dev 4.26
Gaus mean : 43.7576
Gaus Width: 5.1853
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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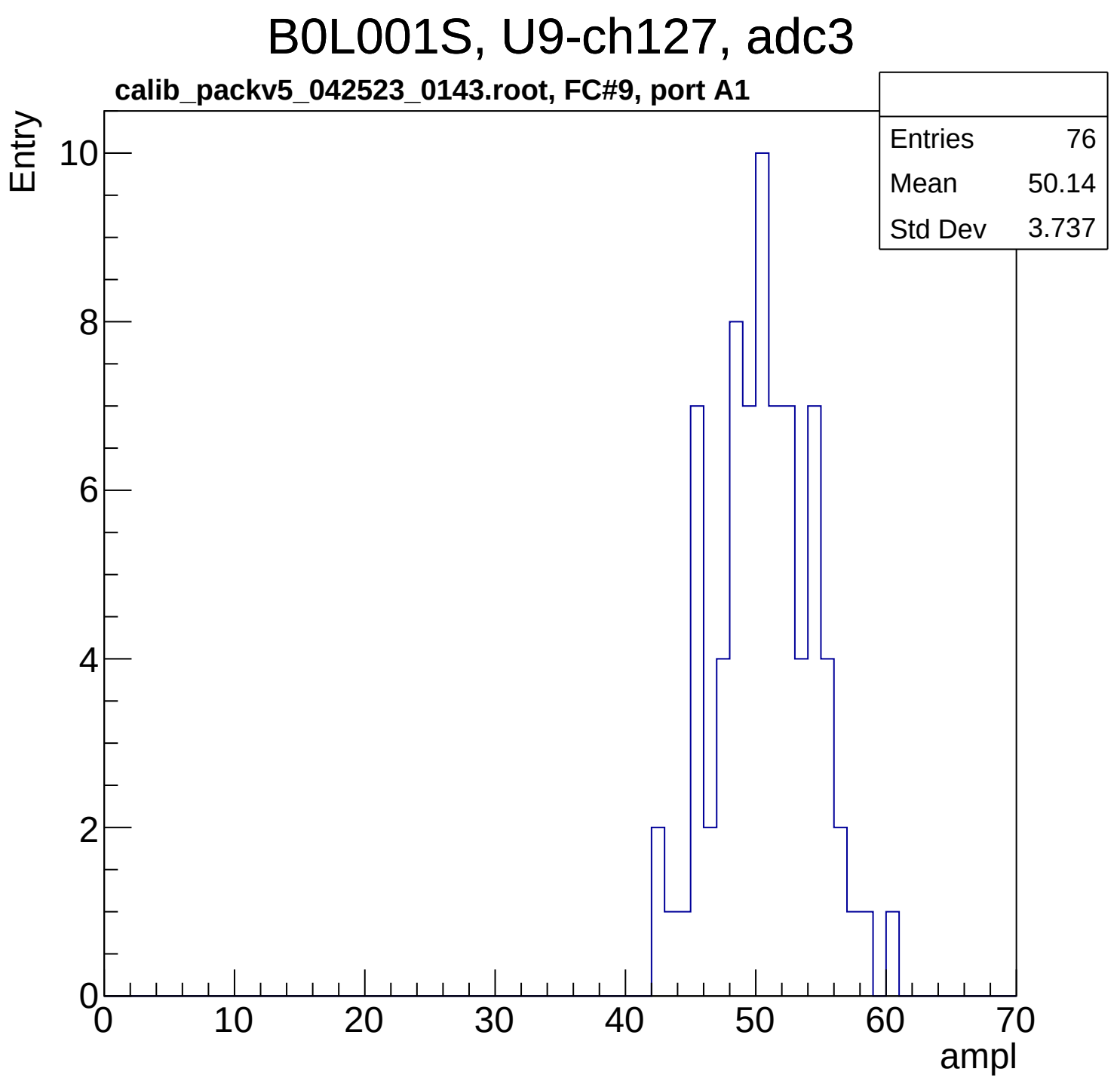
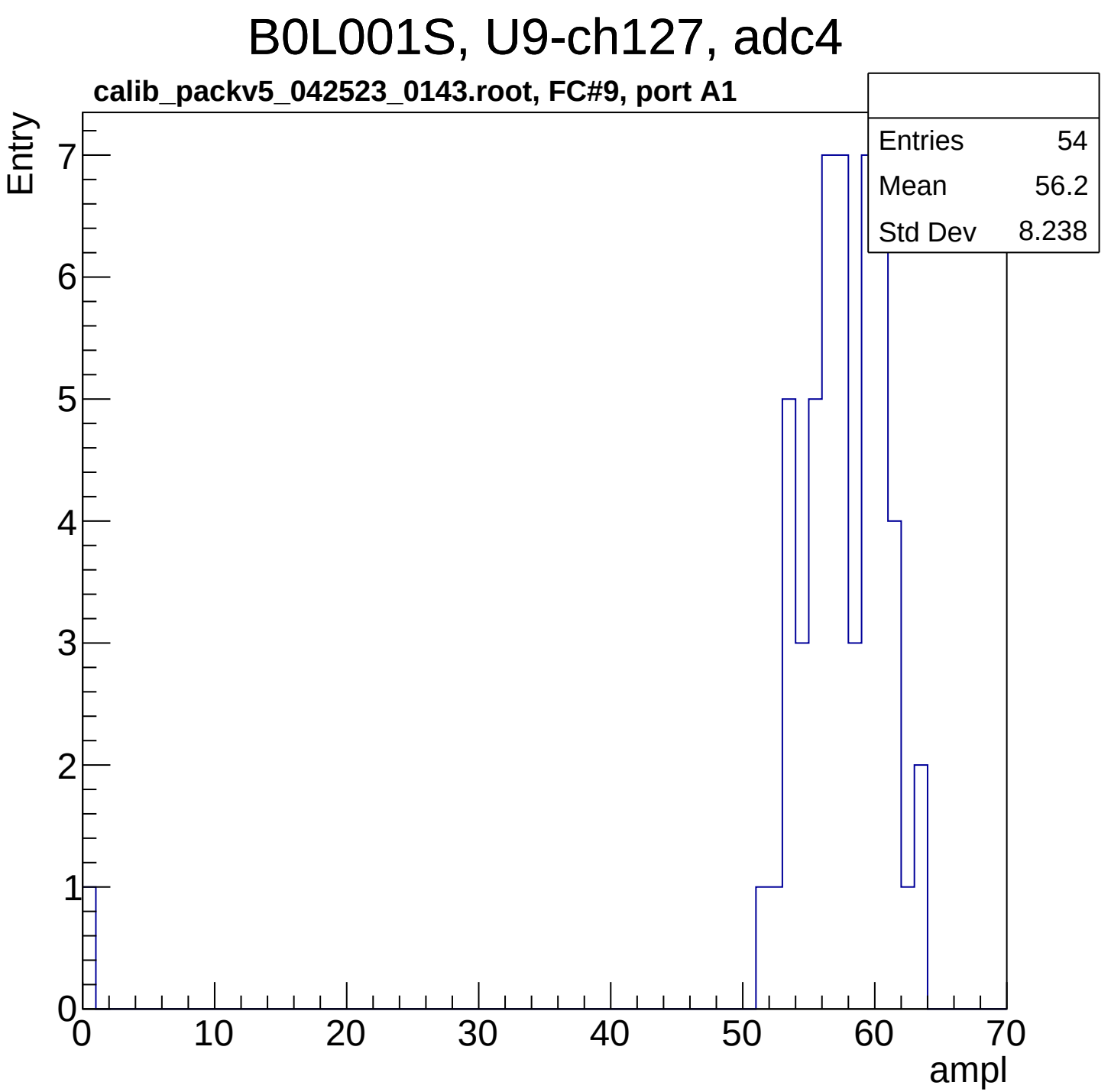
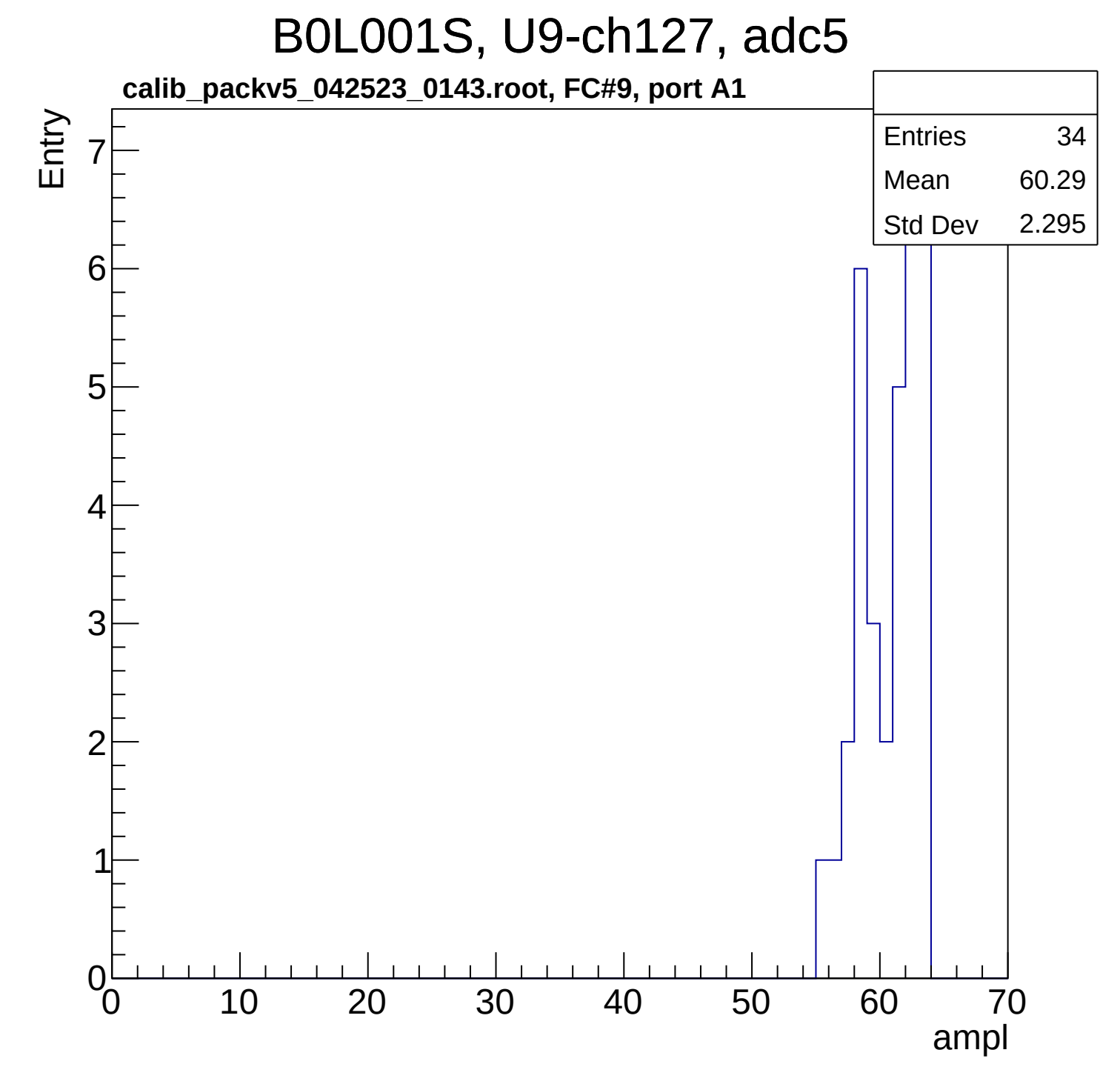
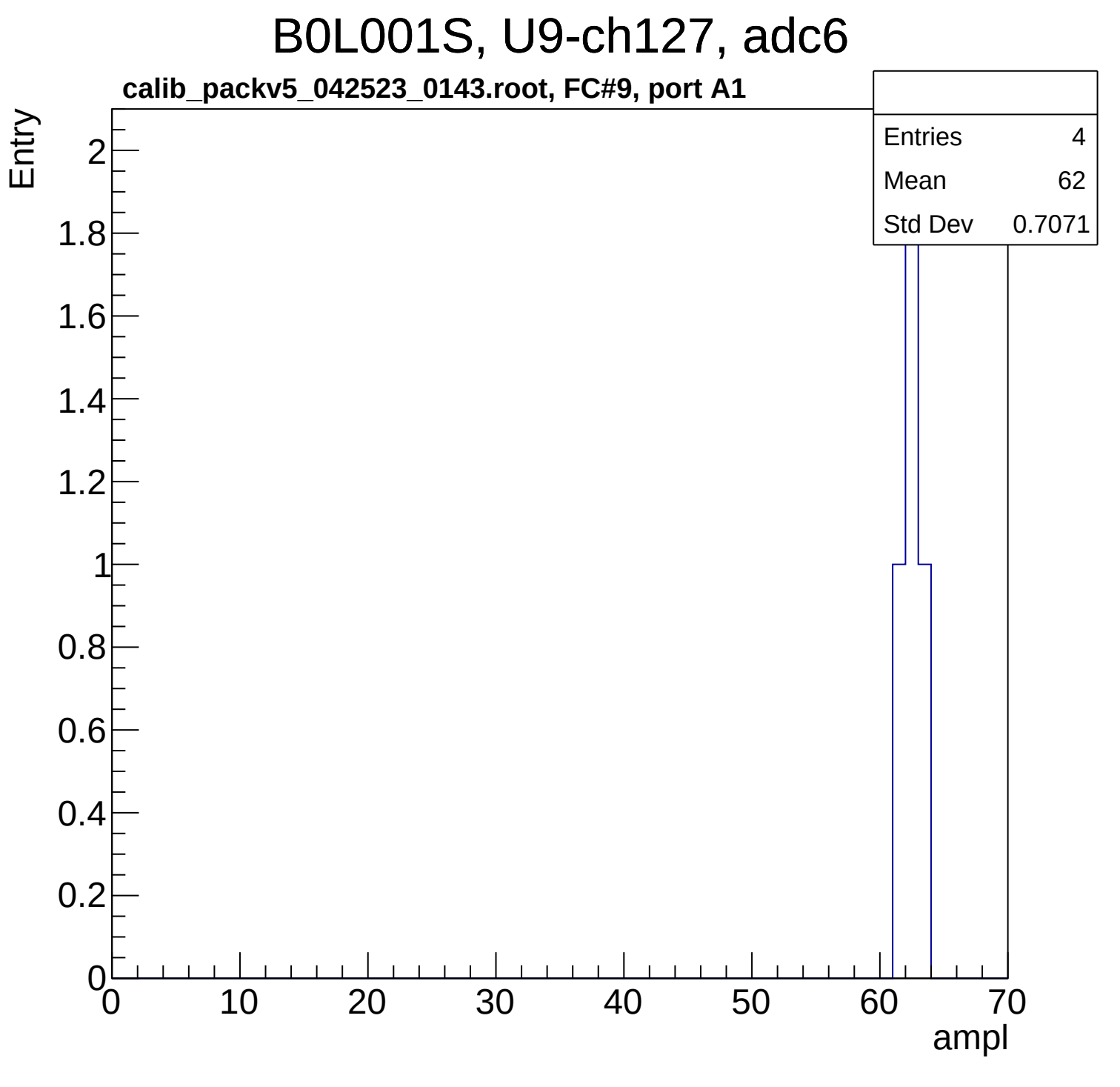
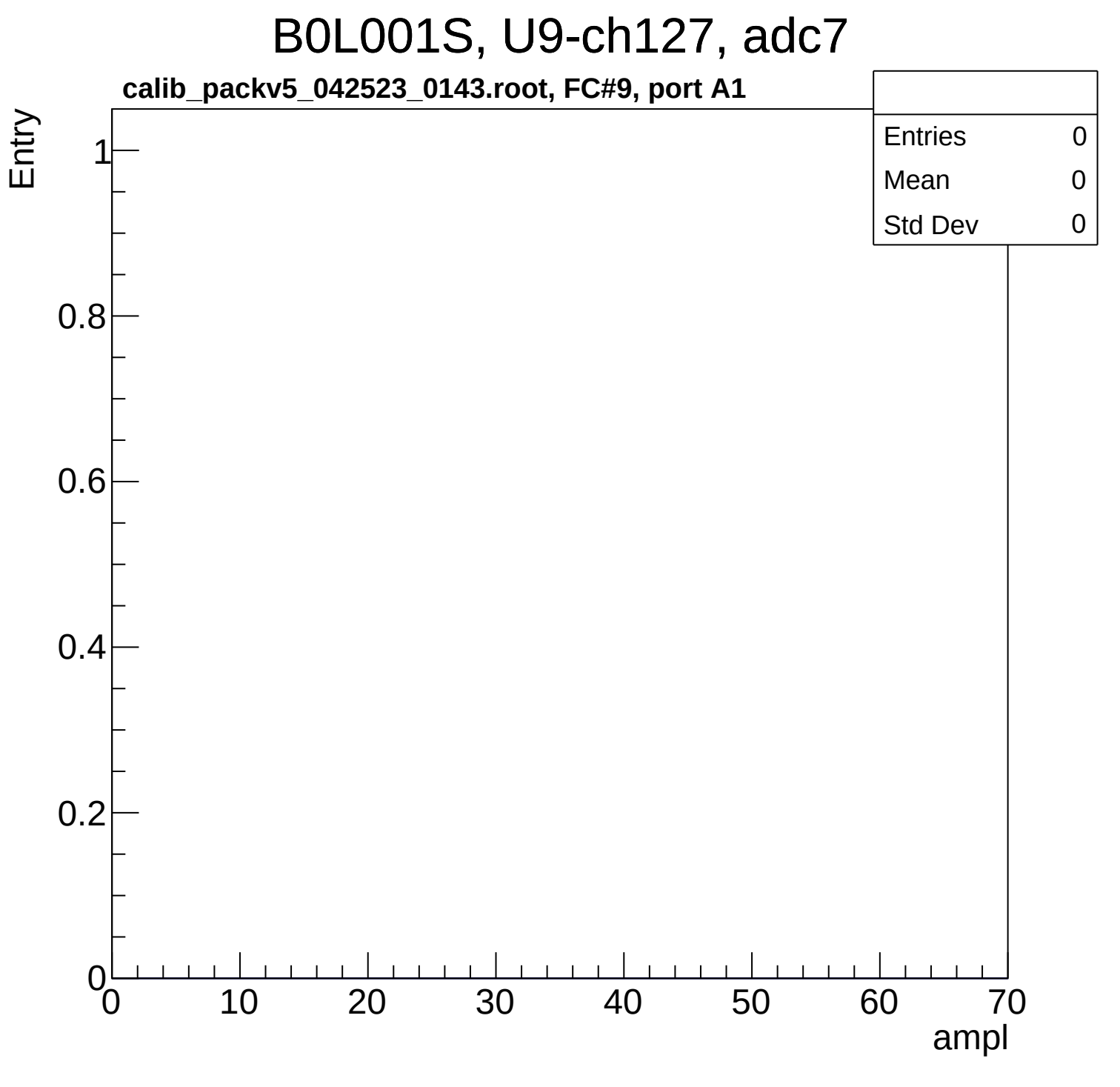
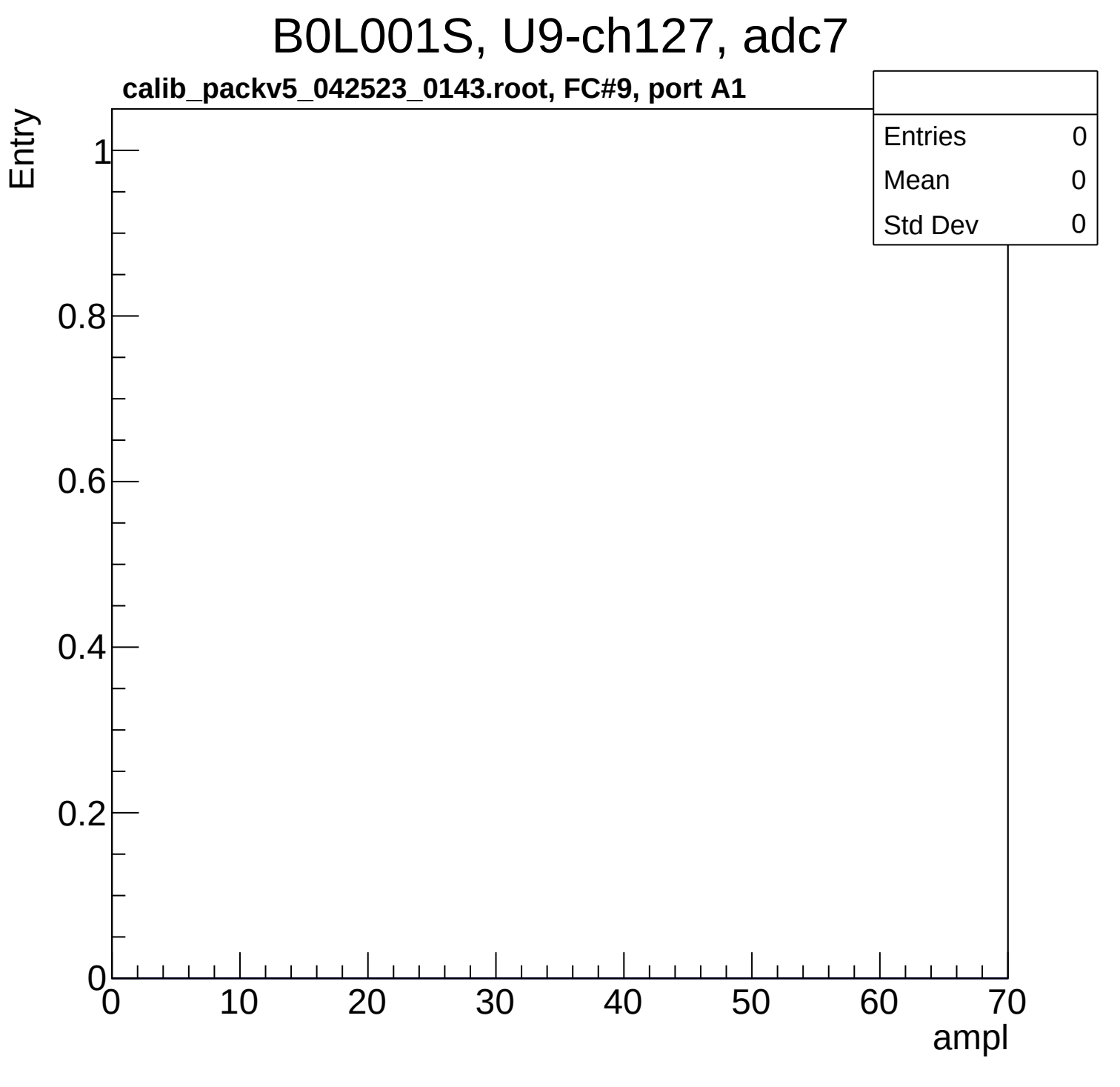

B0L001S, U9-ch127, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 50.14
Std Dev 3.737
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch127, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.2
Std Dev 8.238
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch127, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 60.29
Std Dev 2.295
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U9-ch127, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U9-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U9-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl