

B1L102S, U9-ch0

calib_packv5_042523_0143.root, FC#11, port A2

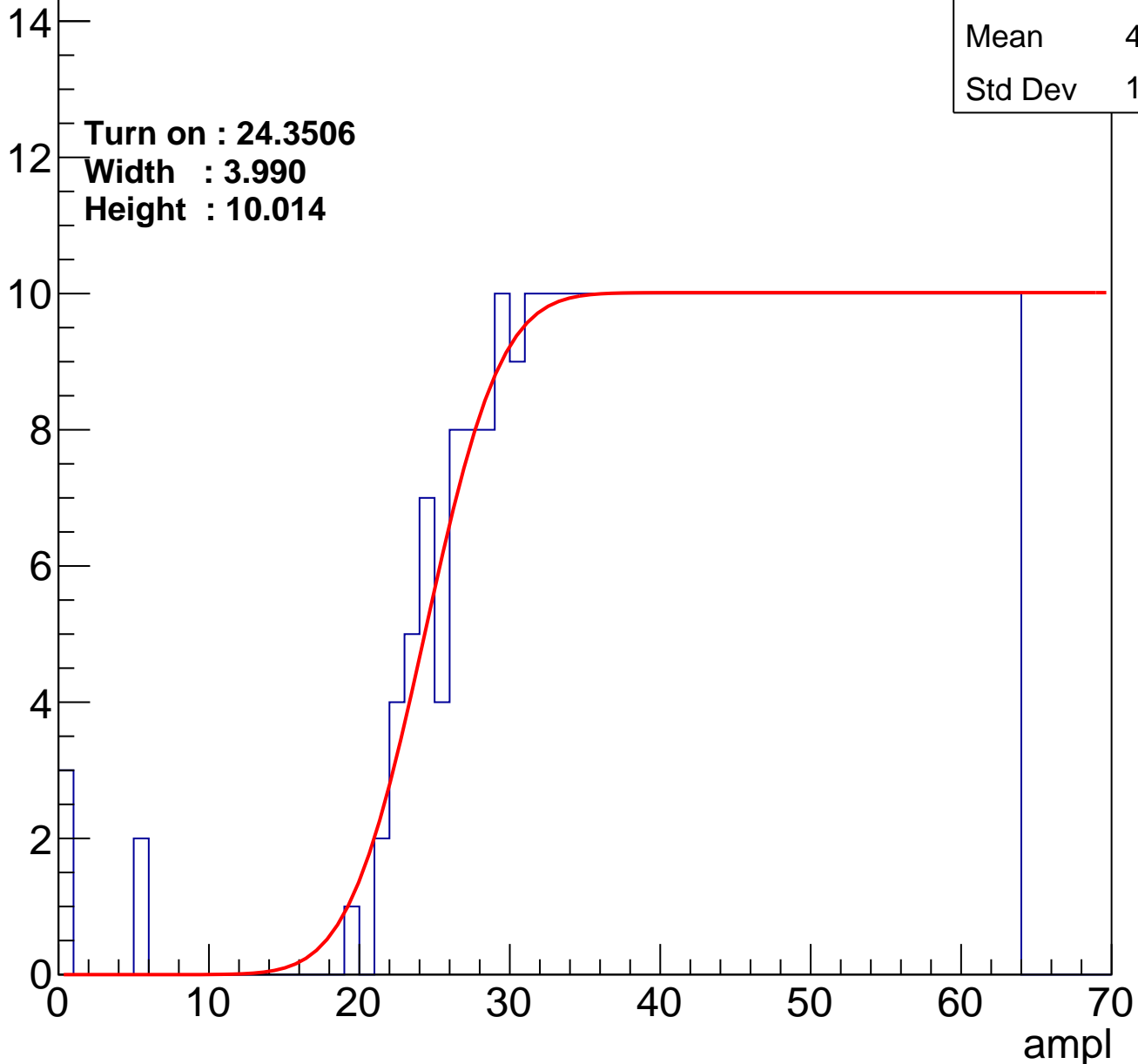
Entries	401
Mean	43.04
Std Dev	12.48

Turn on : 24.3506

Width : 3.990

Height : 10.014

Entry



B1L102S, U9-ch1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.88
Std Dev	11.02

Turn on : 27.3529

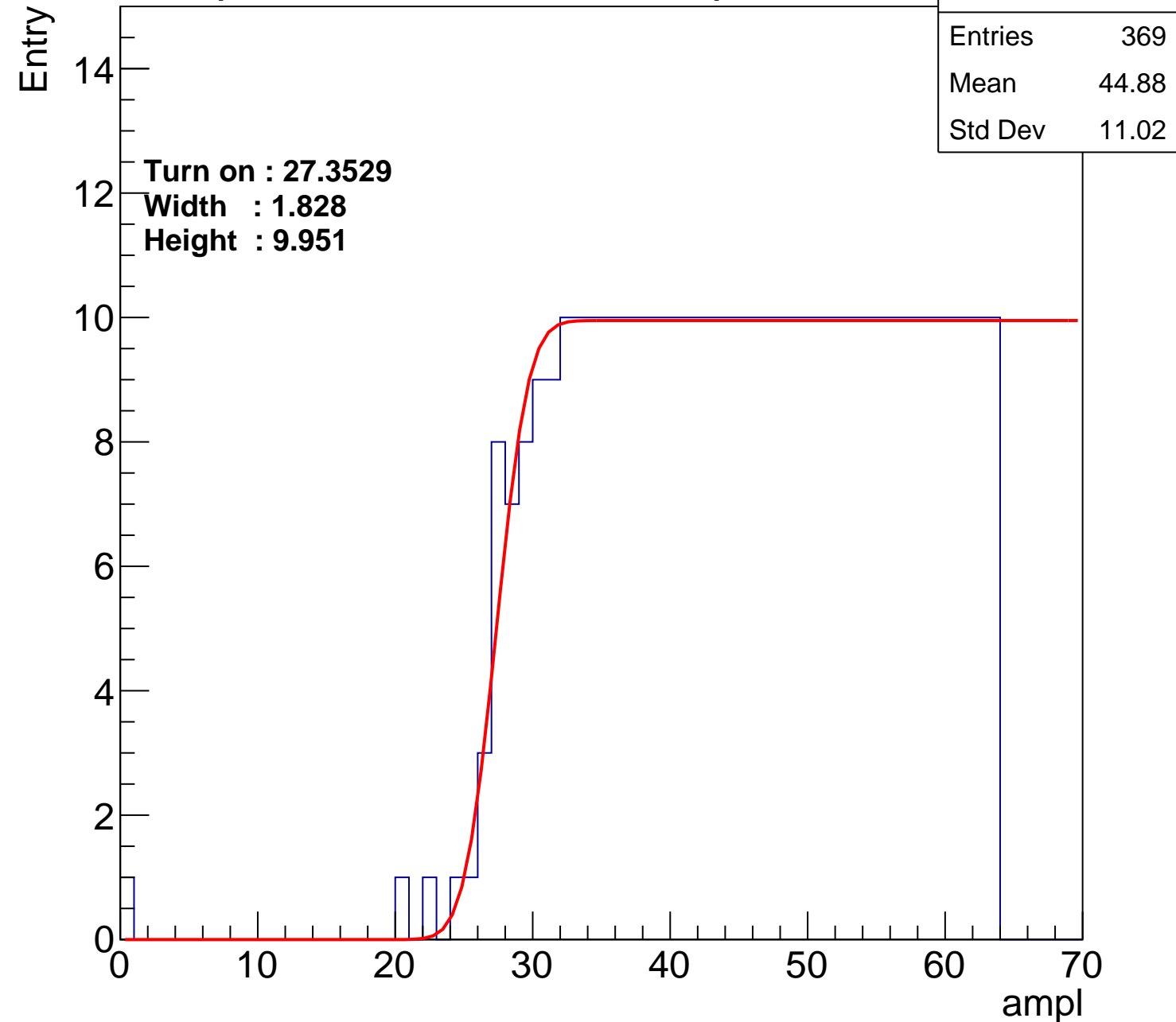
Width : 1.828

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.07
Std Dev	11.74

Turn on : 26.0300

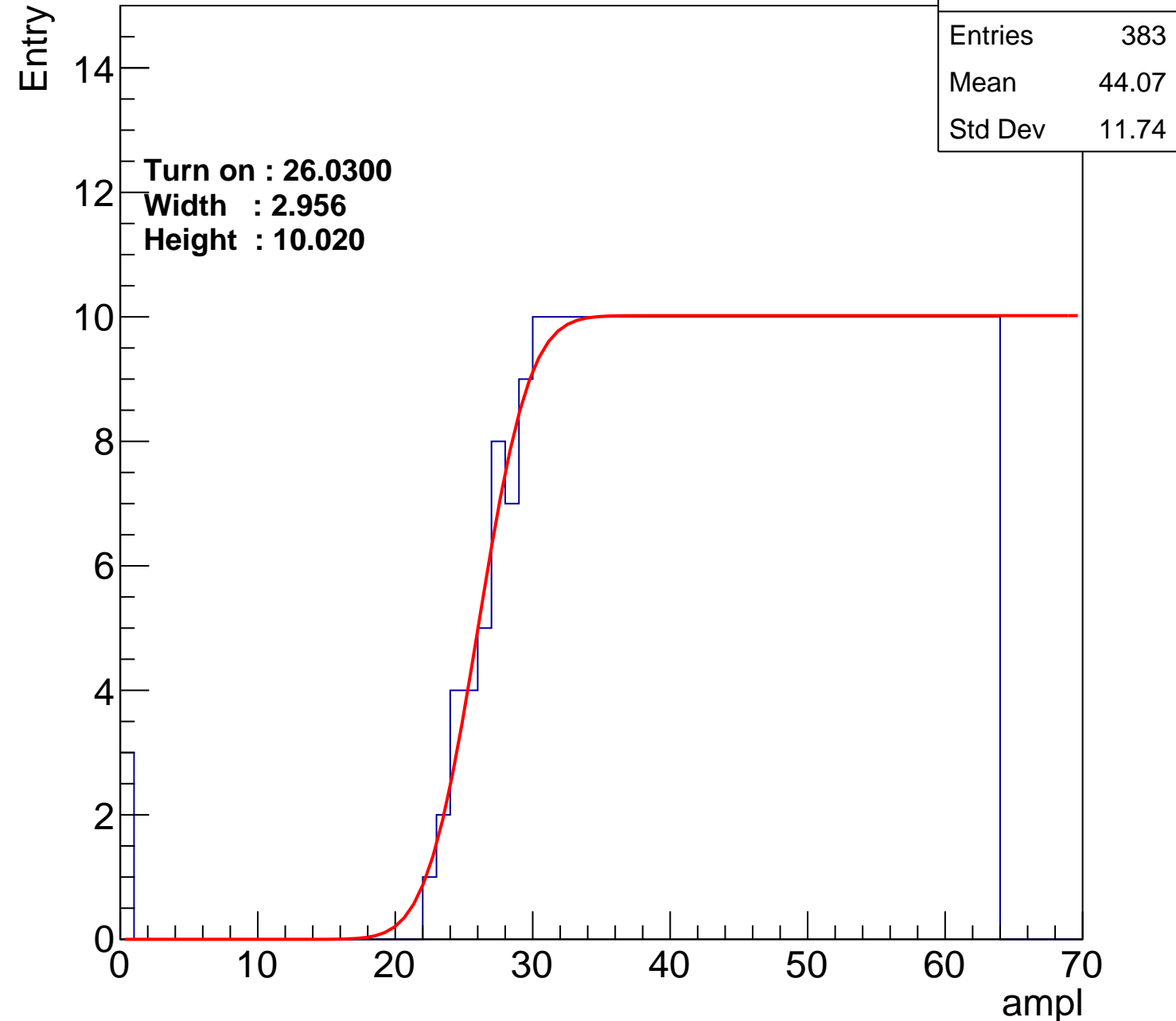
Width : 2.956

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	360
Mean	45.33
Std Dev	10.78

Turn on : 29.1507

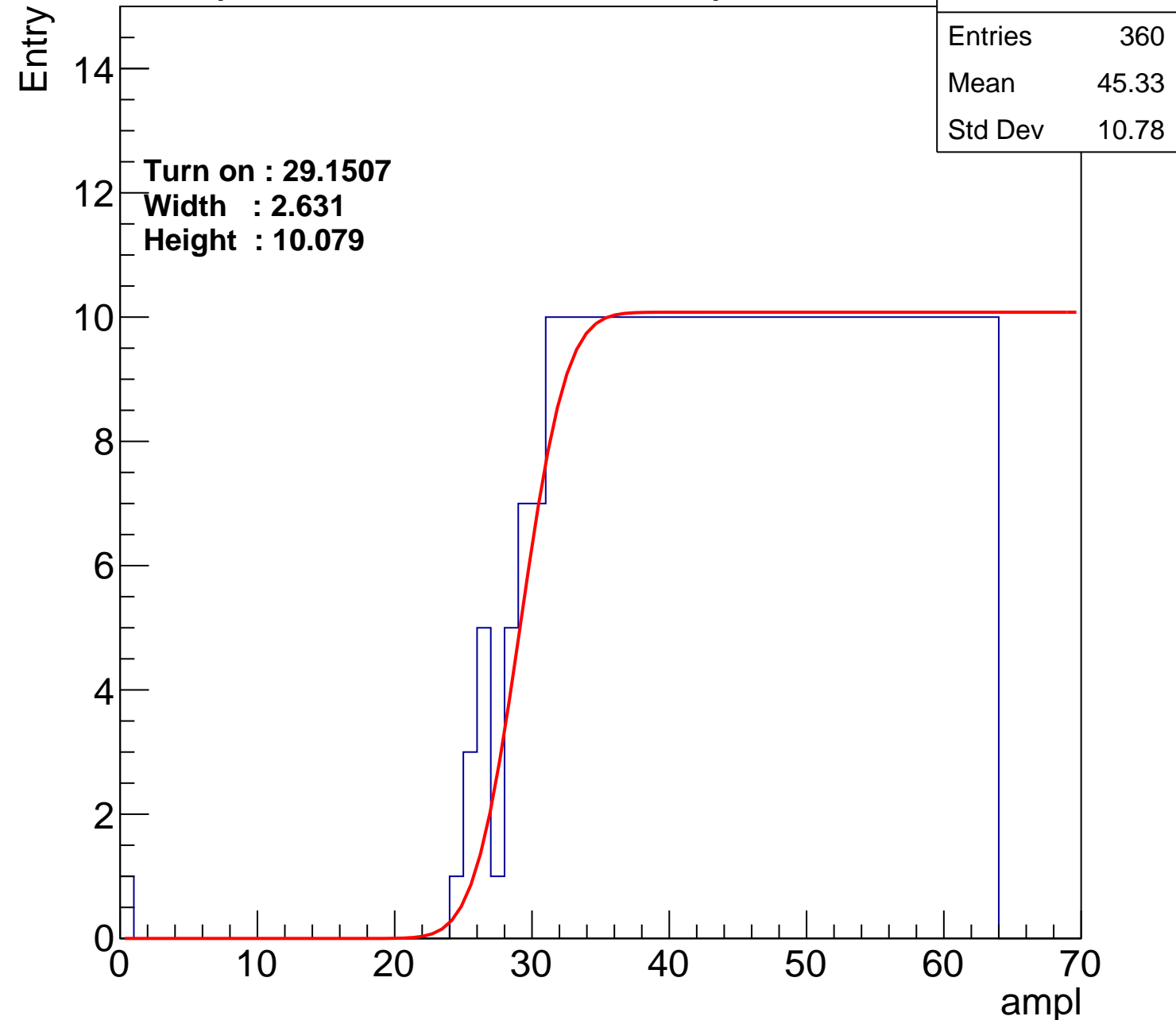
Width : 2.631

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch4

calib_packv5_042523_0143.root, FC#11, port A2

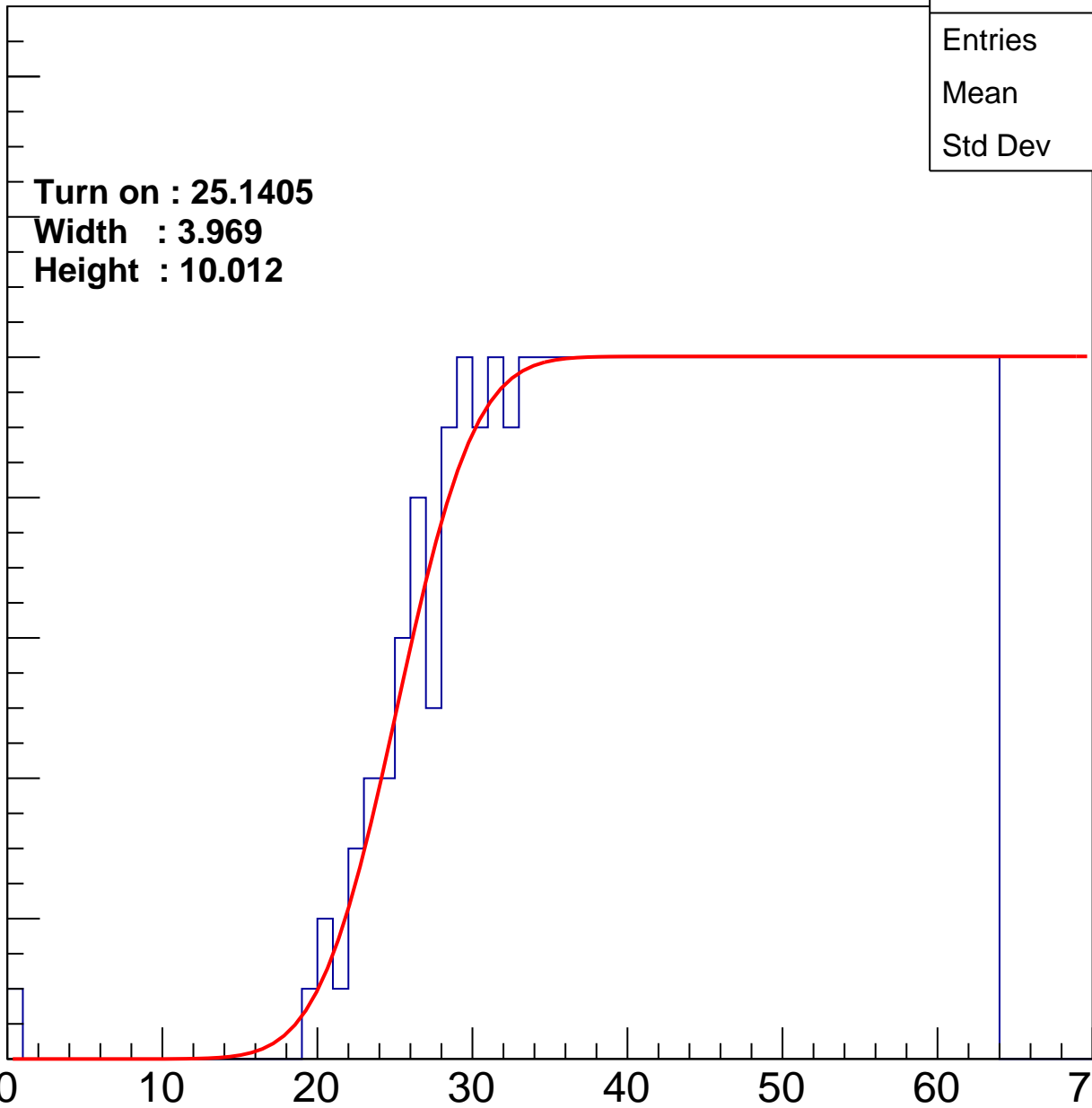
Entry

14
12
10
8
6
4
2
0

Turn on : 25.1405
Width : 3.969
Height : 10.012

Entries	392
Mean	43.67
Std Dev	11.77

ampl



B1L102S, U9-ch5

calib_packv5_042523_0143.root, FC#11, port A2

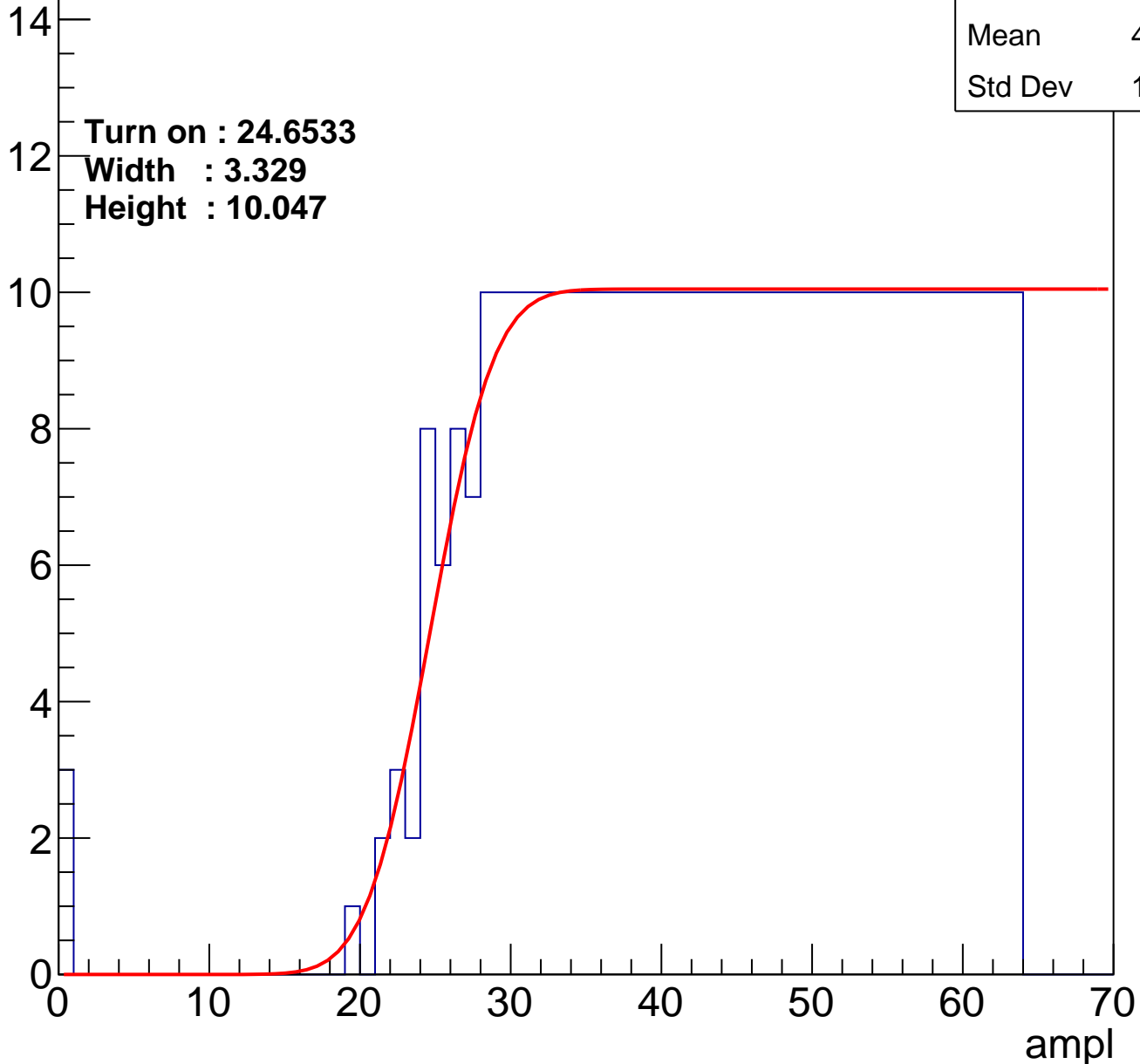
Entries	400
Mean	43.23
Std Dev	12.17

Turn on : 24.6533

Width : 3.329

Height : 10.047

Entry



B1L102S, U9-ch6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.71
Std Dev	11.95

Turn on : 25.4987

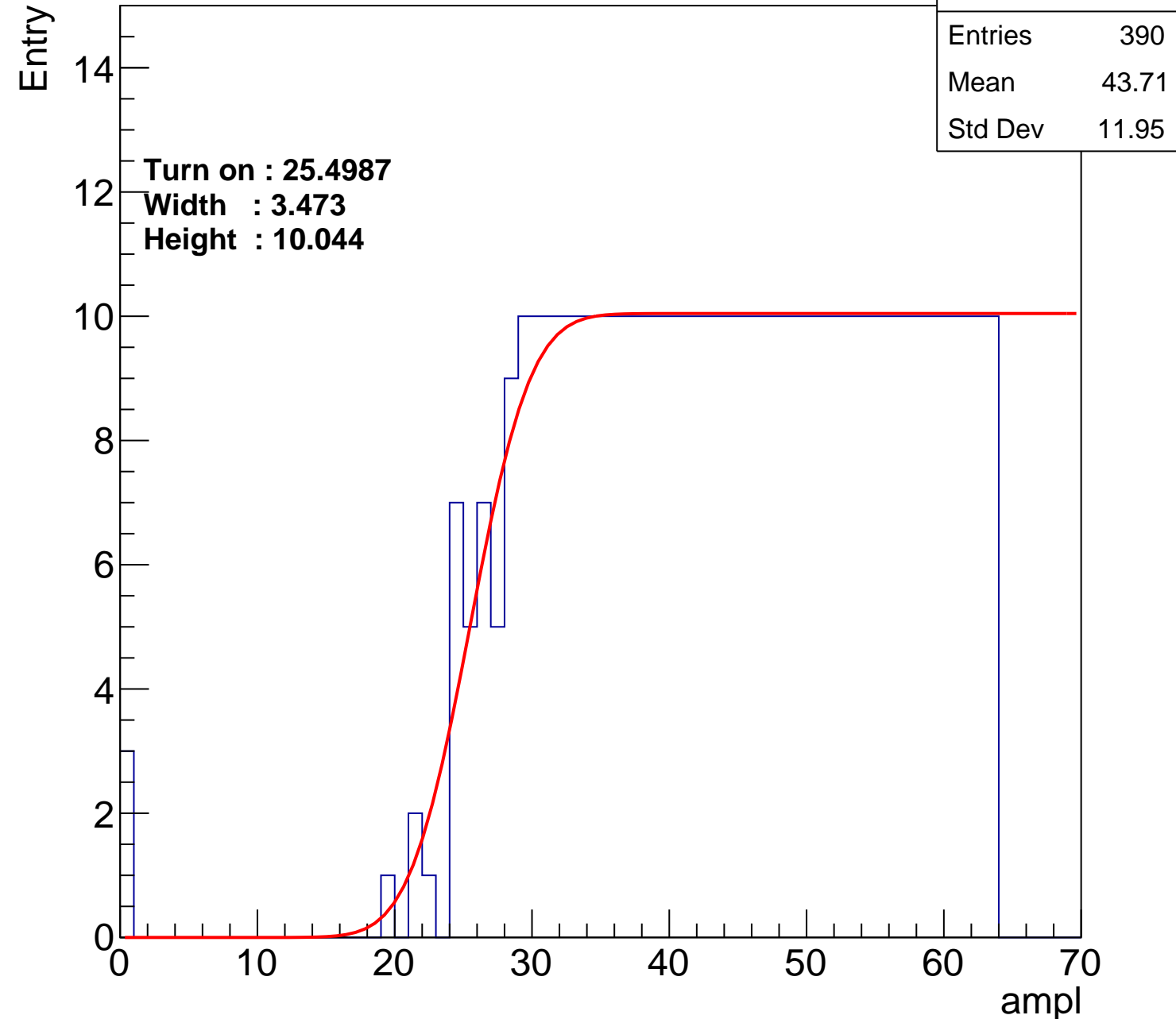
Width : 3.473

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch7

calib_packv5_042523_0143.root, FC#11, port A2

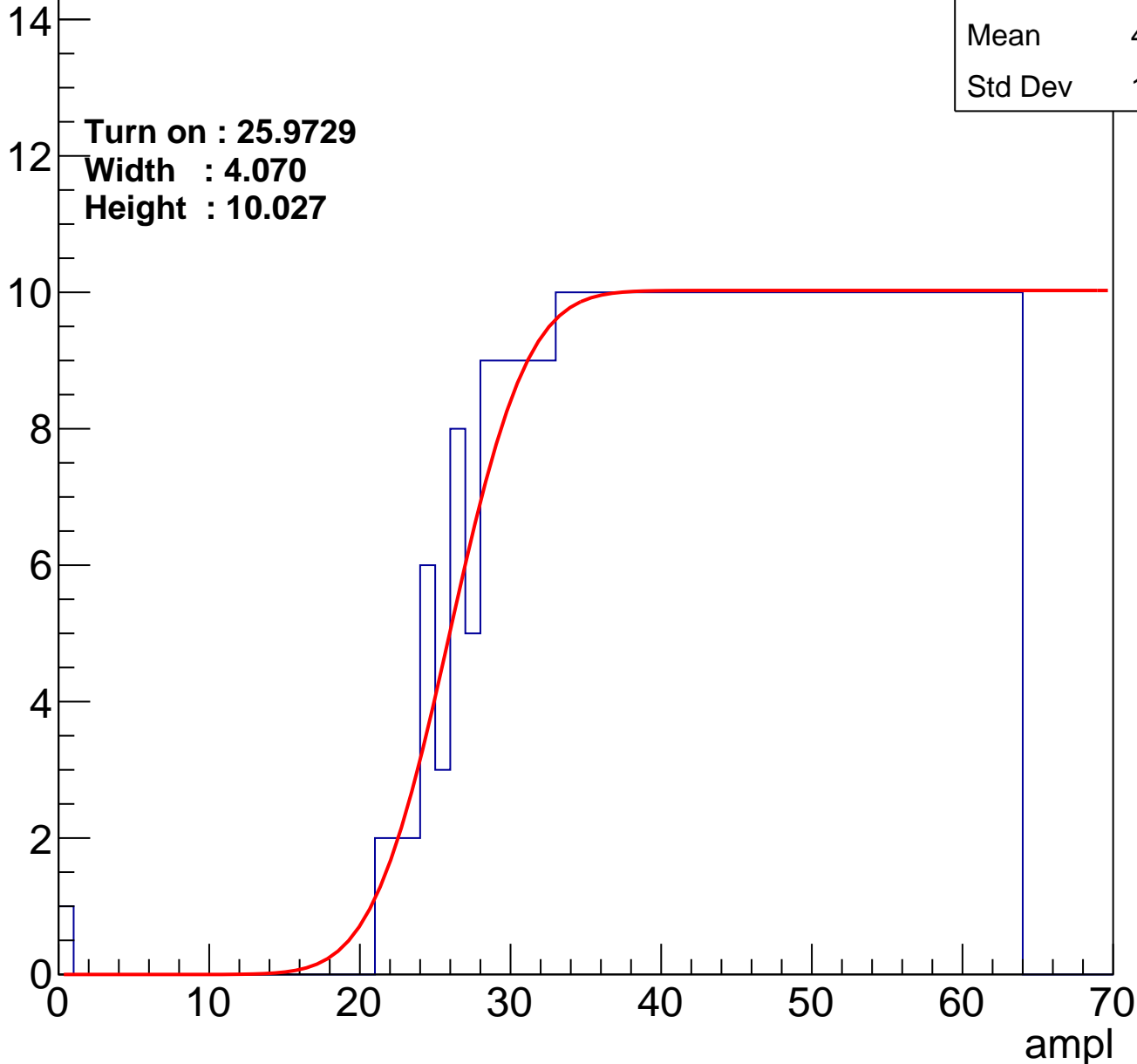
Entries	384
Mean	44.07
Std Dev	11.53

Turn on : 25.9729

Width : 4.070

Height : 10.027

Entry



B1L102S, U9-ch8

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.84
Std Dev	11.57

Turn on : 28.3234

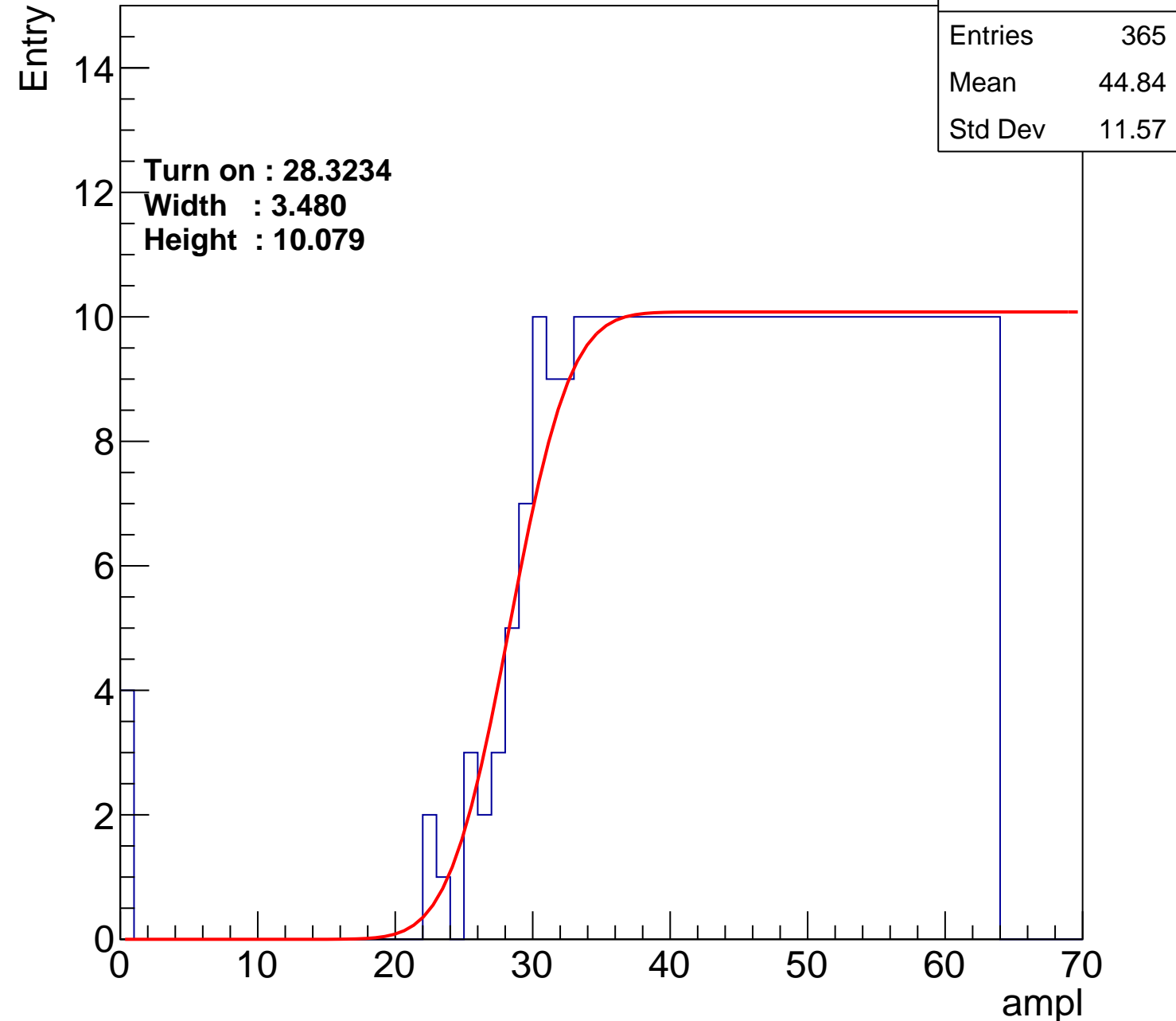
Width : 3.480

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch9

calib_packv5_042523_0143.root, FC#11, port A2

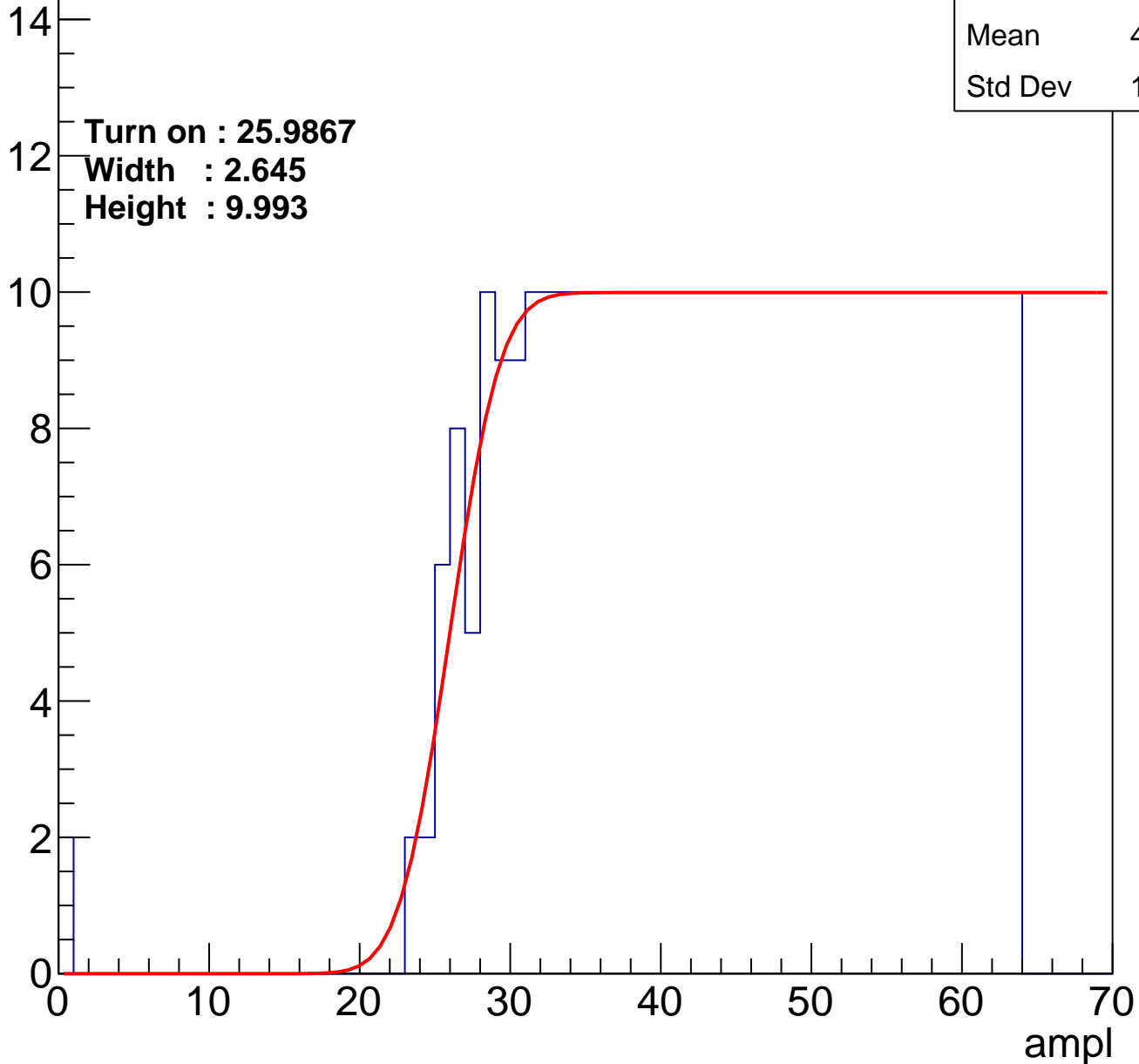
Entries	383
Mean	44.15
Std Dev	11.54

Turn on : 25.9867

Width : 2.645

Height : 9.993

Entry



B1L102S, U9-ch10

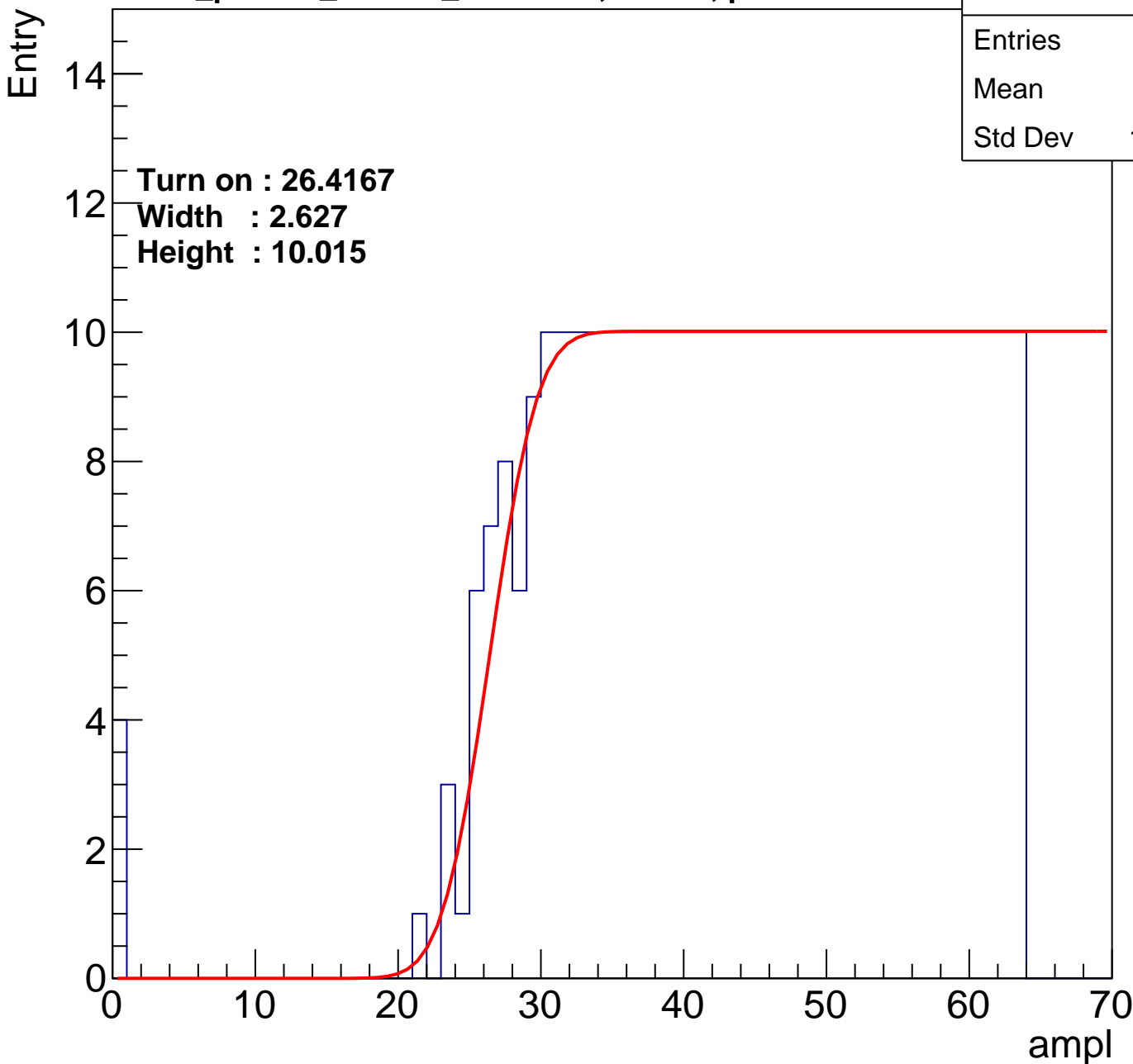
calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.9
Std Dev	11.97

Turn on : 26.4167

Width : 2.627

Height : 10.015



B1L102S, U9-ch11

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.87
Std Dev	11.97

Turn on : 26.2353

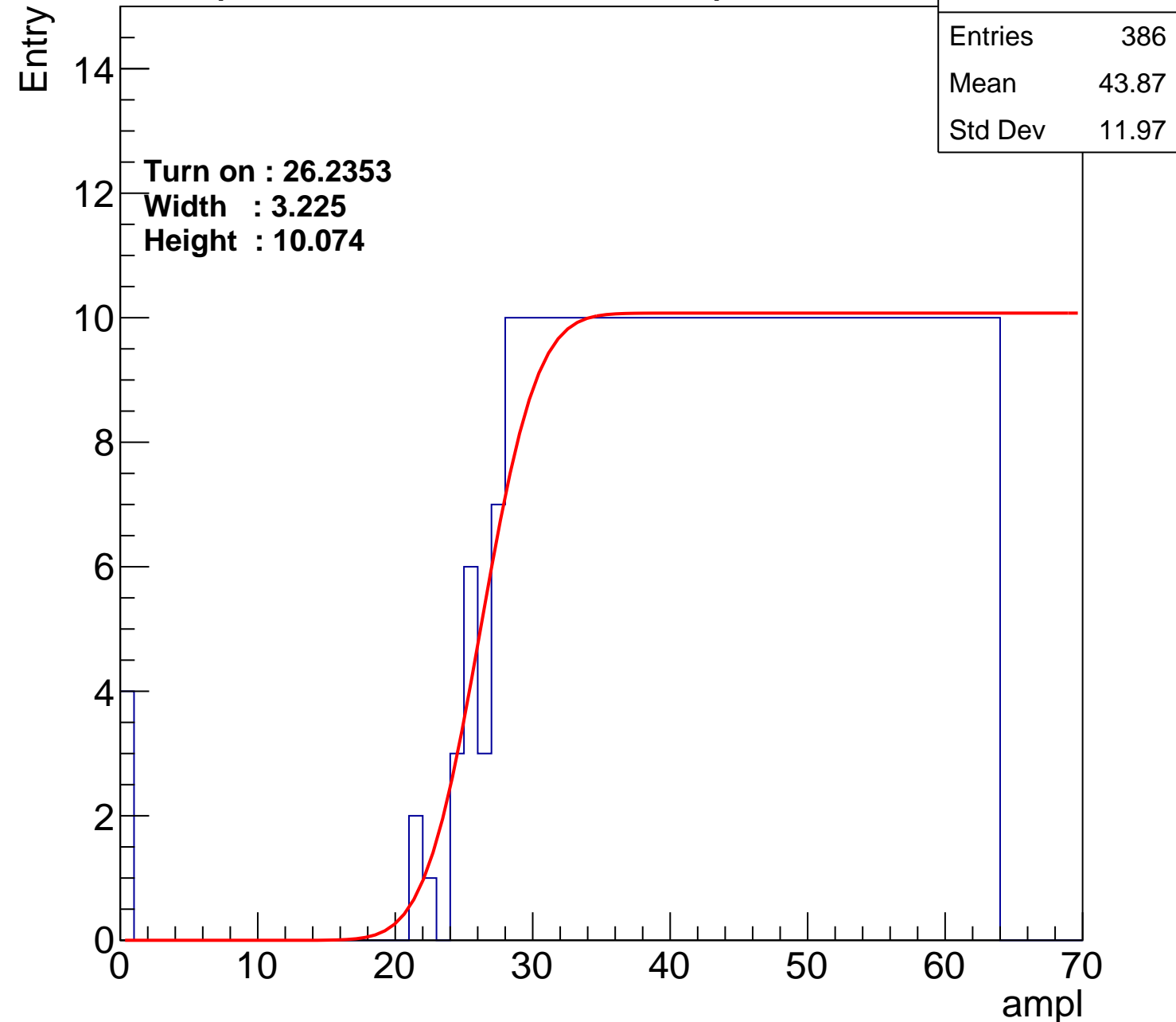
Width : 3.225

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch12

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.82
Std Dev	11.8

Turn on : 26.1146

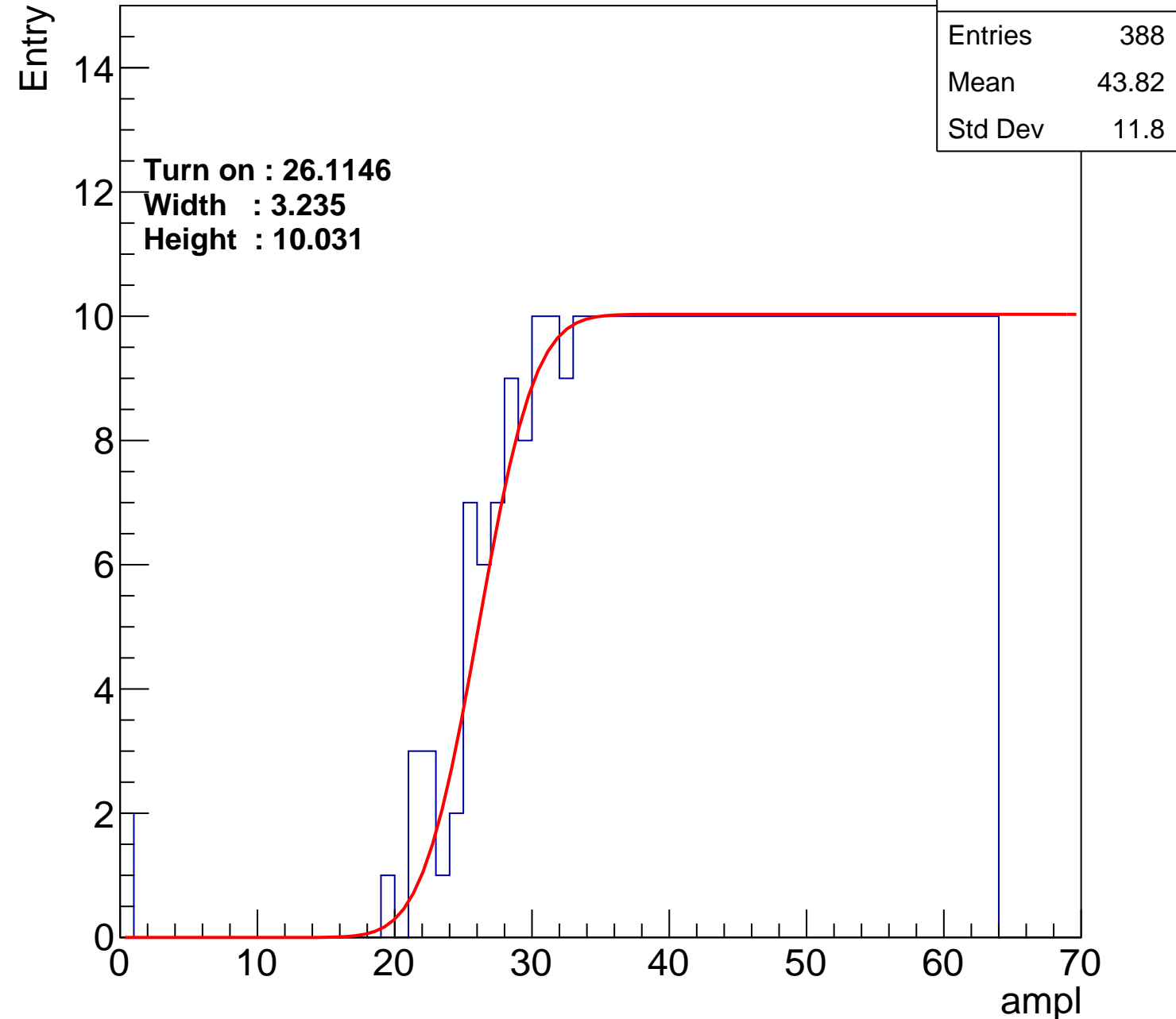
Width : 3.235

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch13

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.99
Std Dev	11.63

Turn on : 25.6945

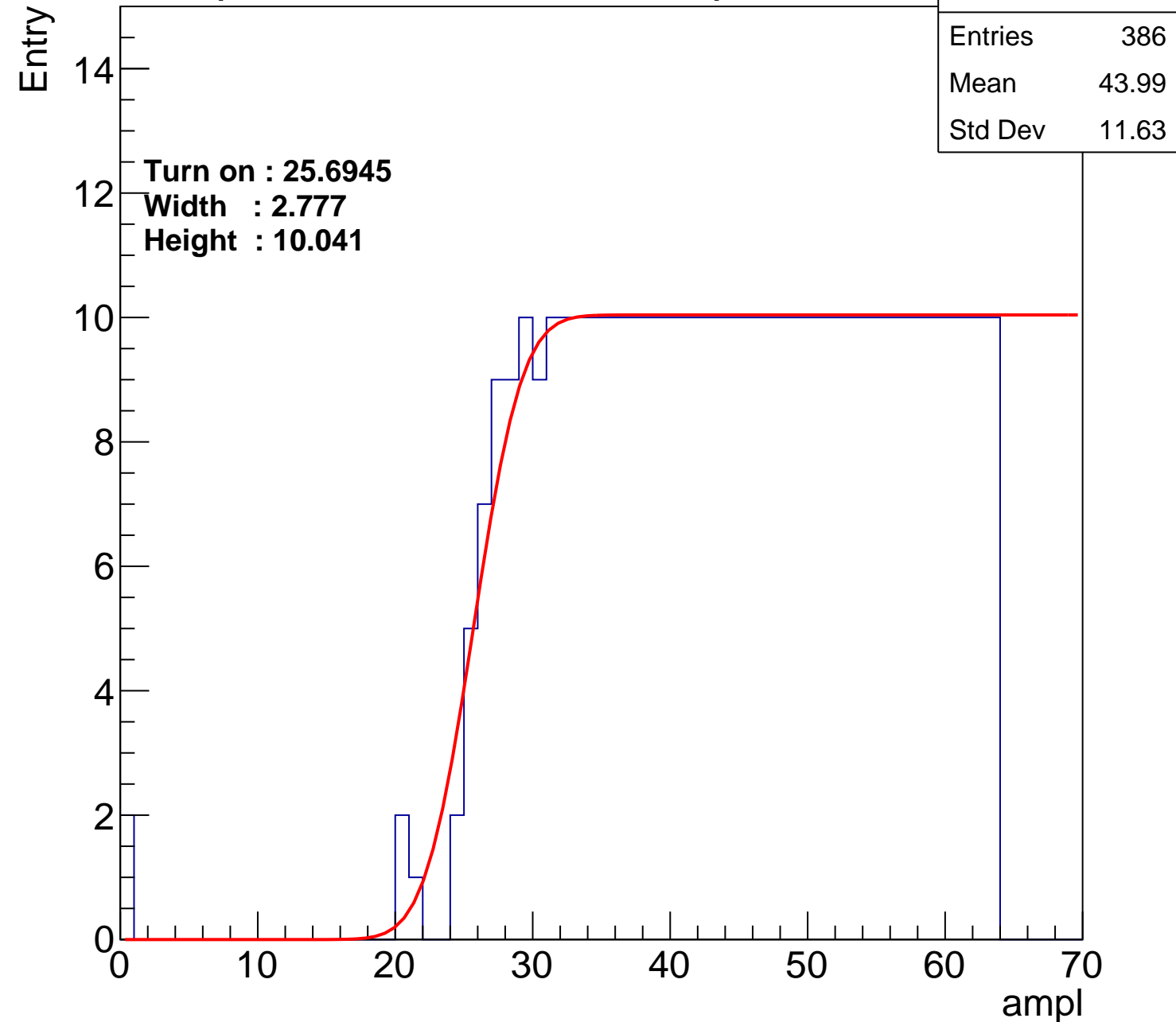
Width : 2.777

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch14

calib_packv5_042523_0143.root, FC#11, port A2

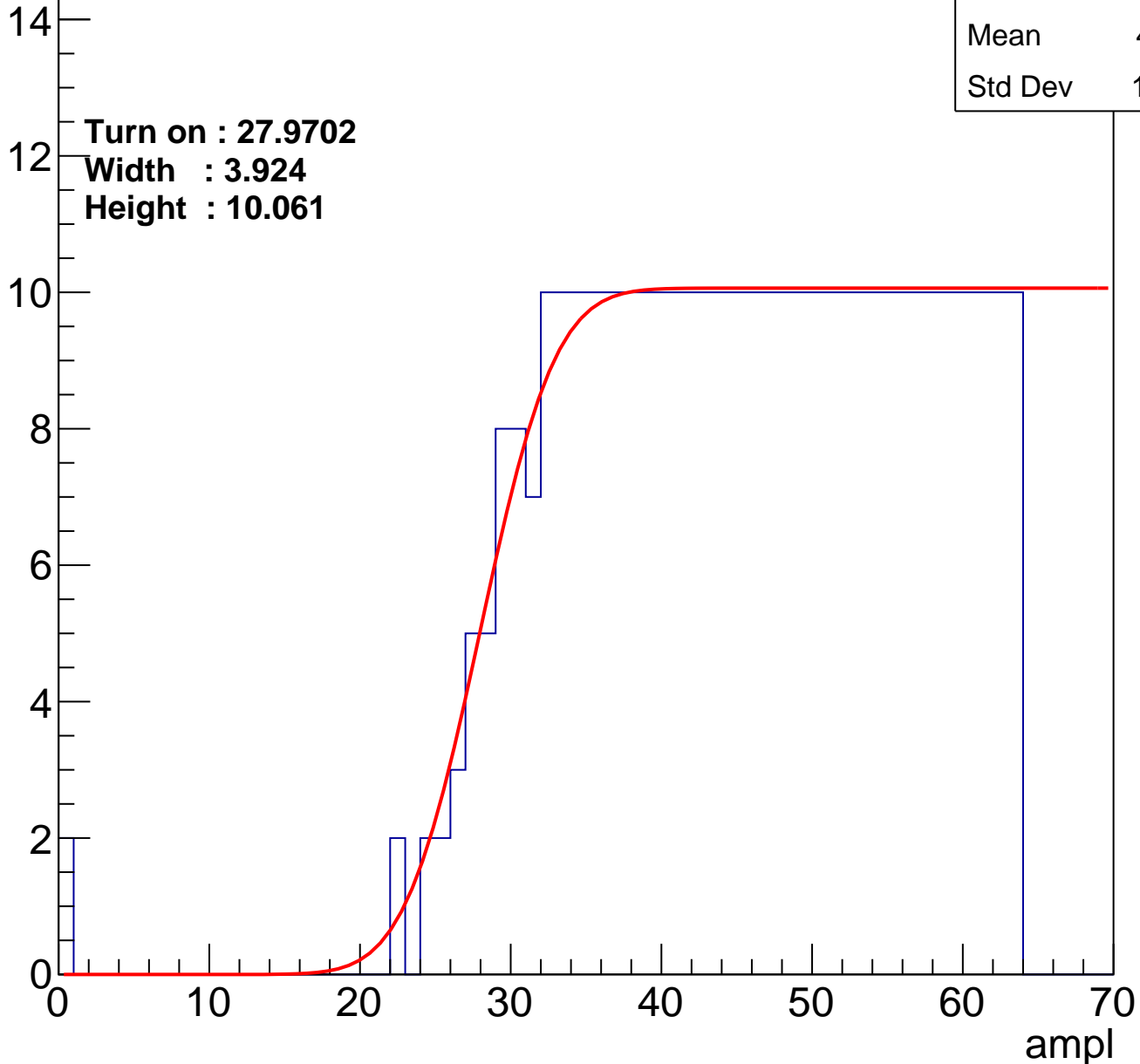
Entries	364
Mean	45.01
Std Dev	11.17

Turn on : 27.9702

Width : 3.924

Height : 10.061

Entry



B1L102S, U9-ch15

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.82
Std Dev	11.22

Turn on : 26.9564

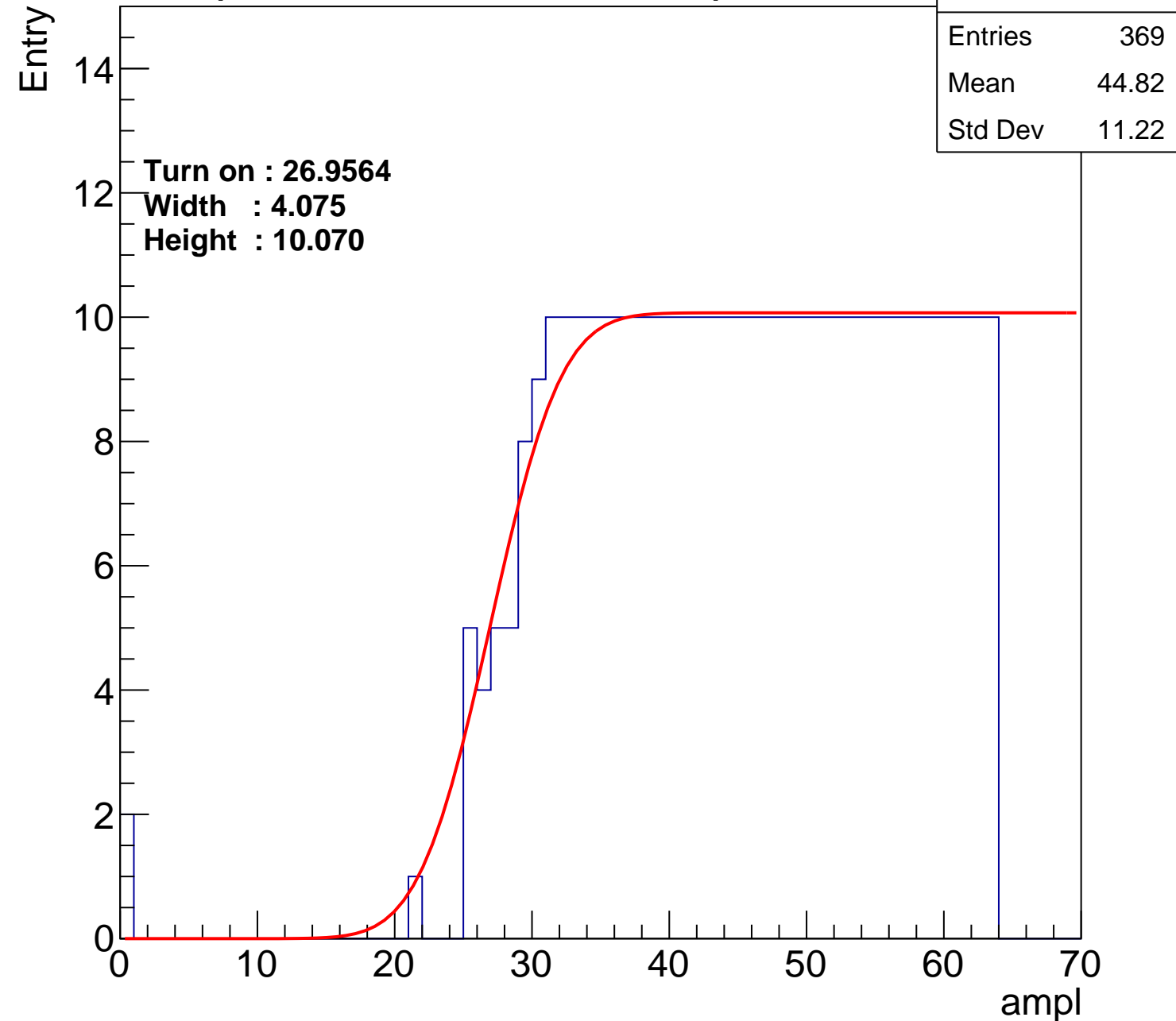
Width : 4.075

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch16

calib_packv5_042523_0143.root, FC#11, port A2

Entries	407
Mean	42.88
Std Dev	12.36

Turn on : 24.0460

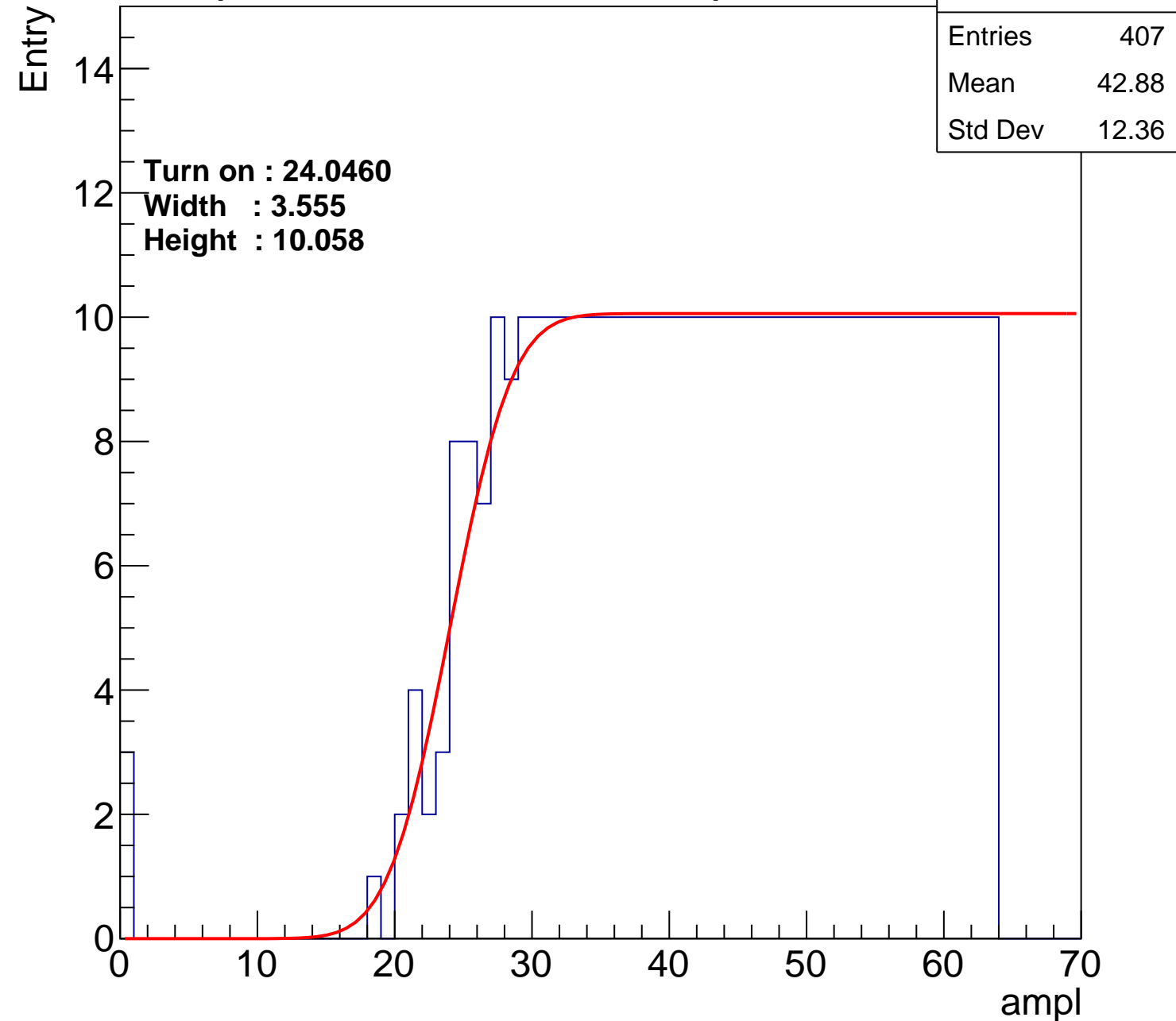
Width : 3.555

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch17

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.88
Std Dev	11.04

Turn on : 27.7602

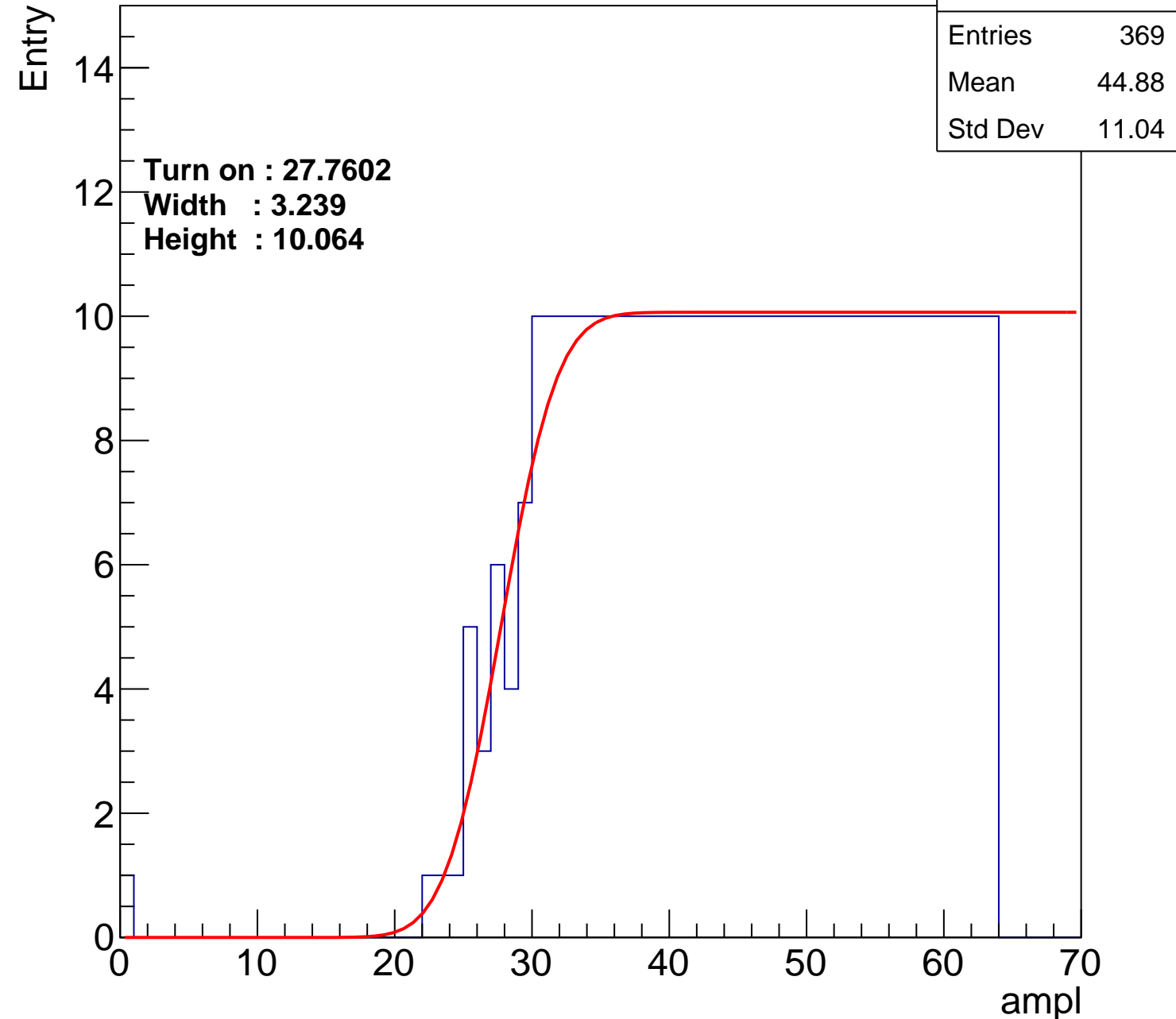
Width : 3.239

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch18

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	43.24
Std Dev	11.93

Turn on : 24.1929

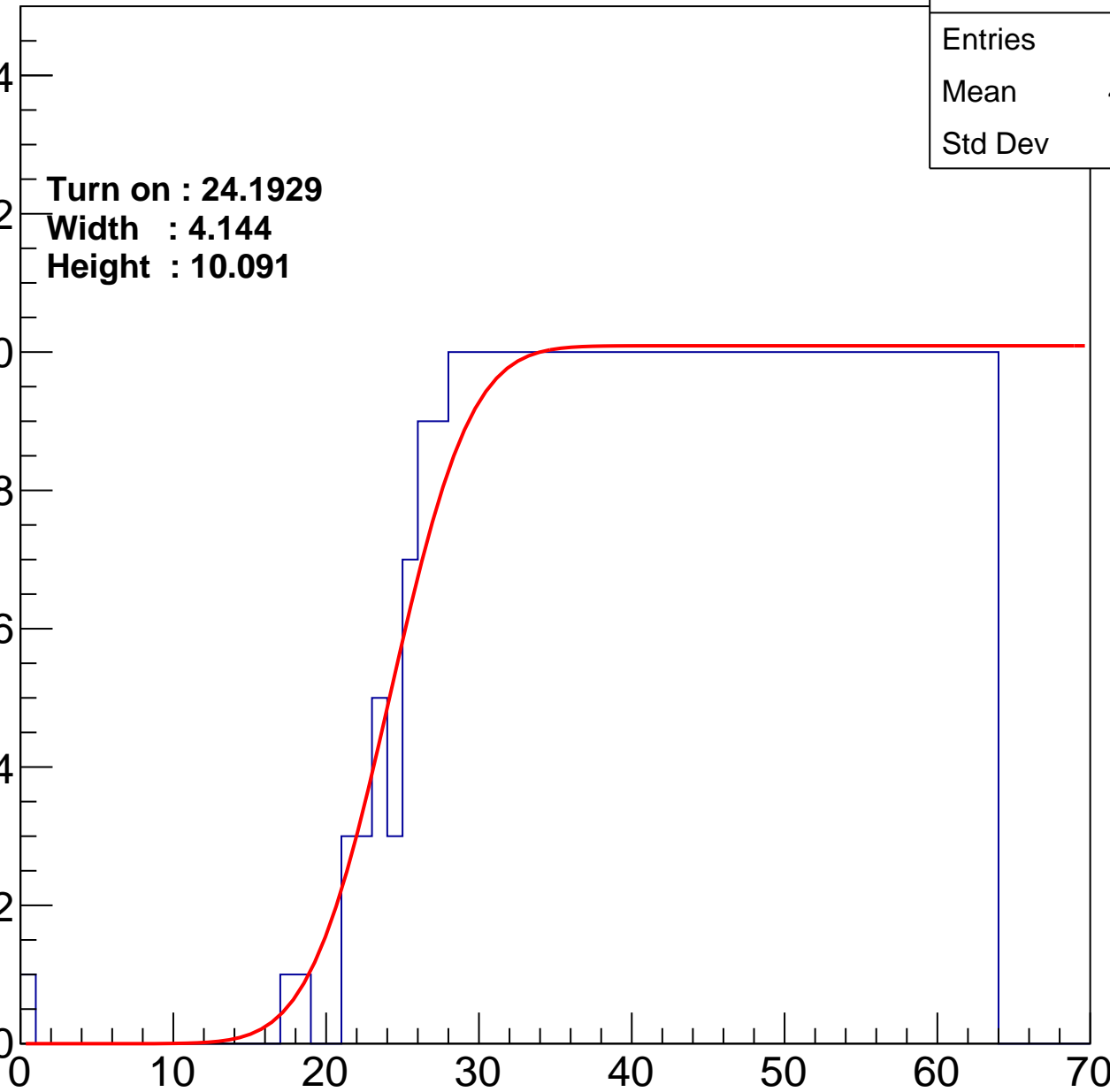
Width : 4.144

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch19

calib_packv5_042523_0143.root, FC#11, port A2

Entries	361
Mean	45.21
Std Dev	11.02

Turn on : 28.6927

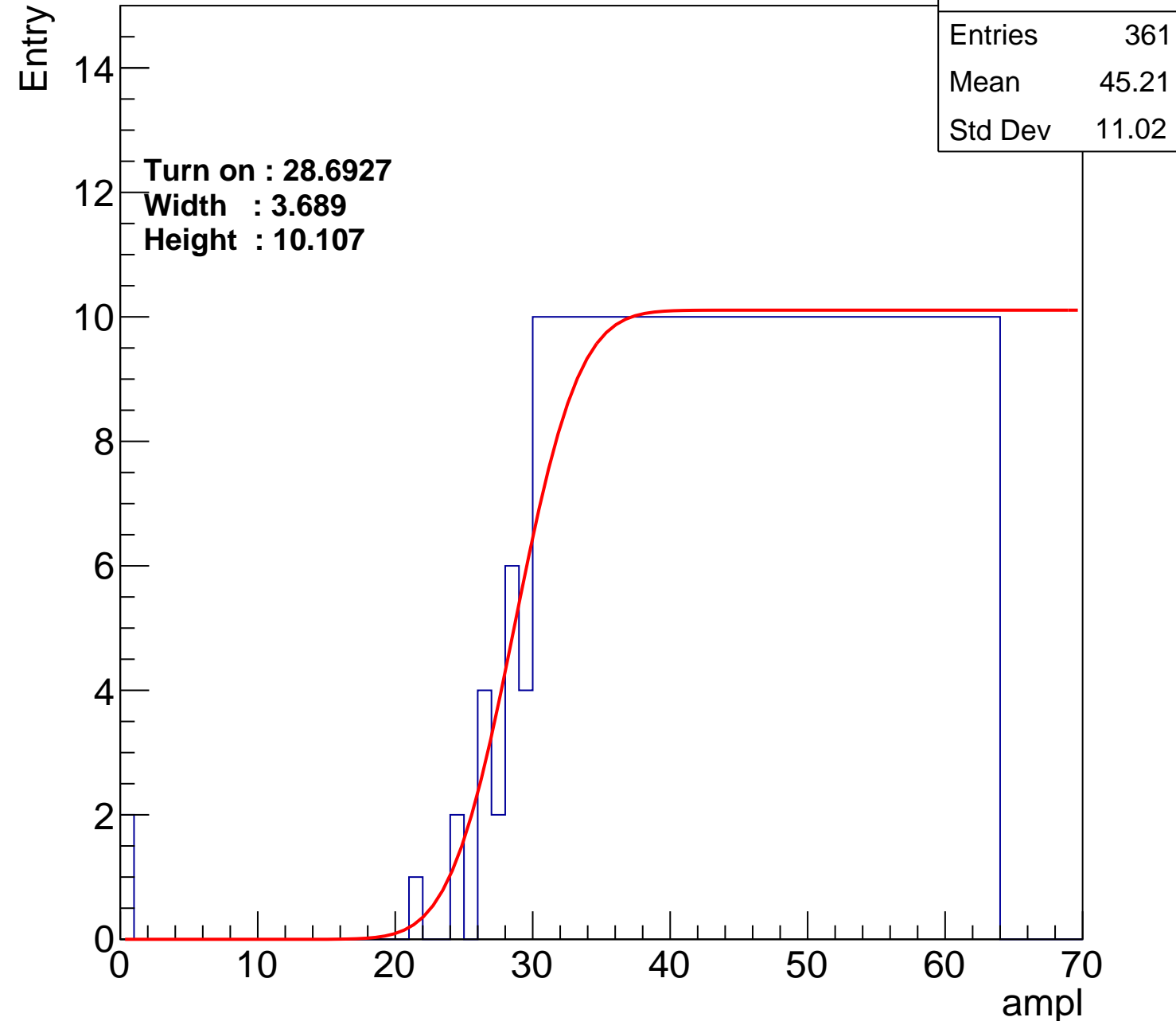
Width : 3.689

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch20

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.91
Std Dev	11.76

Turn on : 26.0329

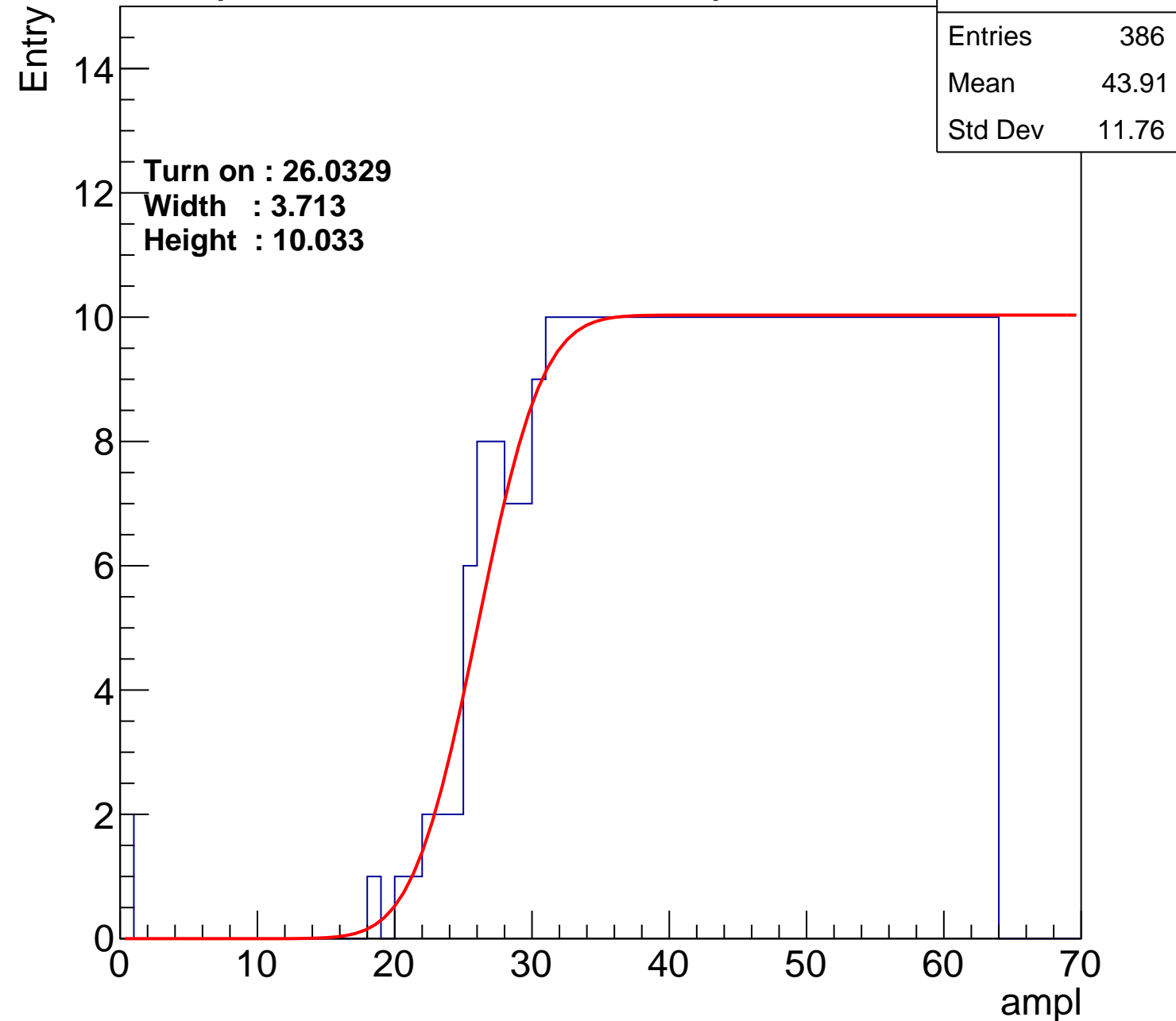
Width : 3.713

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch21

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.41
Std Dev	11.48

Turn on : 26.9793

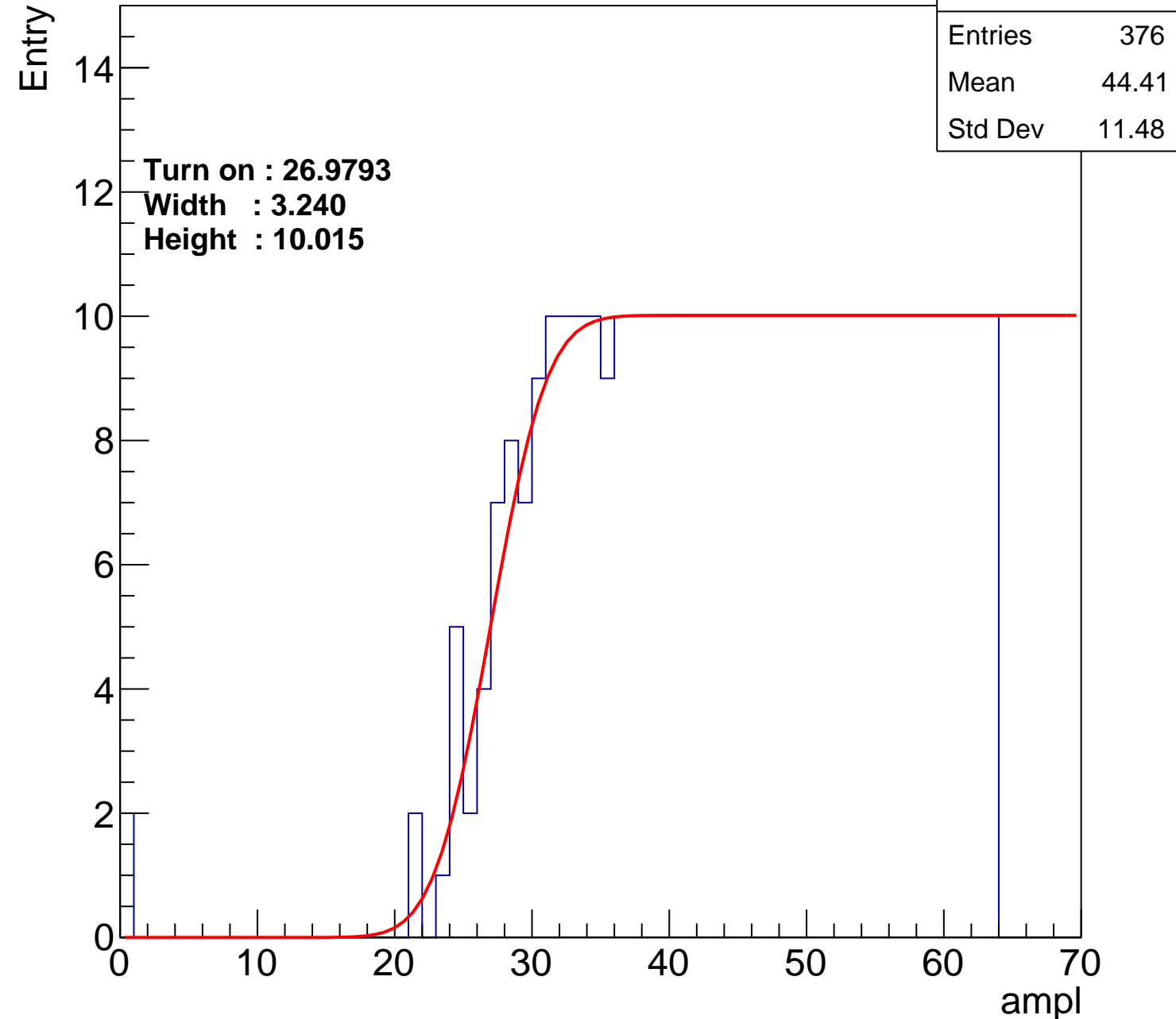
Width : 3.240

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch22

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 26.6293

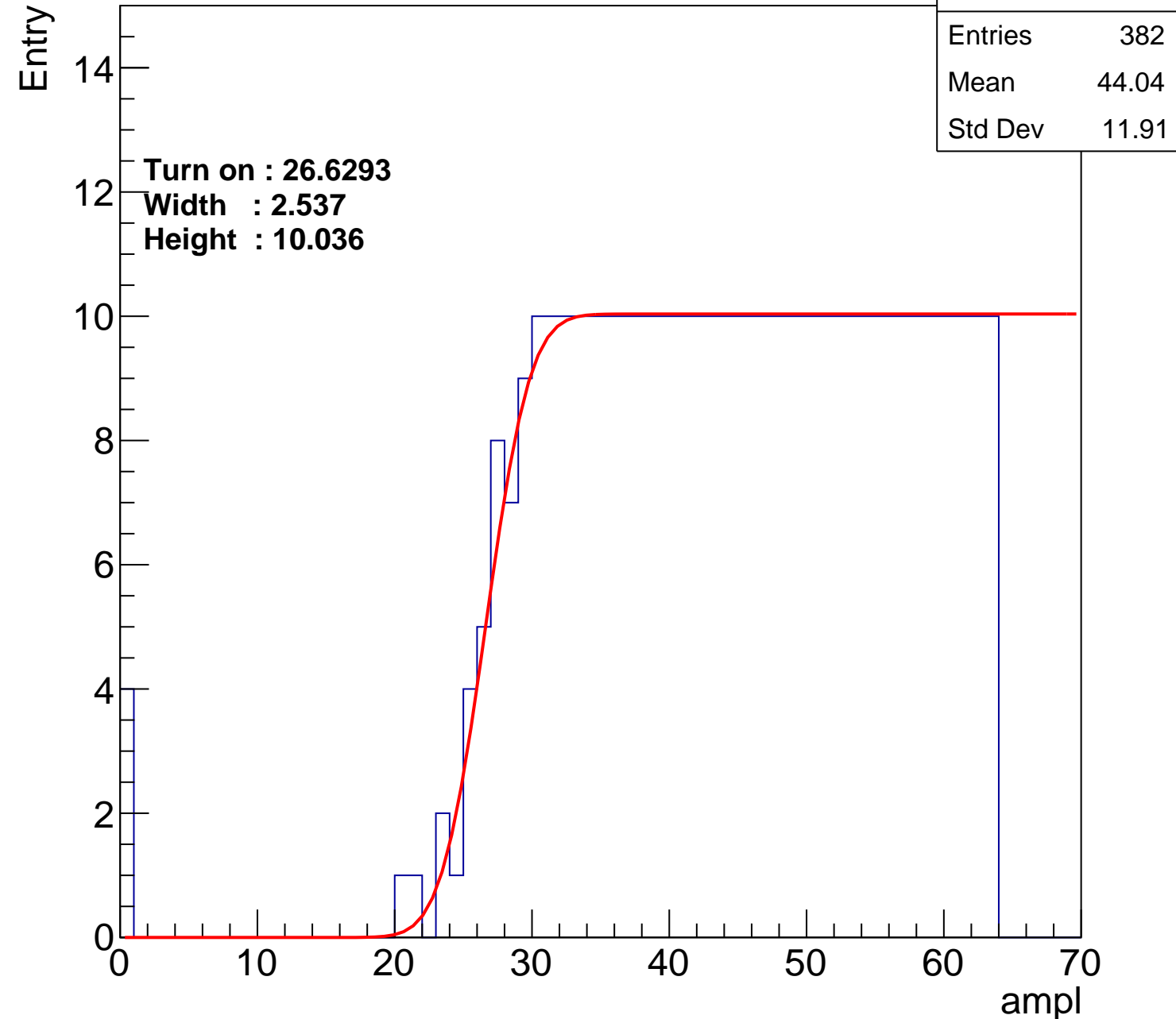
Width : 2.537

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch23

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.43
Std Dev	11.72

Turn on : 27.3856

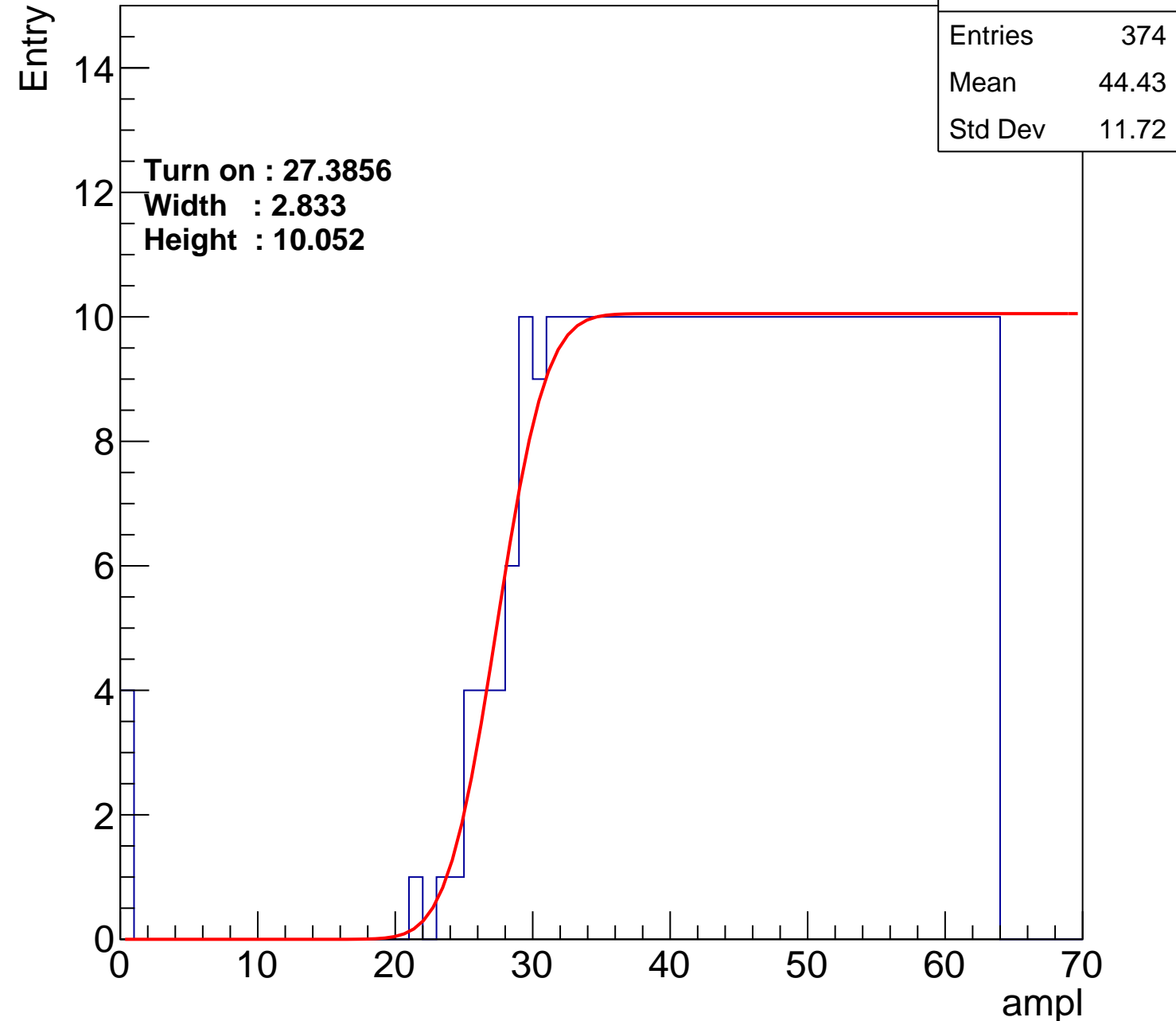
Width : 2.833

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch24

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.8
Std Dev	11.84

Turn on : 25.2836

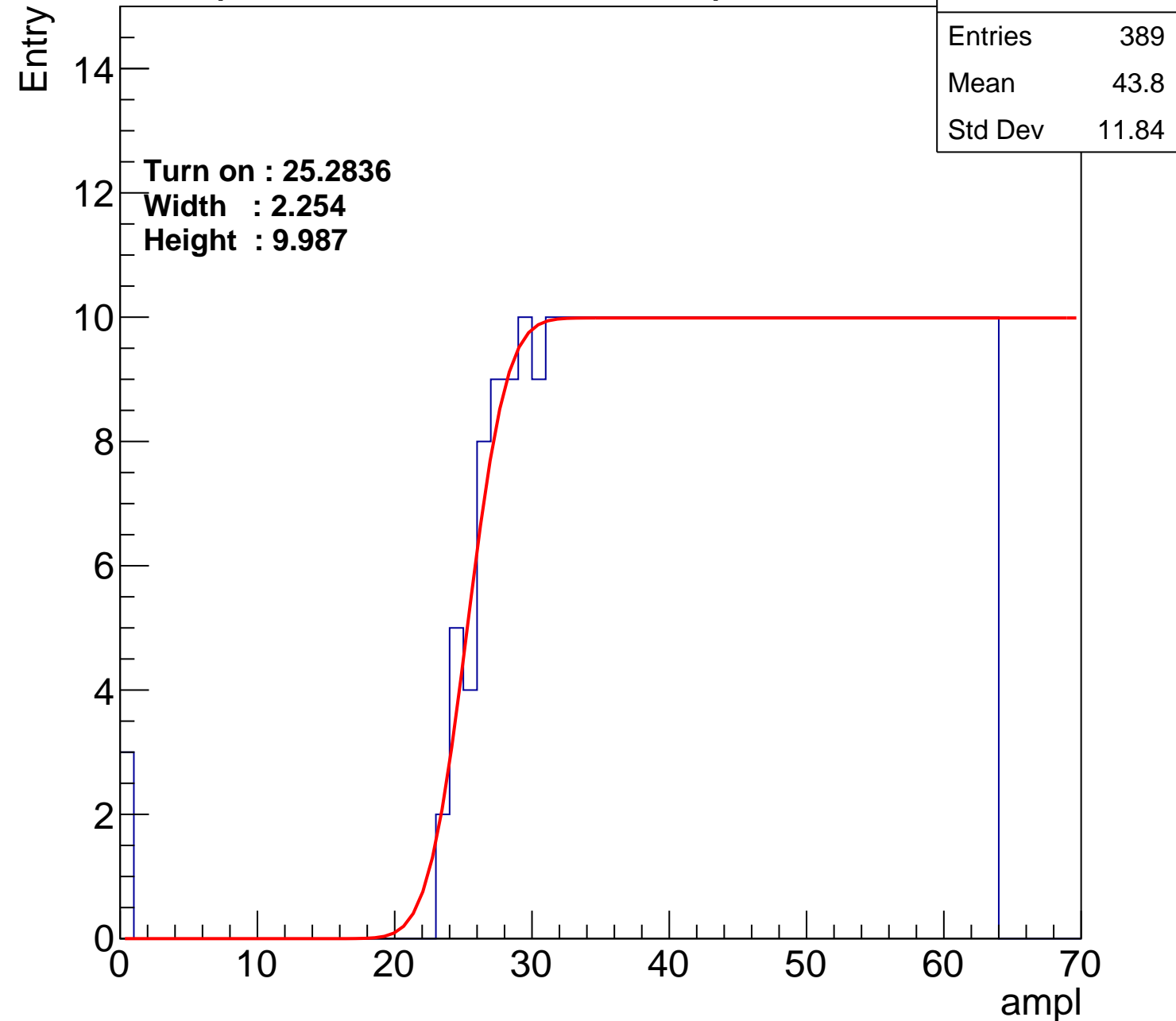
Width : 2.254

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch25

calib_packv5_042523_0143.root, FC#11, port A2

Entries	358
Mean	45.43
Std Dev	10.72

Turn on : 28.5016

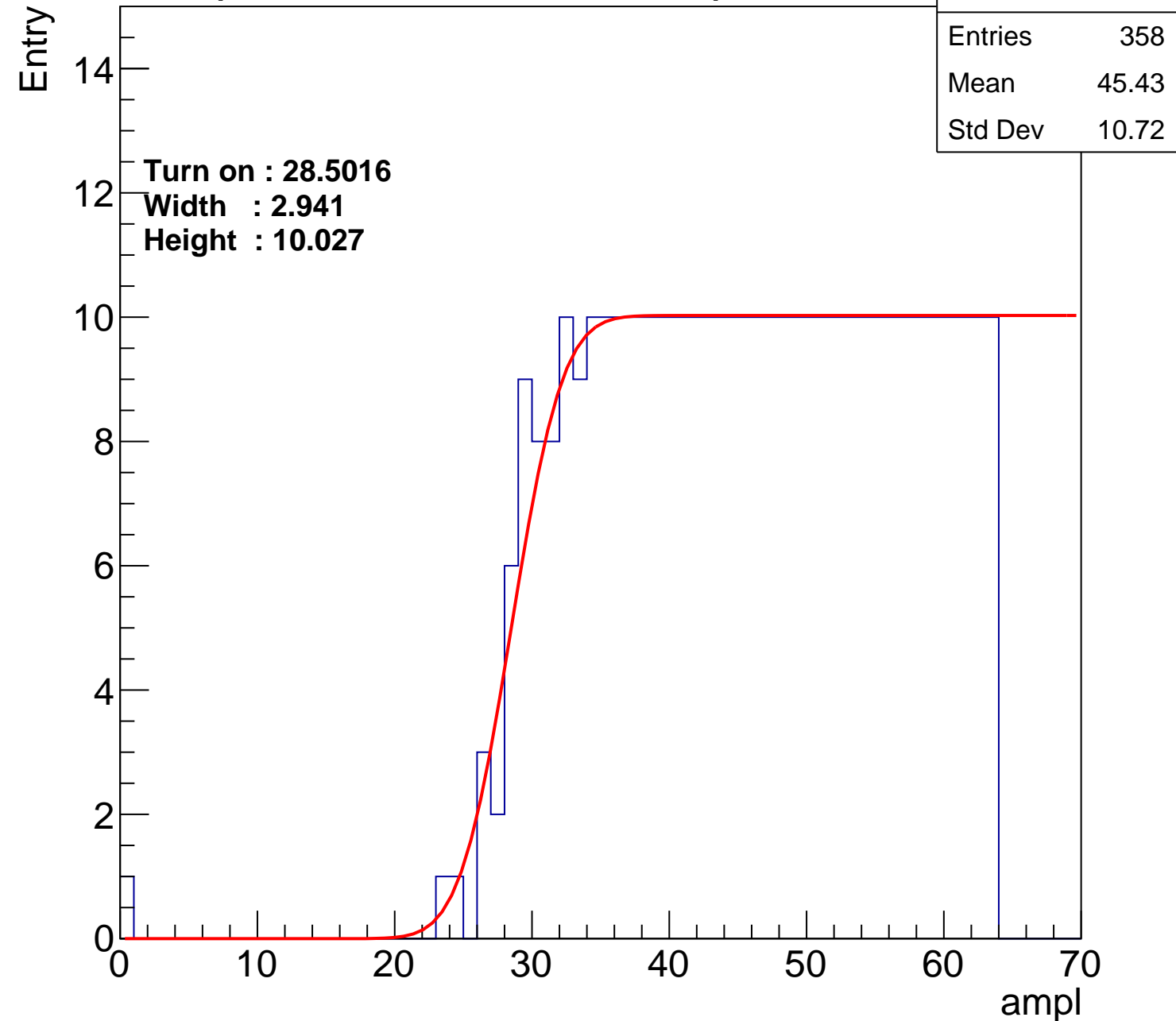
Width : 2.941

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch26

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	44.01
Std Dev	11.52

Turn on : 25.9595

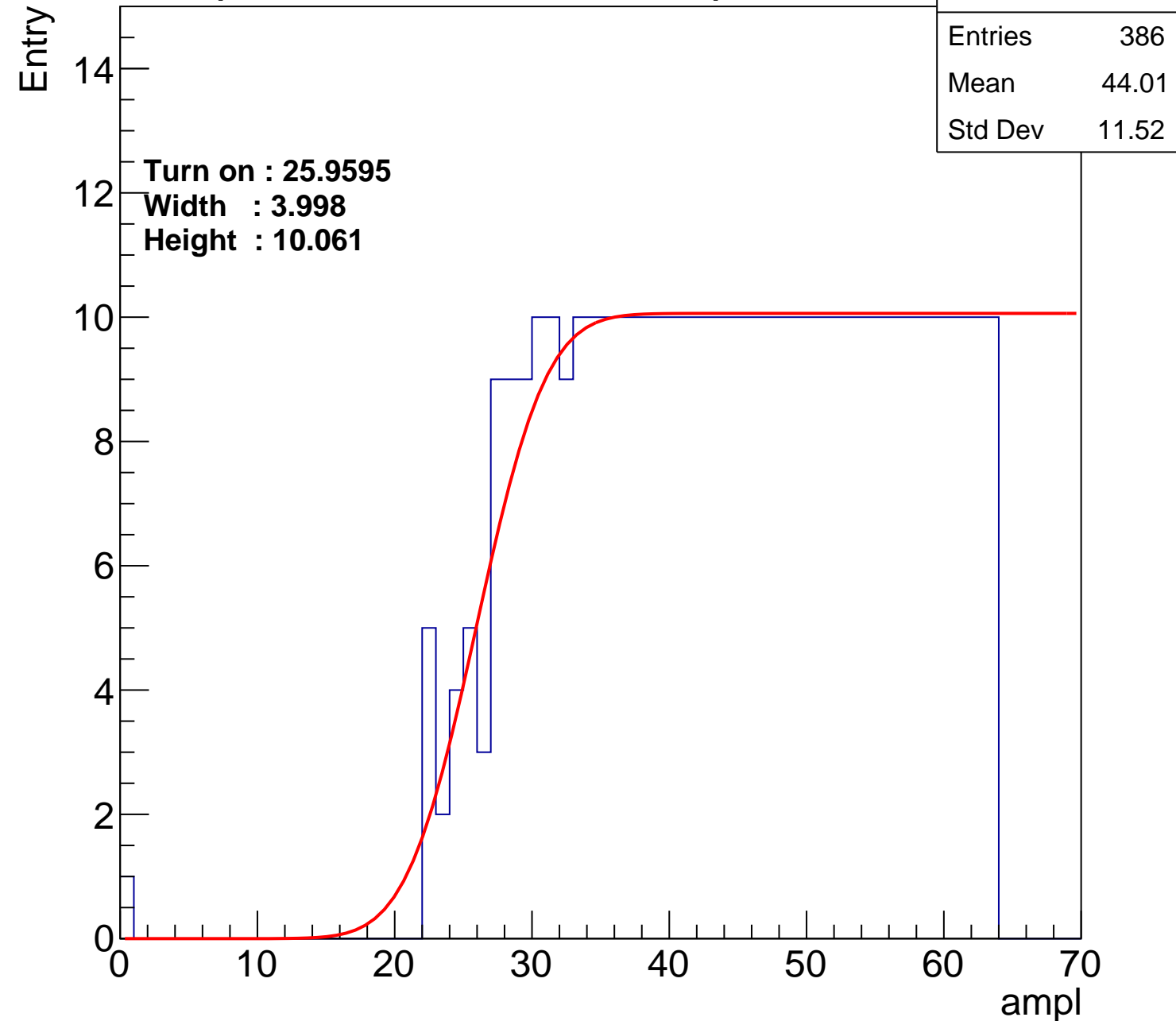
Width : 3.998

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch27

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.0733

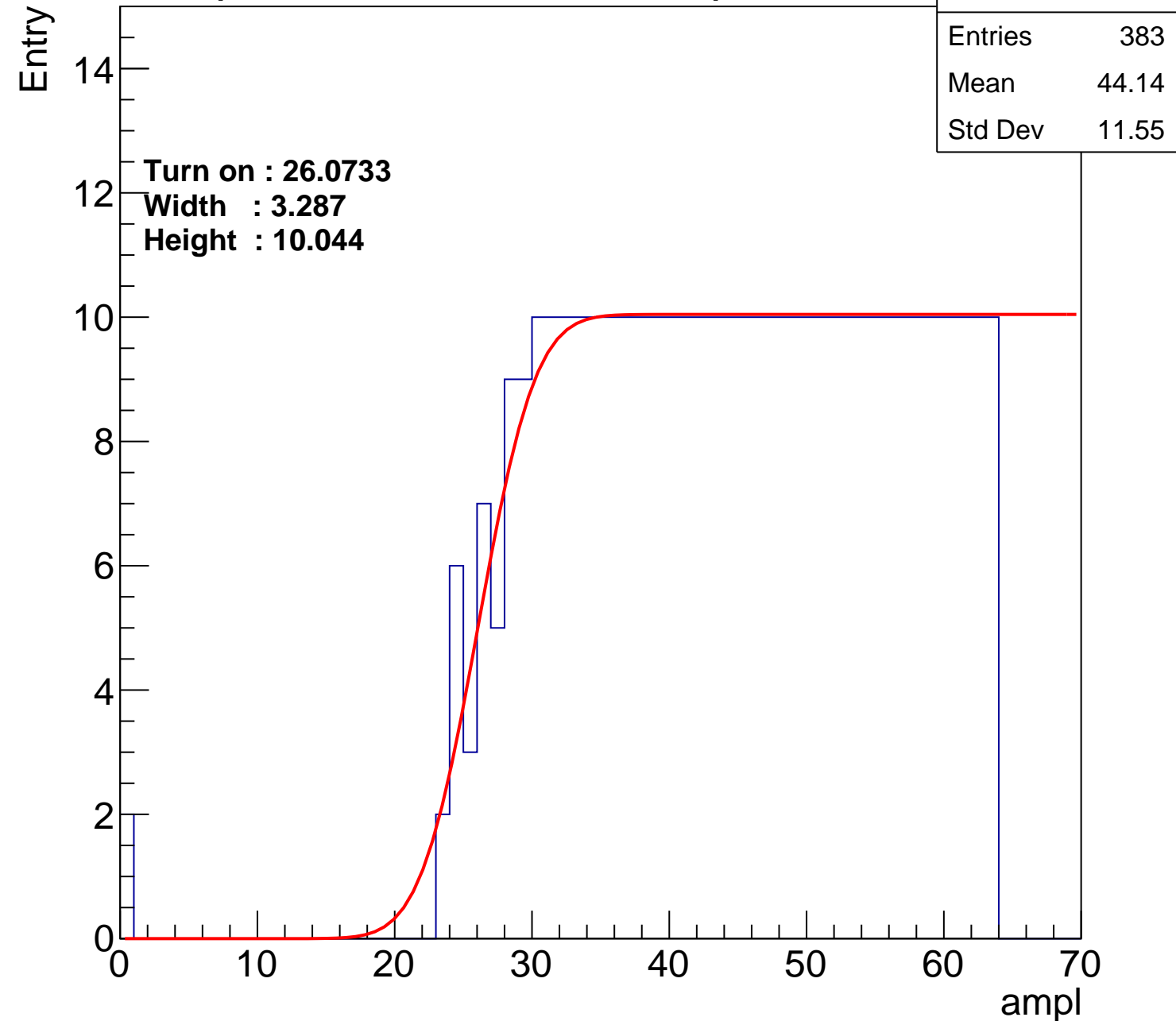
Width : 3.287

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch28

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.59
Std Dev	12.02

Turn on : 25.7089

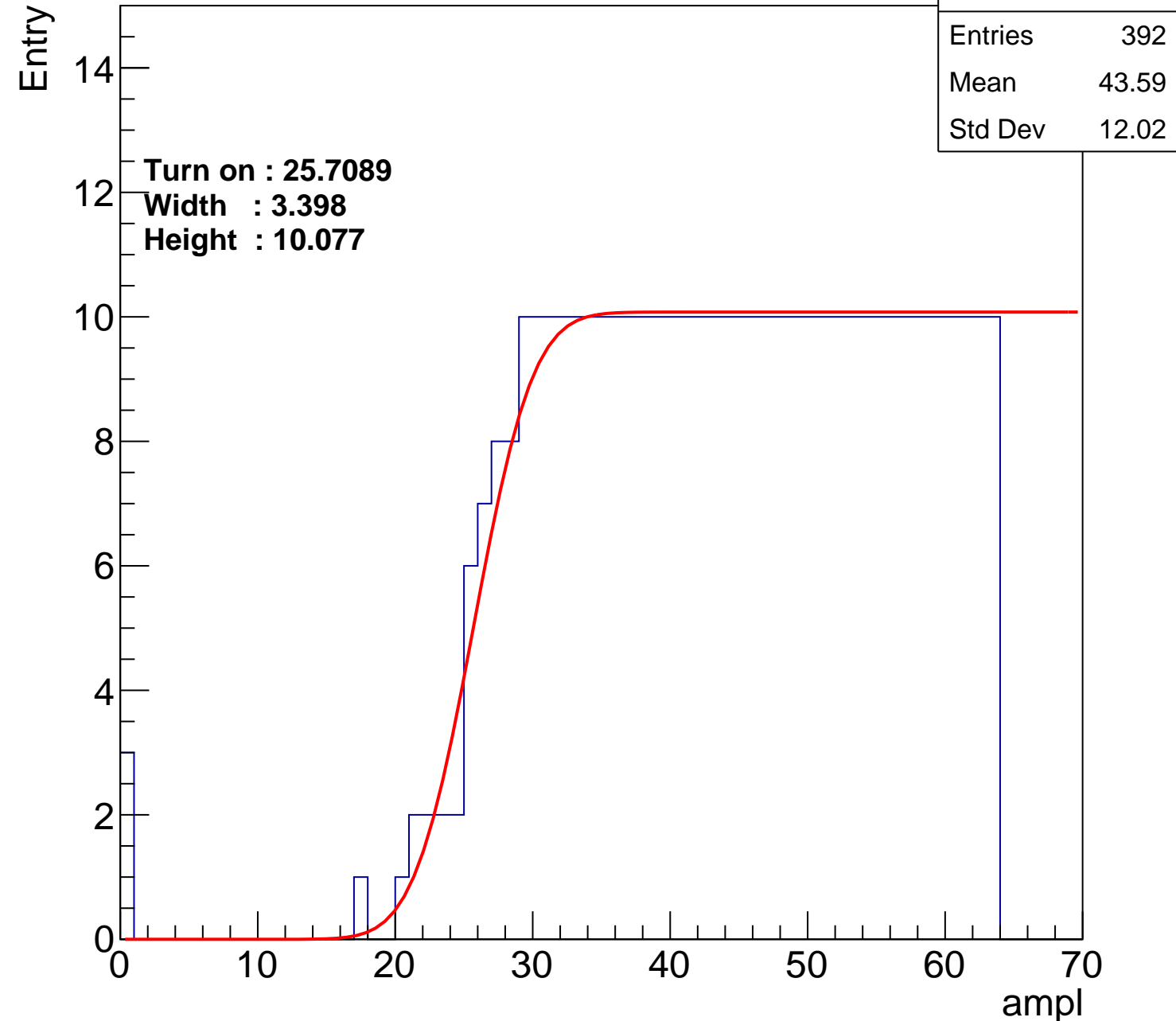
Width : 3.398

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch29

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.34
Std Dev	11.3

Turn on : 26.1621

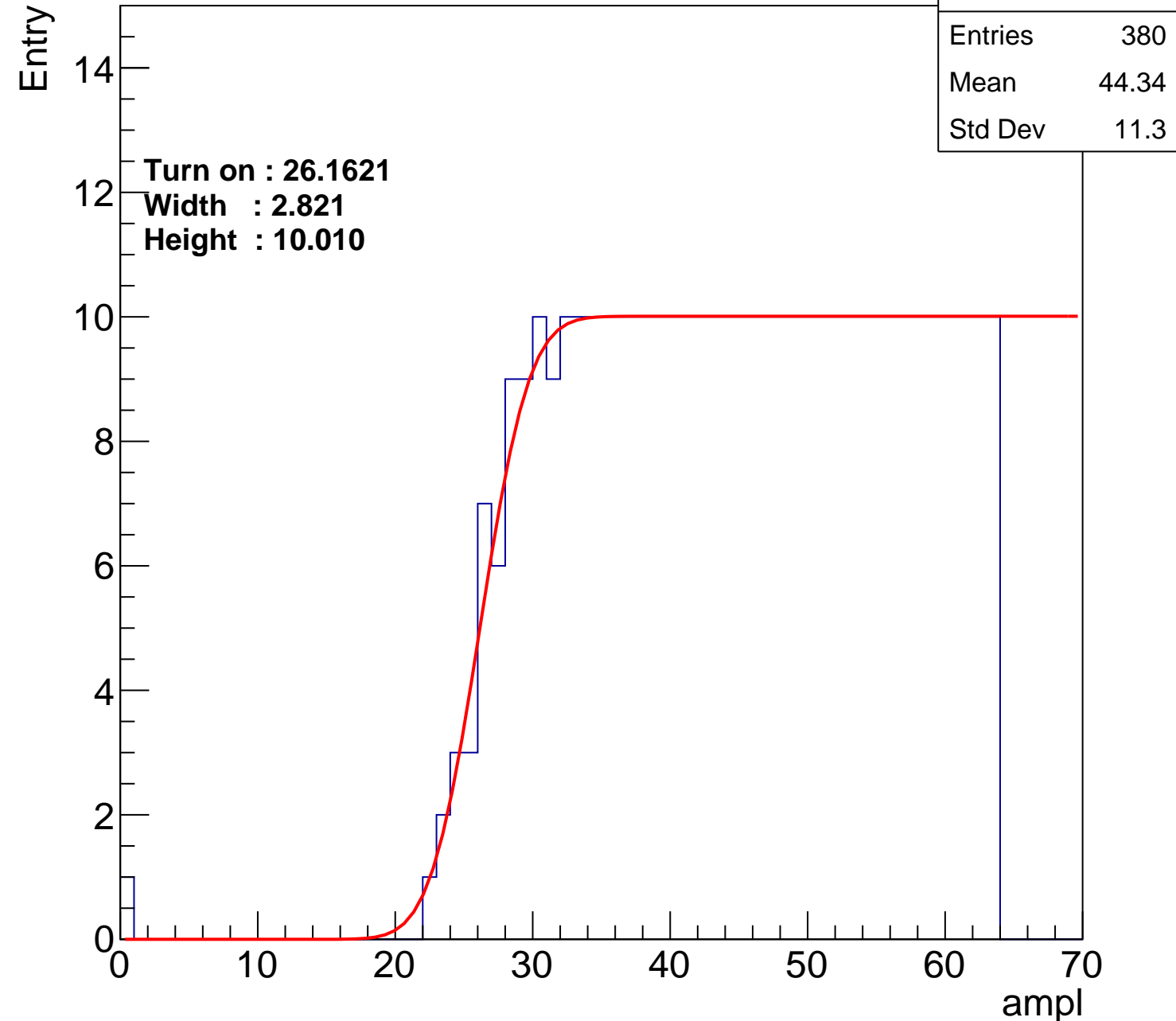
Width : 2.821

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch30

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.1
Std Dev	11.73

Turn on : 26.4046

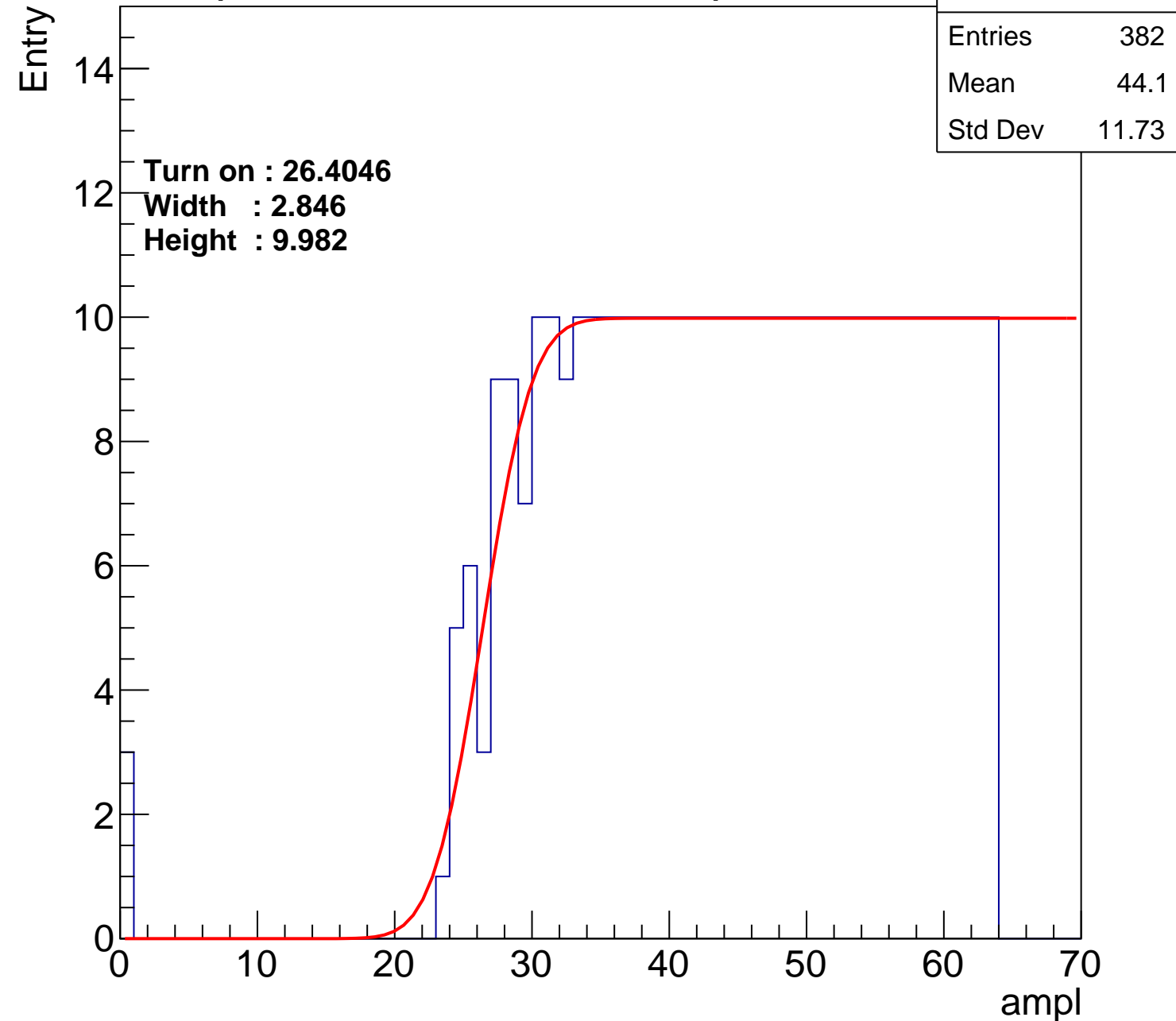
Width : 2.846

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch31

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.52
Std Dev	11.43

Turn on : 26.9713

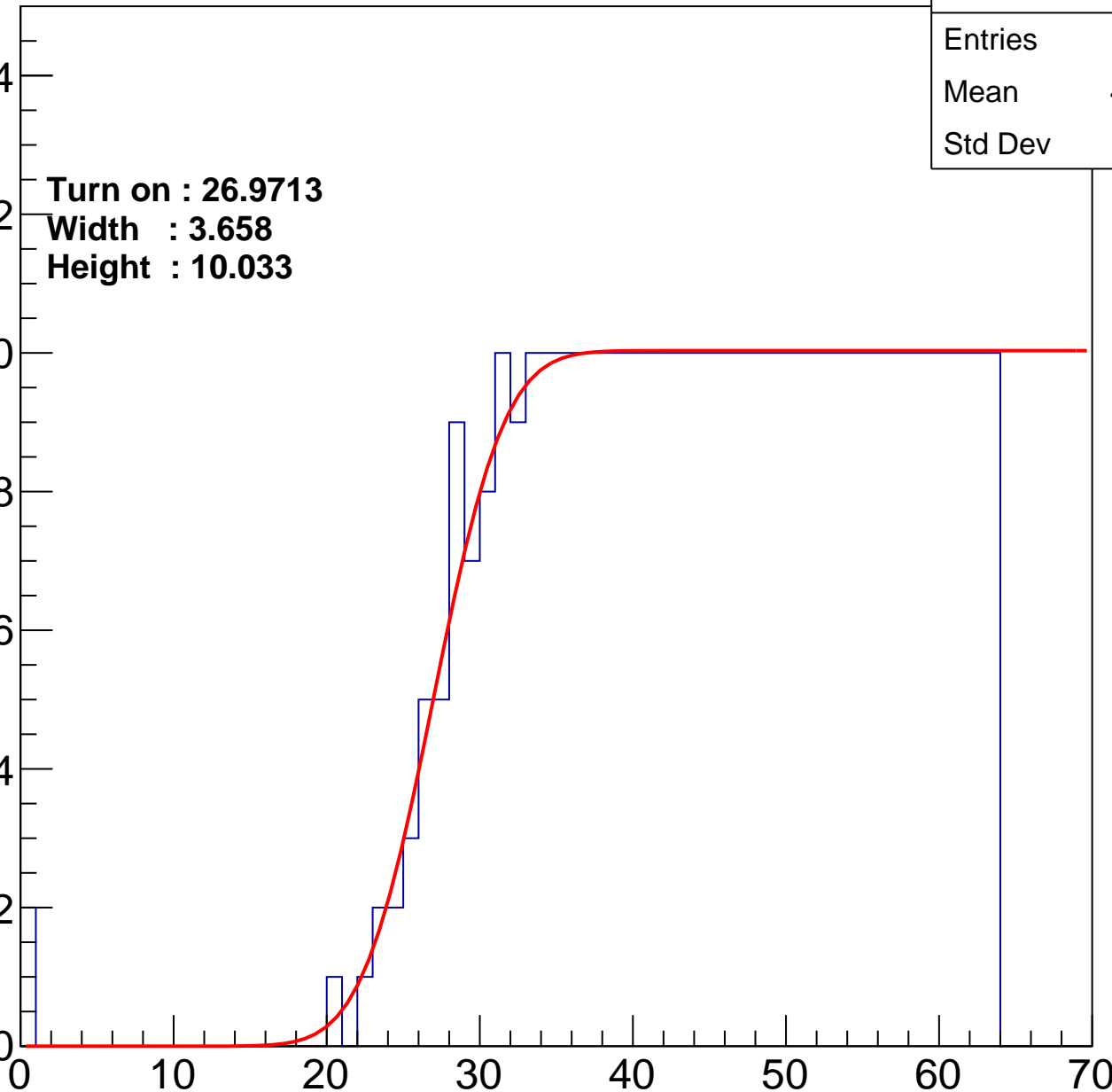
Width : 3.658

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch32

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.29
Std Dev	11.53

Turn on : 26.6461

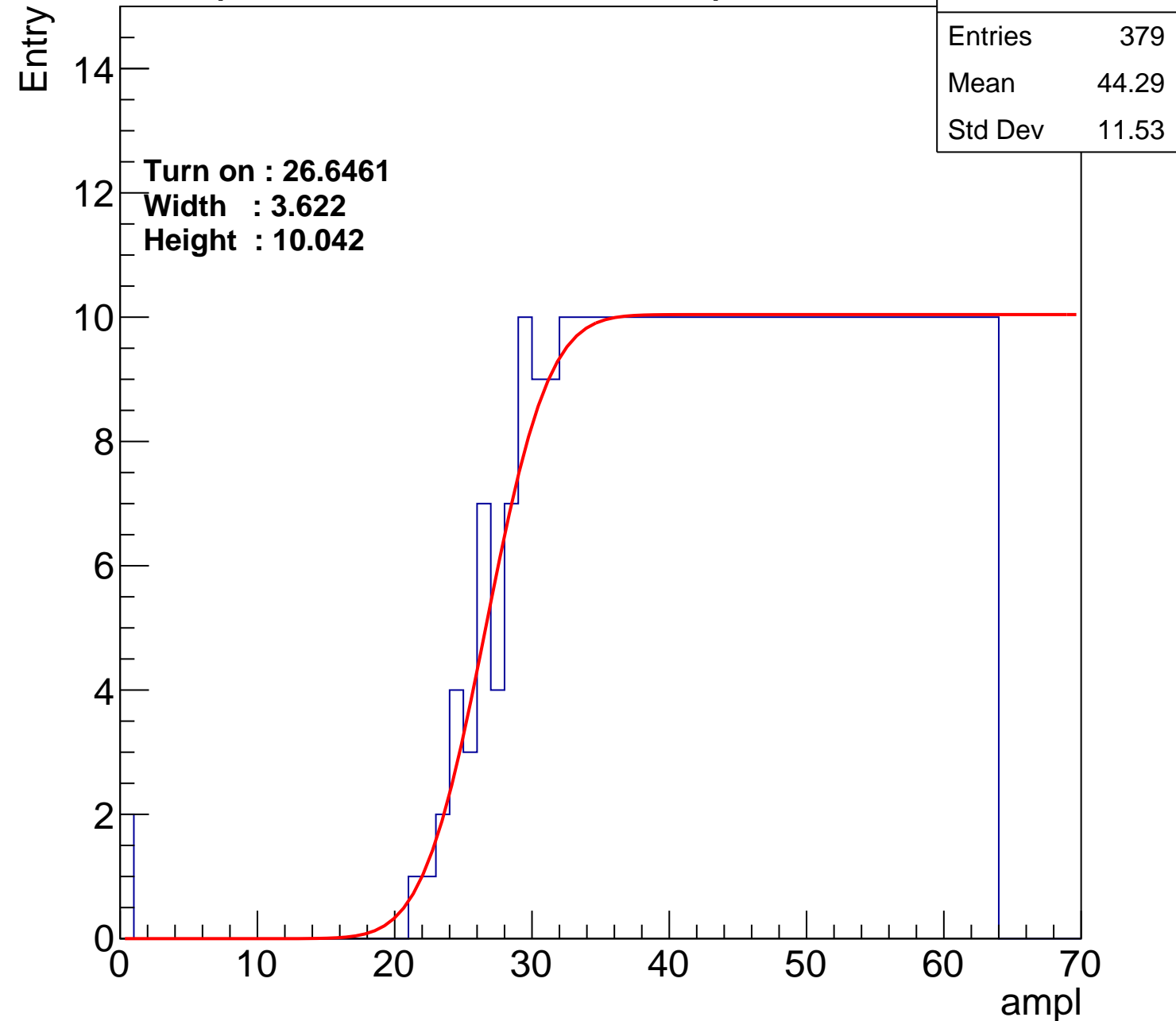
Width : 3.622

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch33

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.17
Std Dev	11.61

Turn on : 26.2203

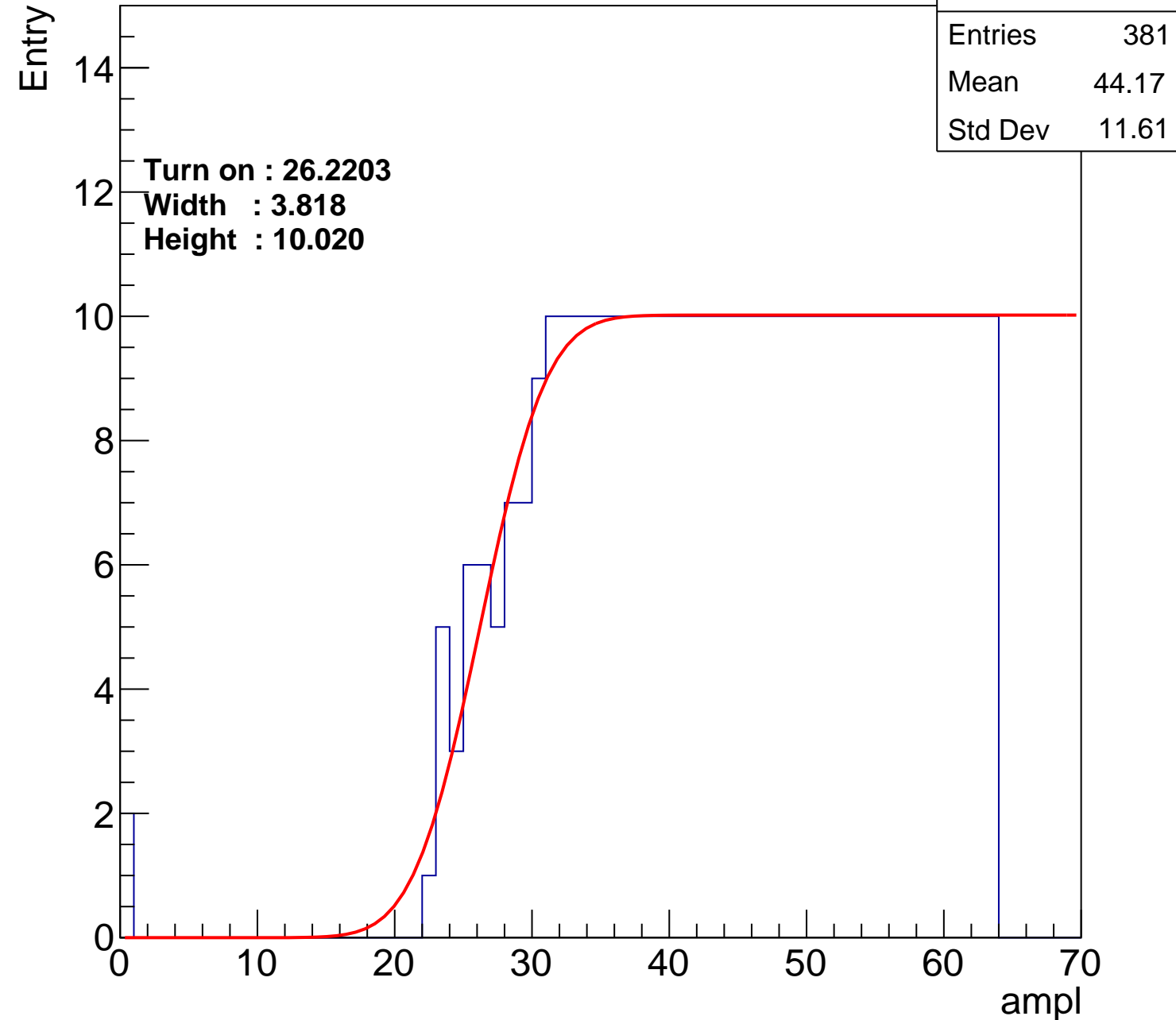
Width : 3.818

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch34

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.12
Std Dev	11.72

Turn on : 26.5621

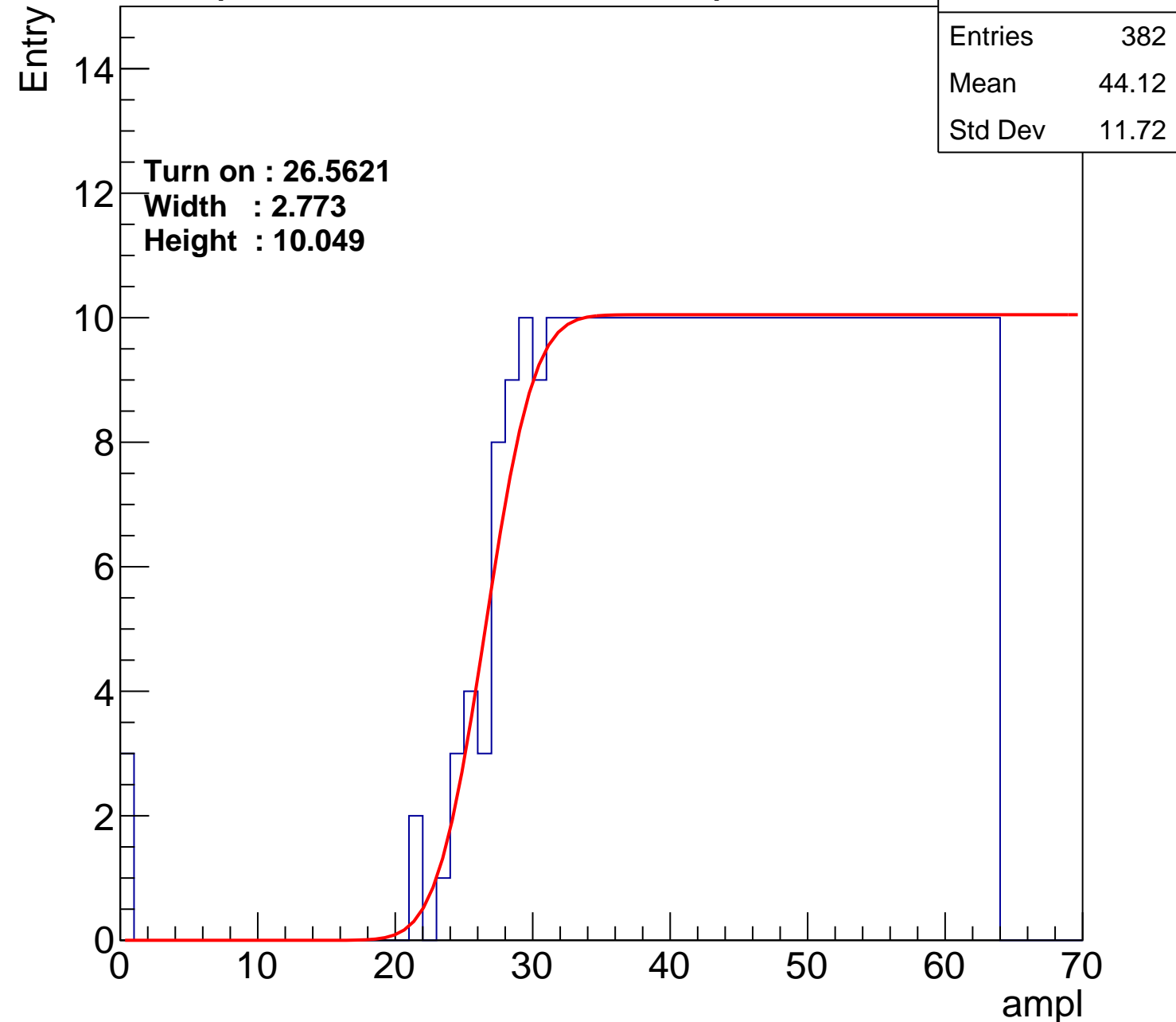
Width : 2.773

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch35

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 26.1011

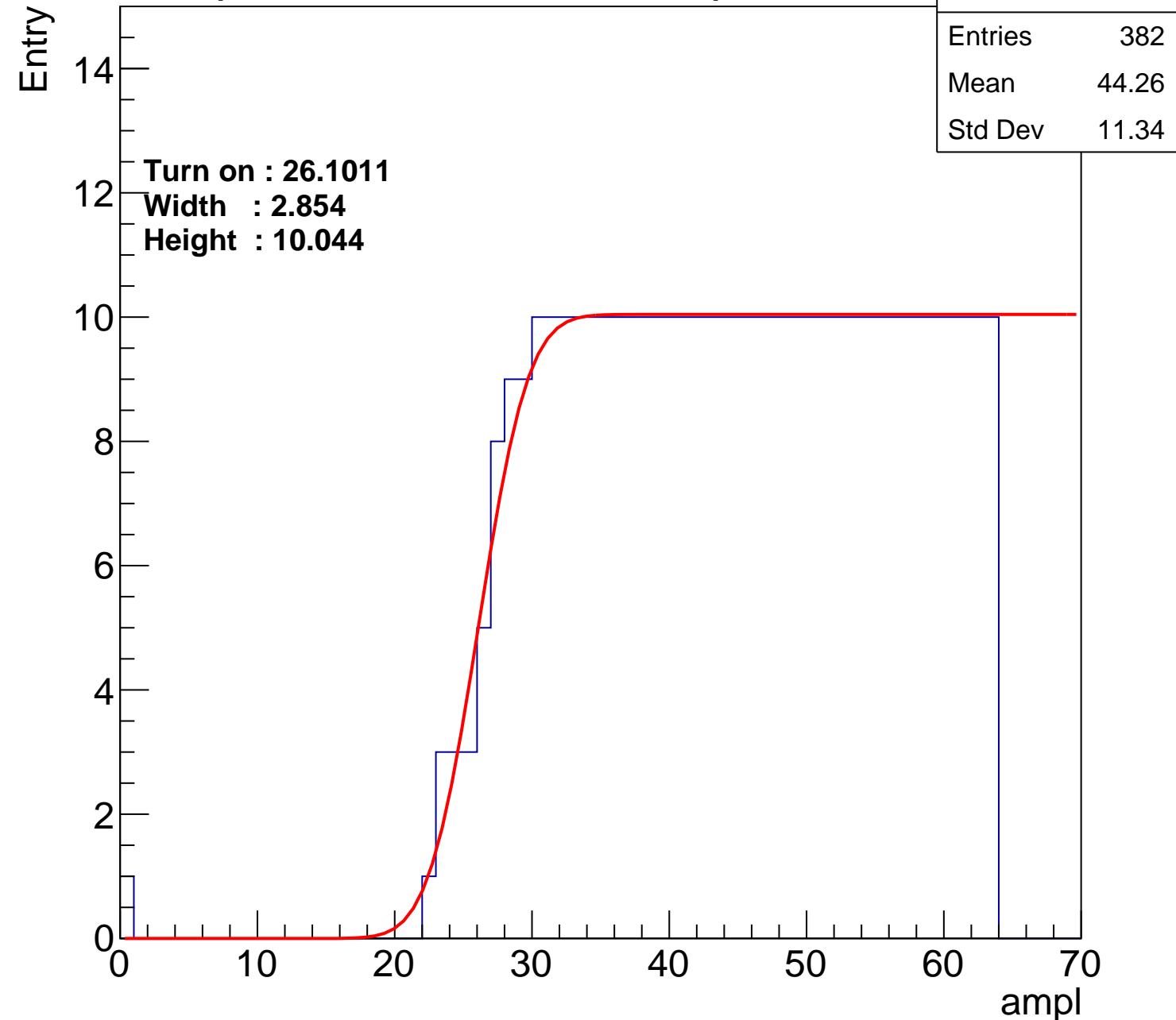
Width : 2.854

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch36

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	43.19
Std Dev	12.07

Turn on : 24.2471

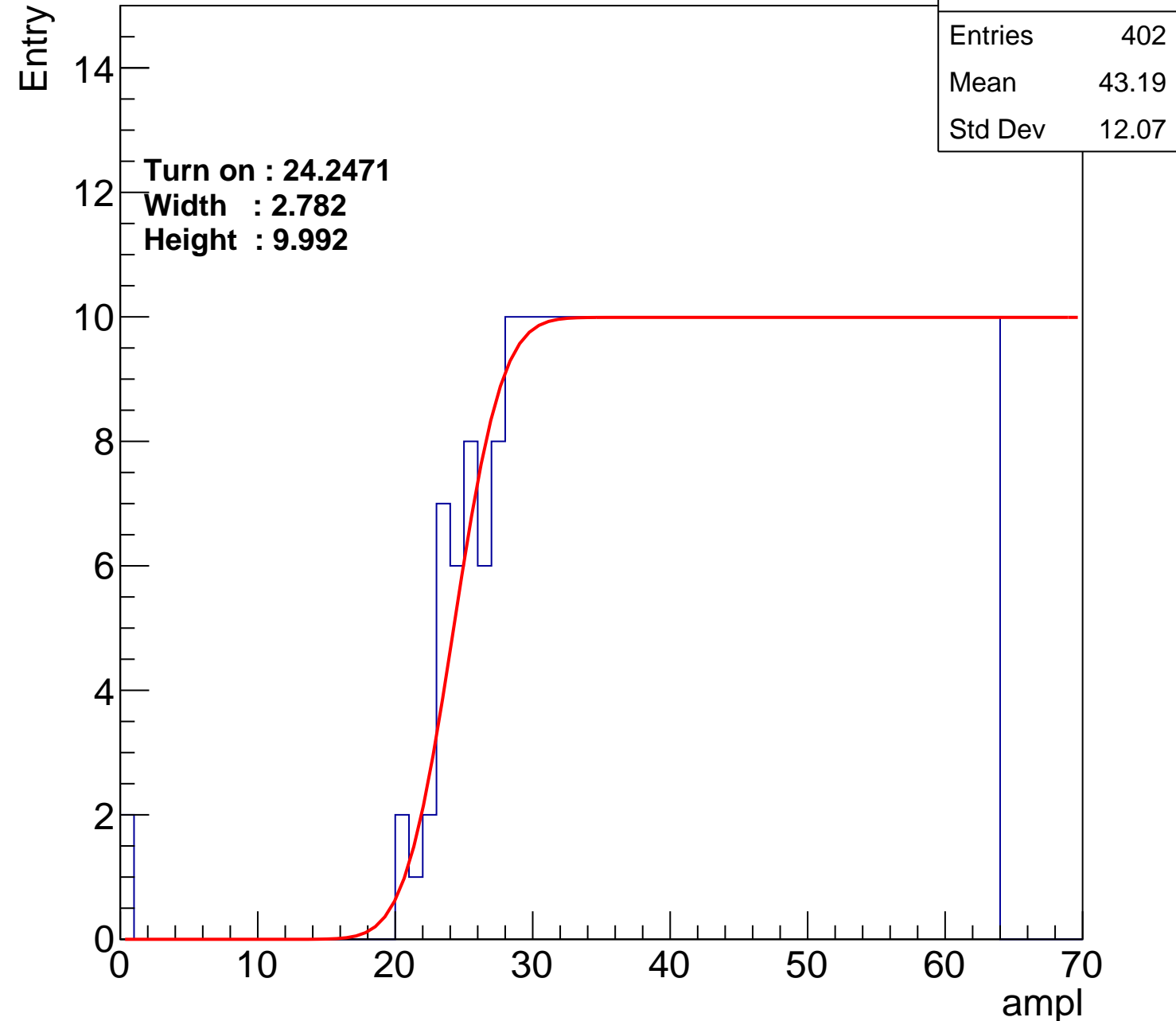
Width : 2.782

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch37

calib_packv5_042523_0143.root, FC#11, port A2

Entries	359
Mean	45.38
Std Dev	10.75

Turn on : 28.8356

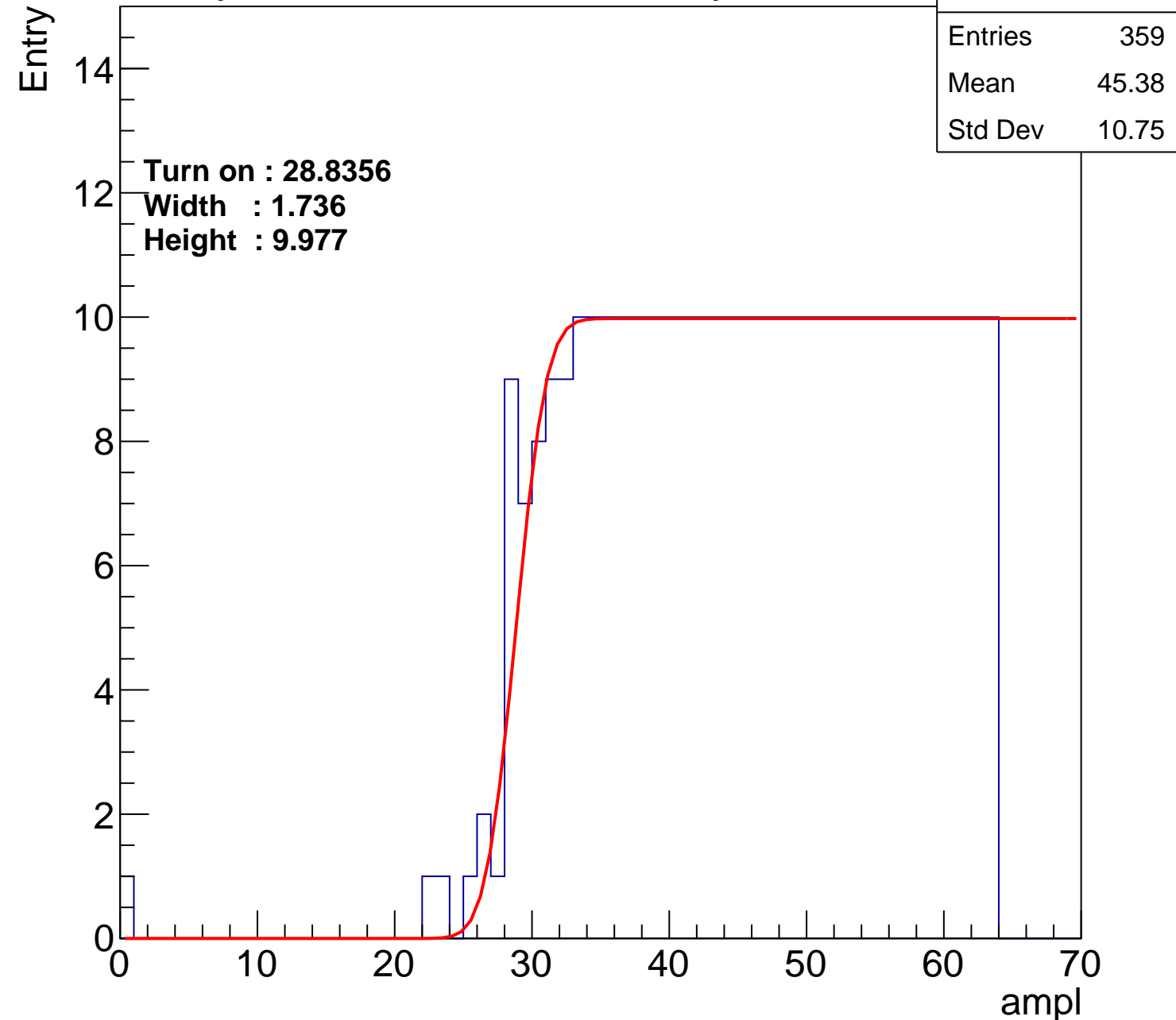
Width : 1.736

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch38

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.61
Std Dev	12.05

Turn on : 25.7981

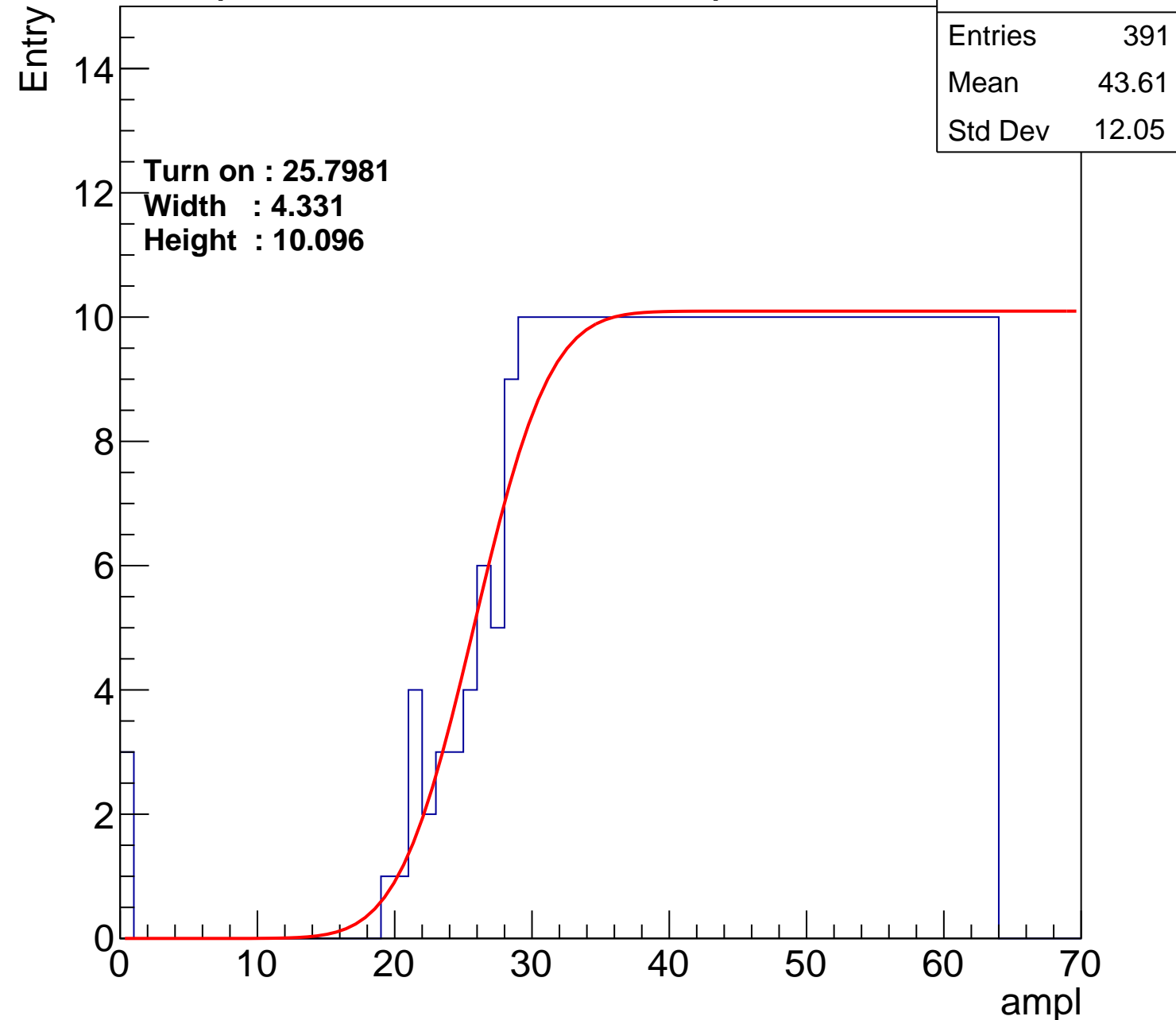
Width : 4.331

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch39

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.6
Std Dev	12.24

Turn on : 25.4316

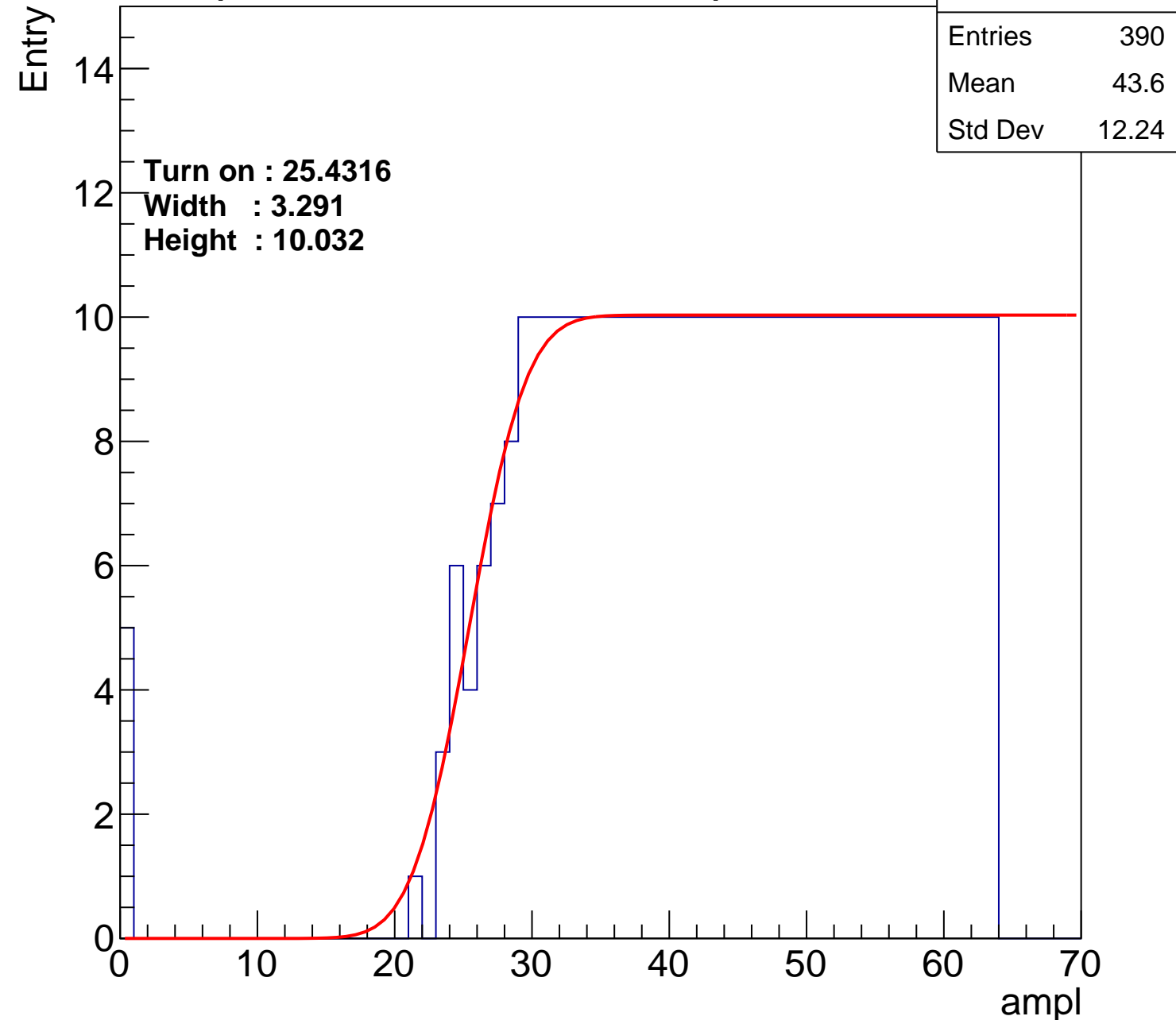
Width : 3.291

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch40

calib_packv5_042523_0143.root, FC#11, port A2

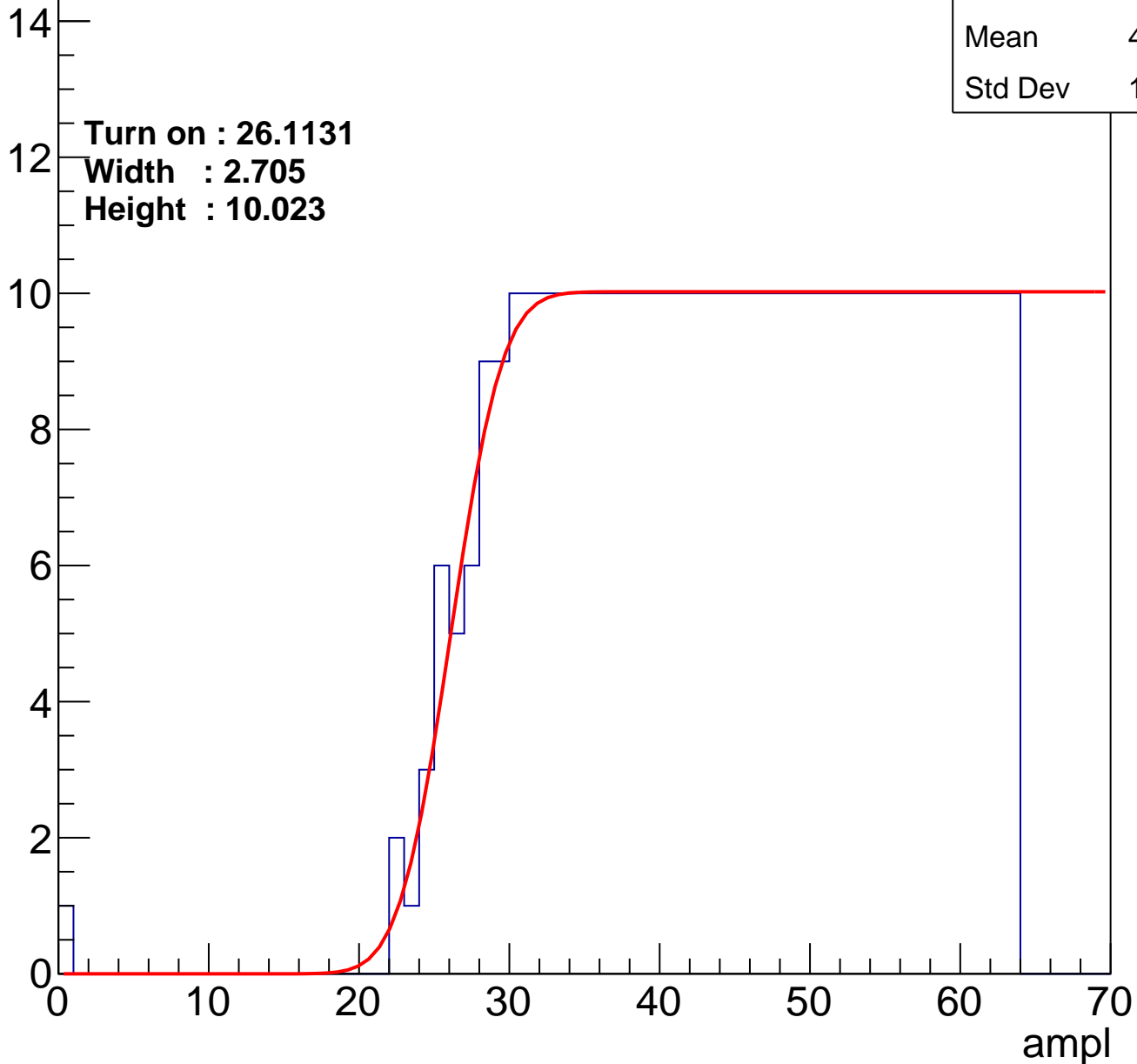
Entry

Entries	382
Mean	44.25
Std Dev	11.35

Turn on : 26.1131

Width : 2.705

Height : 10.023



B1L102S, U9-ch41

calib_packv5_042523_0143.root, FC#11, port A2

Entries	360
Mean	45.32
Std Dev	10.8

Turn on : 29.0008

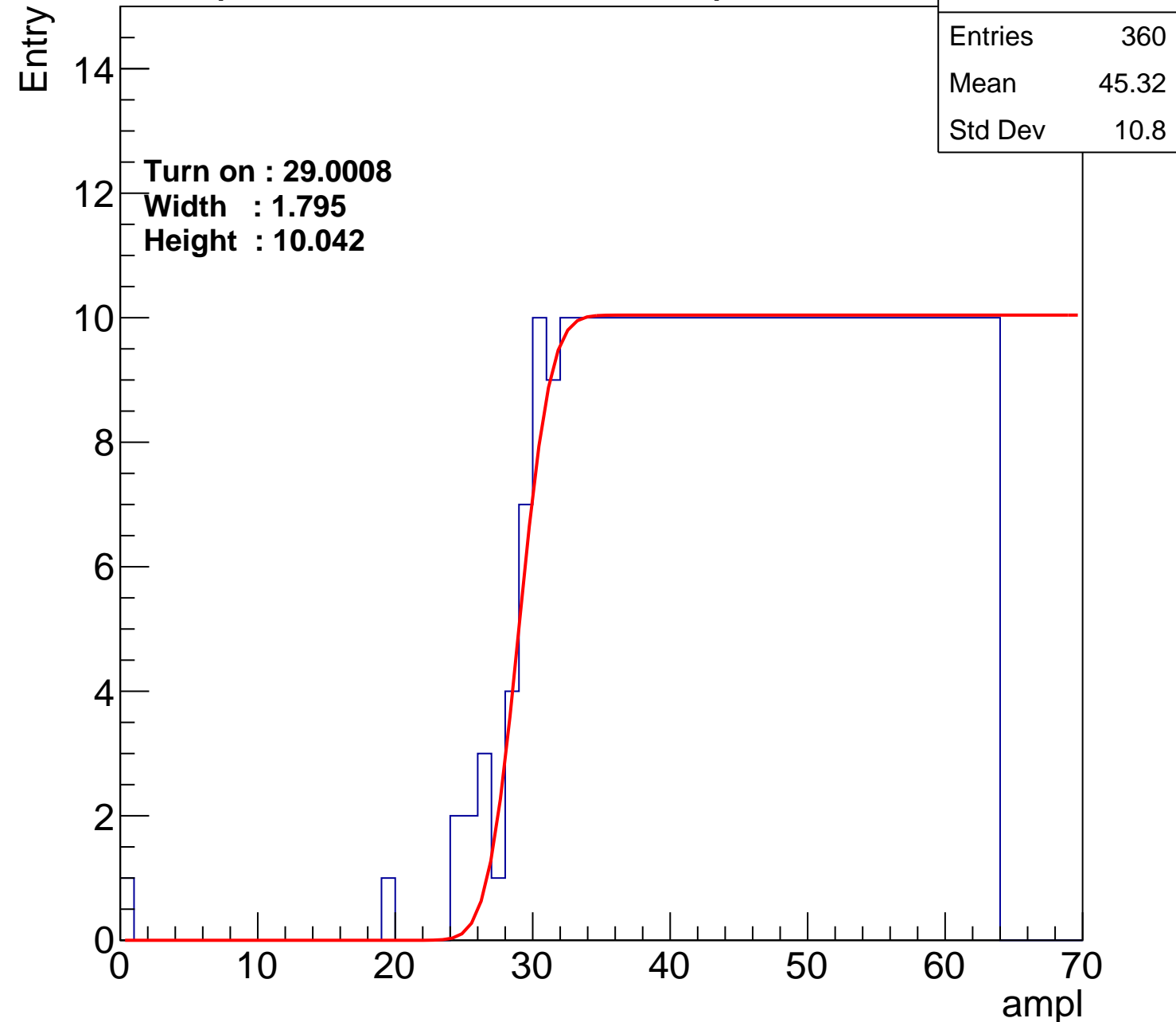
Width : 1.795

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch42

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.72
Std Dev	11.55

Turn on : 27.8466

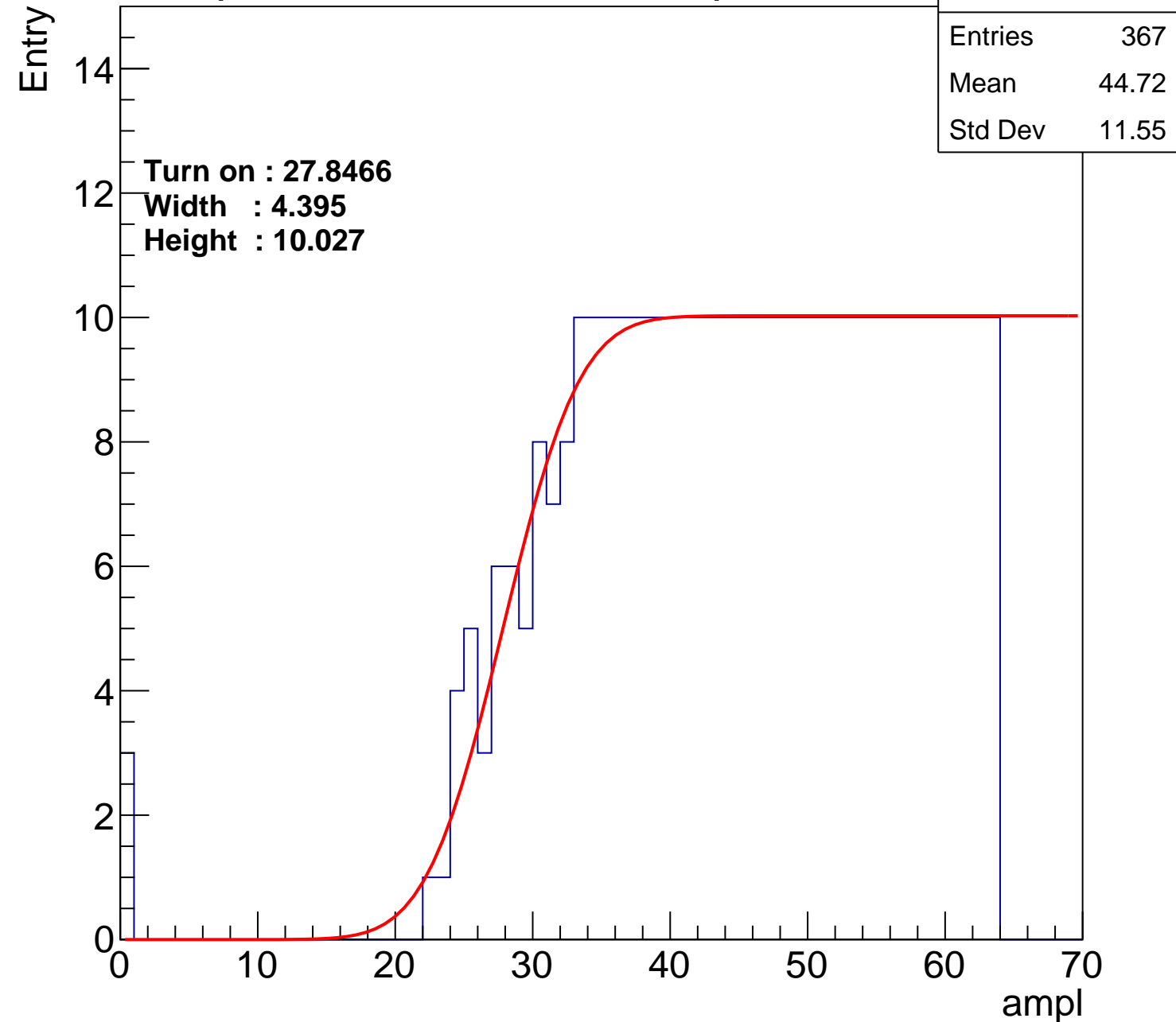
Width : 4.395

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch43

calib_packv5_042523_0143.root, FC#11, port A2

Entries	370
Mean	44.83
Std Dev	11.04

Turn on : 27.2694

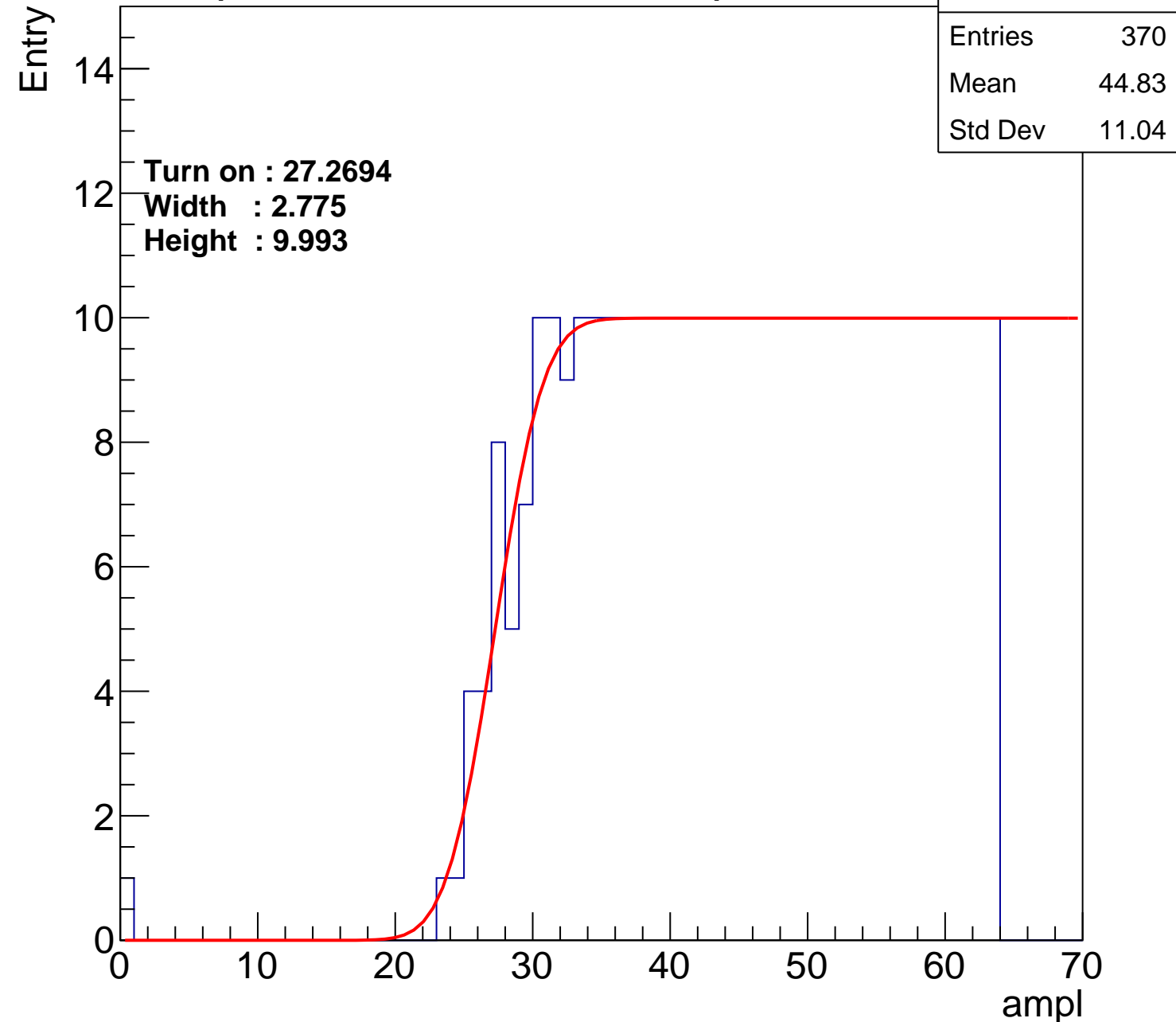
Width : 2.775

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch44

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 25.9322

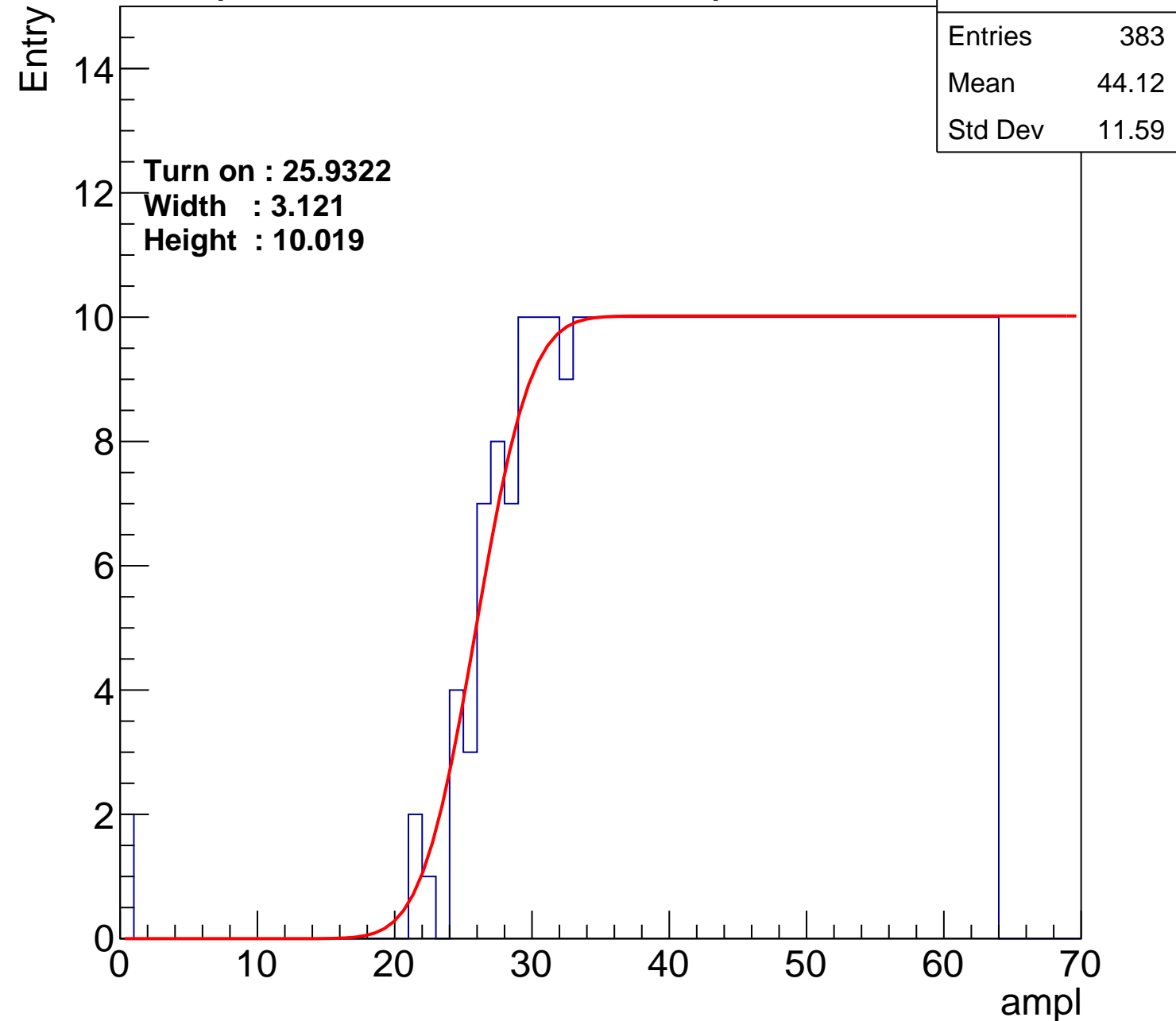
Width : 3.121

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch45

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.42
Std Dev	12.12

Turn on : 24.9551

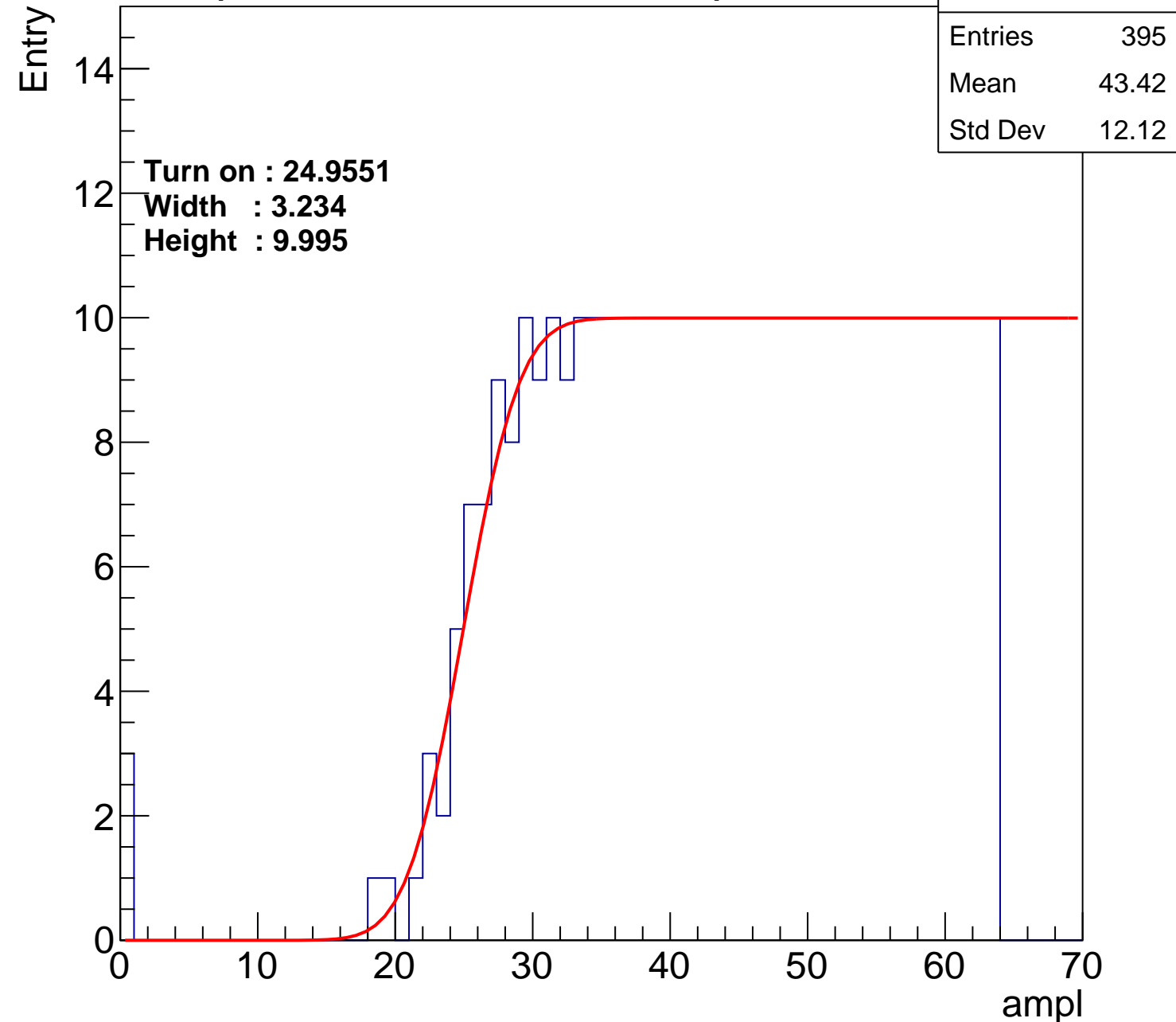
Width : 3.234

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch46

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.72
Std Dev	12.04

Turn on : 25.8240

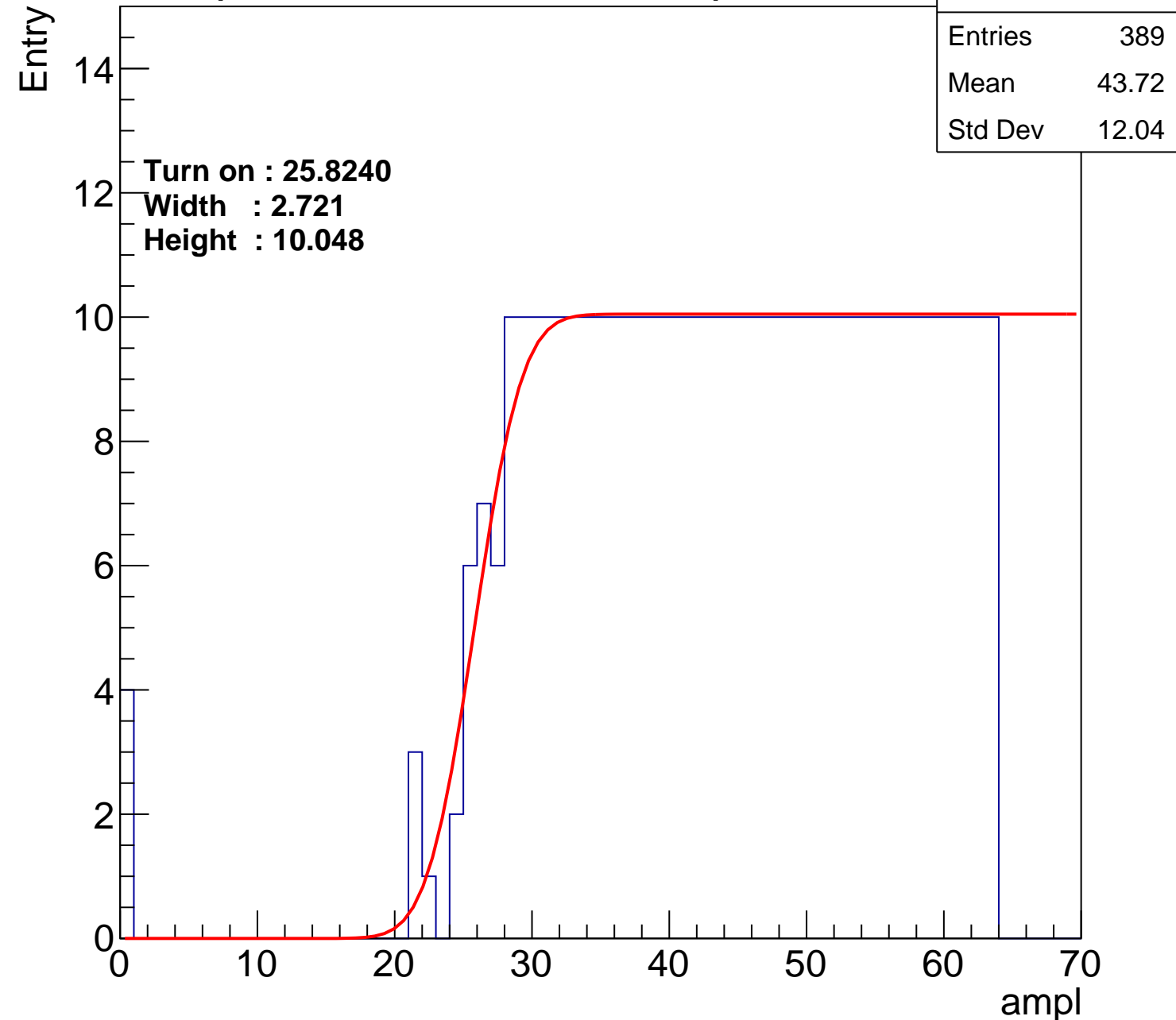
Width : 2.721

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch47

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.64
Std Dev	11.65

Turn on : 27.7382

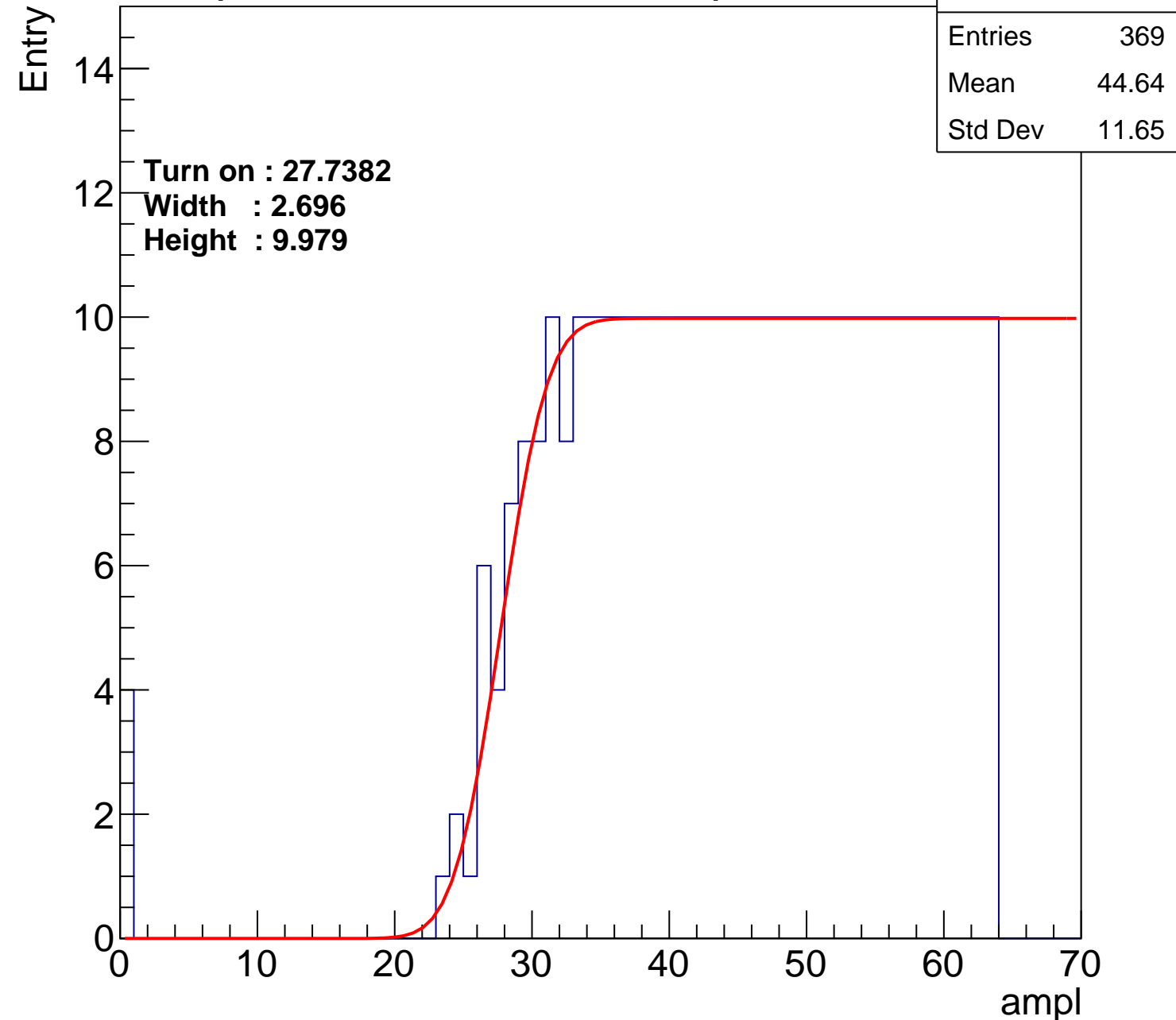
Width : 2.696

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch48

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.44
Std Dev	12.05

Turn on : 24.4830

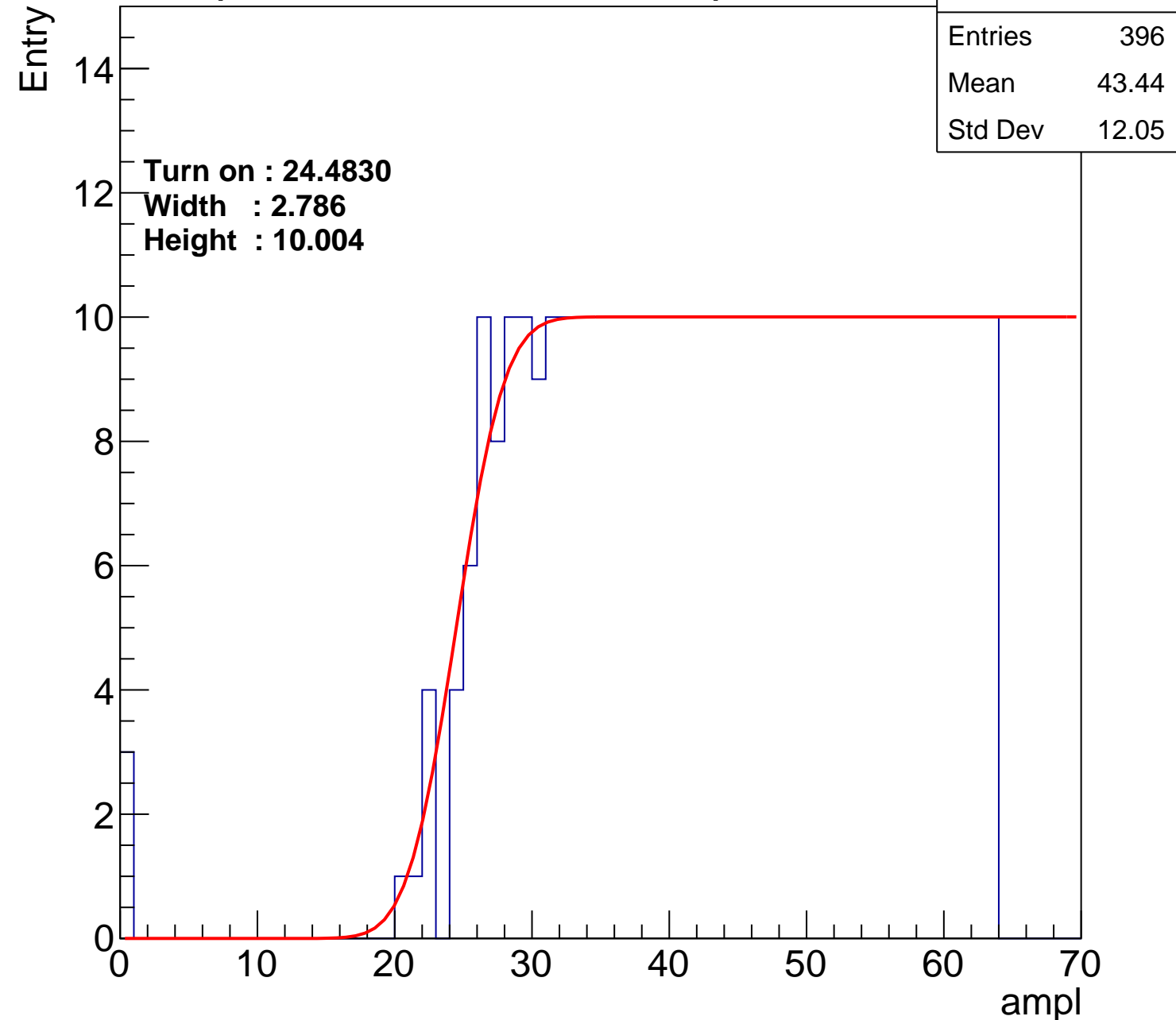
Width : 2.786

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch49

calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.34
Std Dev	11.98

Turn on : 24.5170

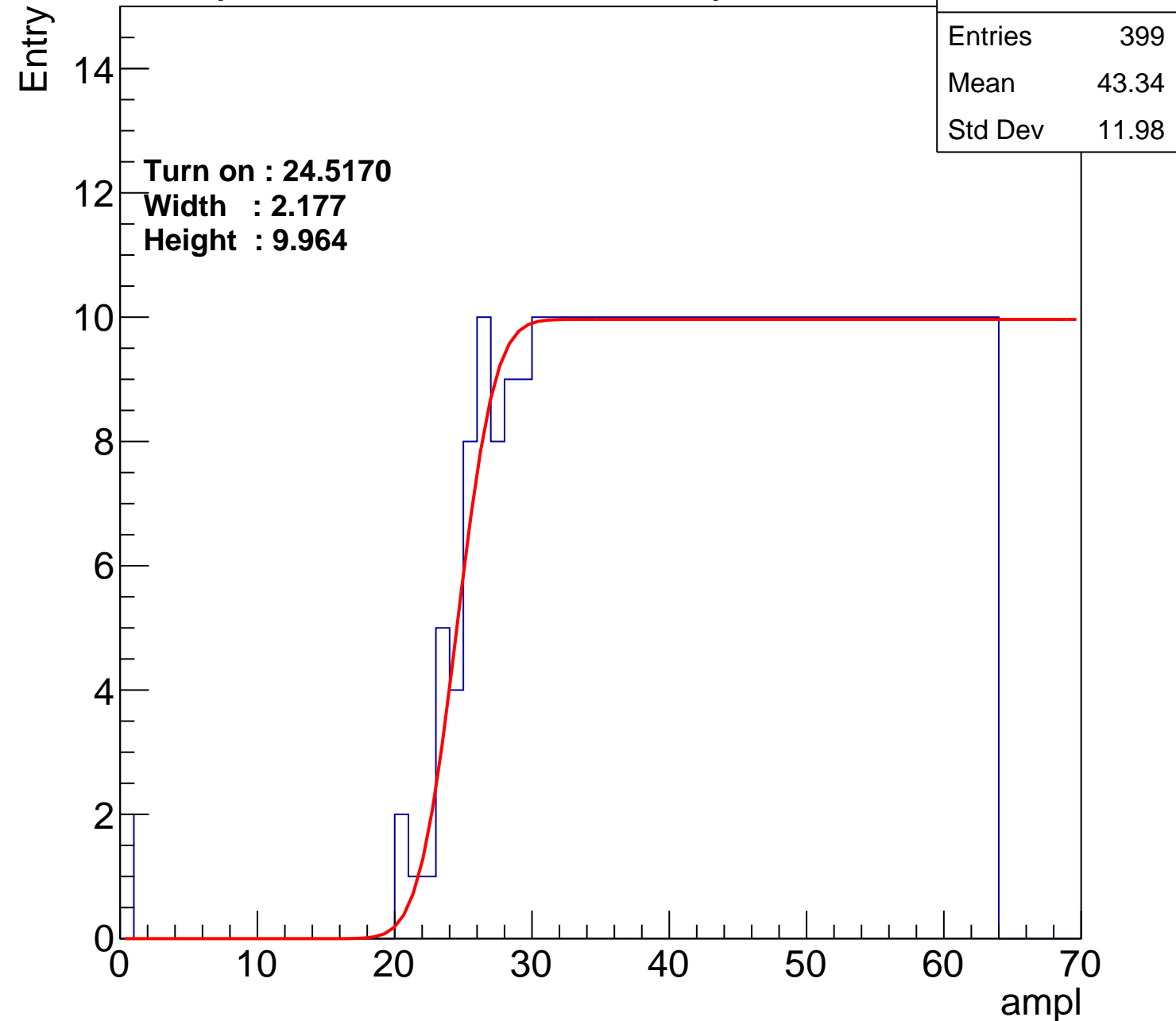
Width : 2.177

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch50

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.42
Std Dev	12.12

Turn on : 24.7872

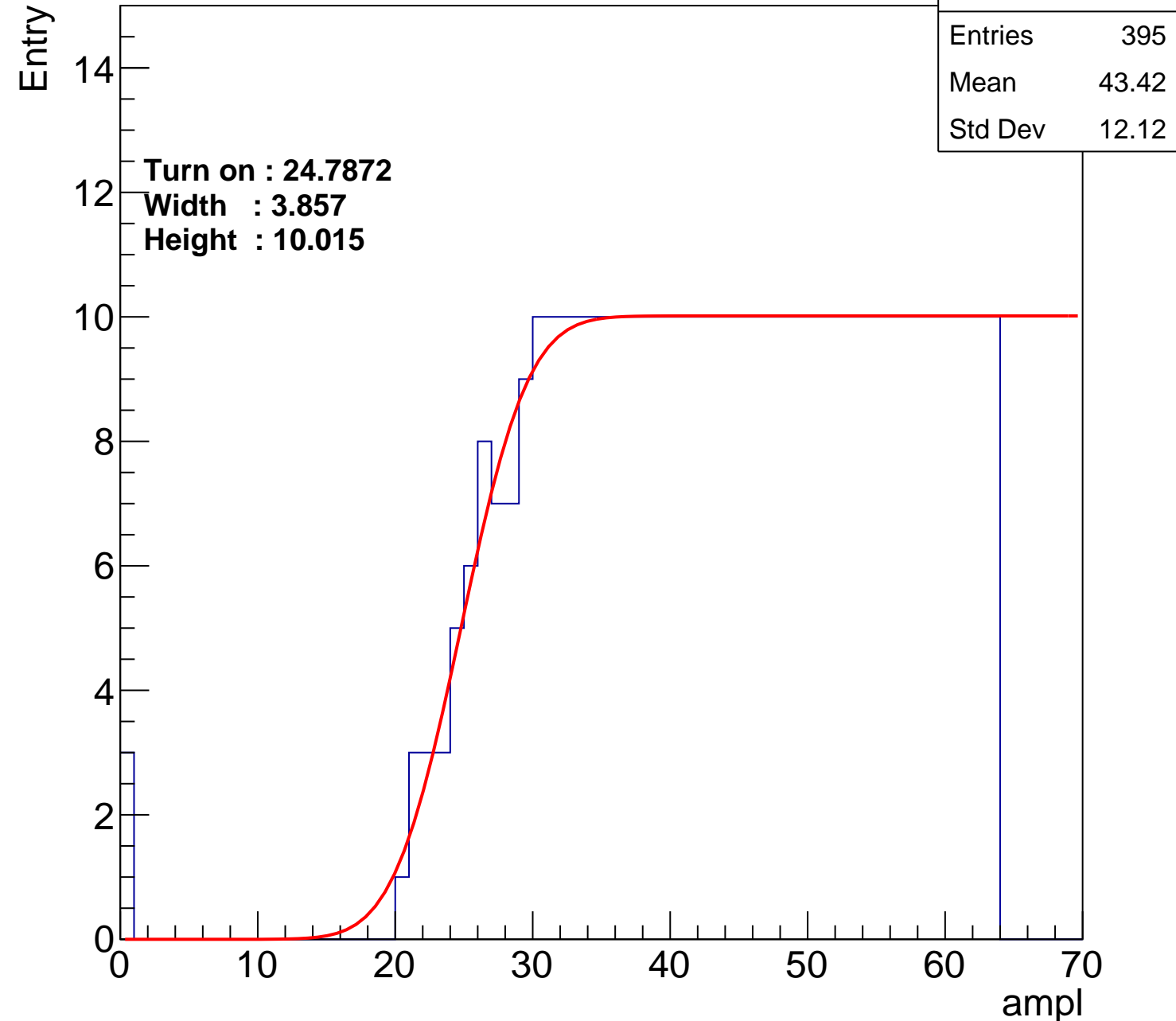
Width : 3.857

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch51

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.84
Std Dev	11.57

Turn on : 27.8754

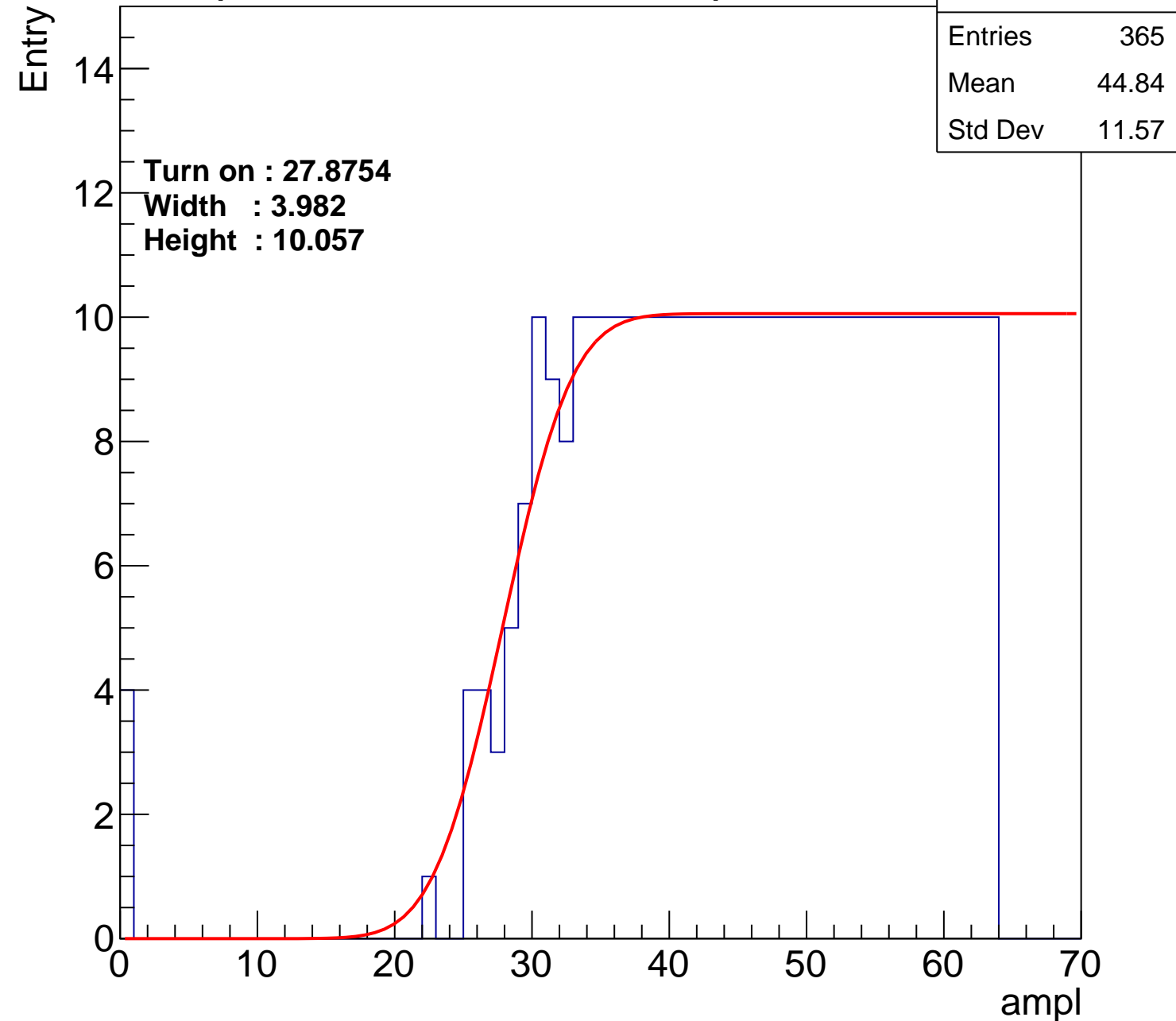
Width : 3.982

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch52

calib_packv5_042523_0143.root, FC#11, port A2

Entries	403
Mean	43.11
Std Dev	12.14

Turn on : 23.8853

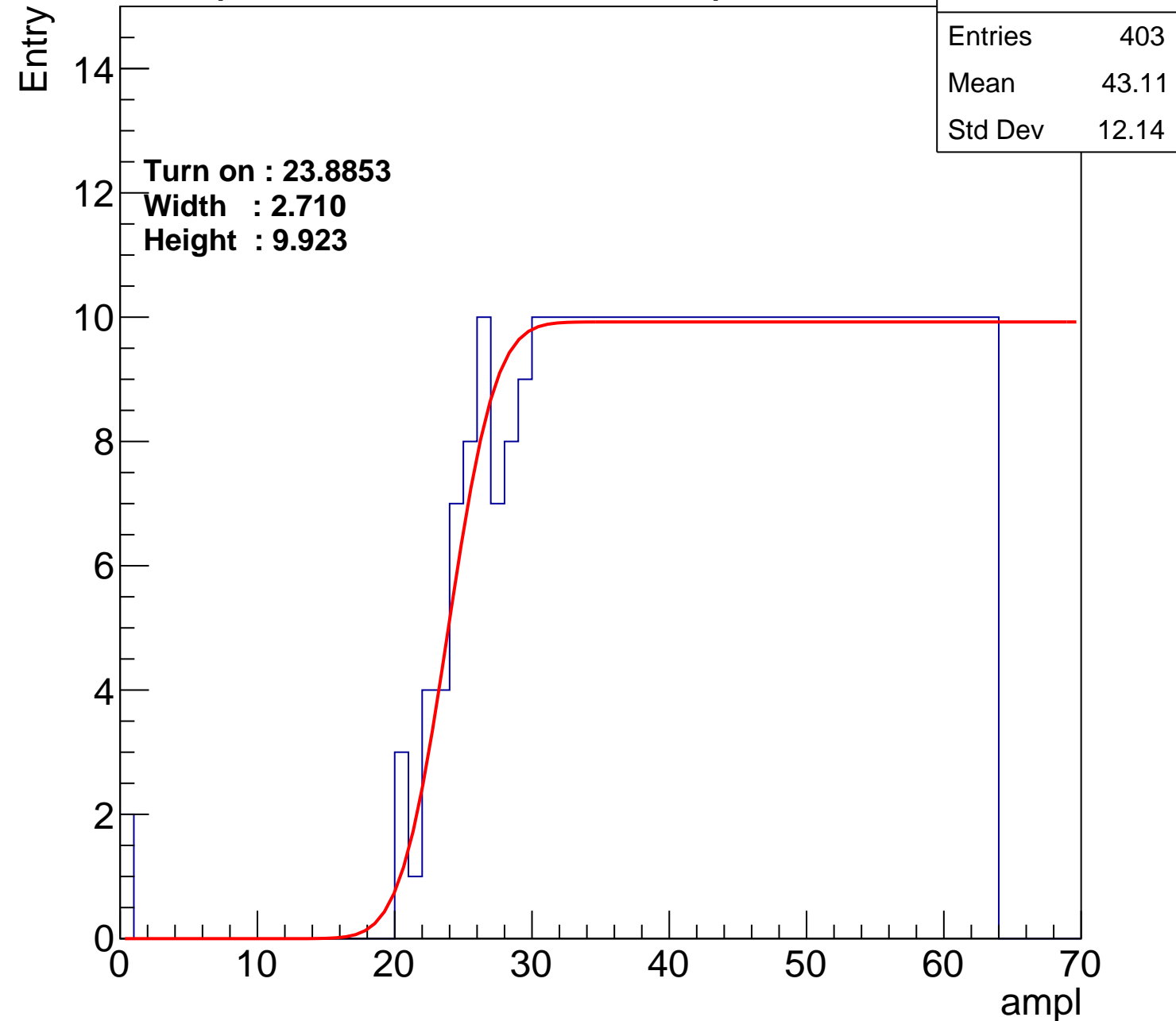
Width : 2.710

Height : 9.923

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch53

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	43.23
Std Dev	11.95

Turn on : 24.2467

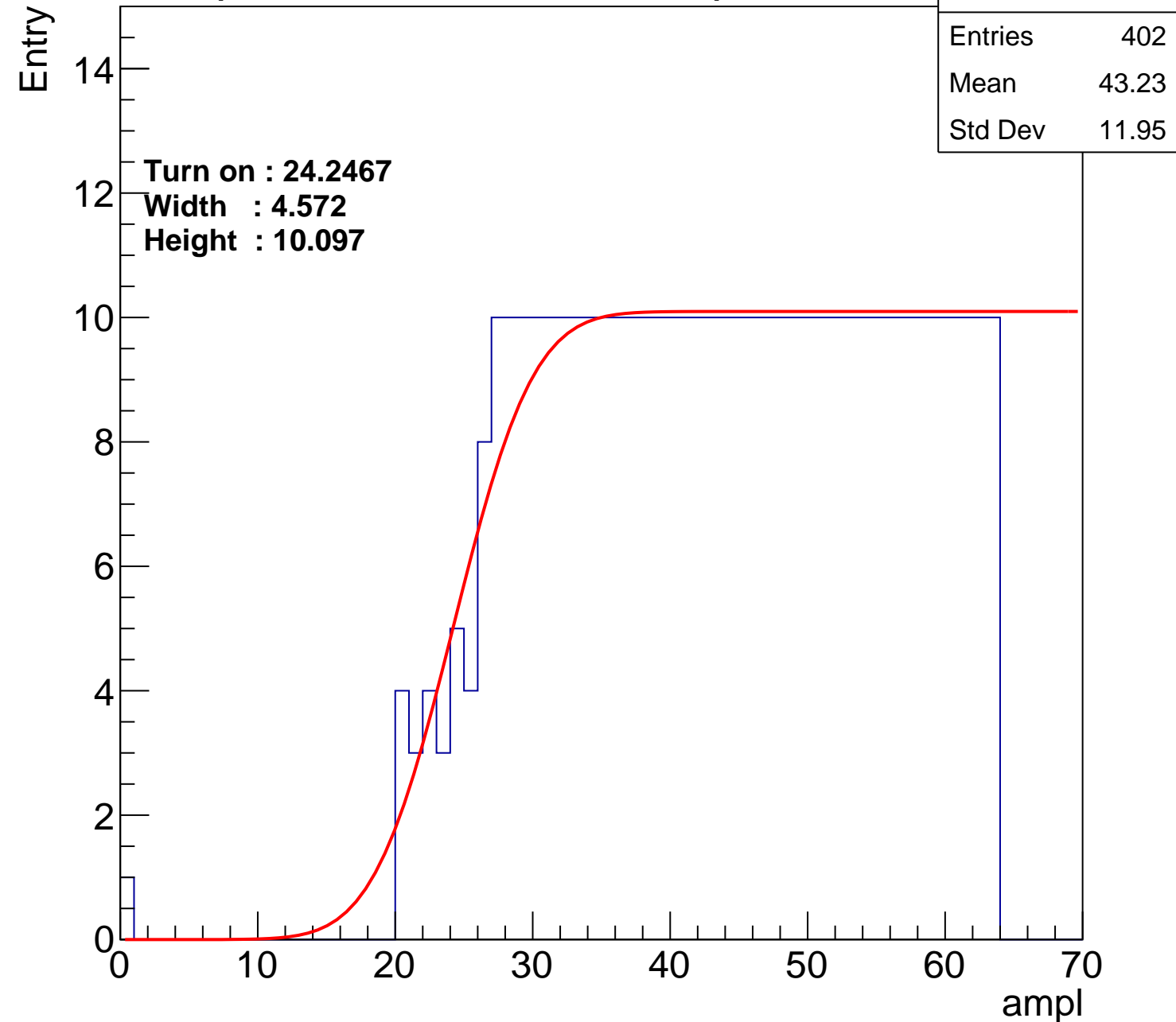
Width : 4.572

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch54

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	44
Std Dev	11.54

Turn on : 25.8714

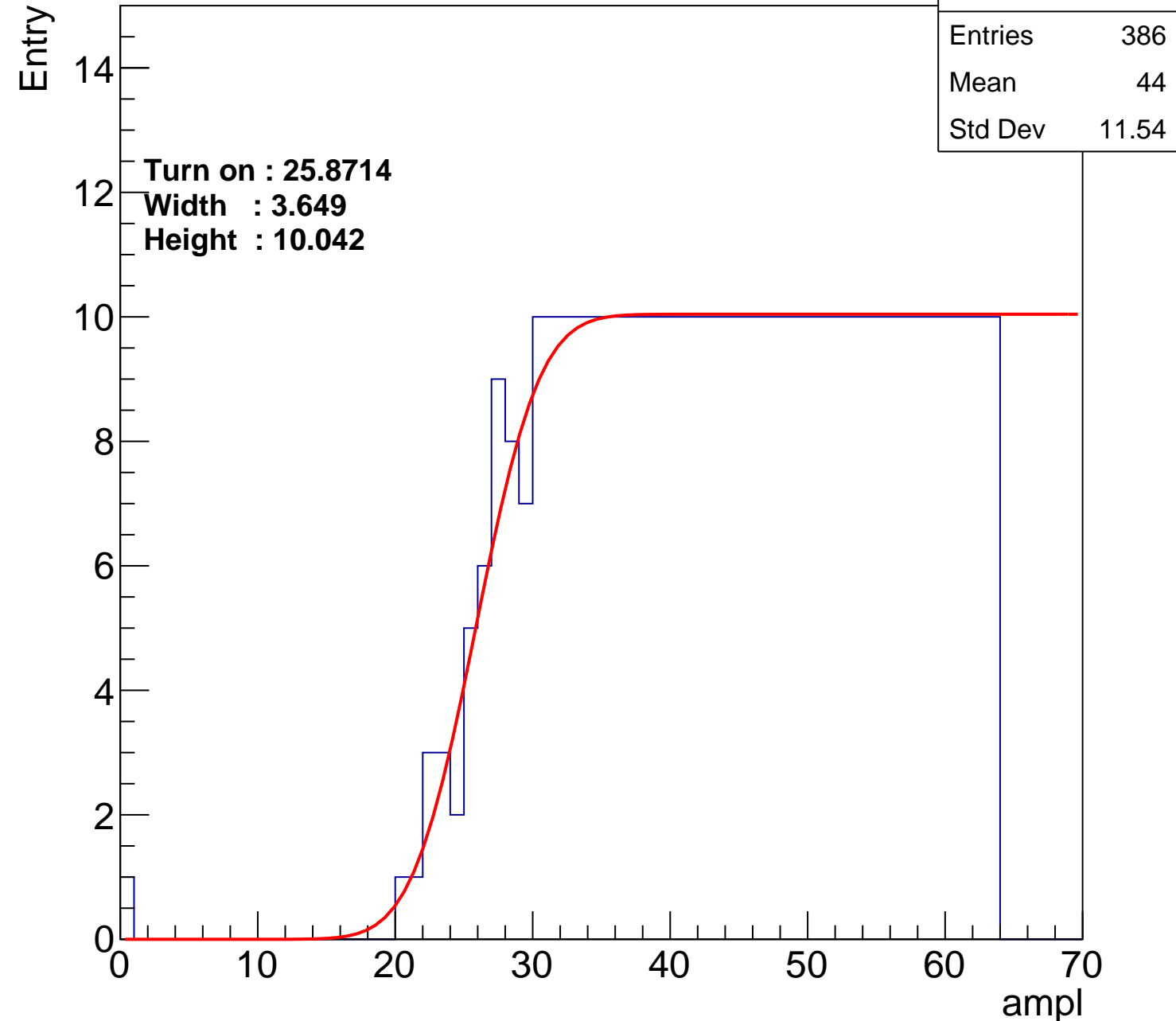
Width : 3.649

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch55

calib_packv5_042523_0143.root, FC#11, port A2

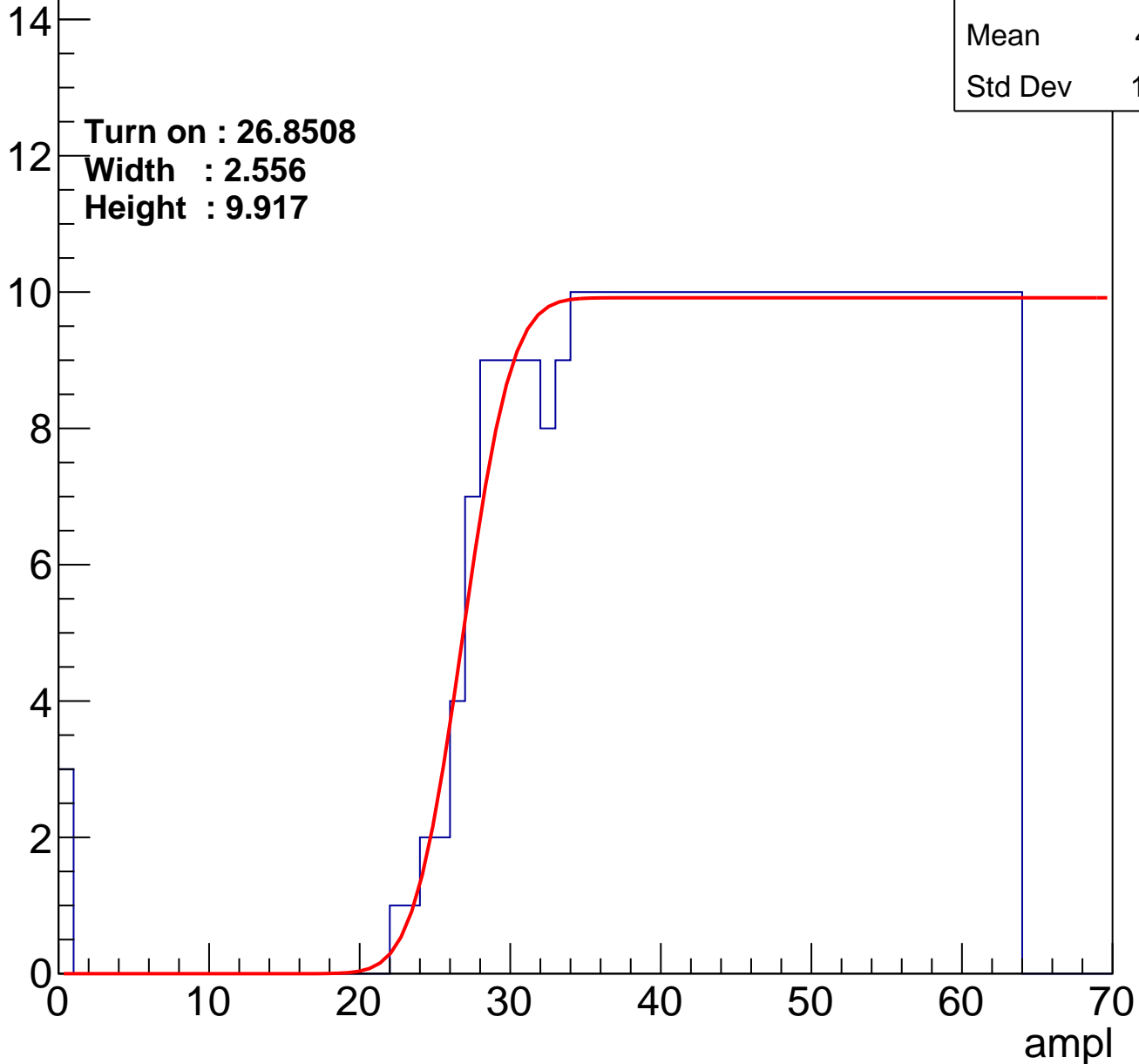
Entries	373
Mean	44.51
Std Dev	11.56

Turn on : 26.8508

Width : 2.556

Height : 9.917

Entry



B1L102S, U9-ch56

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.5994

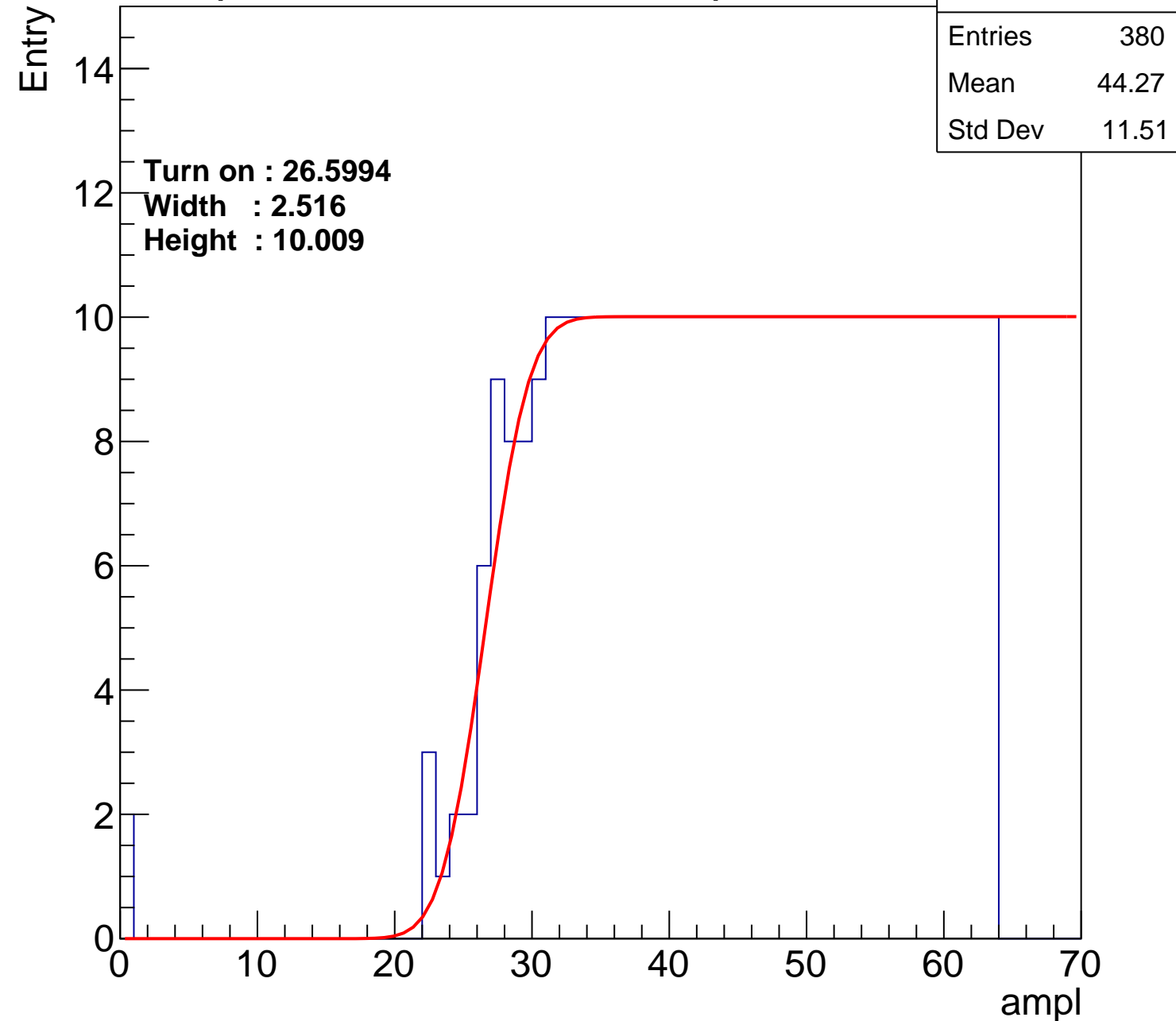
Width : 2.516

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch57

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.89
Std Dev	11.28

Turn on : 28.5816

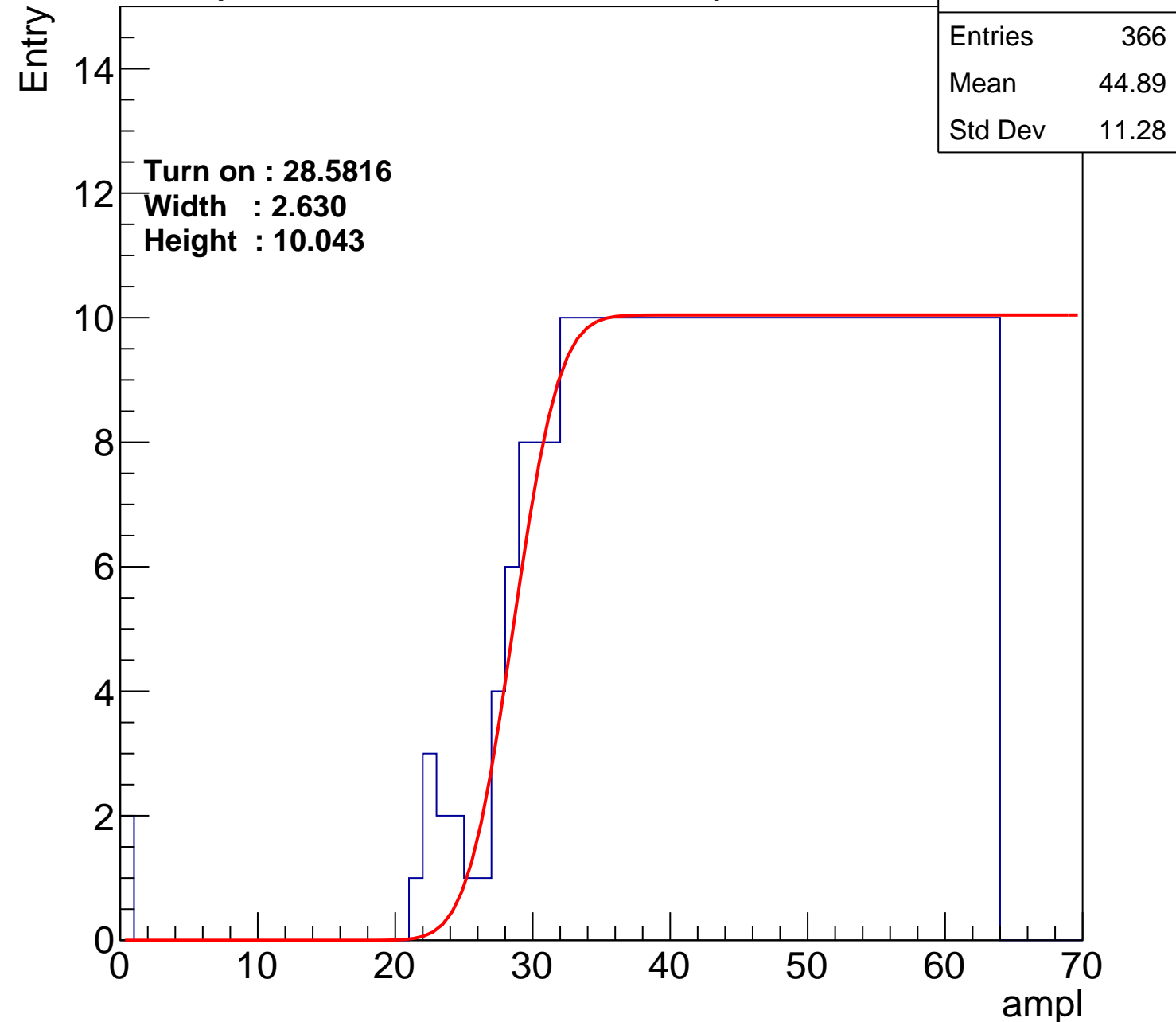
Width : 2.630

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch58

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.38
Std Dev	11.81

Turn on : 24.3964

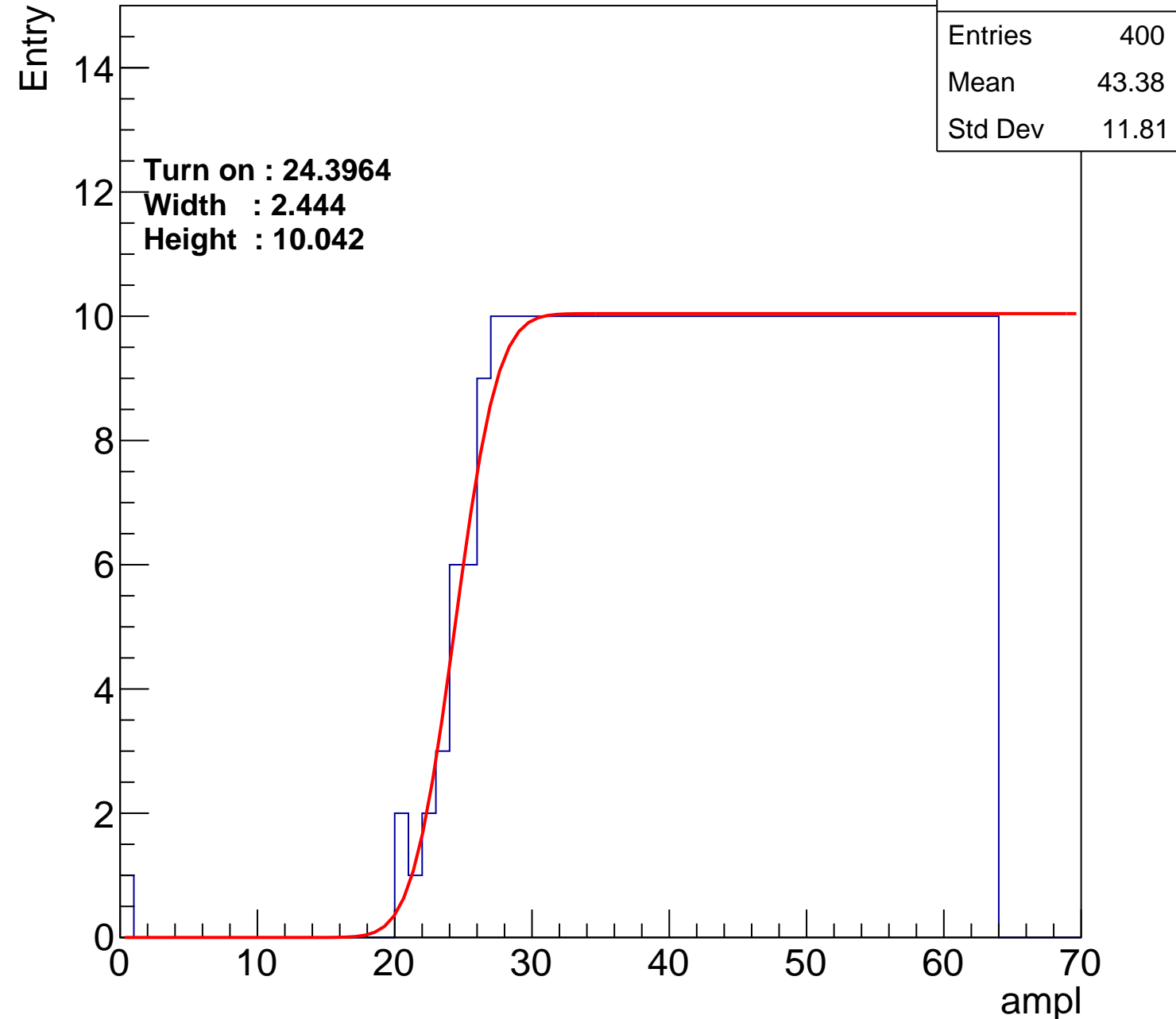
Width : 2.444

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch59

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 27.1418

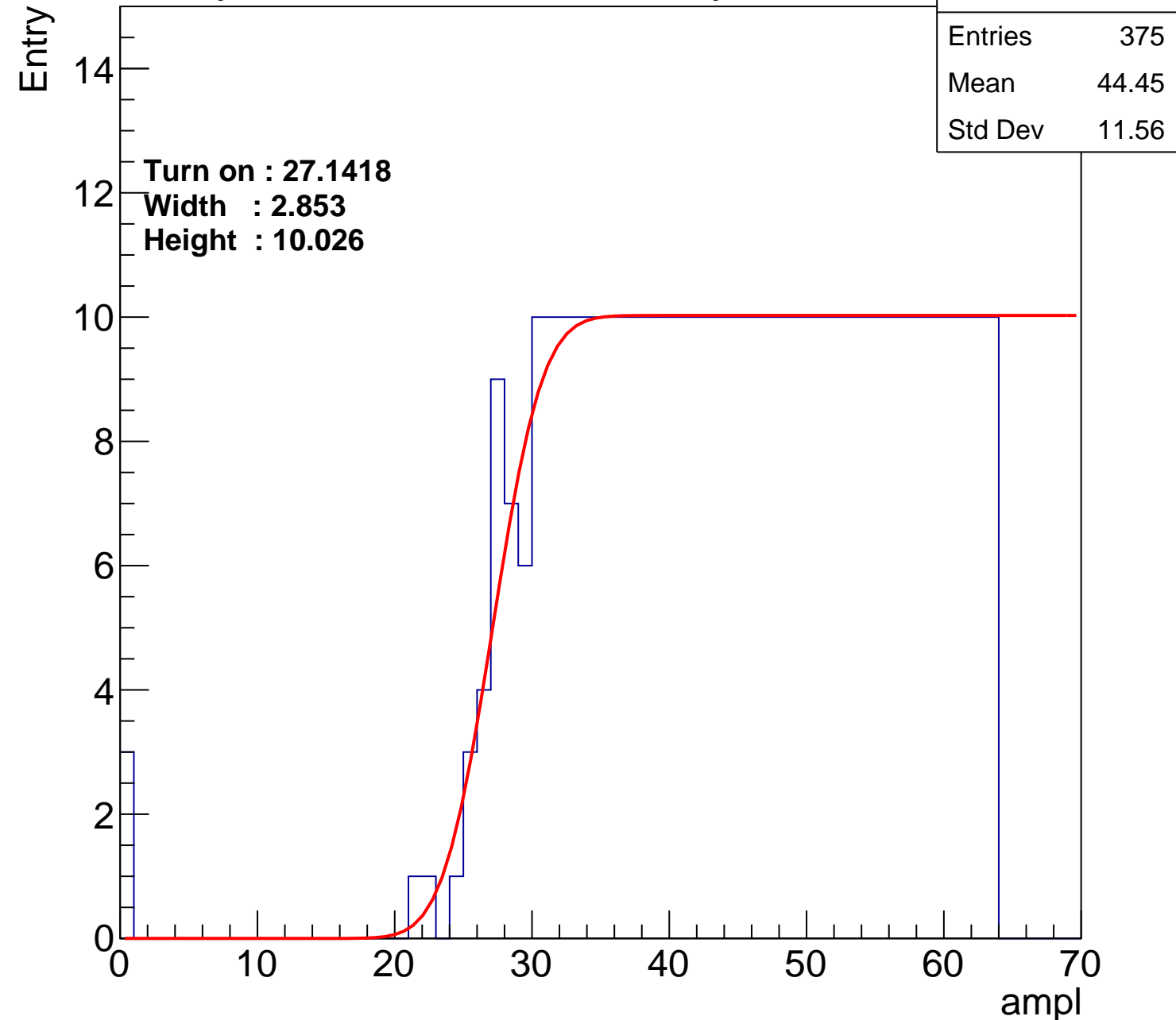
Width : 2.853

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch60

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.89
Std Dev	11.87

Turn on : 25.9310

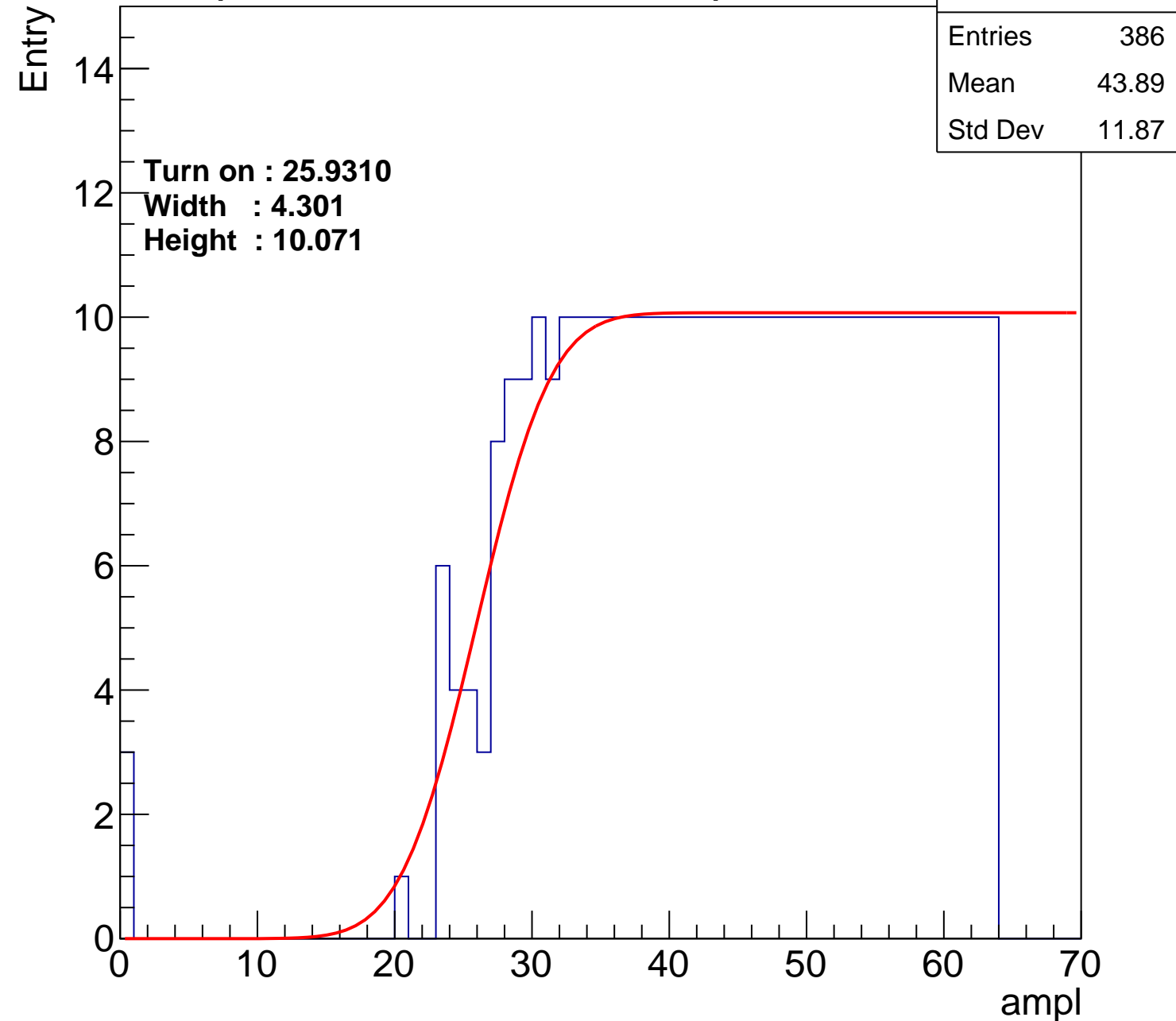
Width : 4.301

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch61

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.34
Std Dev	11.44

Turn on : 26.1114

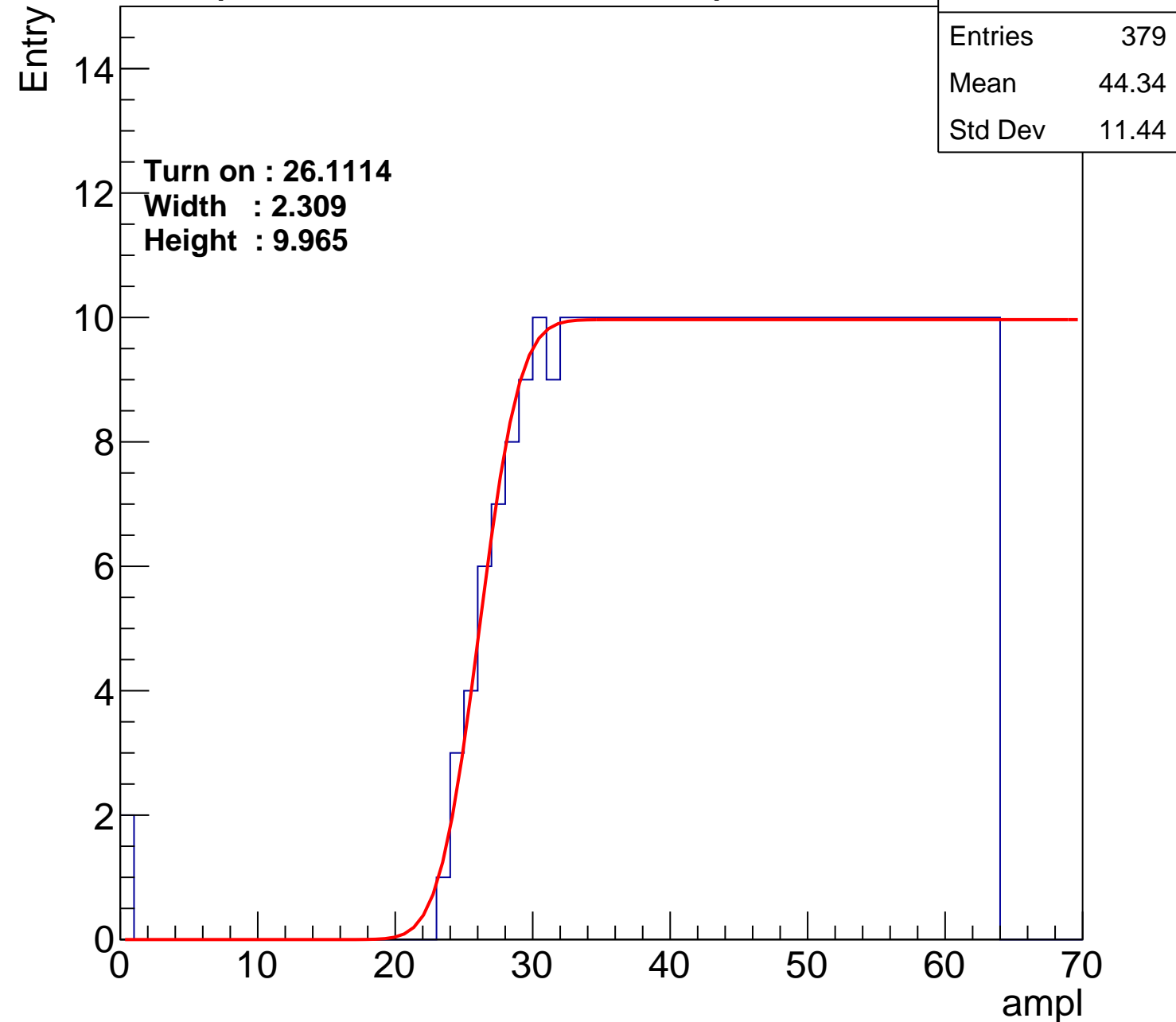
Width : 2.309

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch62

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	45.18
Std Dev	11.02

Turn on : 28.3946

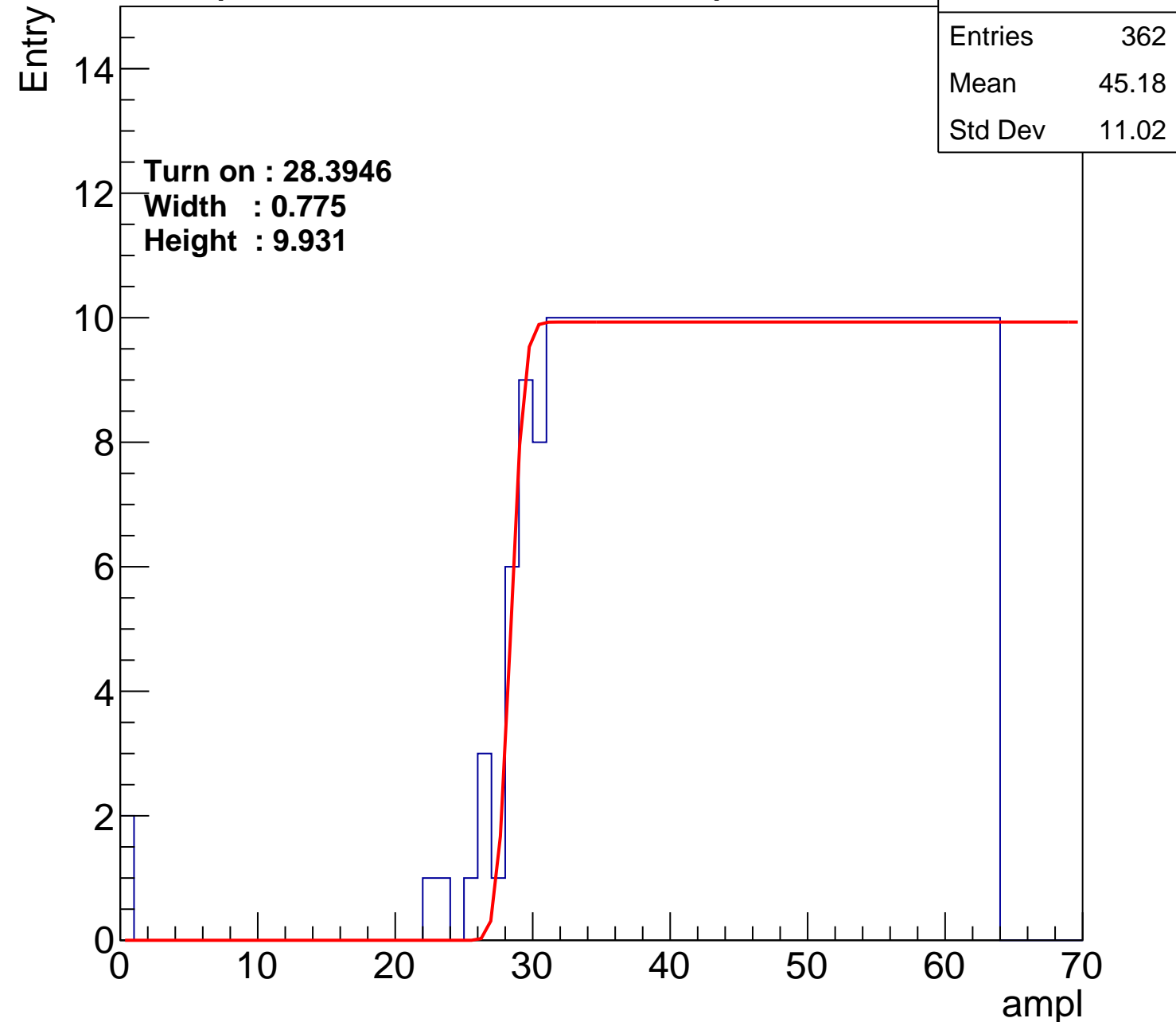
Width : 0.775

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch63

calib_packv5_042523_0143.root, FC#11, port A2

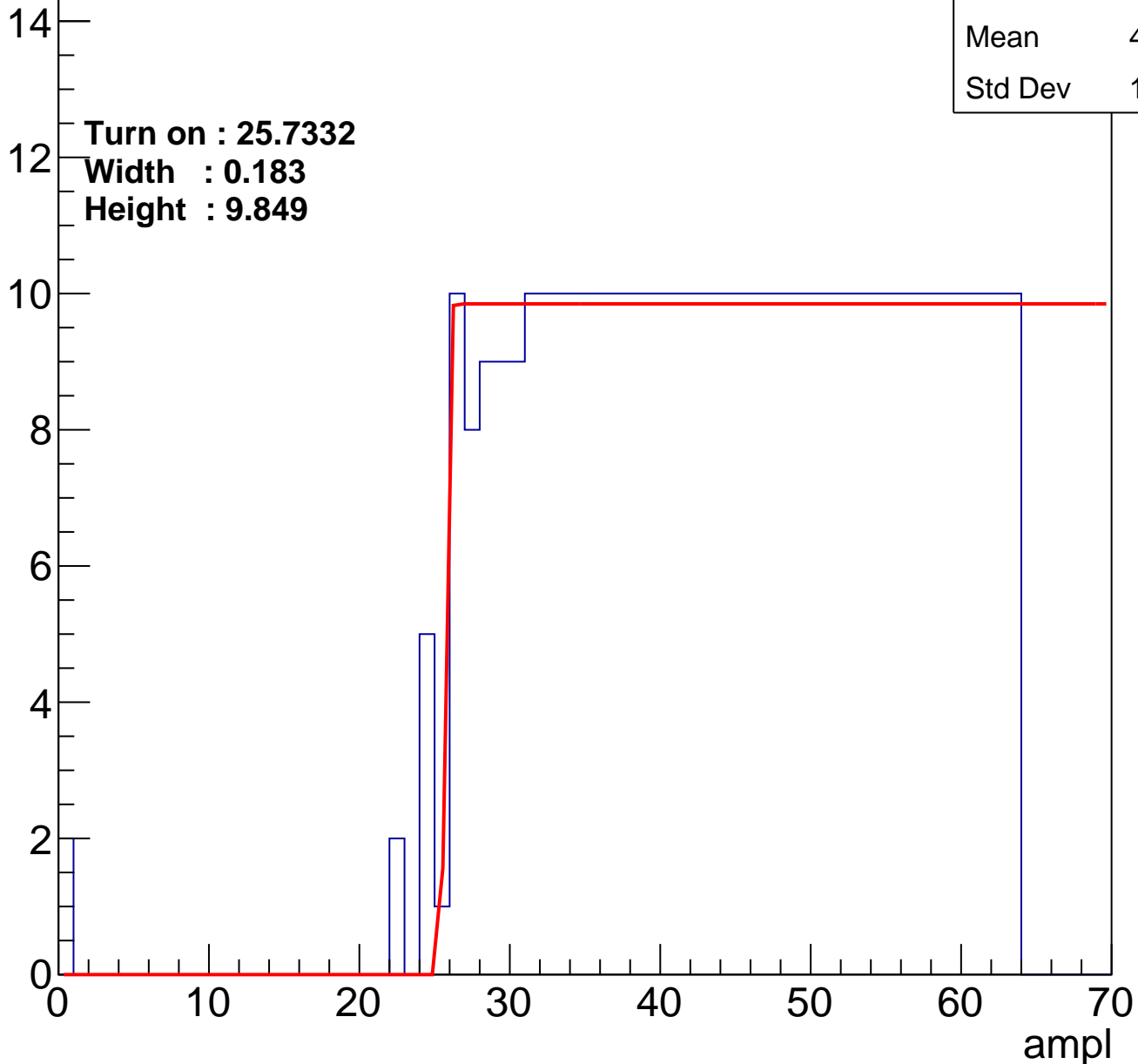
Entries	385
Mean	44.05
Std Dev	11.59

Turn on : 25.7332

Width : 0.183

Height : 9.849

Entry



B1L102S, U9-ch64

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.72
Std Dev	12.21

Turn on : 25.9346

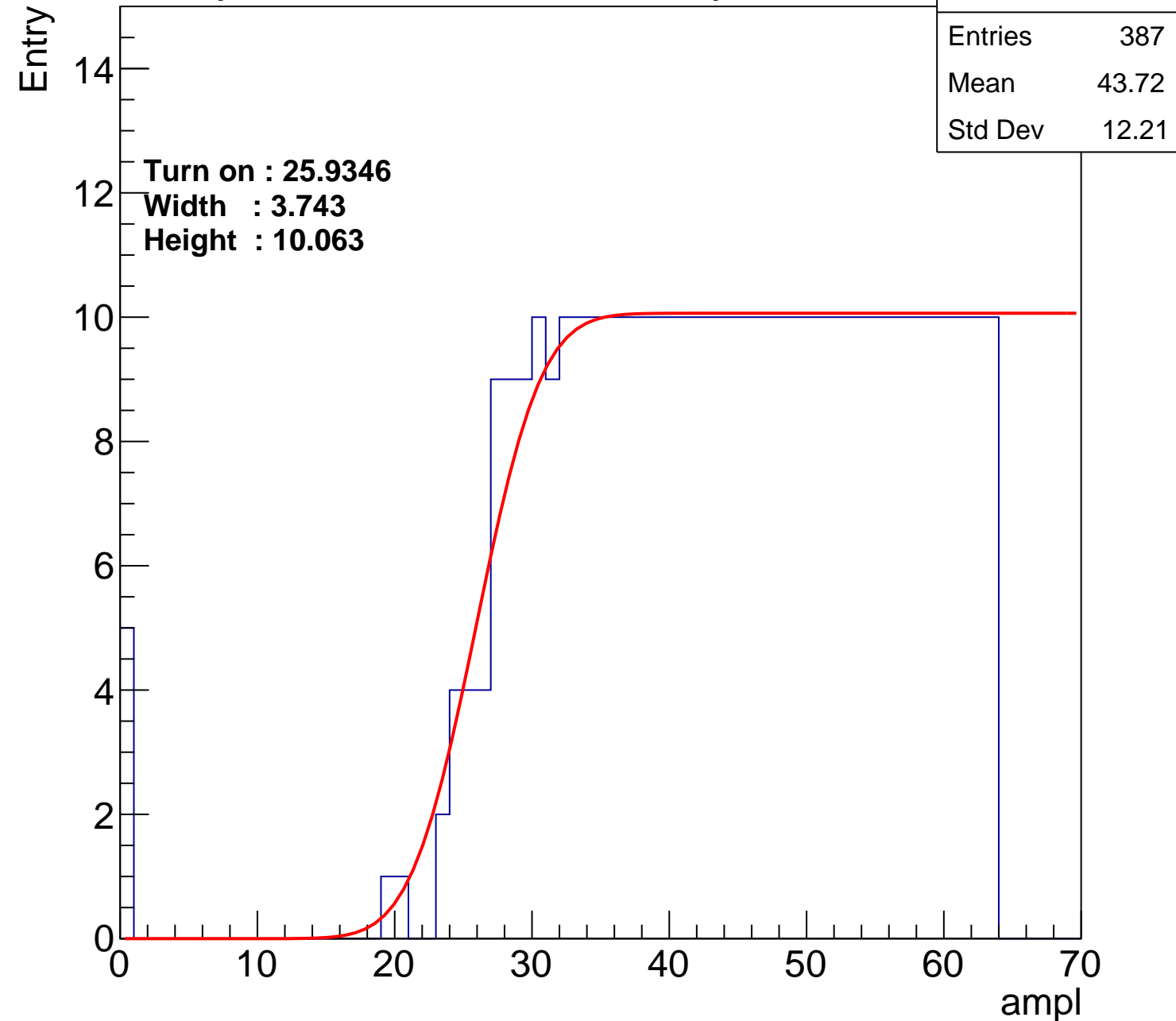
Width : 3.743

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch65

calib_packv5_042523_0143.root, FC#11, port A2

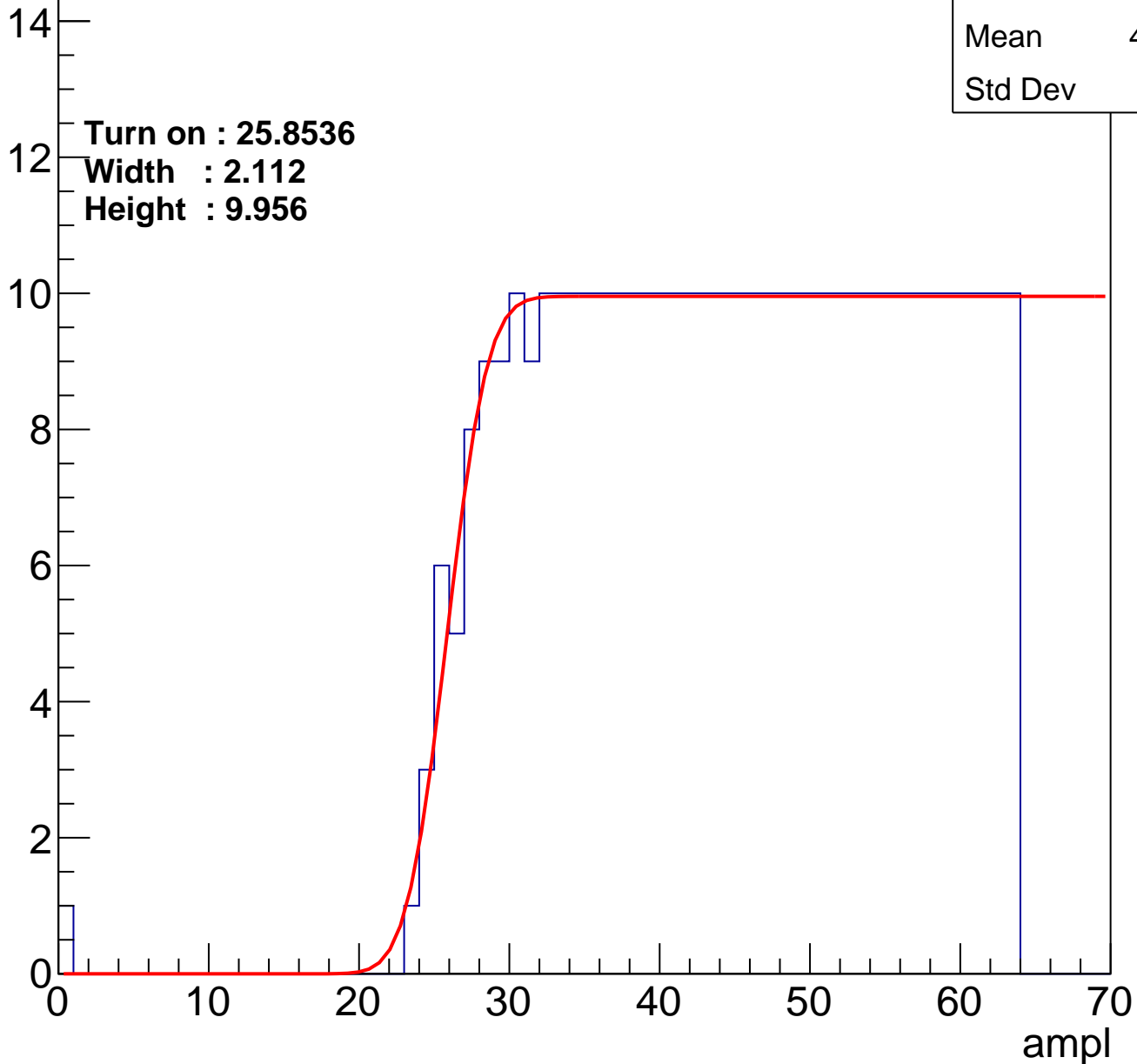
Entries	381
Mean	44.31
Std Dev	11.3

Turn on : 25.8536

Width : 2.112

Height : 9.956

Entry



B1L102S, U9-ch66

calib_packv5_042523_0143.root, FC#11, port A2

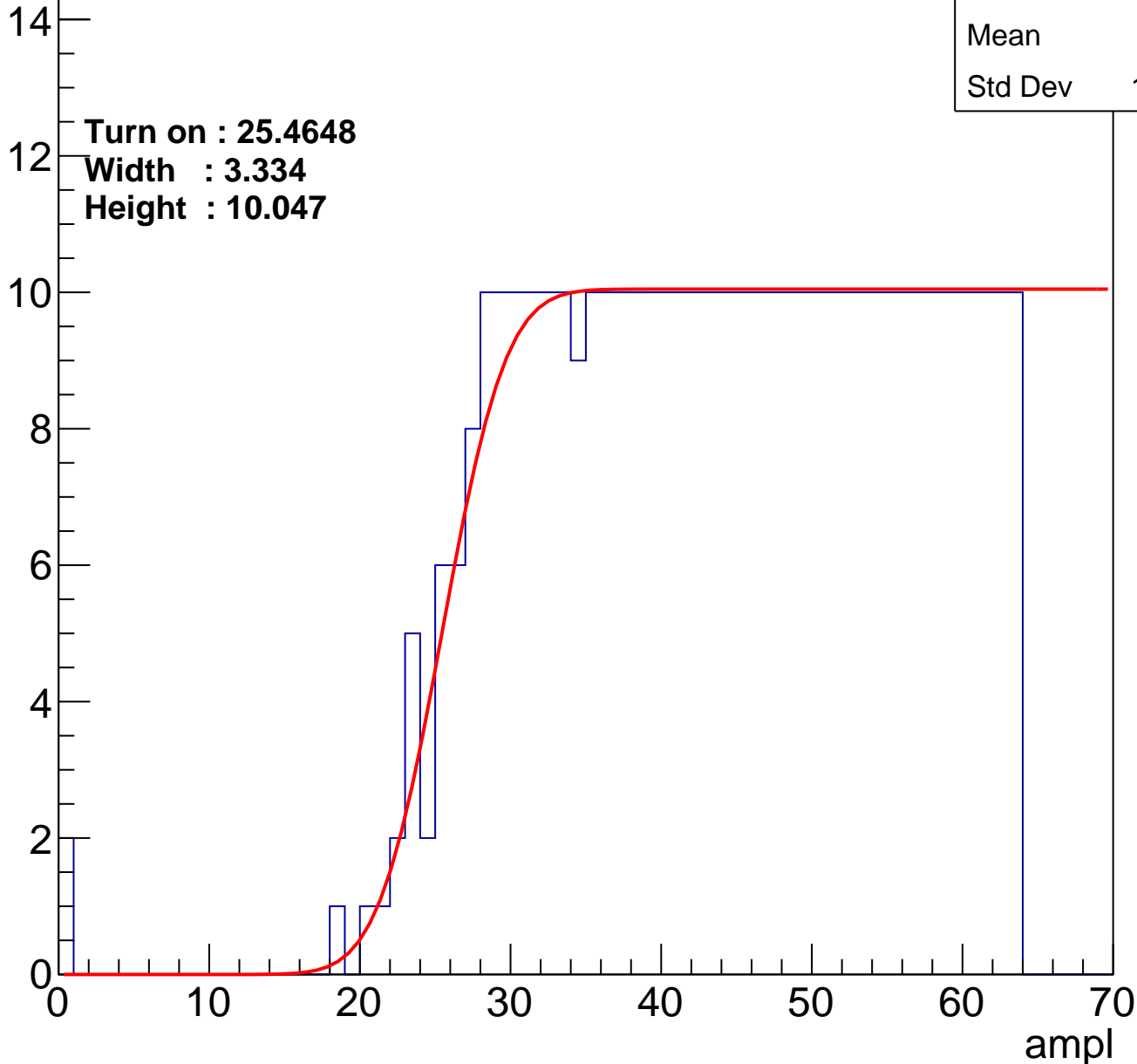
Entries	393
Mean	43.6
Std Dev	11.89

Turn on : 25.4648

Width : 3.334

Height : 10.047

Entry



B1L102S, U9-ch67

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.86
Std Dev	11.52

Turn on : 28.1562

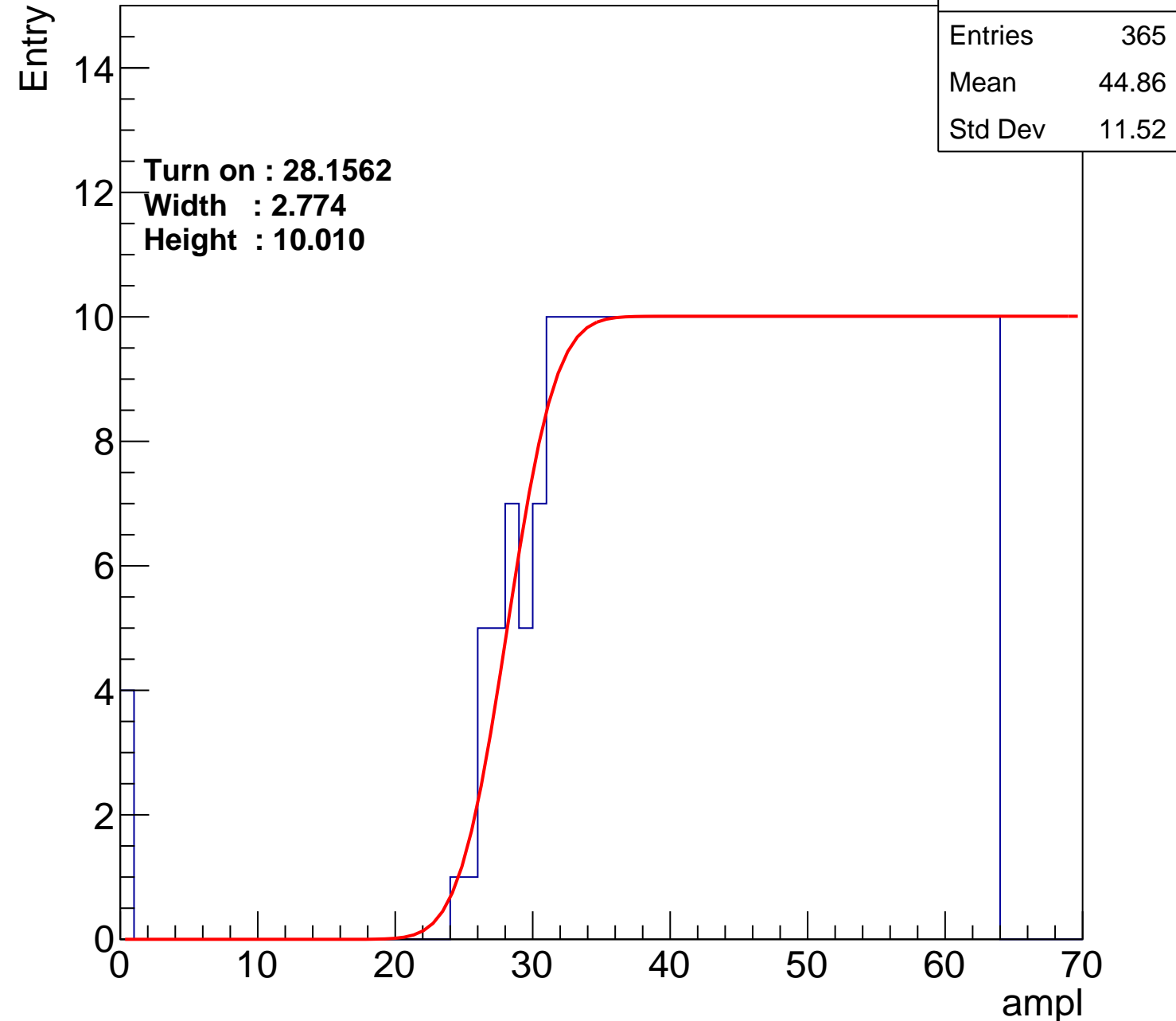
Width : 2.774

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch68

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.46
Std Dev	12.13

Turn on : 25.2004

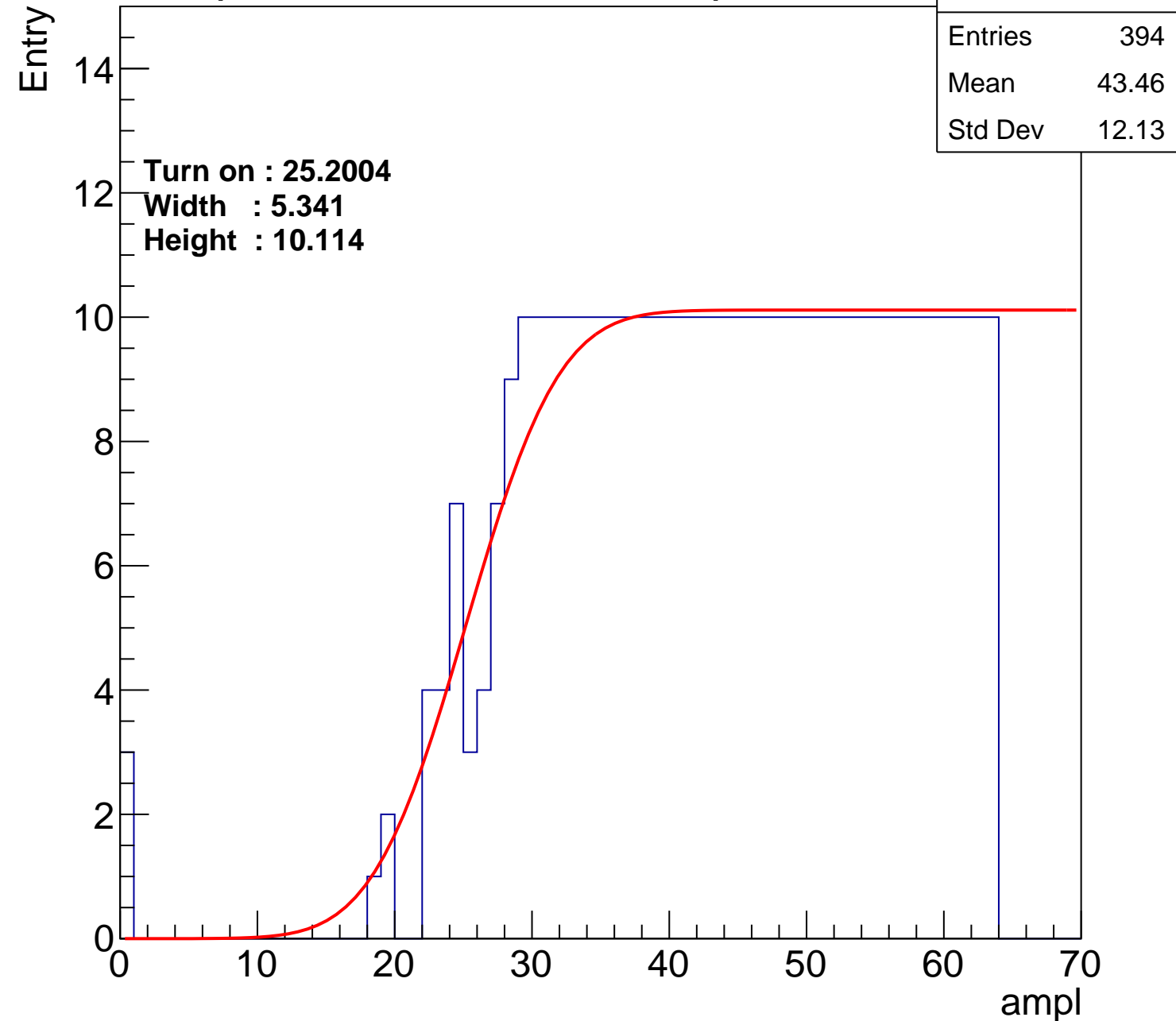
Width : 5.341

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch69

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.43
Std Dev	12.06

Turn on : 25.1482

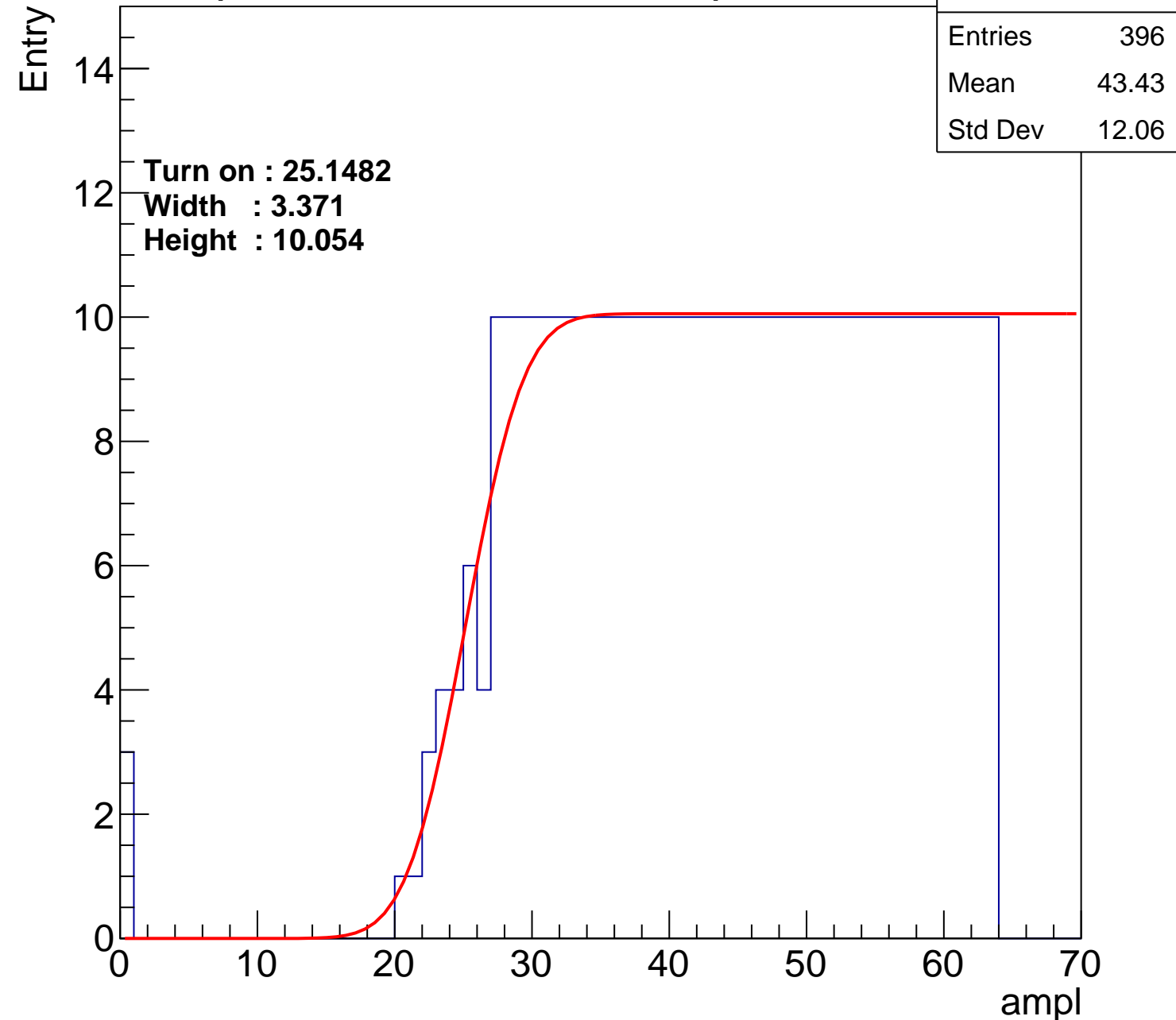
Width : 3.371

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch70

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.99
Std Dev	11.64

Turn on : 25.9569

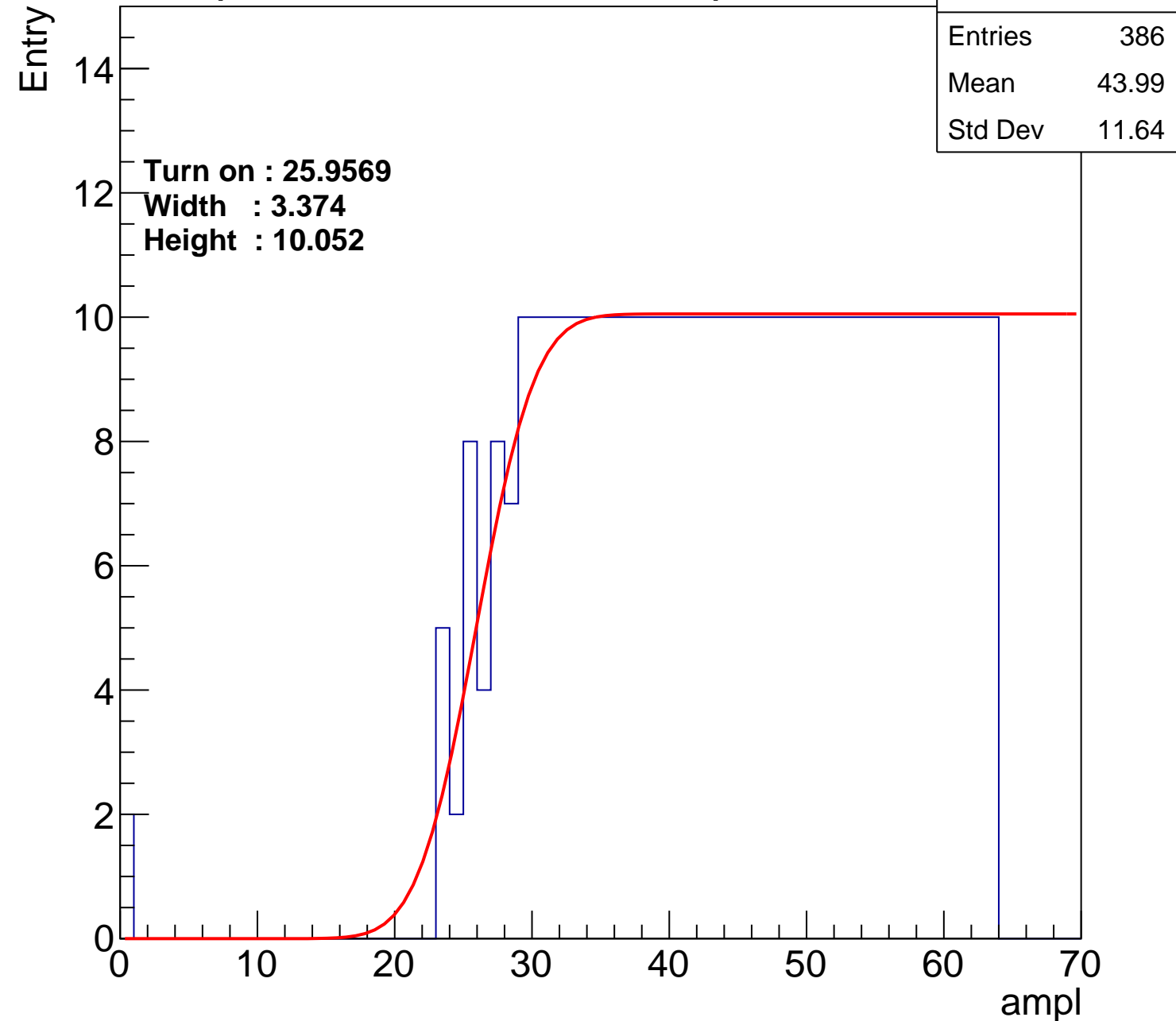
Width : 3.374

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch71

calib_packv5_042523_0143.root, FC#11, port A2

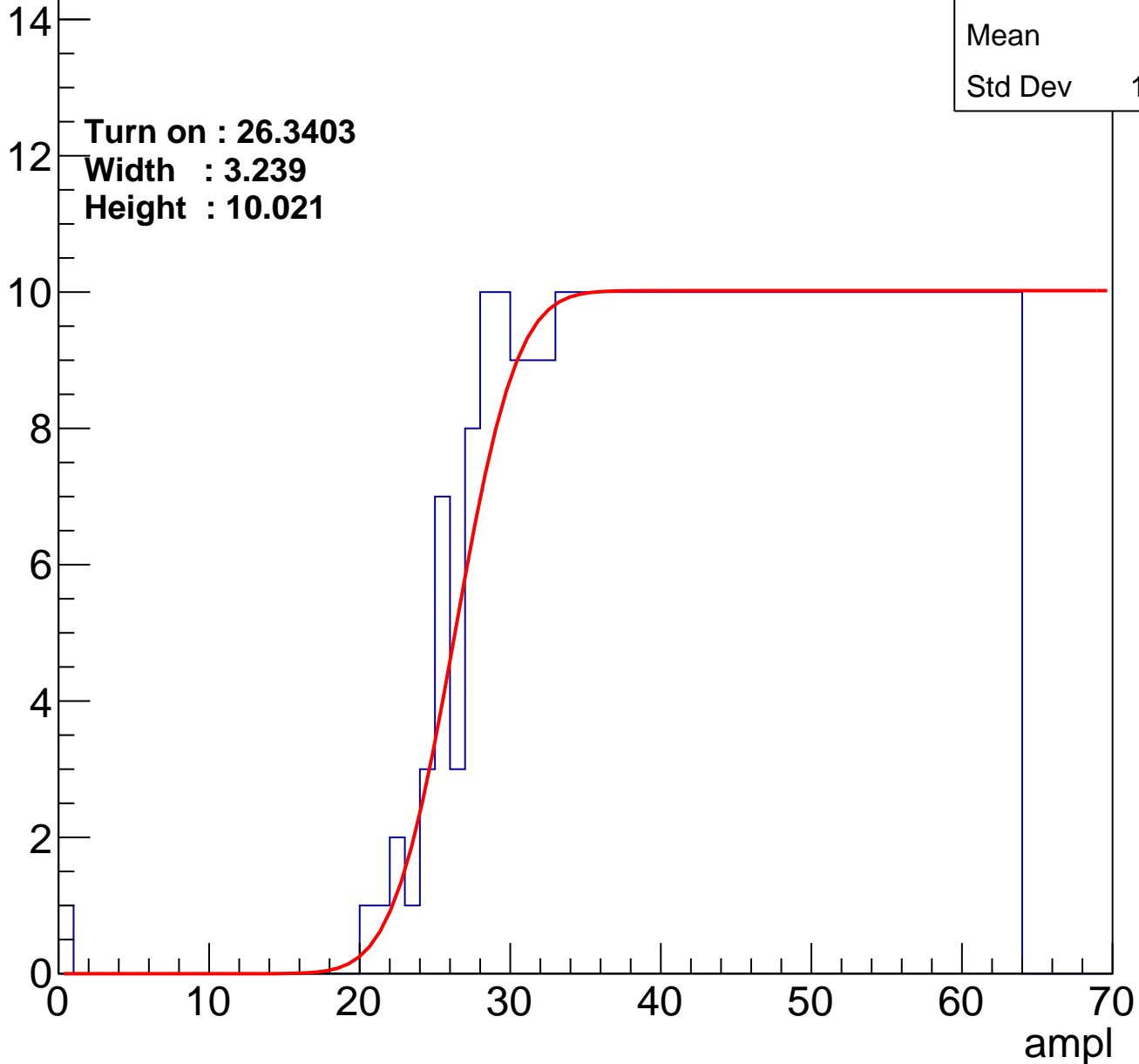
Entries	384
Mean	44.1
Std Dev	11.48

Turn on : 26.3403

Width : 3.239

Height : 10.021

Entry



B1L102S, U9-ch72

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.63
Std Dev	11.77

Turn on : 24.7795

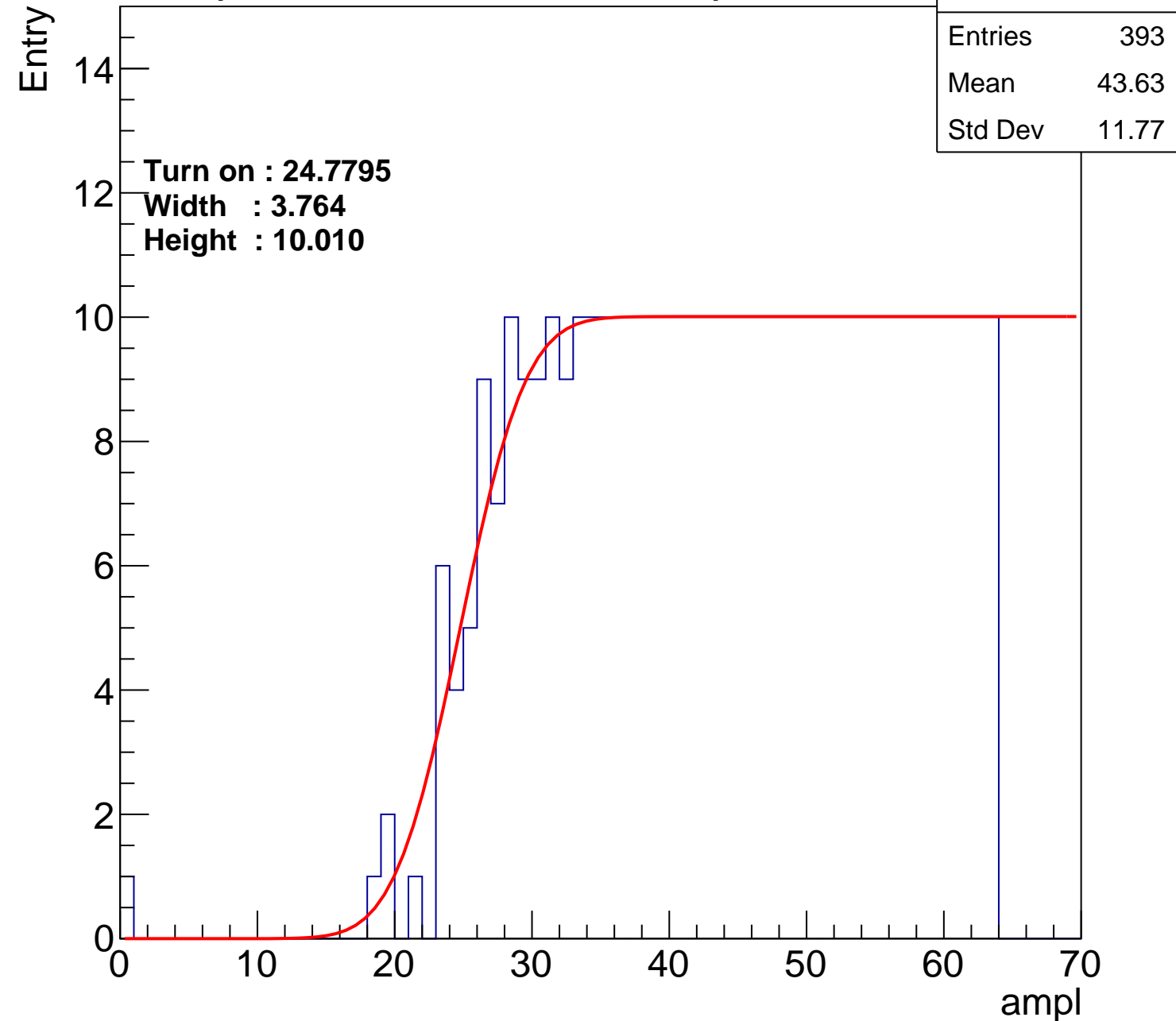
Width : 3.764

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch73

calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.27
Std Dev	12.16

Turn on : 24.7091

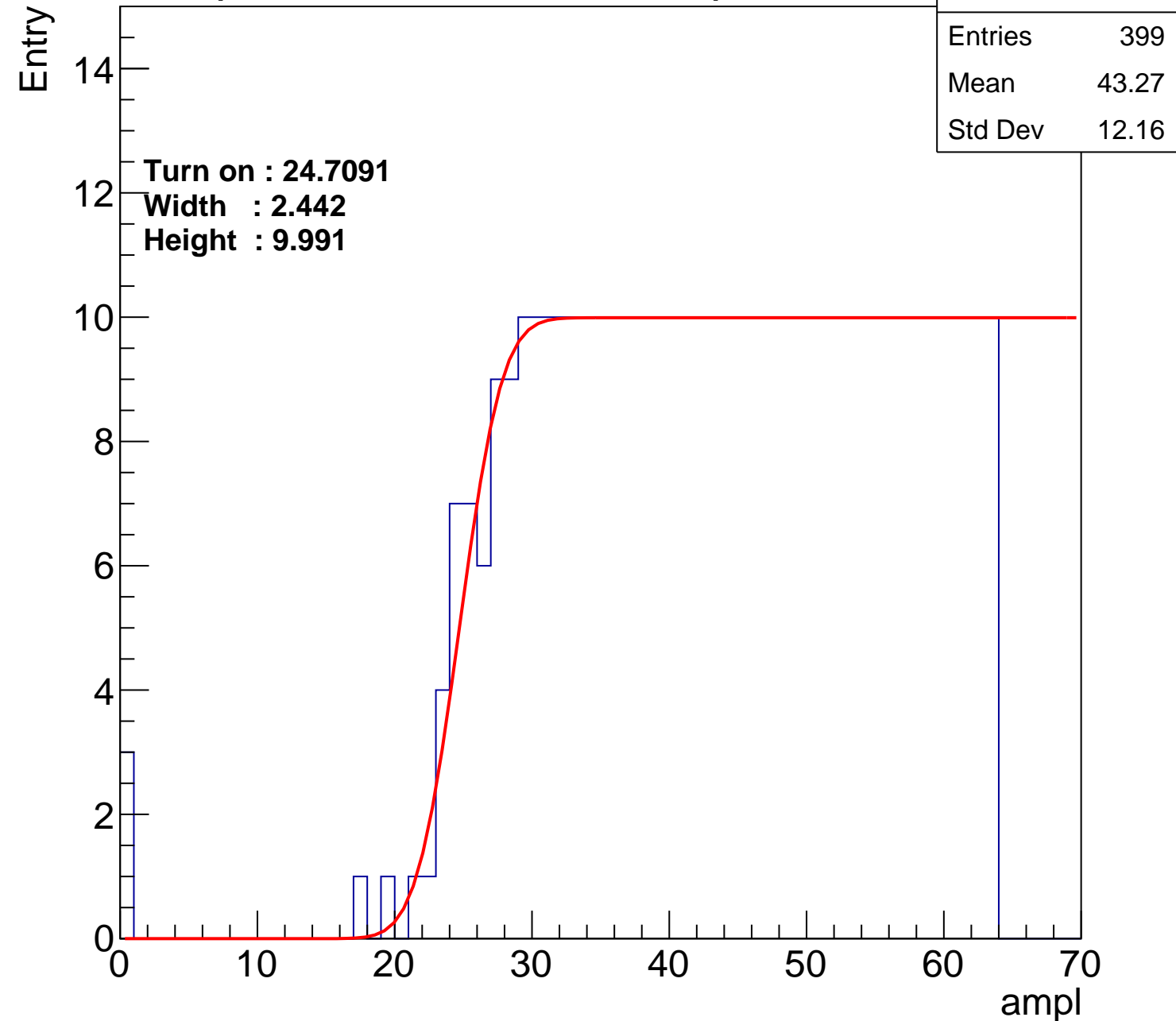
Width : 2.442

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch74

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.02
Std Dev	11.95

Turn on : 26.7697

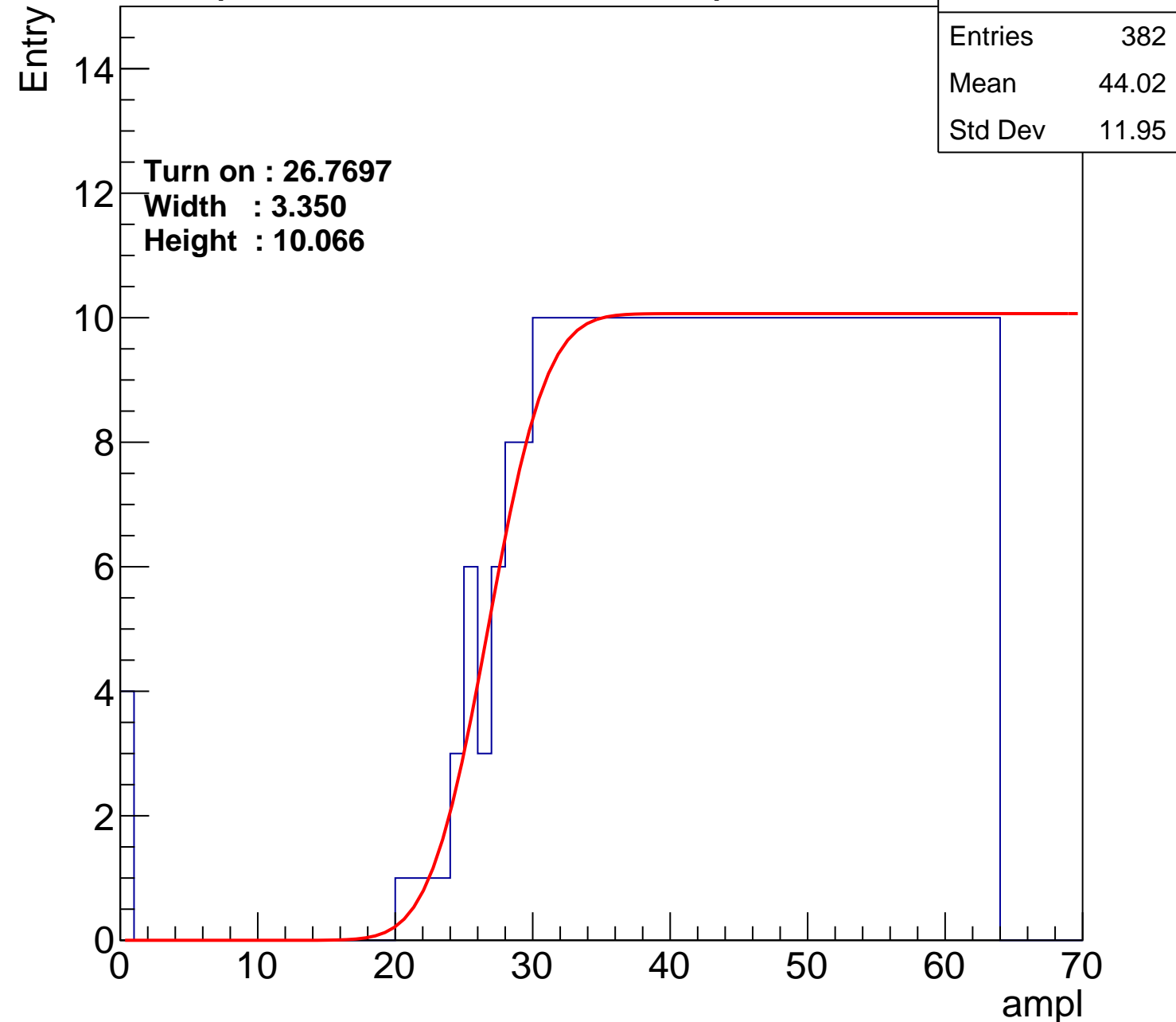
Width : 3.350

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch75

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 26.9567

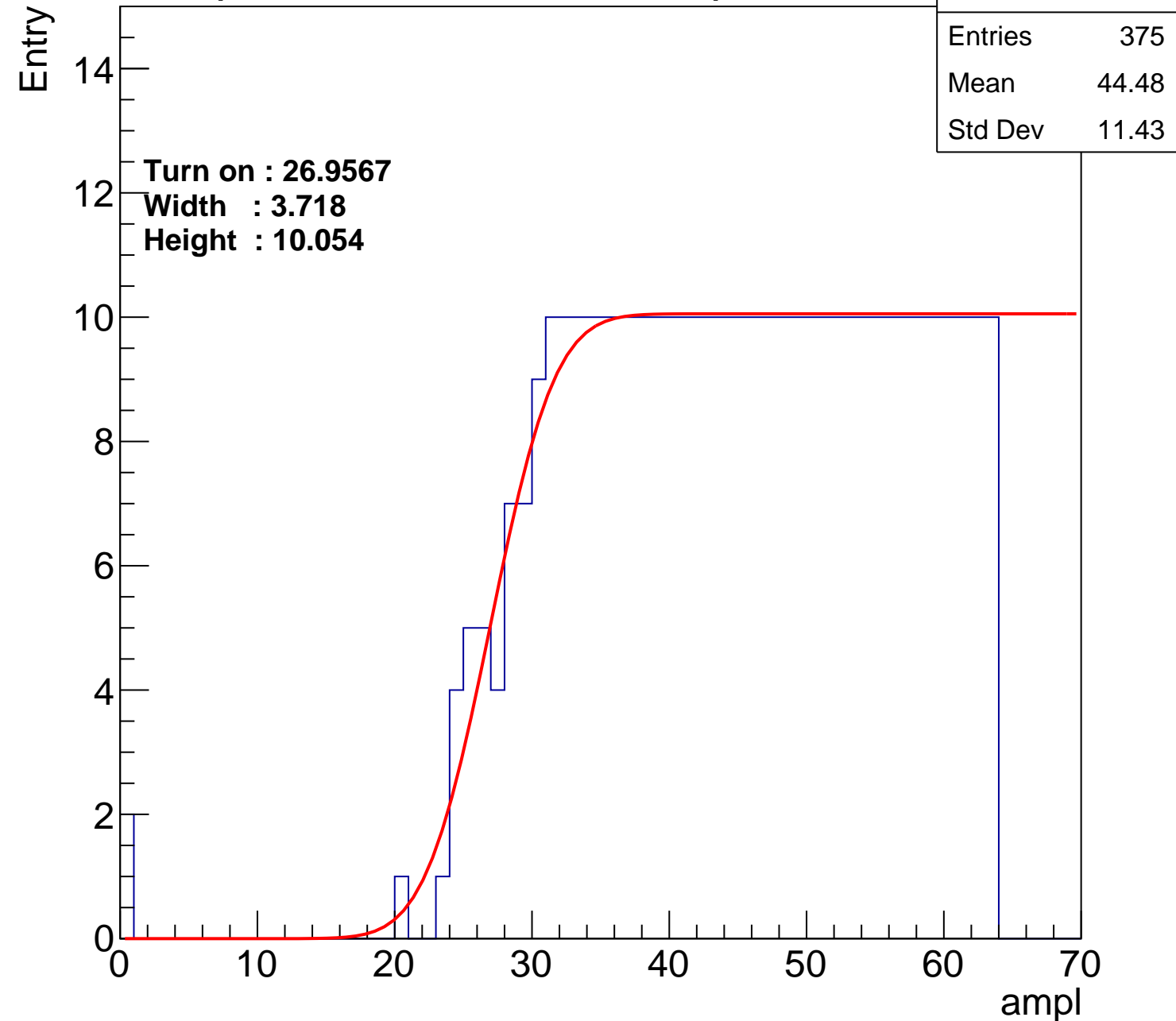
Width : 3.718

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch76

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 26.2970

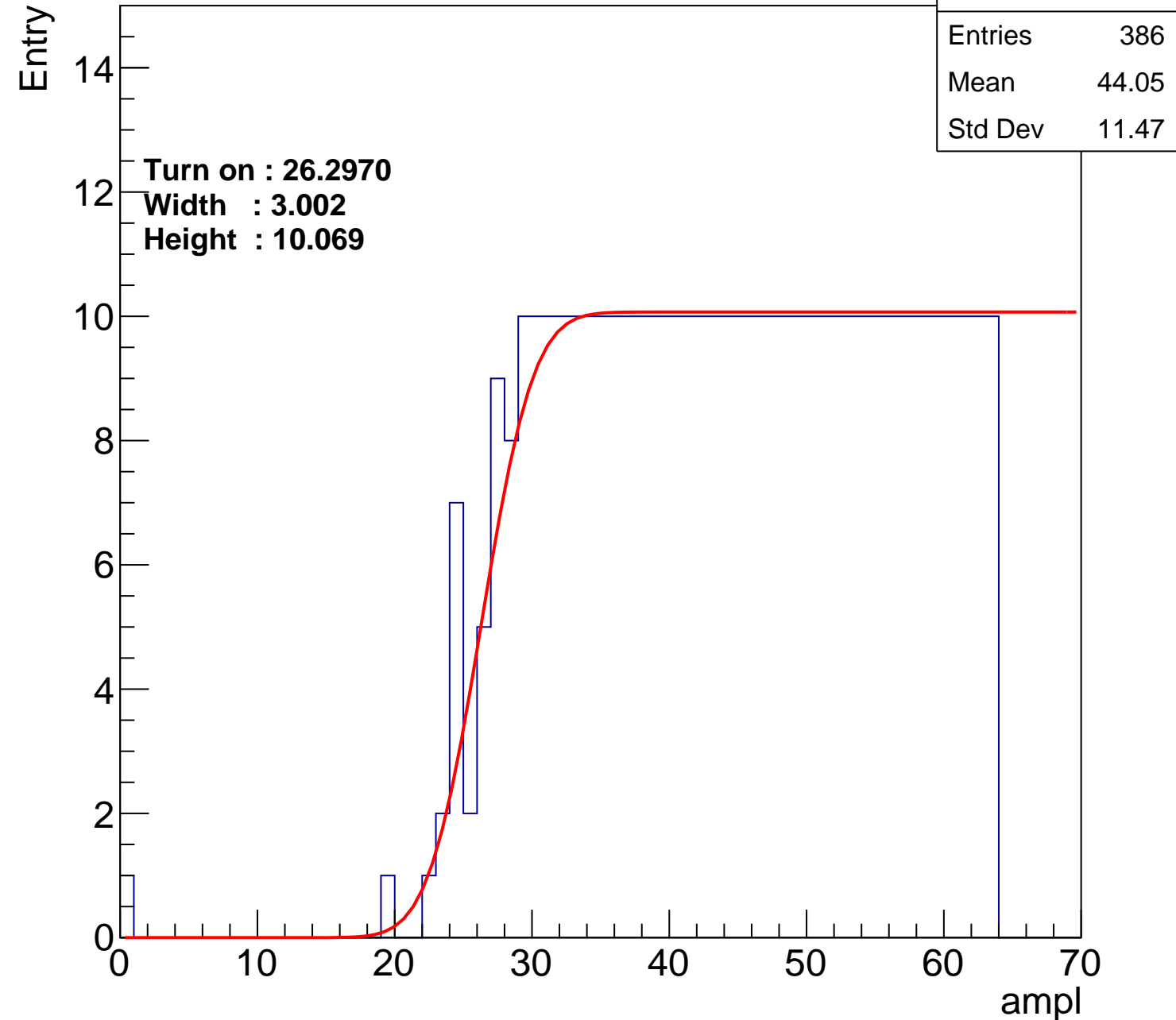
Width : 3.002

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch77

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.93
Std Dev	11.33

Turn on : 28.0788

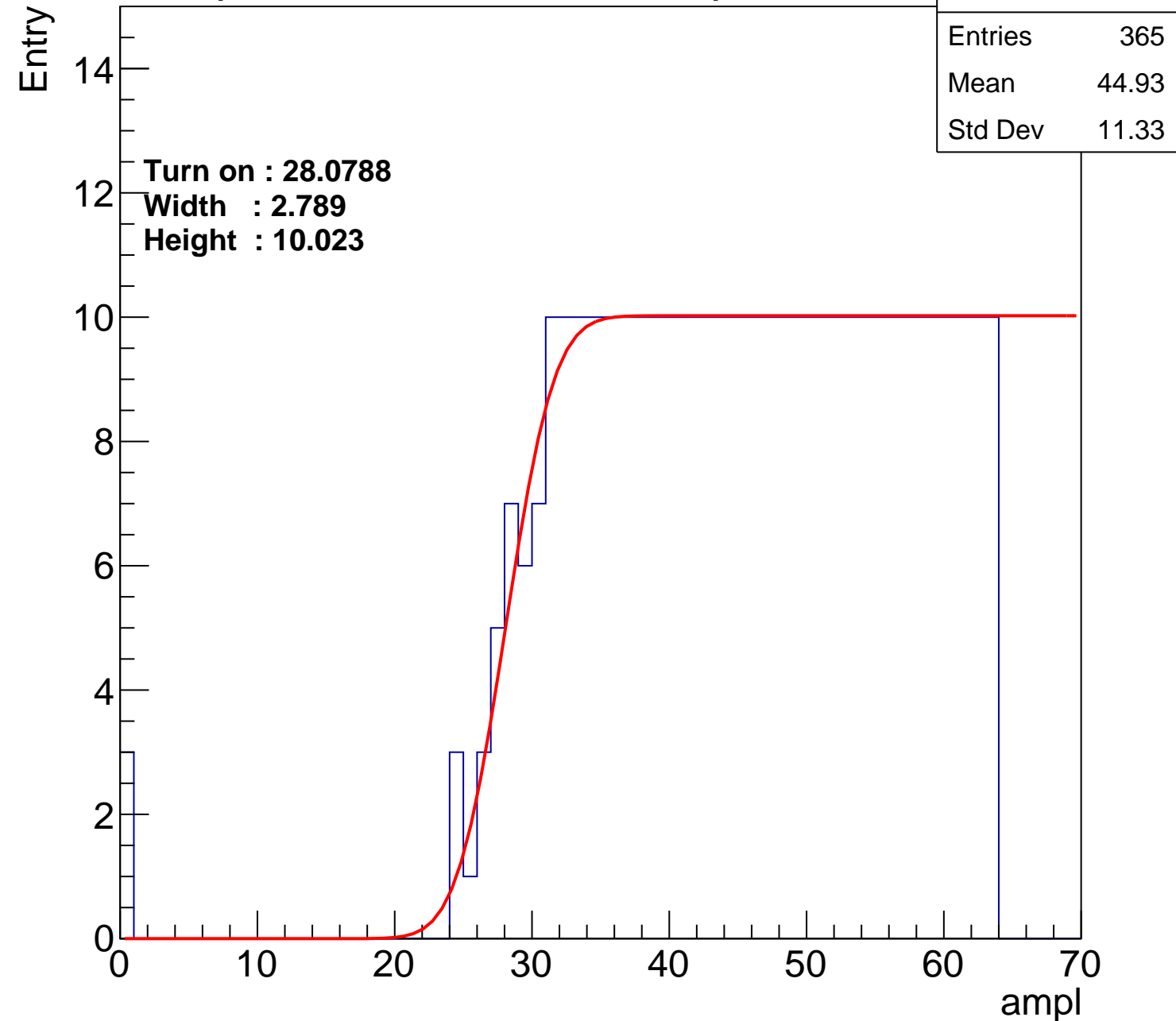
Width : 2.789

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch78

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.2655

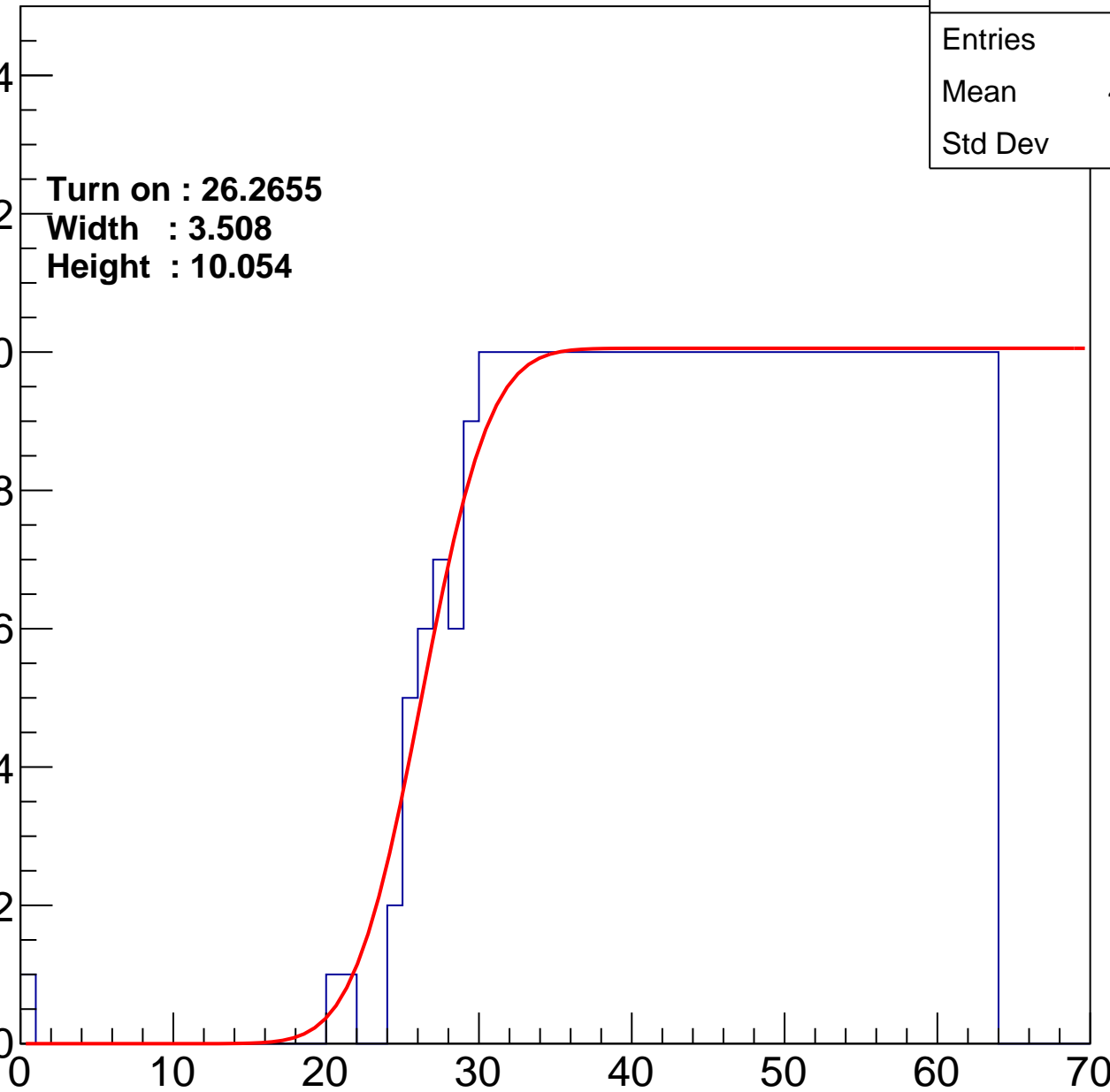
Width : 3.508

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch79

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.23
Std Dev	11.56

Turn on : 26.3704

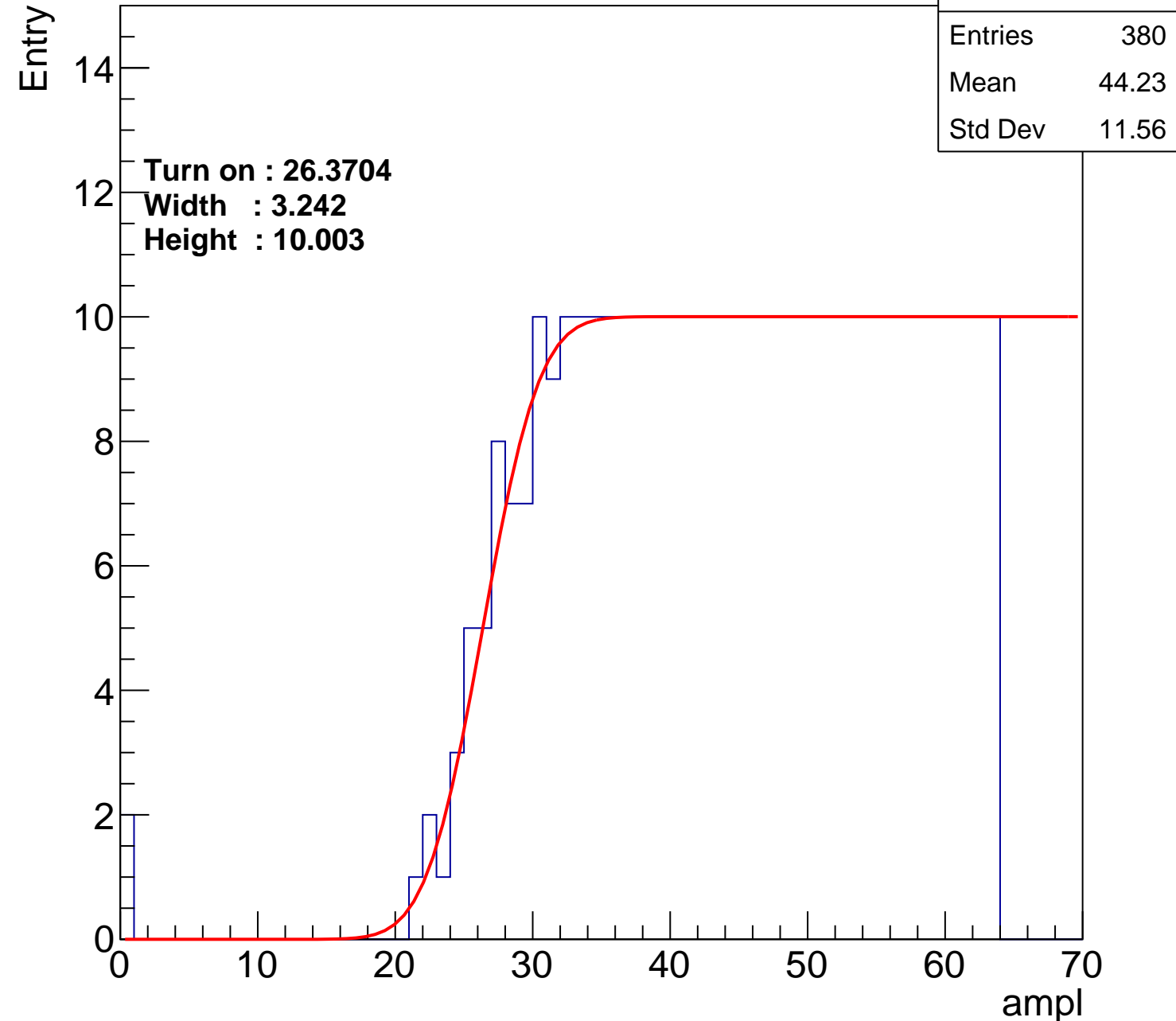
Width : 3.242

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch80

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.29
Std Dev	11.52

Turn on : 26.2894

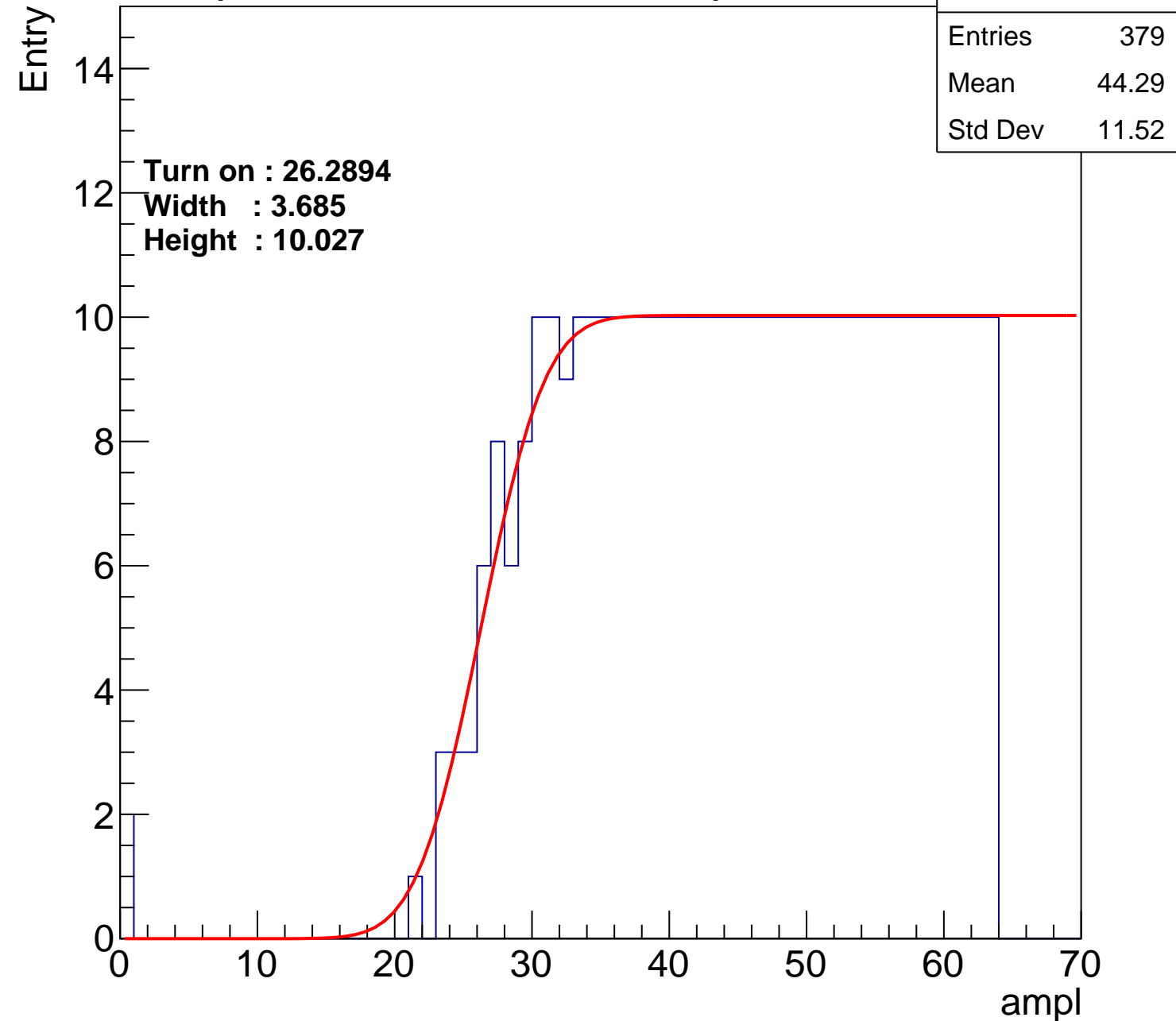
Width : 3.685

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch81

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.91
Std Dev	11.7

Turn on : 25.4988

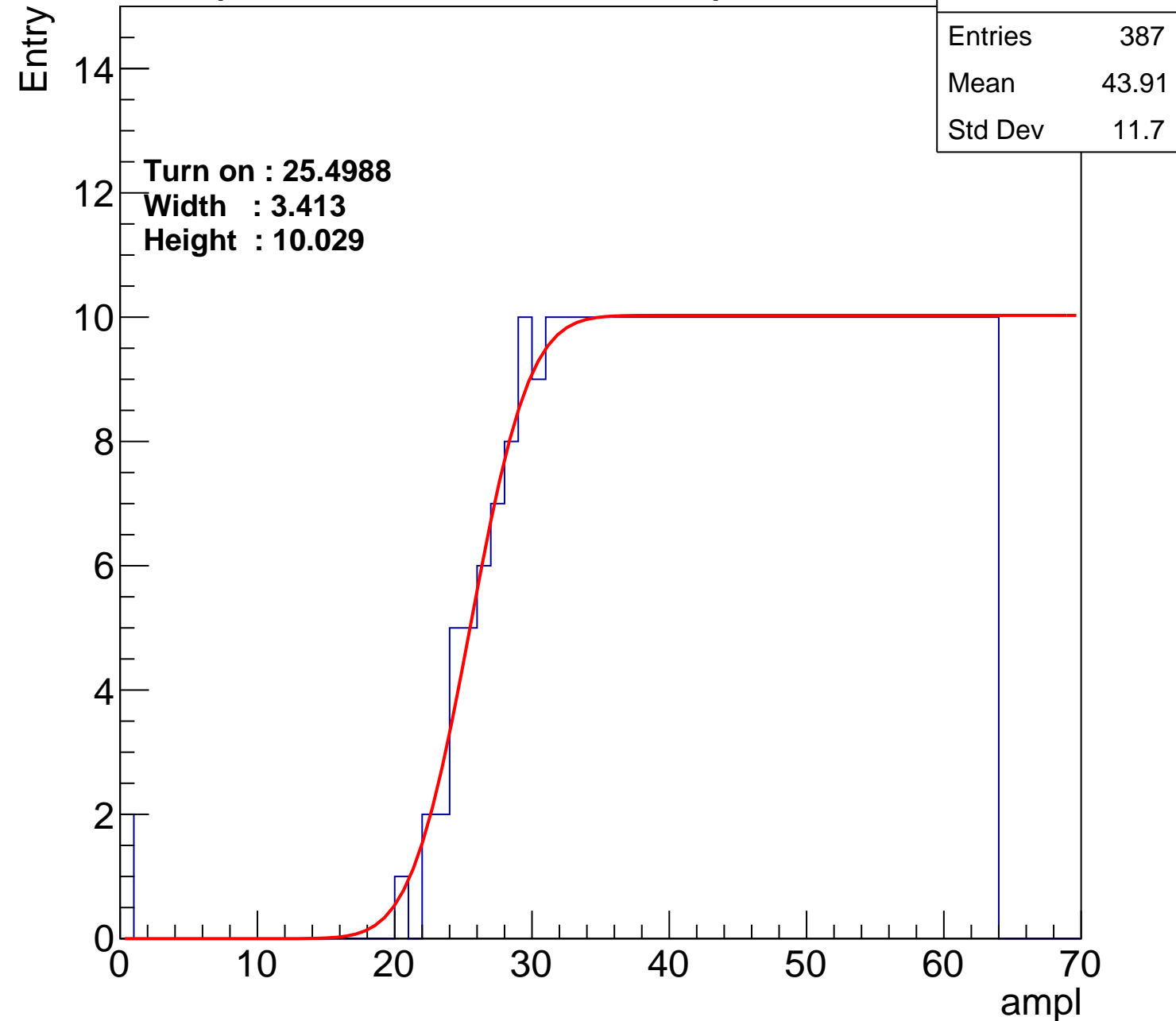
Width : 3.413

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch82

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.2
Std Dev	11.69

Turn on : 27.2072

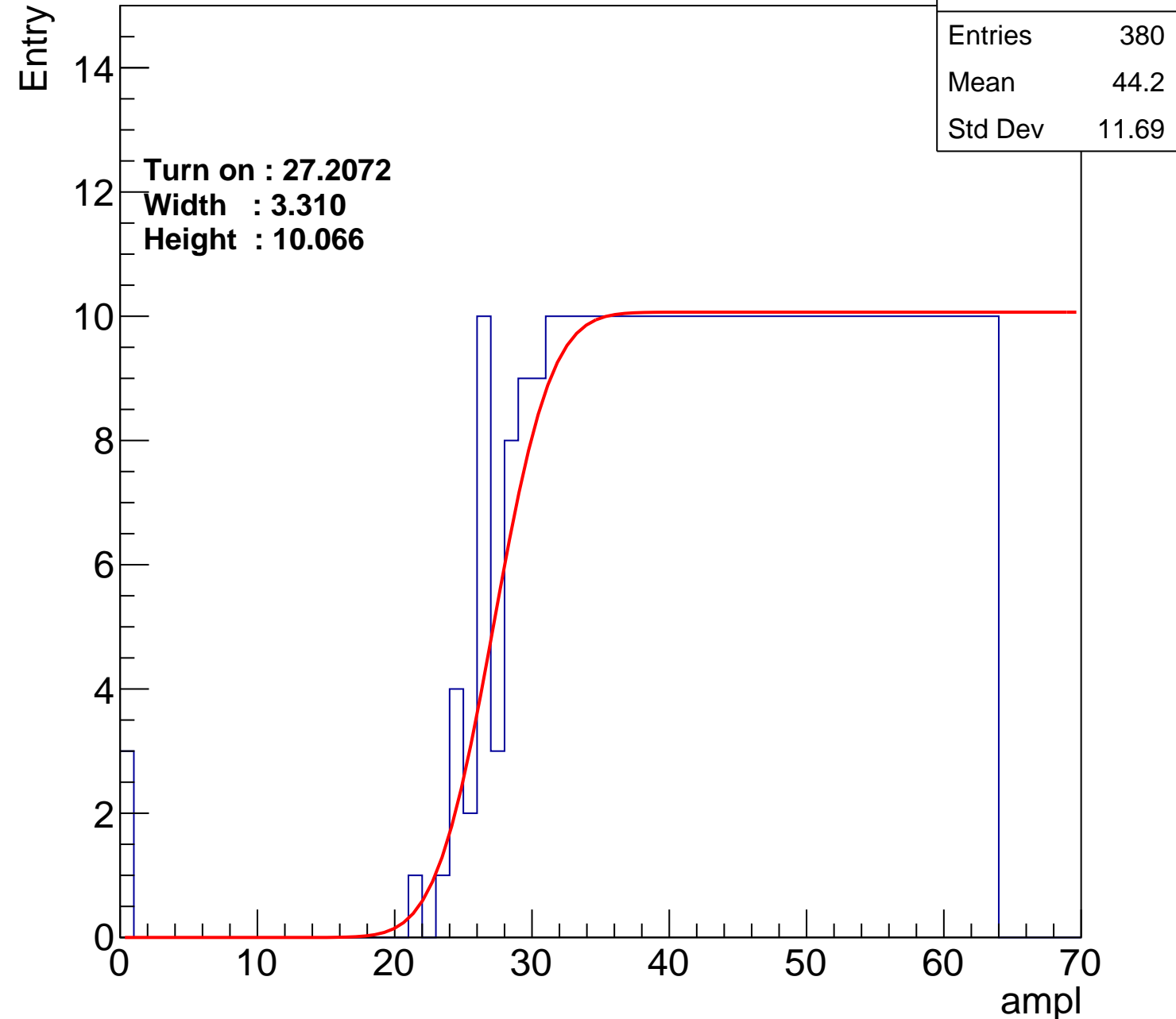
Width : 3.310

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch83

calib_packv5_042523_0143.root, FC#11, port A2

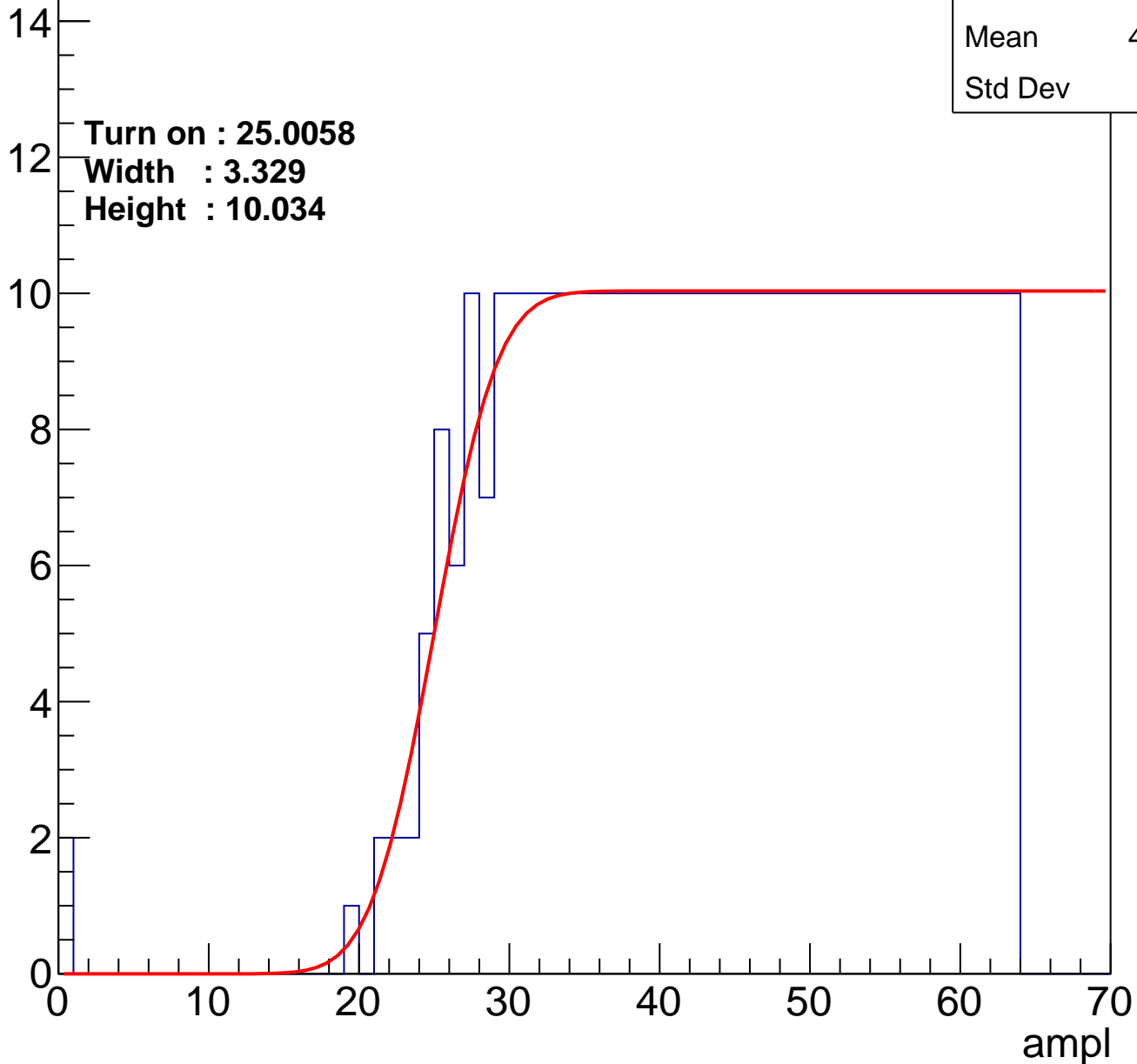
Entries	395
Mean	43.53
Std Dev	11.9

Turn on : 25.0058

Width : 3.329

Height : 10.034

Entry



B1L102S, U9-ch84

calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.31
Std Dev	12.03

Turn on : 25.1921

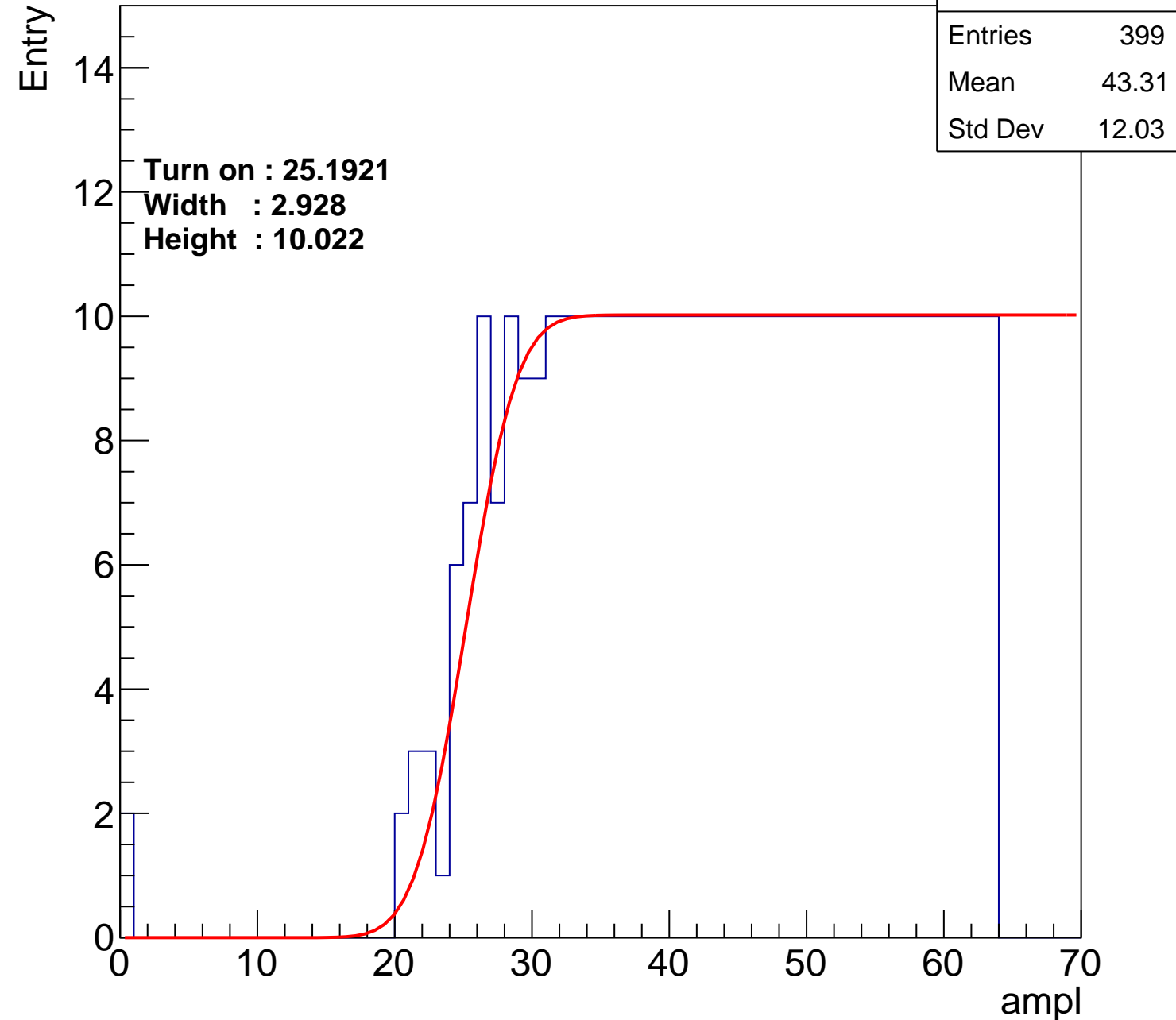
Width : 2.928

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch85

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.66
Std Dev	12.13

Turn on : 25.6211

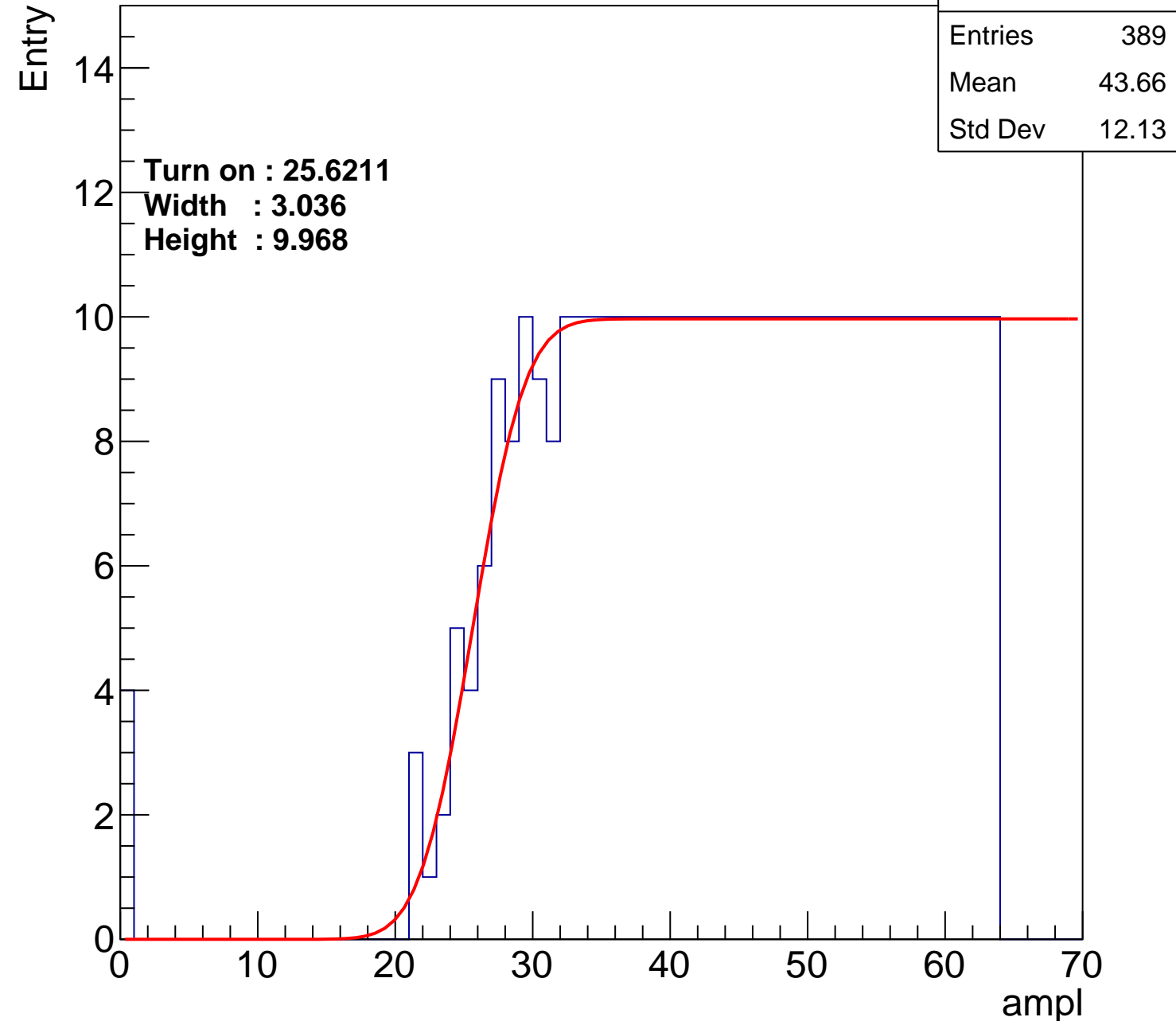
Width : 3.036

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch86

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	42.97
Std Dev	12.57

Turn on : 24.4277

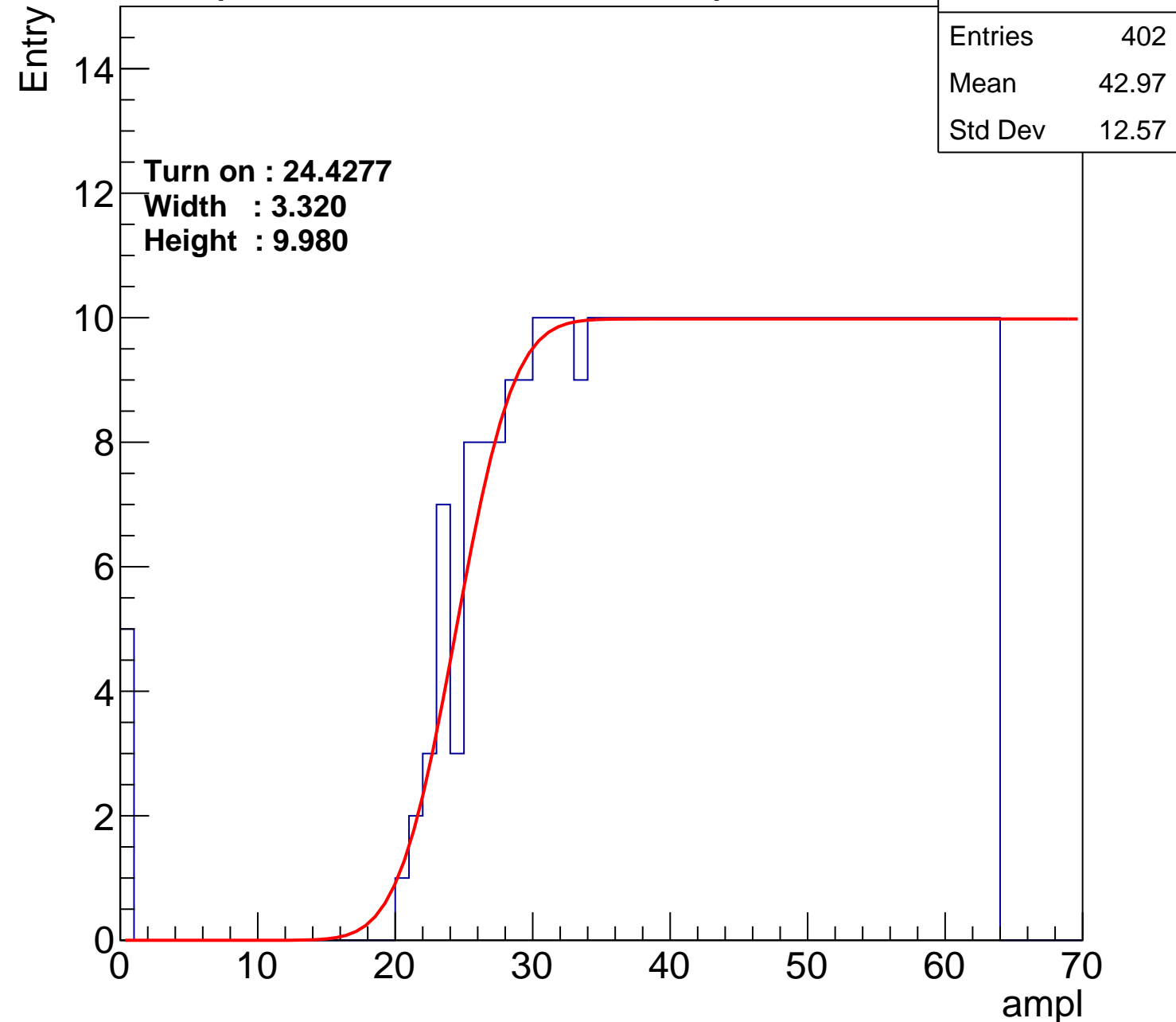
Width : 3.320

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch87

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.48
Std Dev	12.04

Turn on : 25.3905

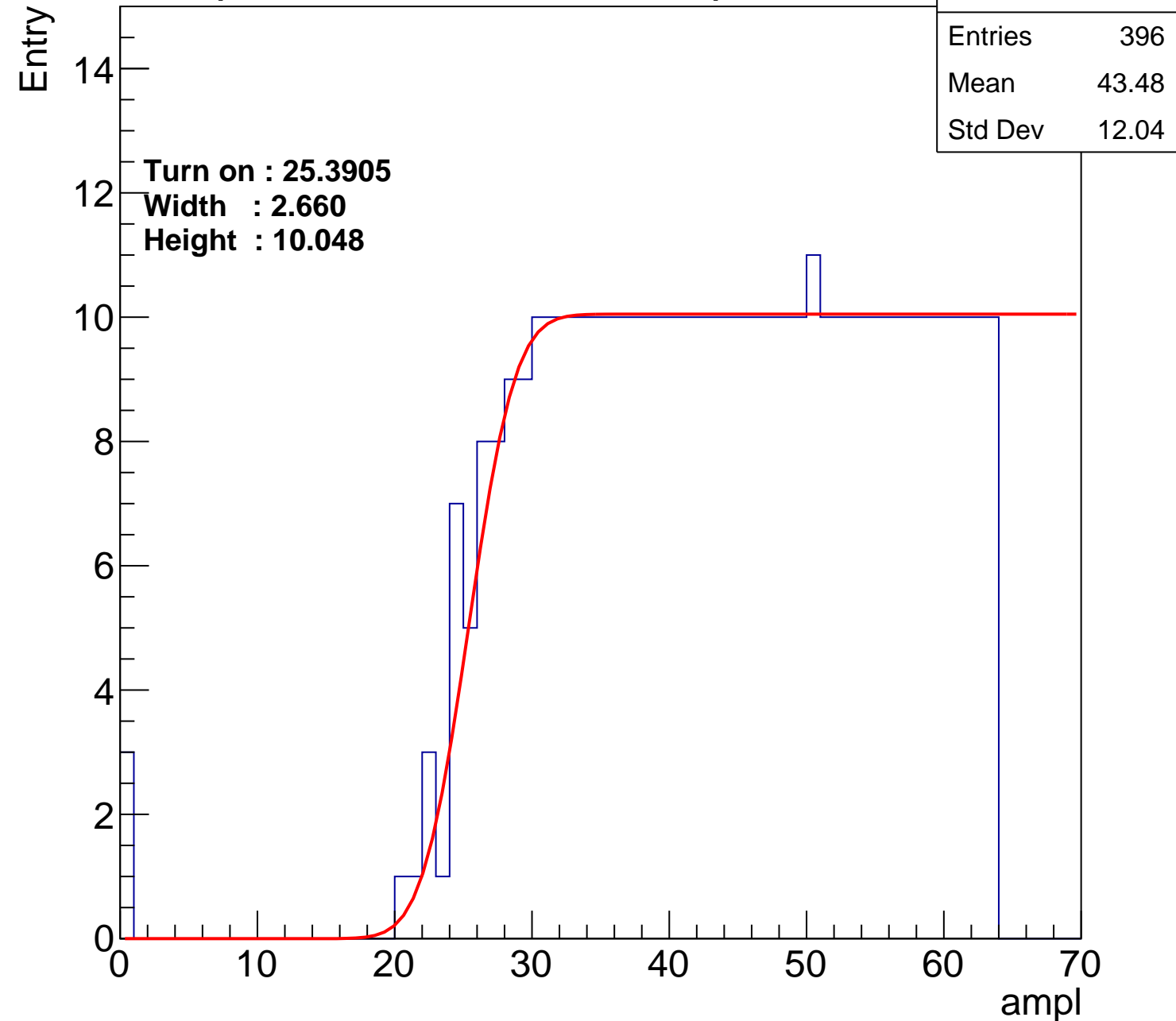
Width : 2.660

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch88

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.46
Std Dev	12.23

Turn on : 25.7761

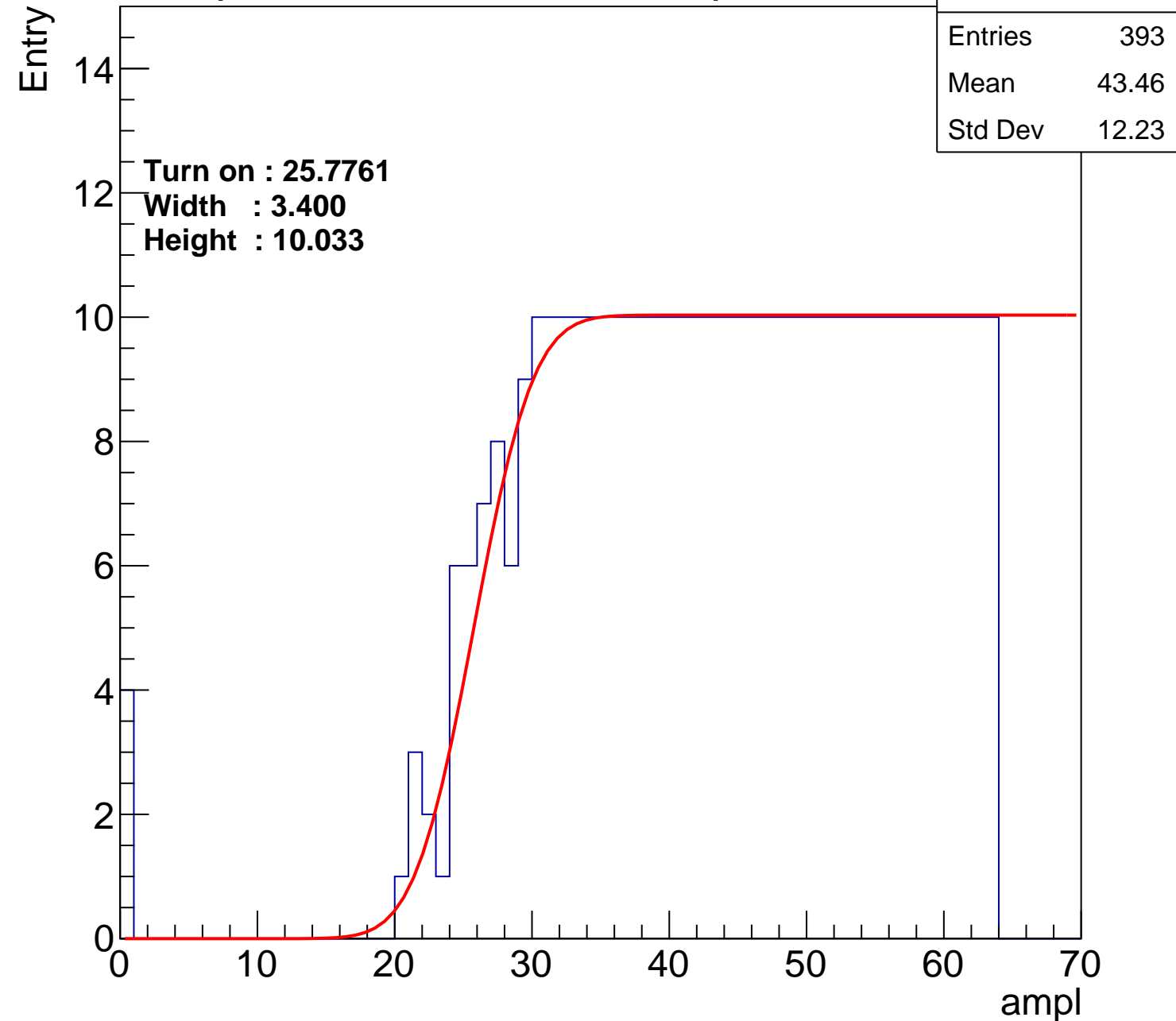
Width : 3.400

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch89

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.61
Std Dev	12.04

Turn on : 25.5109

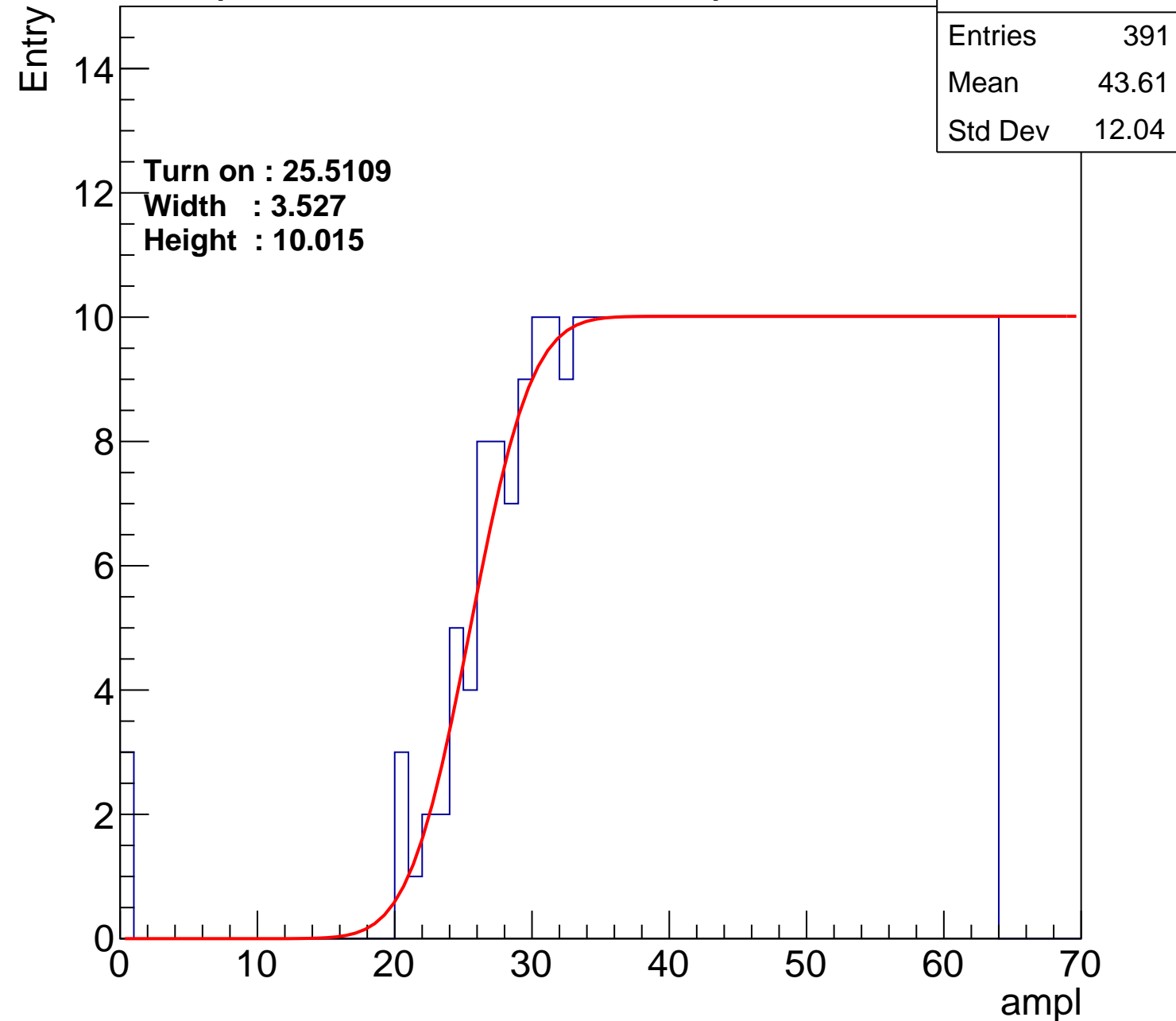
Width : 3.527

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch90

calib_packv5_042523_0143.root, FC#11, port A2

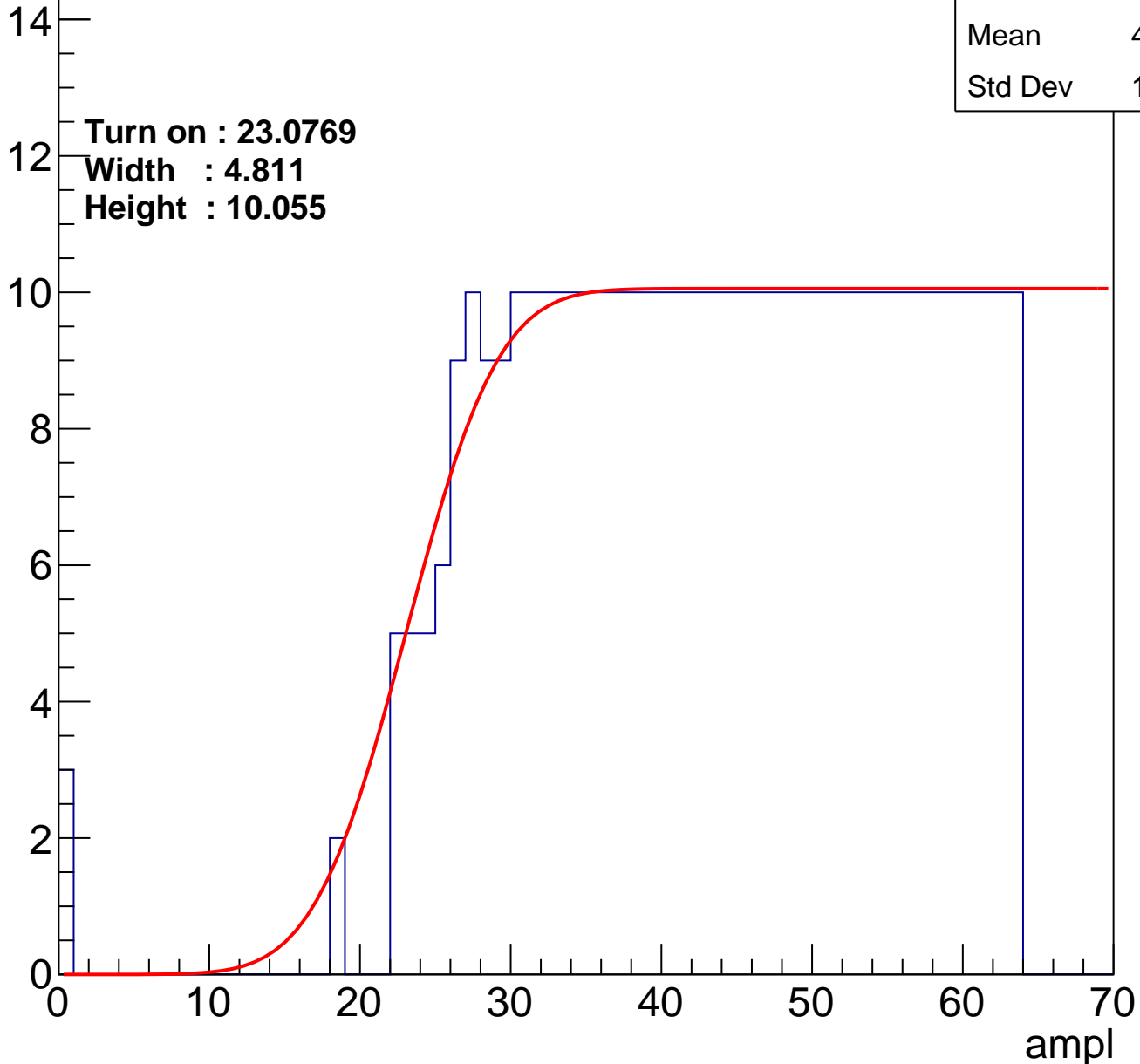
Entries	403
Mean	43.07
Std Dev	12.26

Turn on : 23.0769

Width : 4.811

Height : 10.055

Entry



B1L102S, U9-ch91

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.57
Std Dev	12.04

Turn on : 25.0272

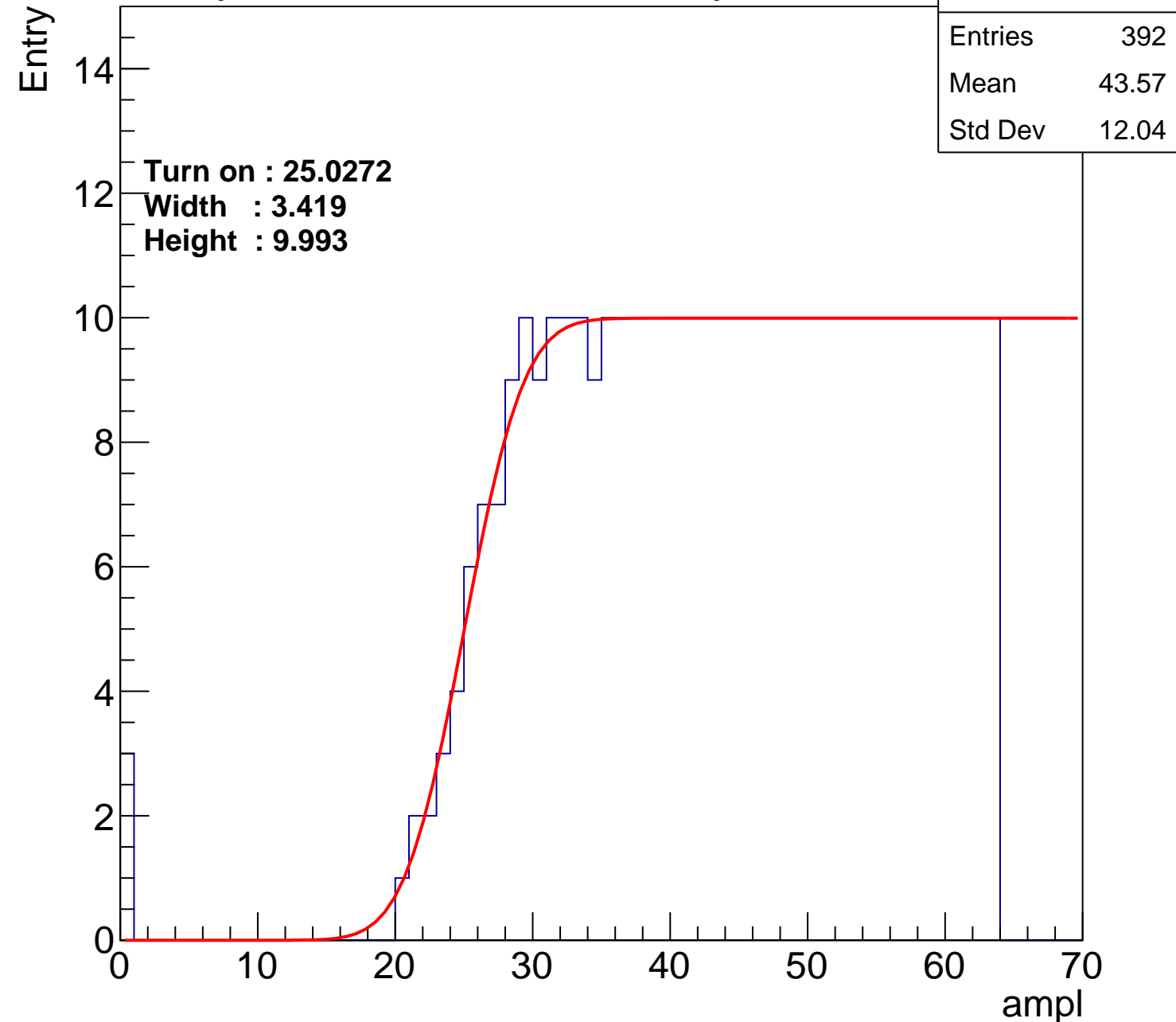
Width : 3.419

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch92

calib_packv5_042523_0143.root, FC#11, port A2

Turn on : 24.5563

Width : 2.562

Height : 9.986

Entries	396
Mean	43.5
Std Dev	11.88



B1L102S, U9-ch93

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	45.02
Std Dev	10.96

Turn on : 27.1701

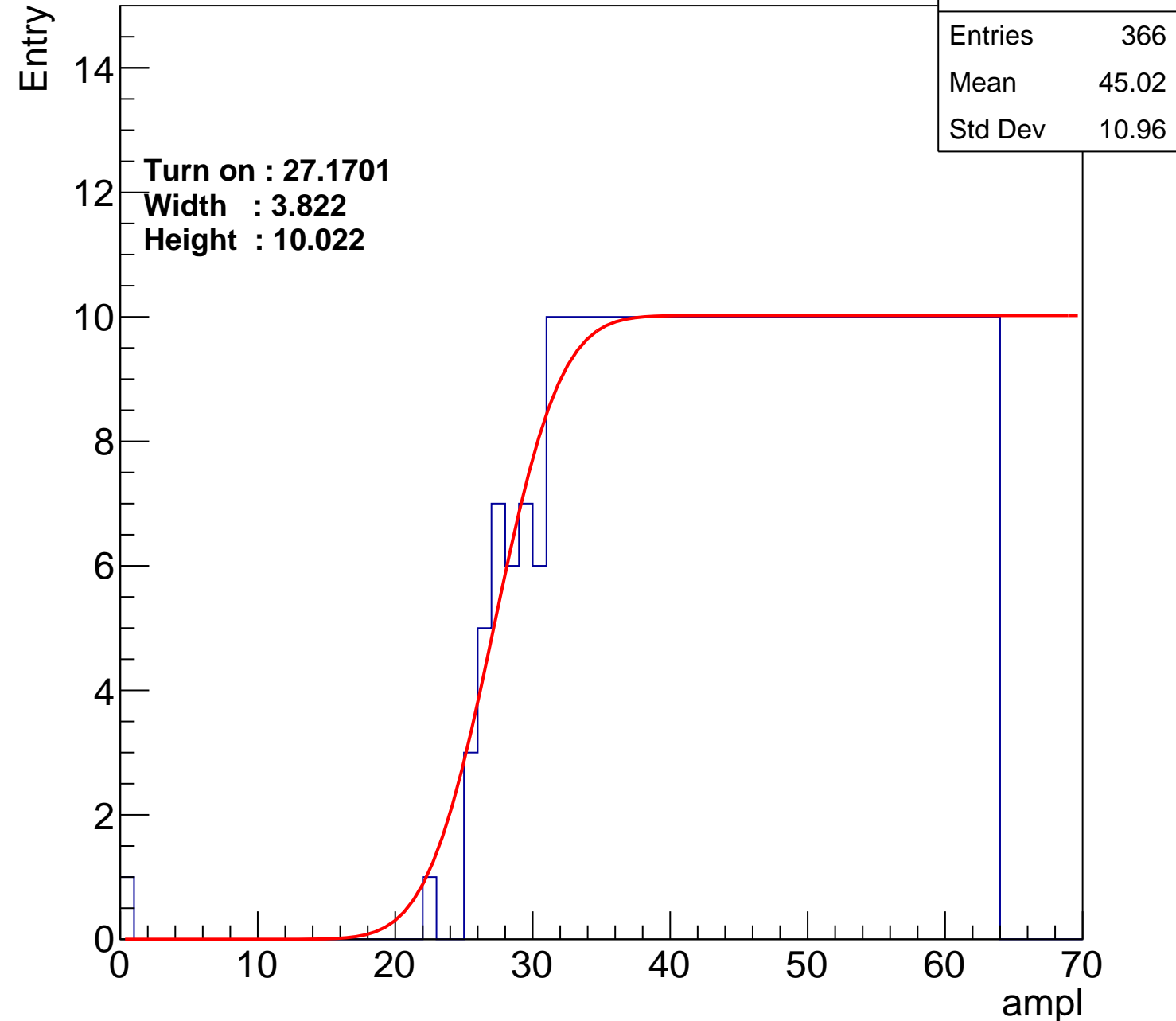
Width : 3.822

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch94

calib_packv5_042523_0143.root, FC#11, port A2

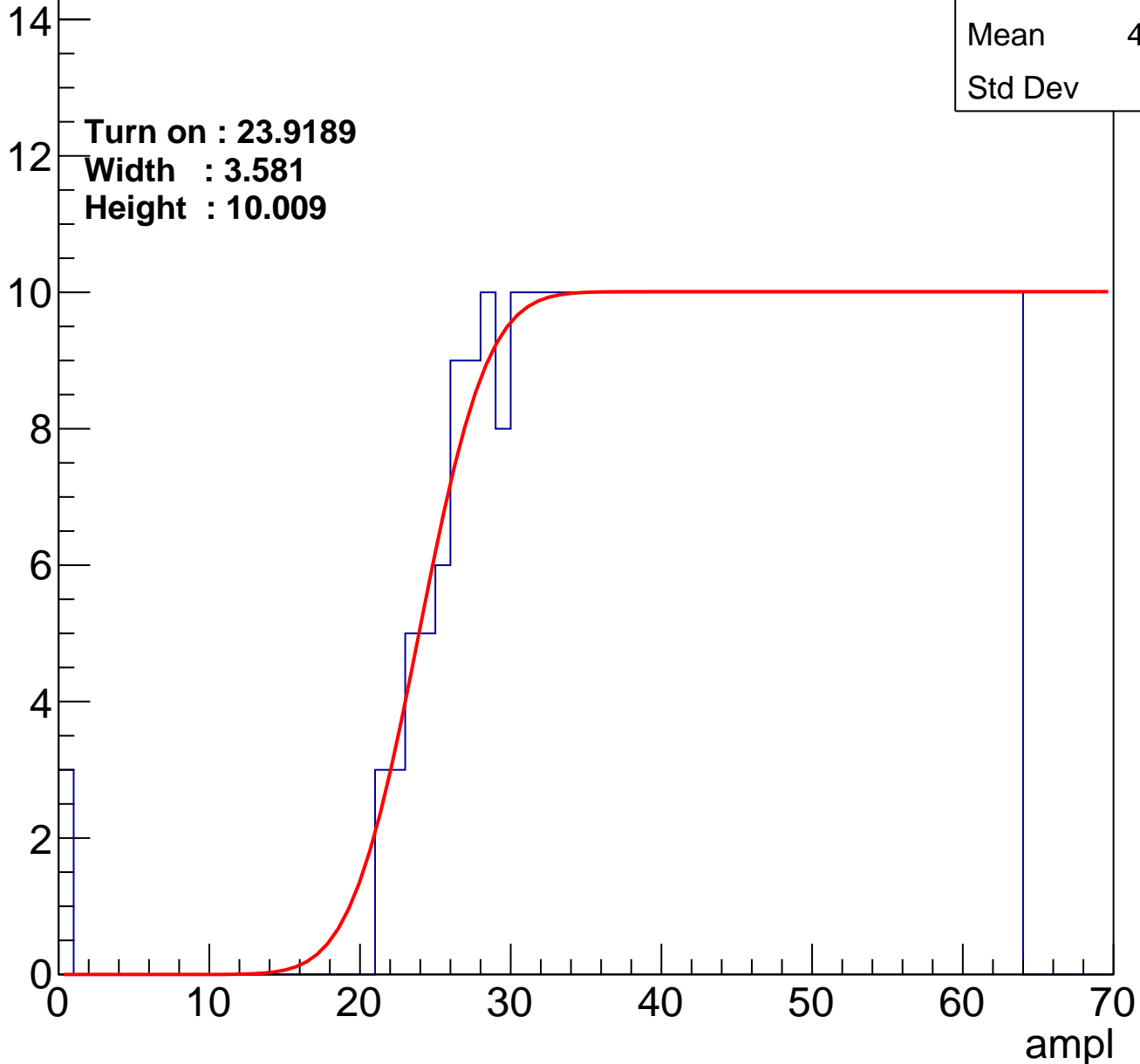
Entries	401
Mean	43.17
Std Dev	12.2

Turn on : 23.9189

Width : 3.581

Height : 10.009

Entry



B1L102S, U9-ch95

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.44
Std Dev	11.98

Turn on : 24.7074

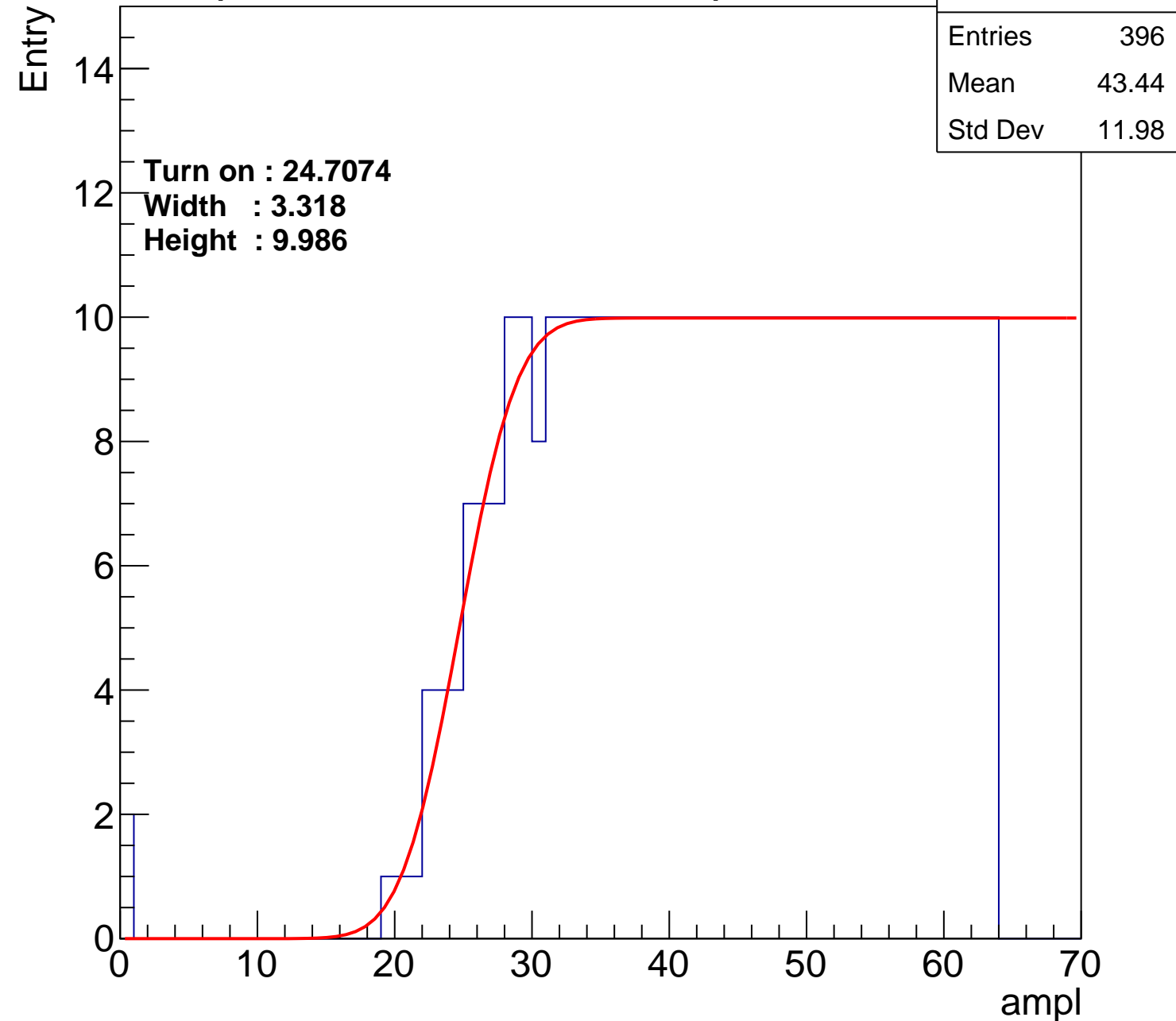
Width : 3.318

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch96

calib_packv5_042523_0143.root, FC#11, port A2

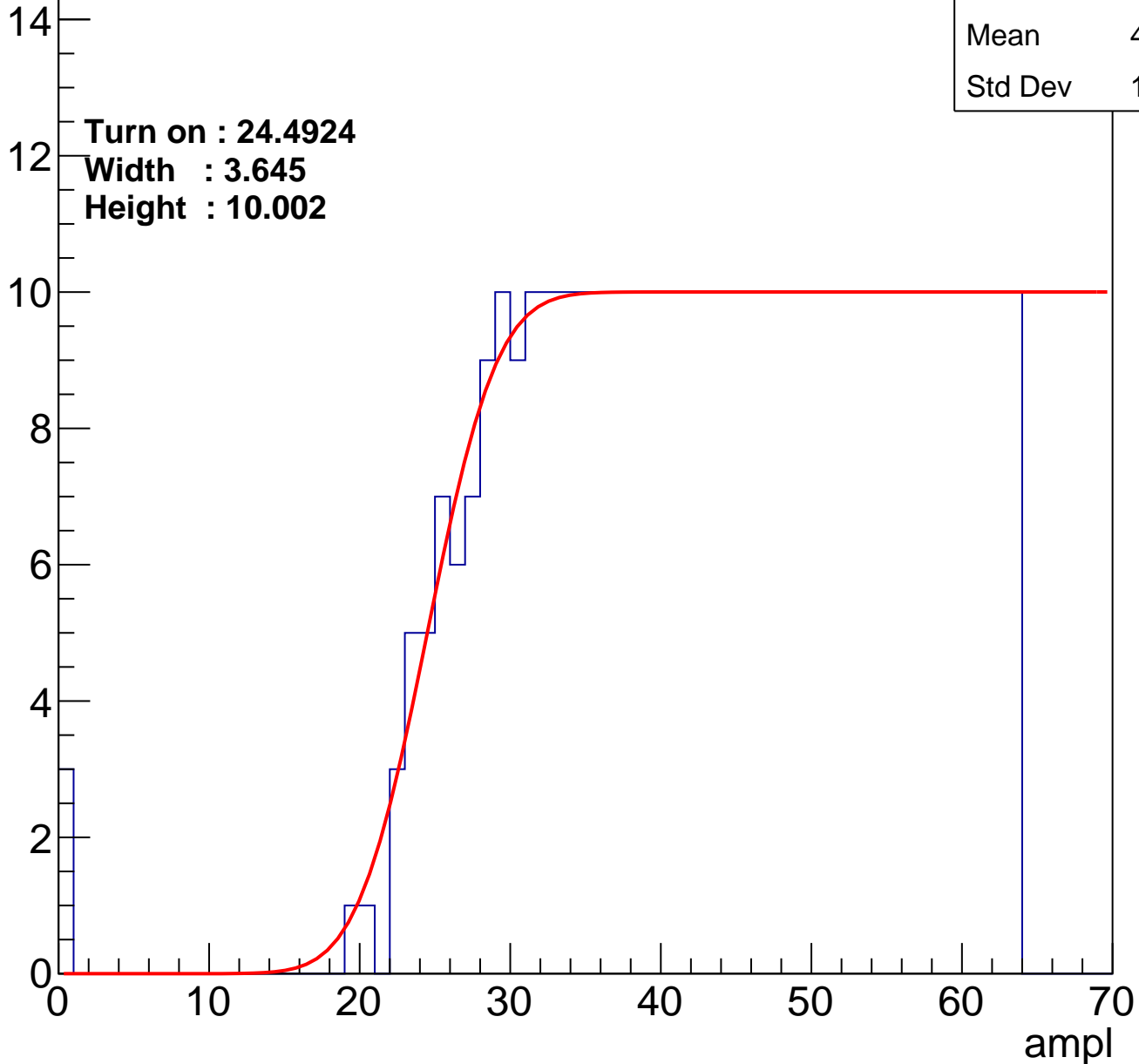
Entries	396
Mean	43.39
Std Dev	12.13

Turn on : 24.4924

Width : 3.645

Height : 10.002

Entry



B1L102S, U9-ch97

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.38
Std Dev	11.66

Turn on : 27.1376

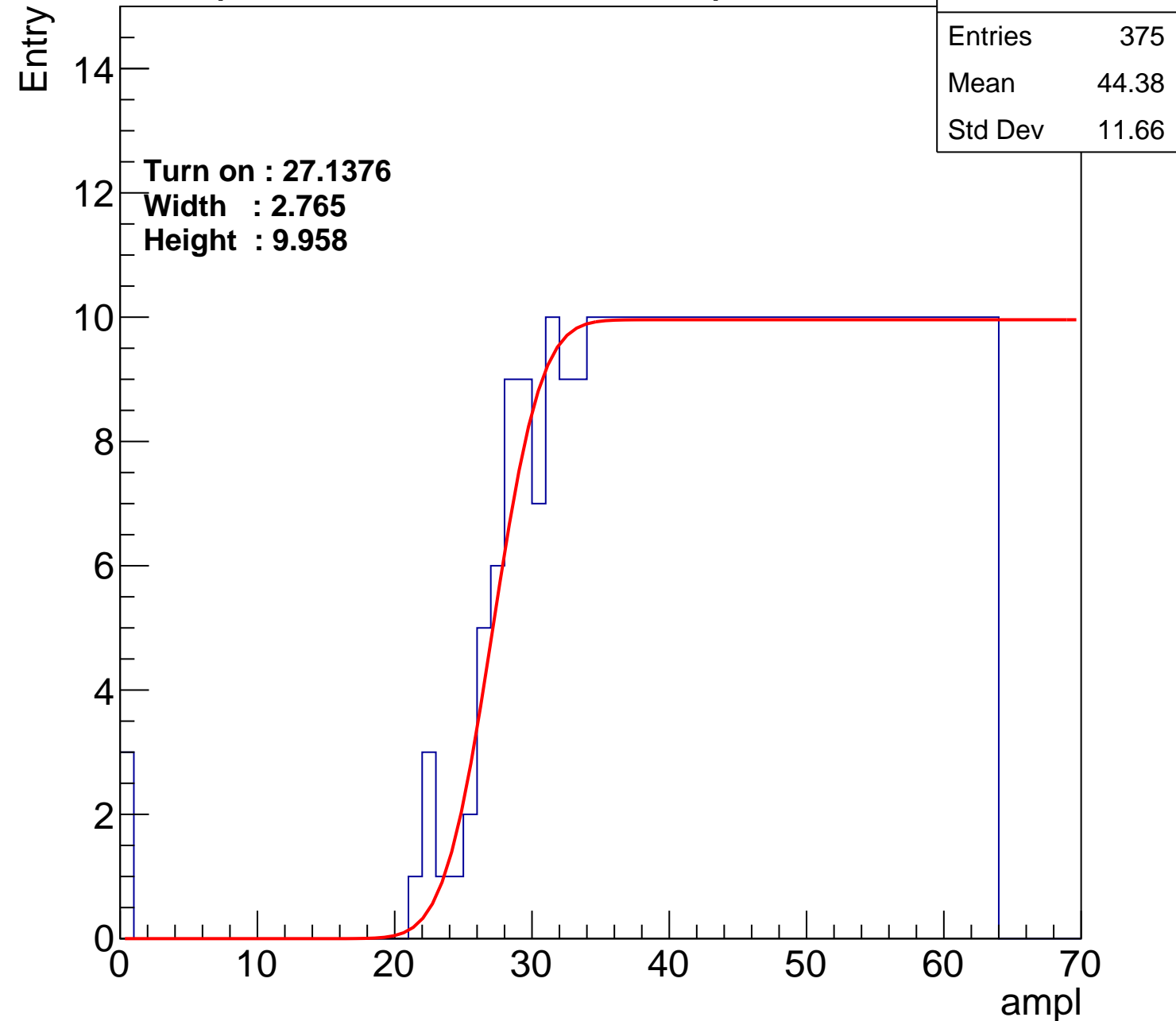
Width : 2.765

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch98

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.88
Std Dev	11.64

Turn on : 25.7442

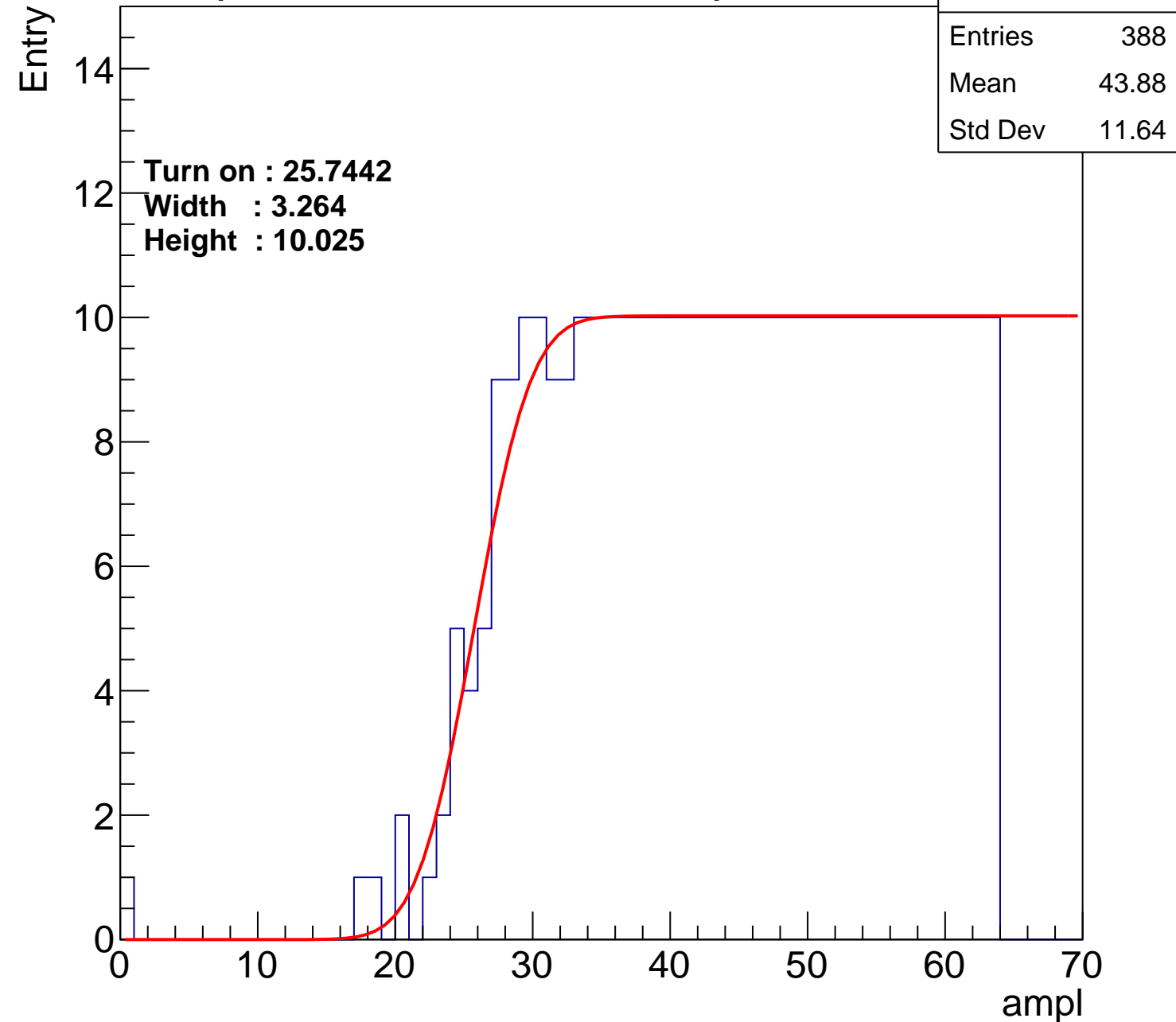
Width : 3.264

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch99

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.64
Std Dev	11.87

Turn on : 25.4998

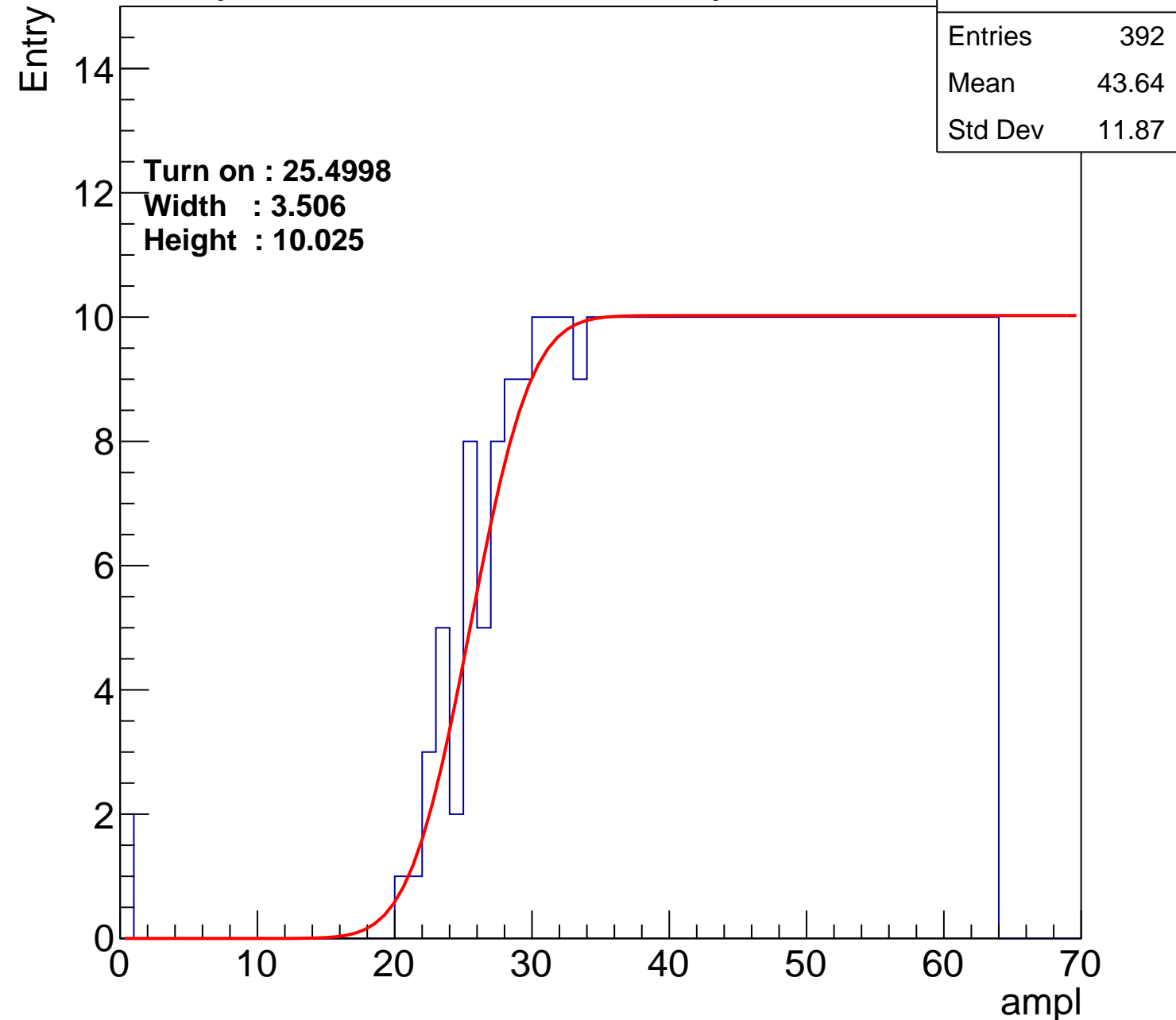
Width : 3.506

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch100

calib_packv5_042523_0143.root, FC#11, port A2

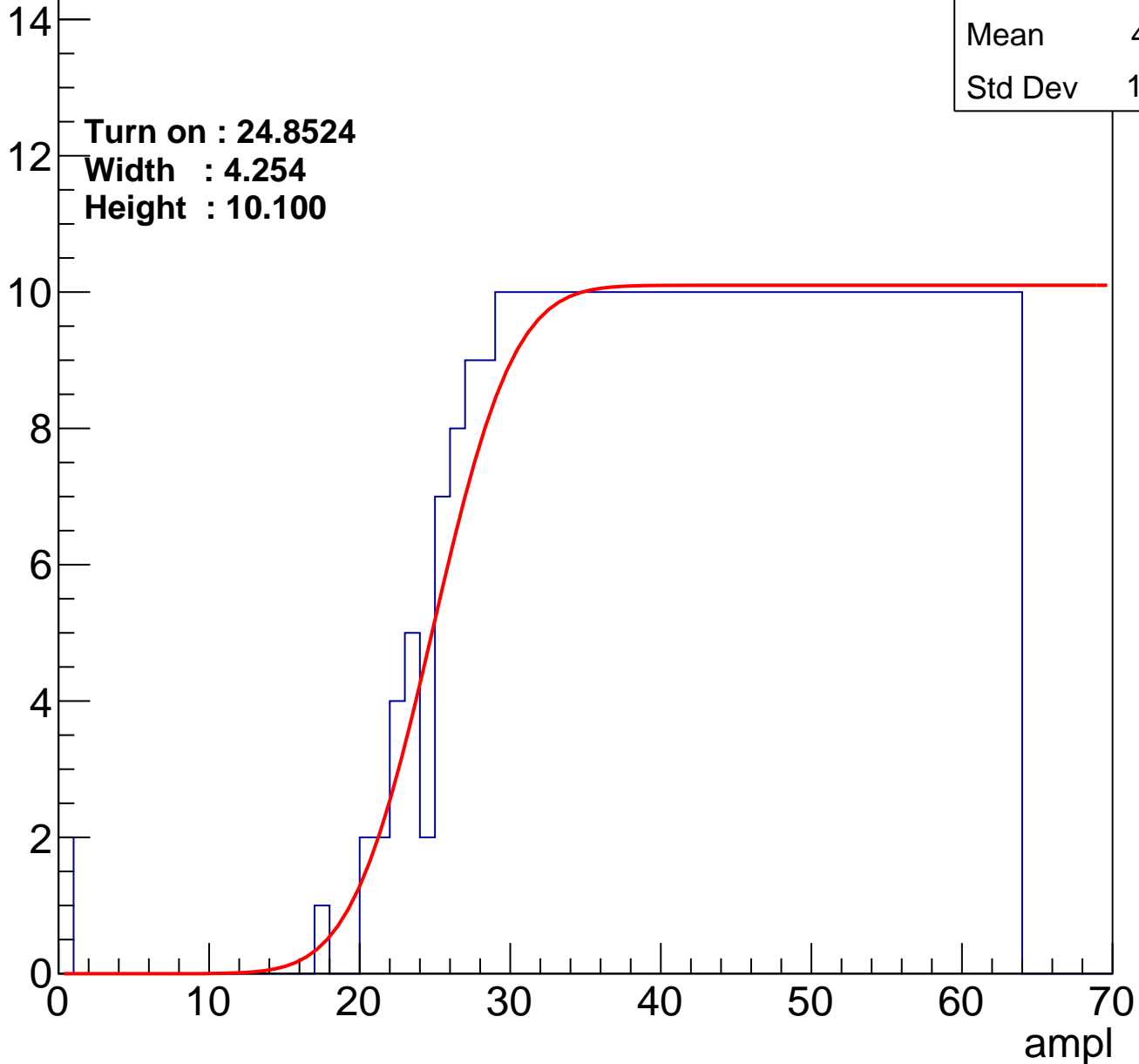
Entries	401
Mean	43.21
Std Dev	12.09

Turn on : 24.8524

Width : 4.254

Height : 10.100

Entry



B1L102S, U9-ch101

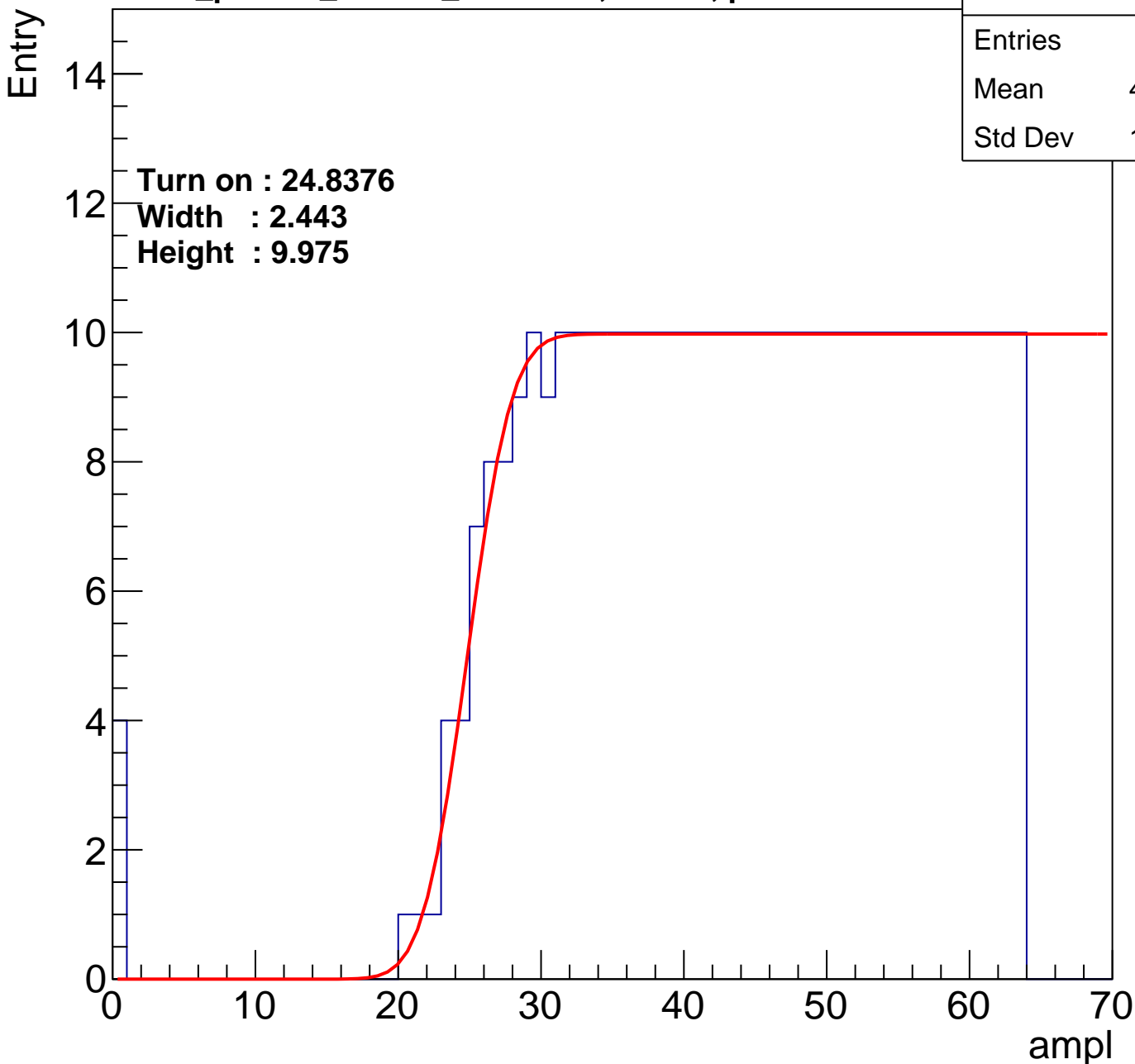
calib_packv5_042523_0143.root, FC#11, port A2

Turn on : 24.8376

Width : 2.443

Height : 9.975

Entries	396
Mean	43.36
Std Dev	12.22



B1L102S, U9-ch102

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	43.94
Std Dev	11.89

Turn on : 26.7309

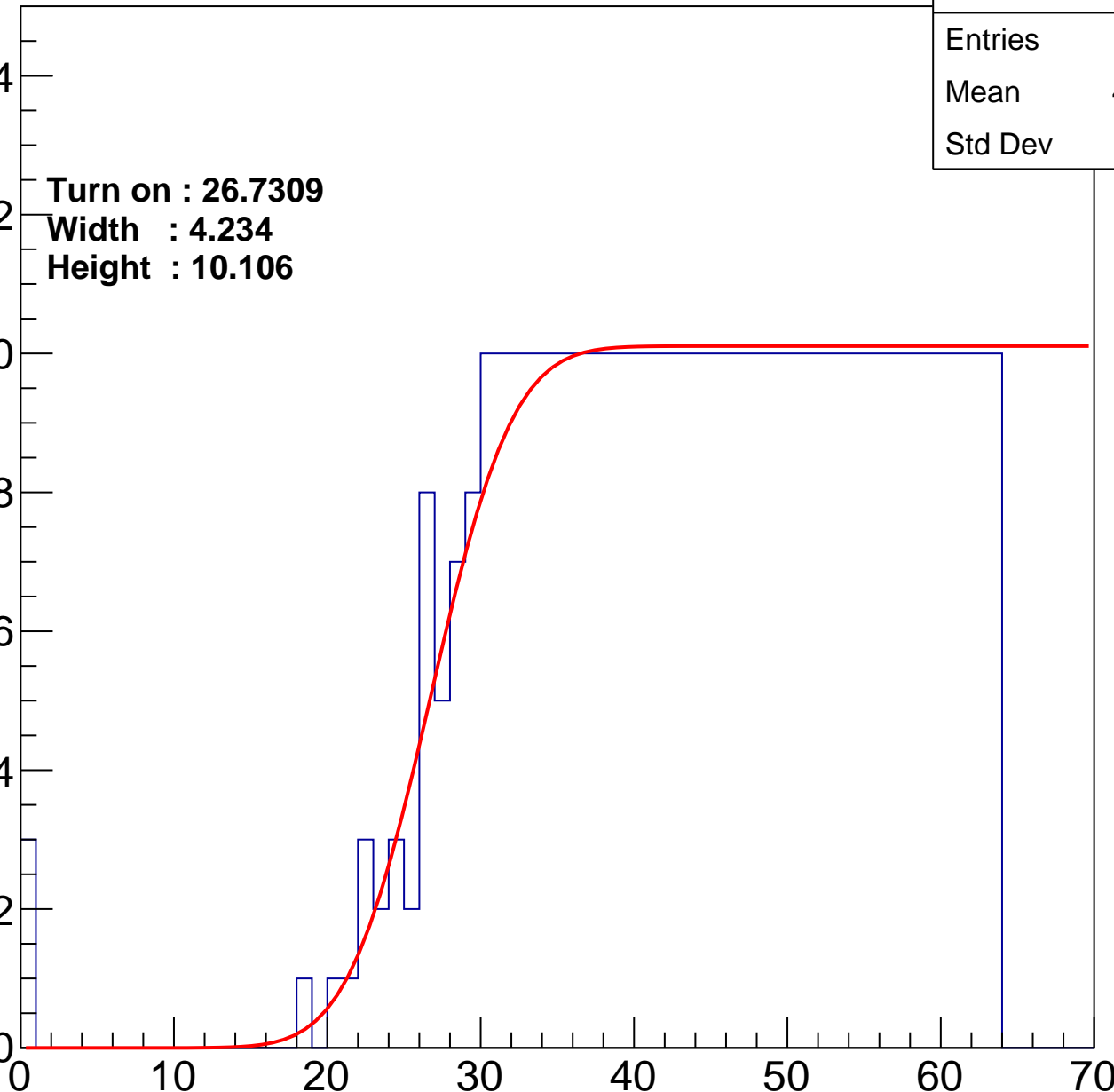
Width : 4.234

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch103

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.57
Std Dev	11.99

Turn on : 25.0761

Width : 2.826

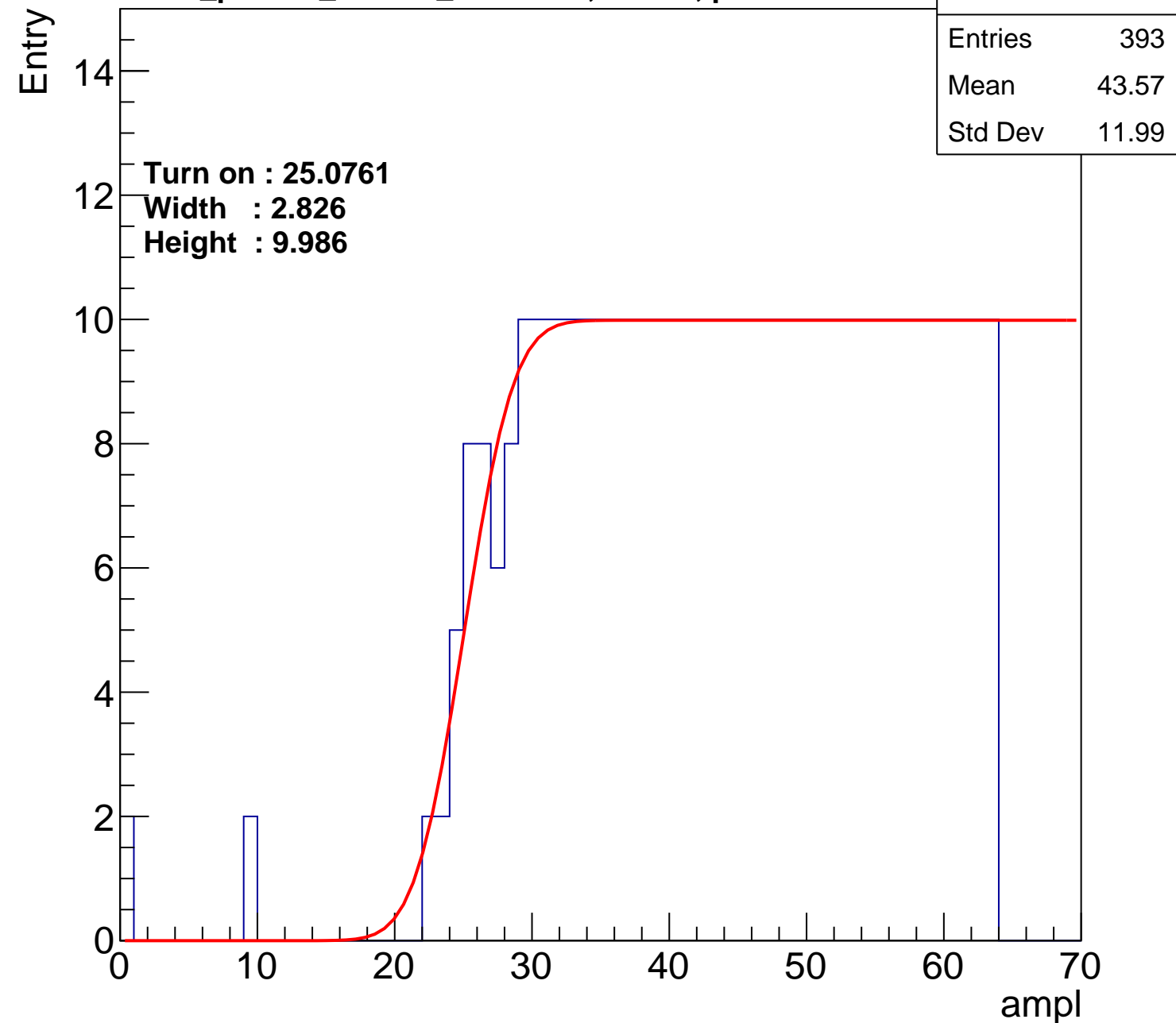
Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U9-ch104

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.46
Std Dev	11.4

Turn on : 26.9251

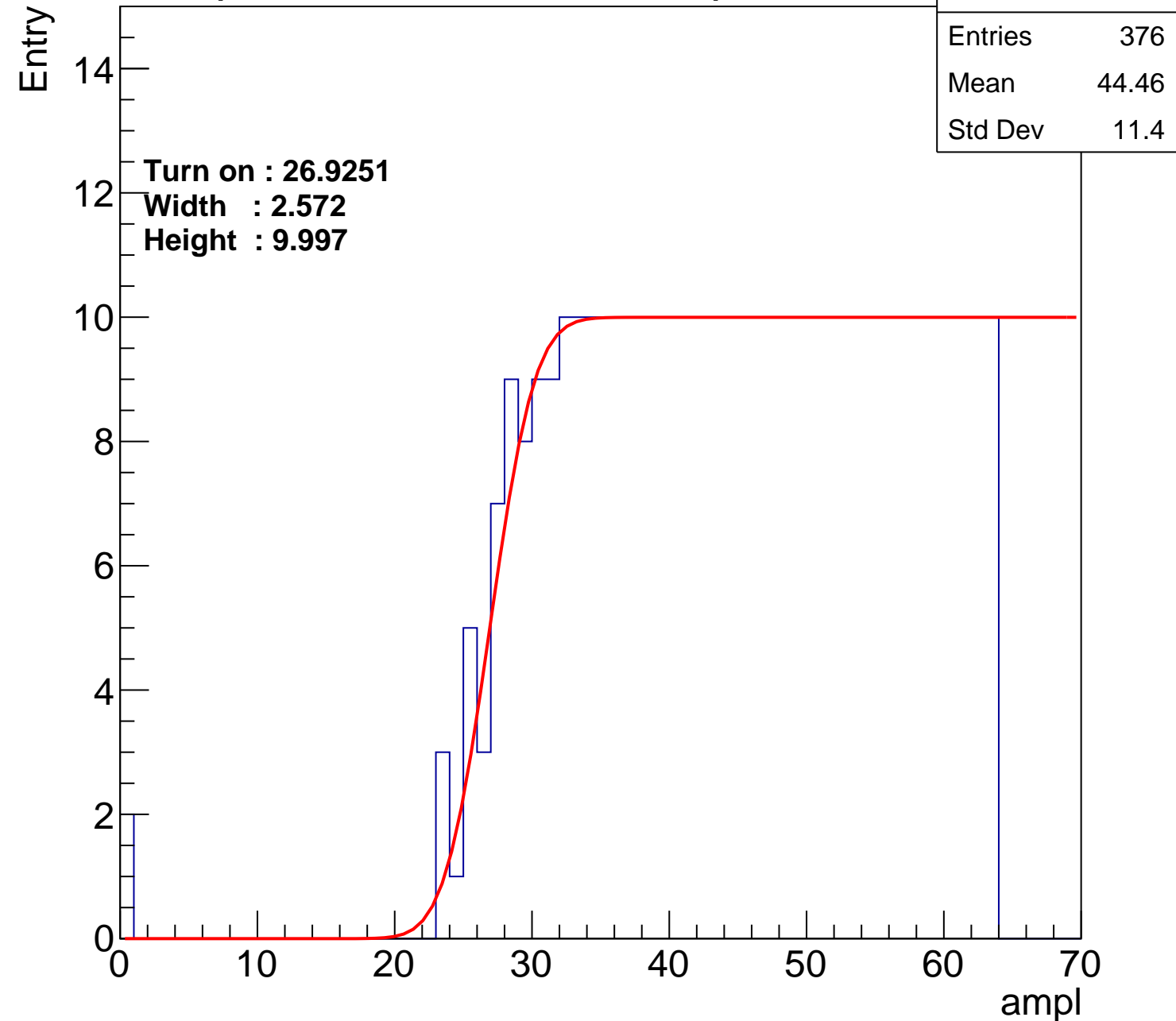
Width : 2.572

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch105

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.6
Std Dev	11.36

Turn on : 27.5808

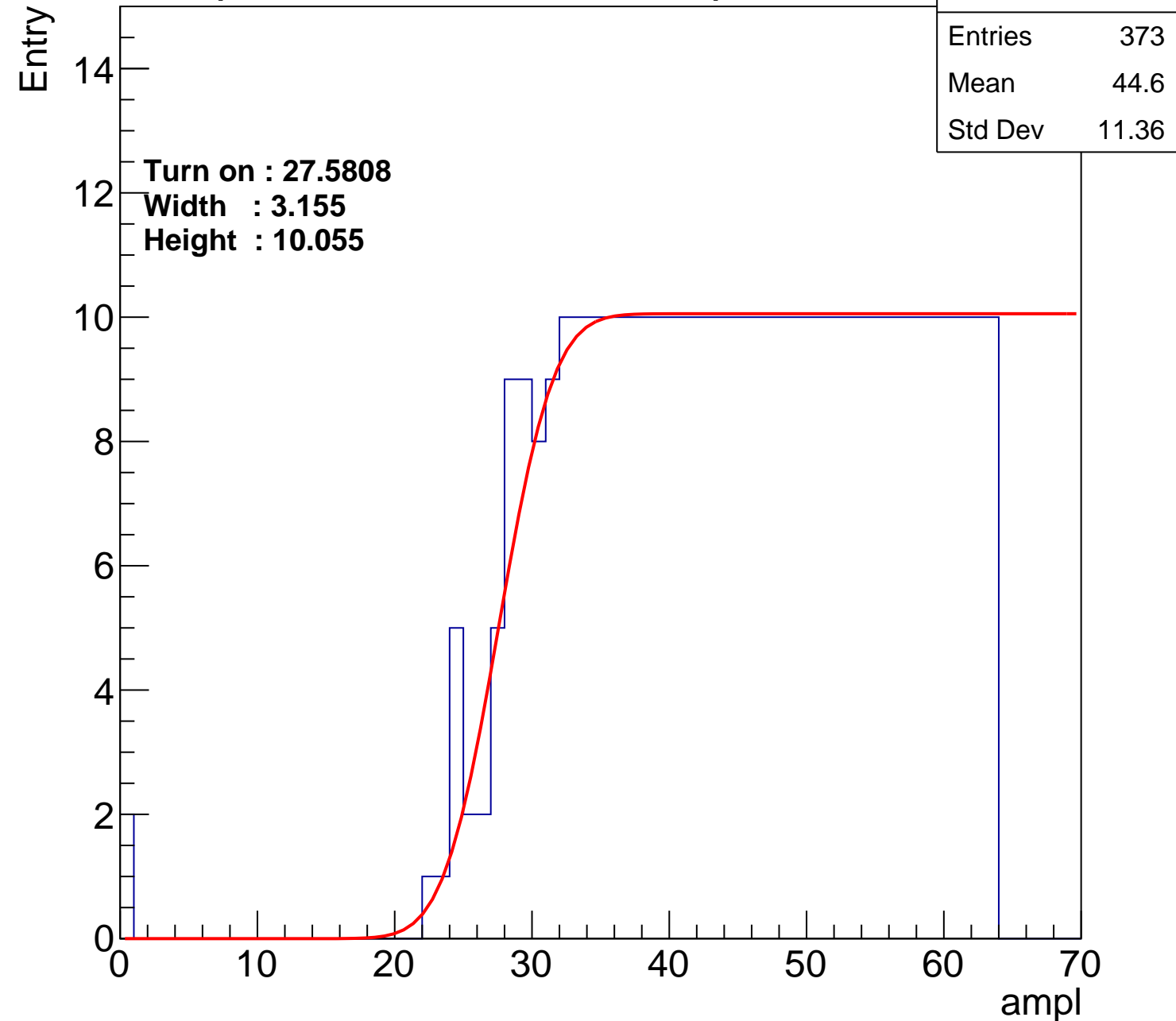
Width : 3.155

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch106

calib_packv5_042523_0143.root, FC#11, port A2

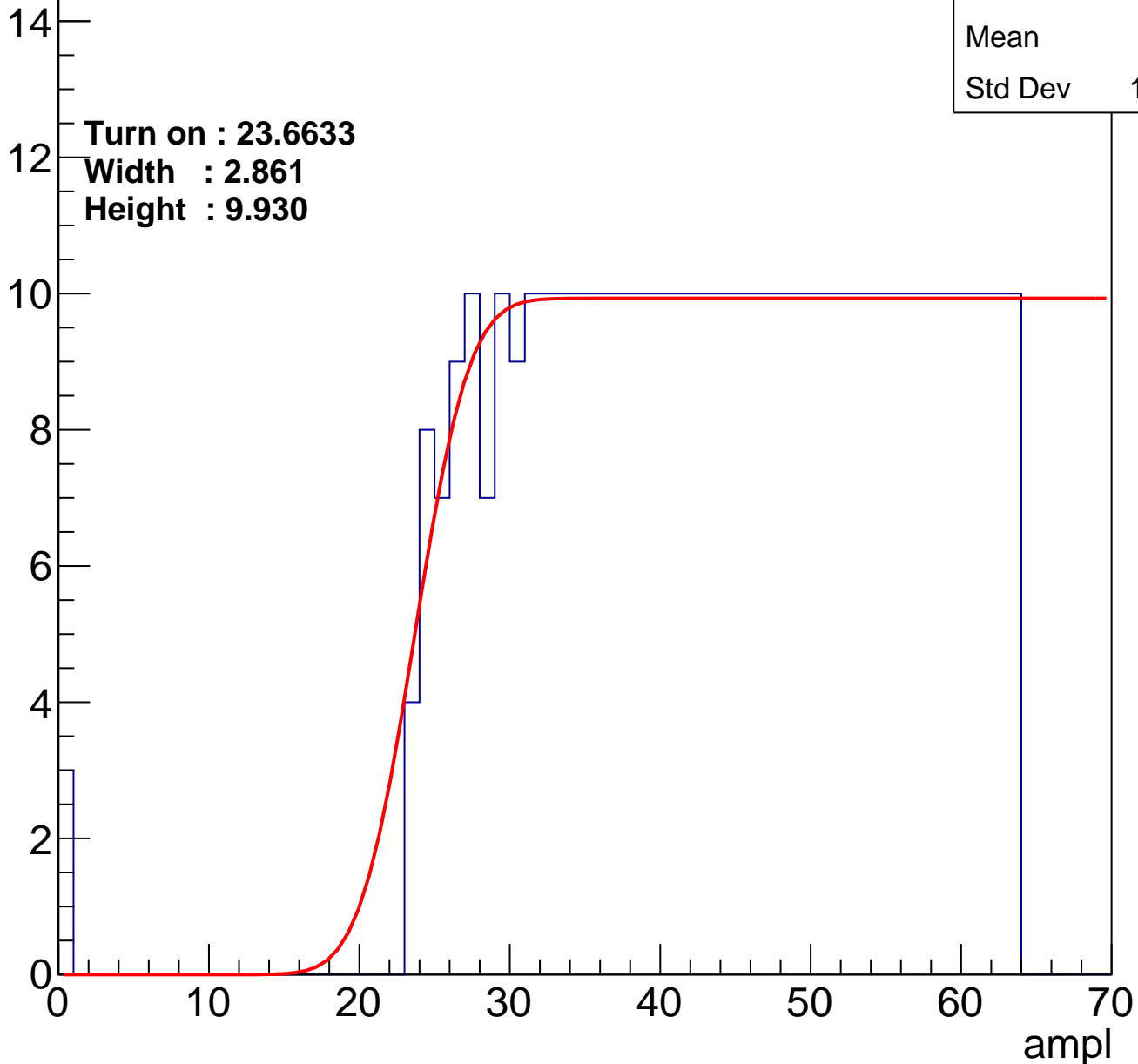
Entries	397
Mean	43.4
Std Dev	12.05

Turn on : 23.6633

Width : 2.861

Height : 9.930

Entry



B1L102S, U9-ch107

calib_packv5_042523_0143.root, FC#11, port A2

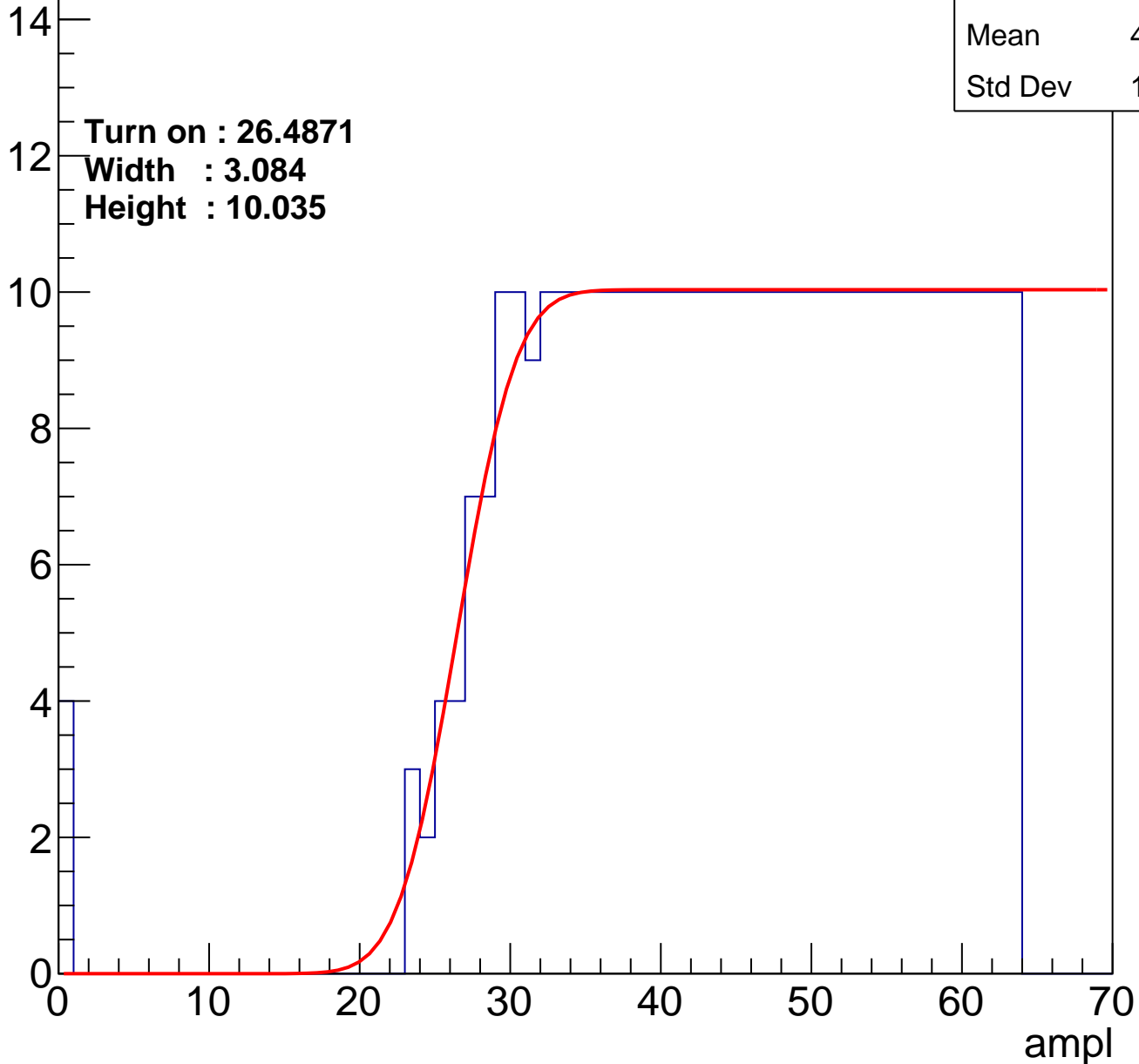
Entries	380
Mean	44.14
Std Dev	11.85

Turn on : 26.4871

Width : 3.084

Height : 10.035

Entry



B1L102S, U9-ch108

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.37
Std Dev	11.59

Turn on : 26.5119

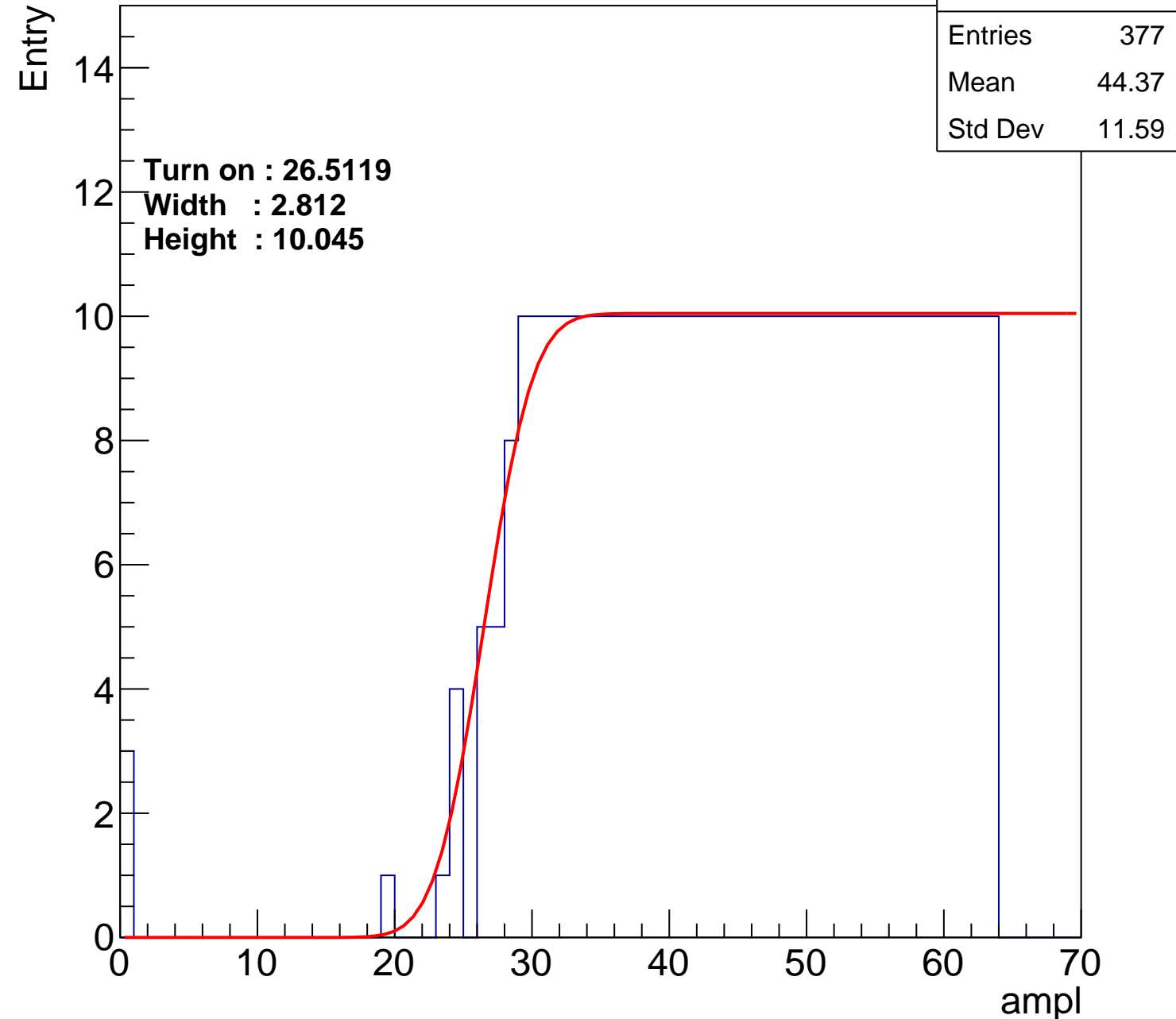
Width : 2.812

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch109

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.98
Std Dev	11.16

Turn on : 27.7247

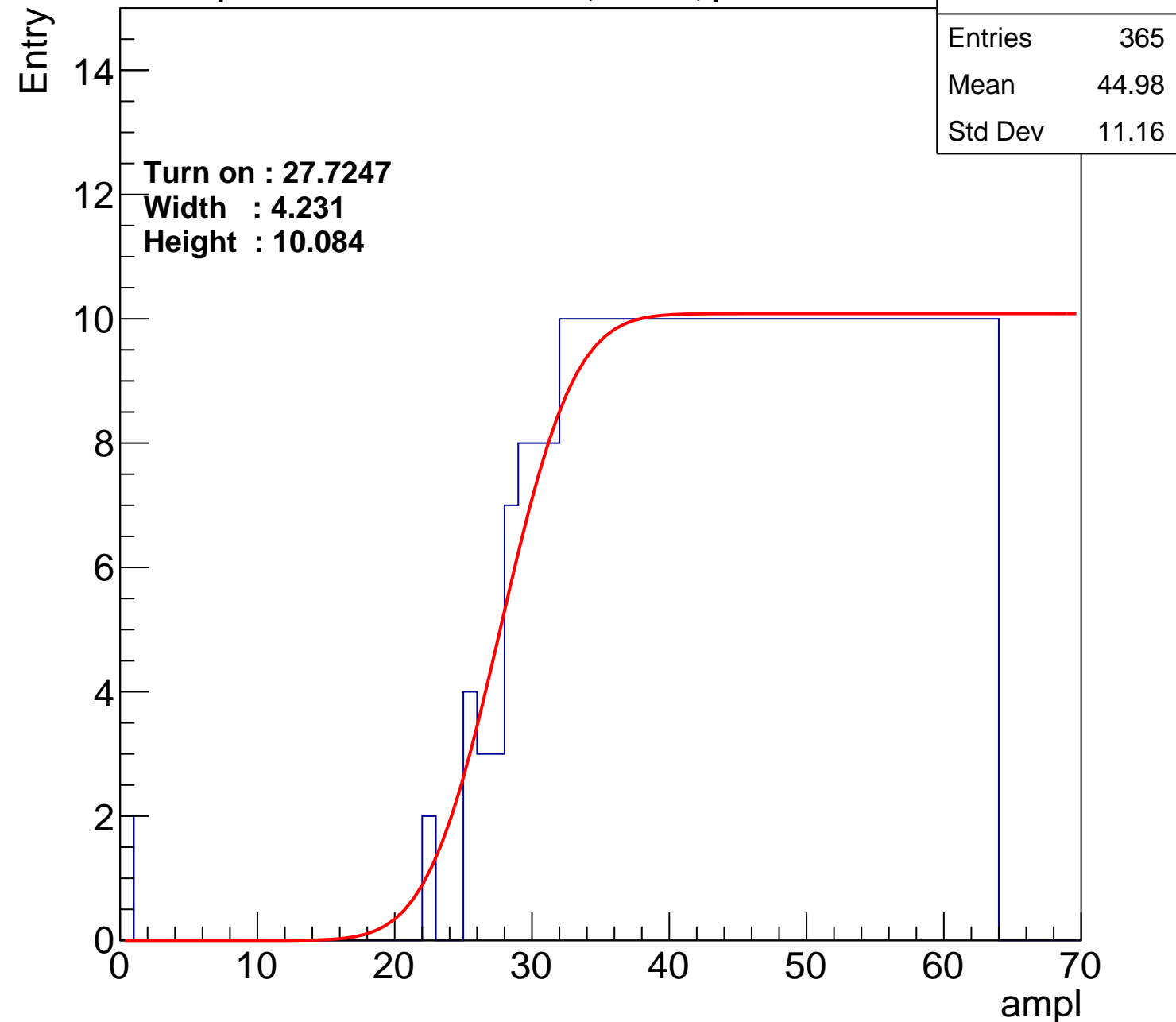
Width : 4.231

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch110

calib_packv5_042523_0143.root, FC#11, port A2

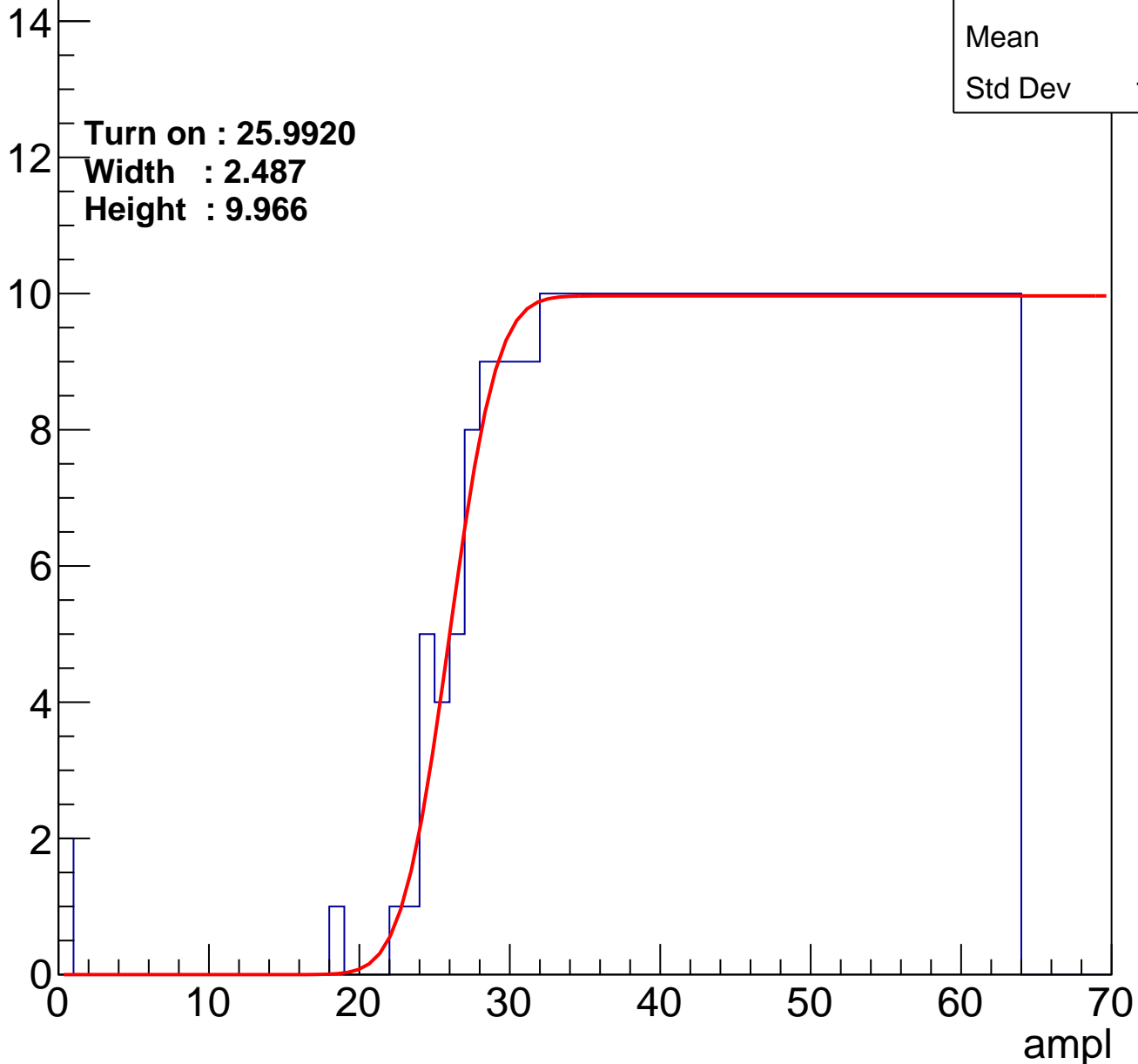
Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 25.9920

Width : 2.487

Height : 9.966

Entry



B1L102S, U9-ch111

calib_packv5_042523_0143.root, FC#11, port A2

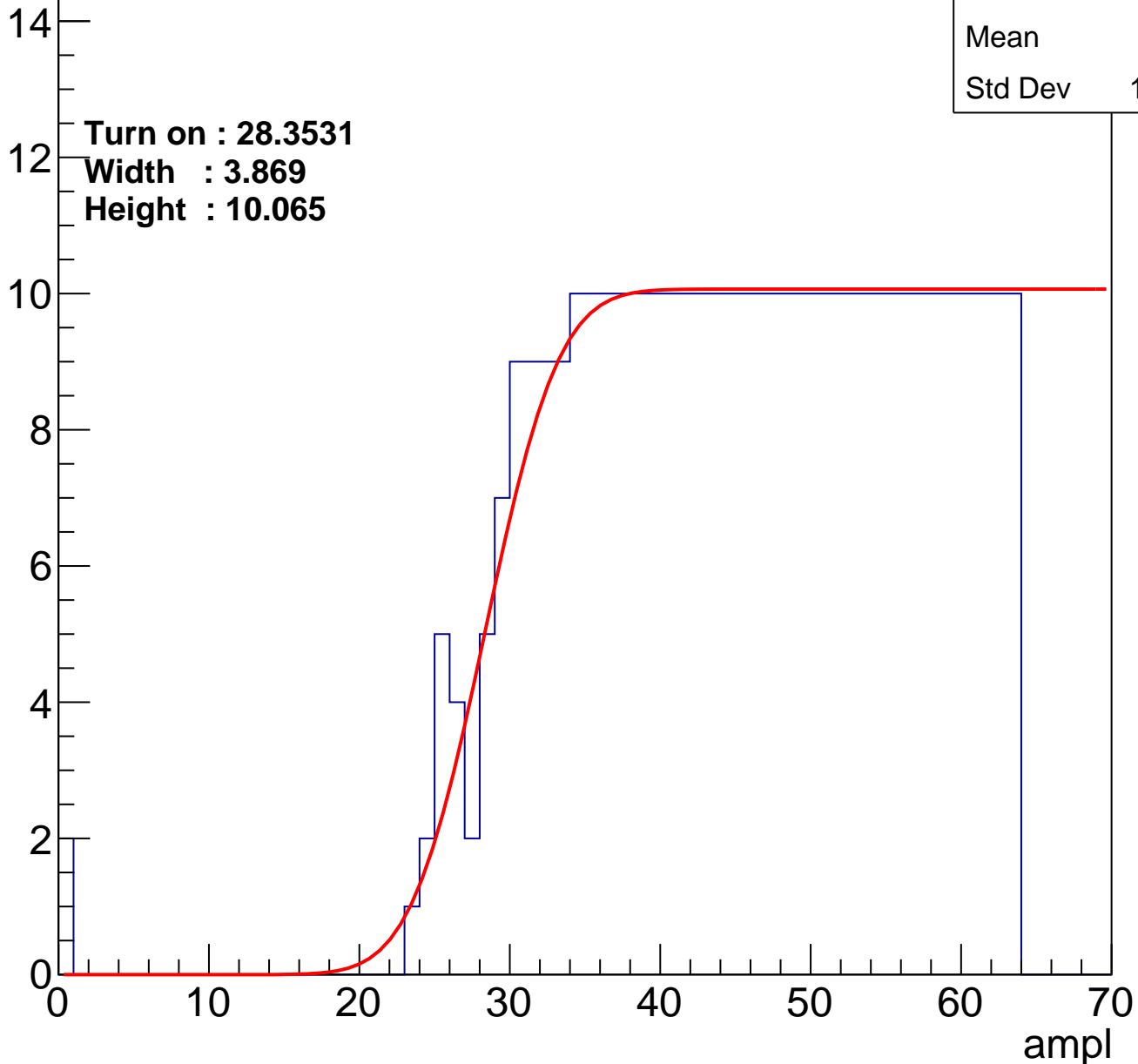
Entries	364
Mean	45
Std Dev	11.18

Turn on : 28.3531

Width : 3.869

Height : 10.065

Entry



B1L102S, U9-ch112

calib_packv5_042523_0143.root, FC#11, port A2

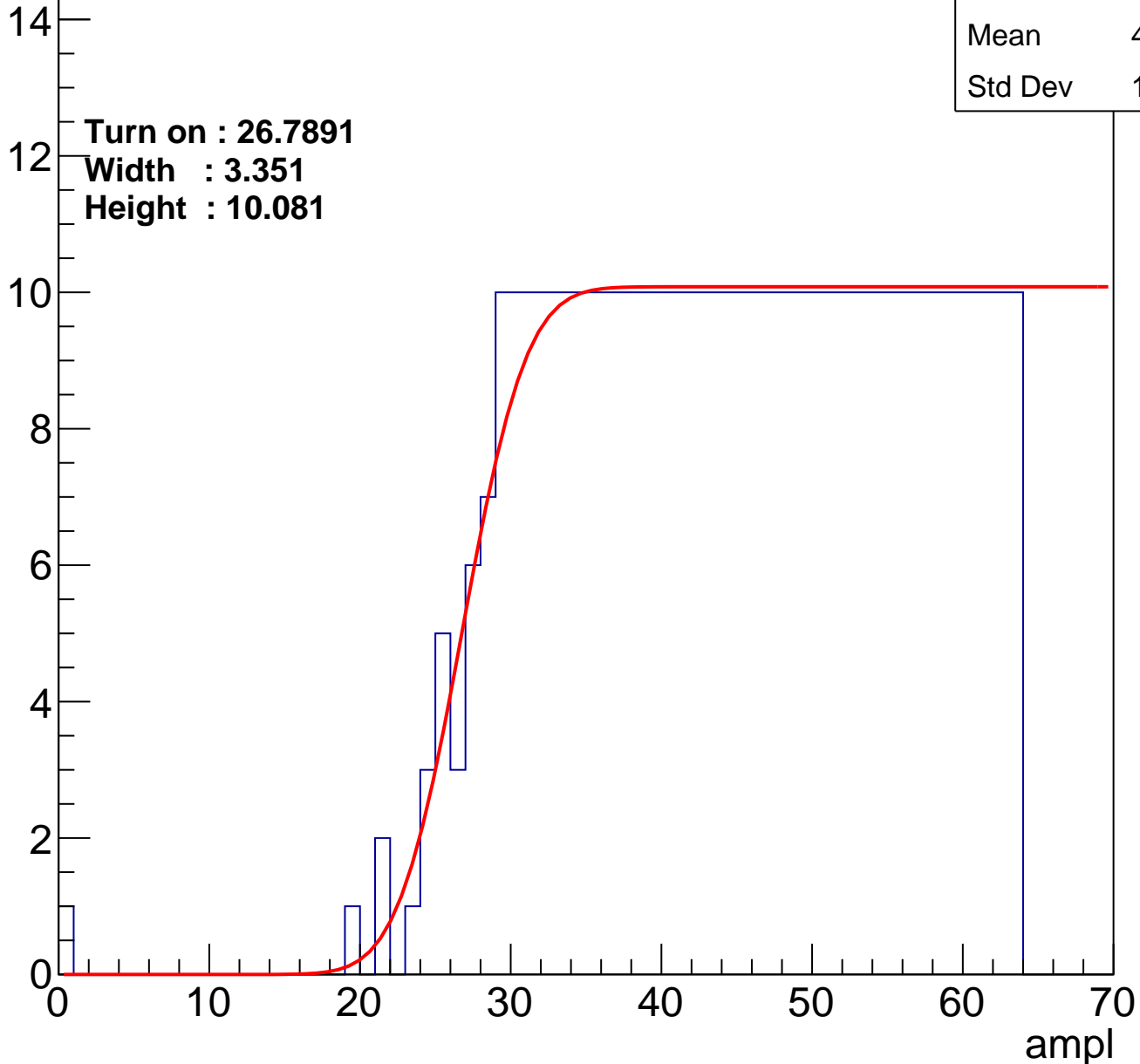
Entries	379
Mean	44.37
Std Dev	11.33

Turn on : 26.7891

Width : 3.351

Height : 10.081

Entry



B1L102S, U9-ch113

calib_packv5_042523_0143.root, FC#11, port A2

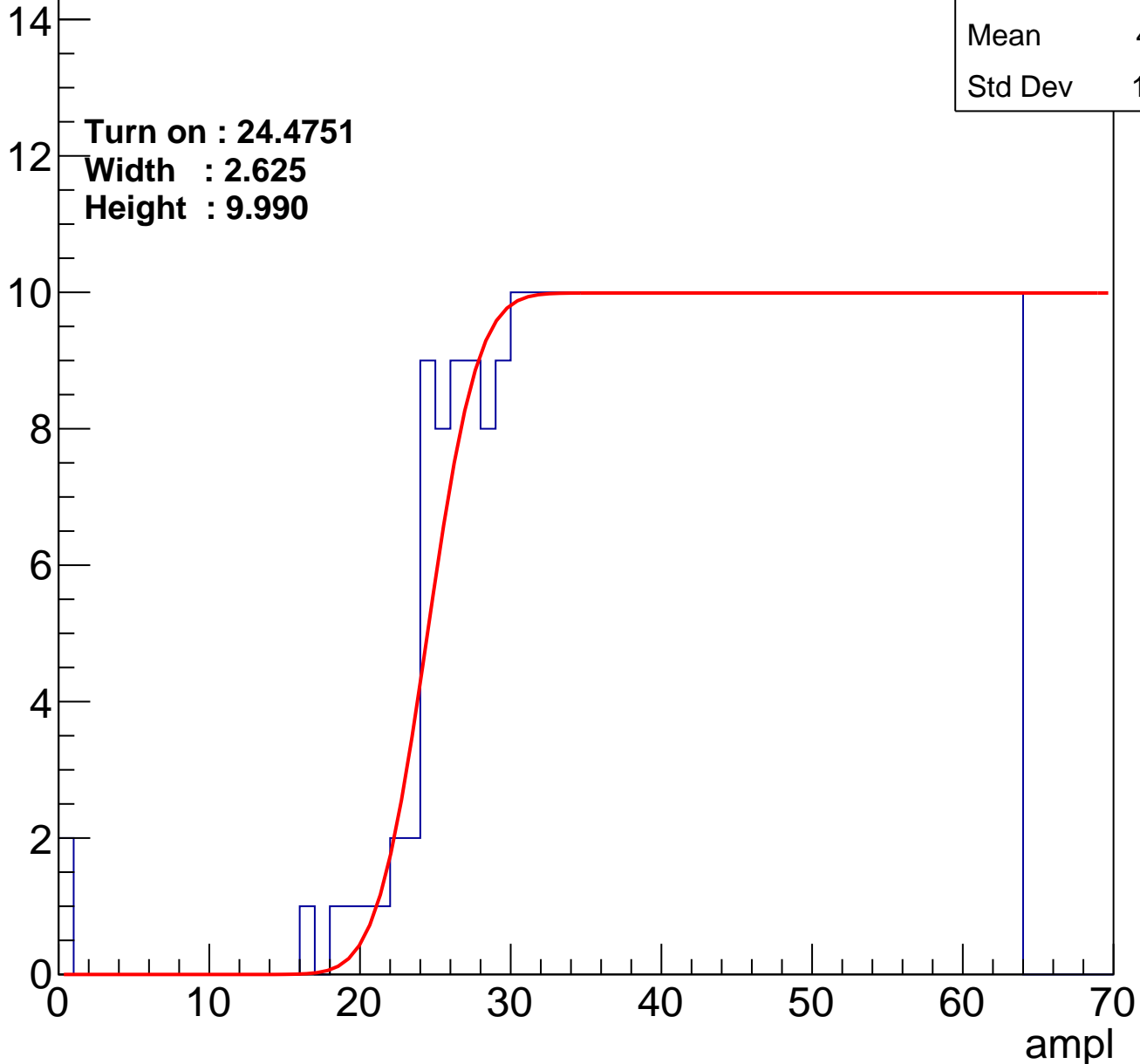
Entries	403
Mean	43.11
Std Dev	12.15

Turn on : 24.4751

Width : 2.625

Height : 9.990

Entry



B1L102S, U9-ch114

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.54
Std Dev	12.17

Turn on : 25.6521

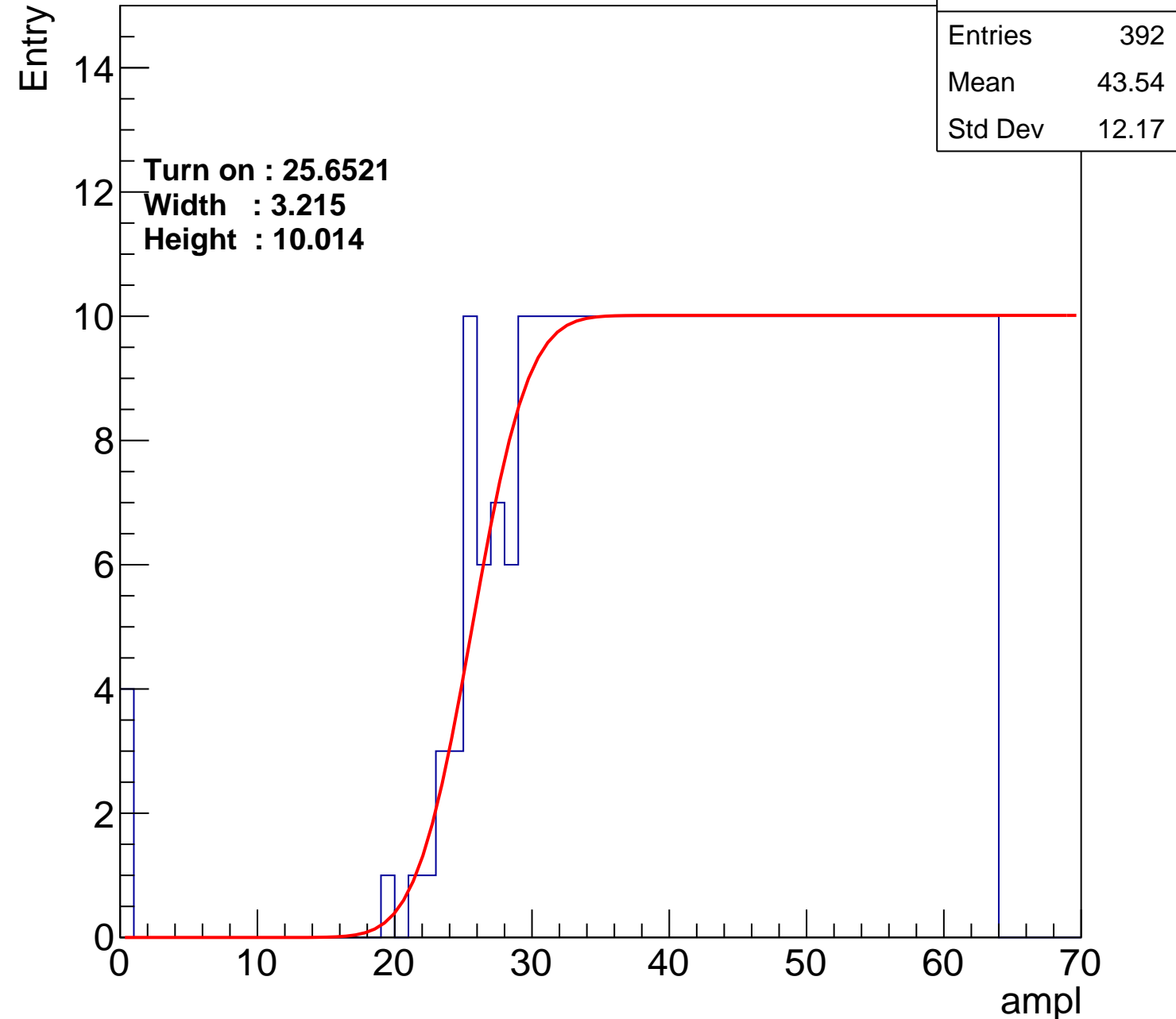
Width : 3.215

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch115

calib_packv5_042523_0143.root, FC#11, port A2

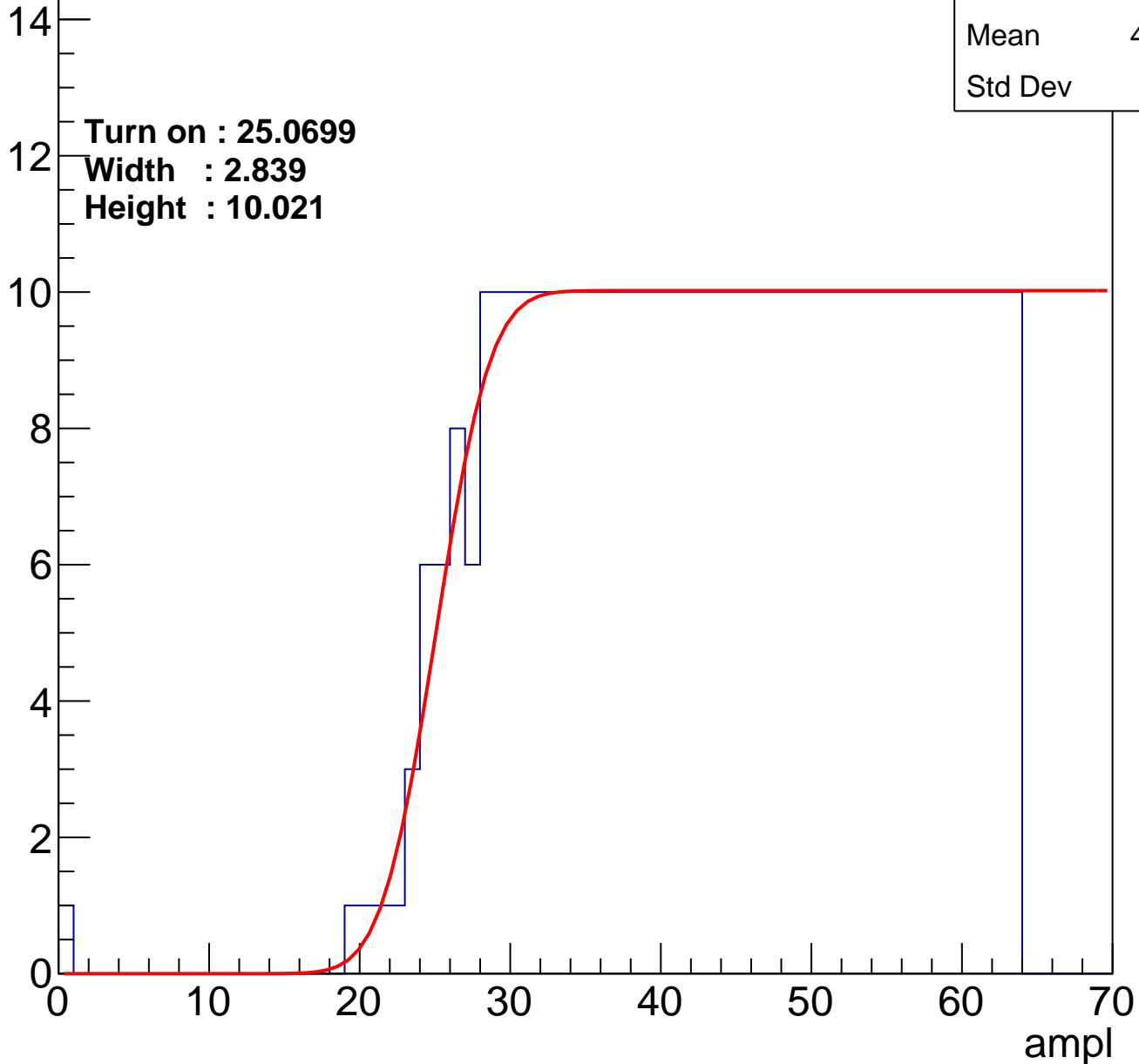
Entries	394
Mean	43.64
Std Dev	11.7

Turn on : 25.0699

Width : 2.839

Height : 10.021

Entry



B1L102S, U9-ch116

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.91
Std Dev	11.65

Turn on : 25.9696

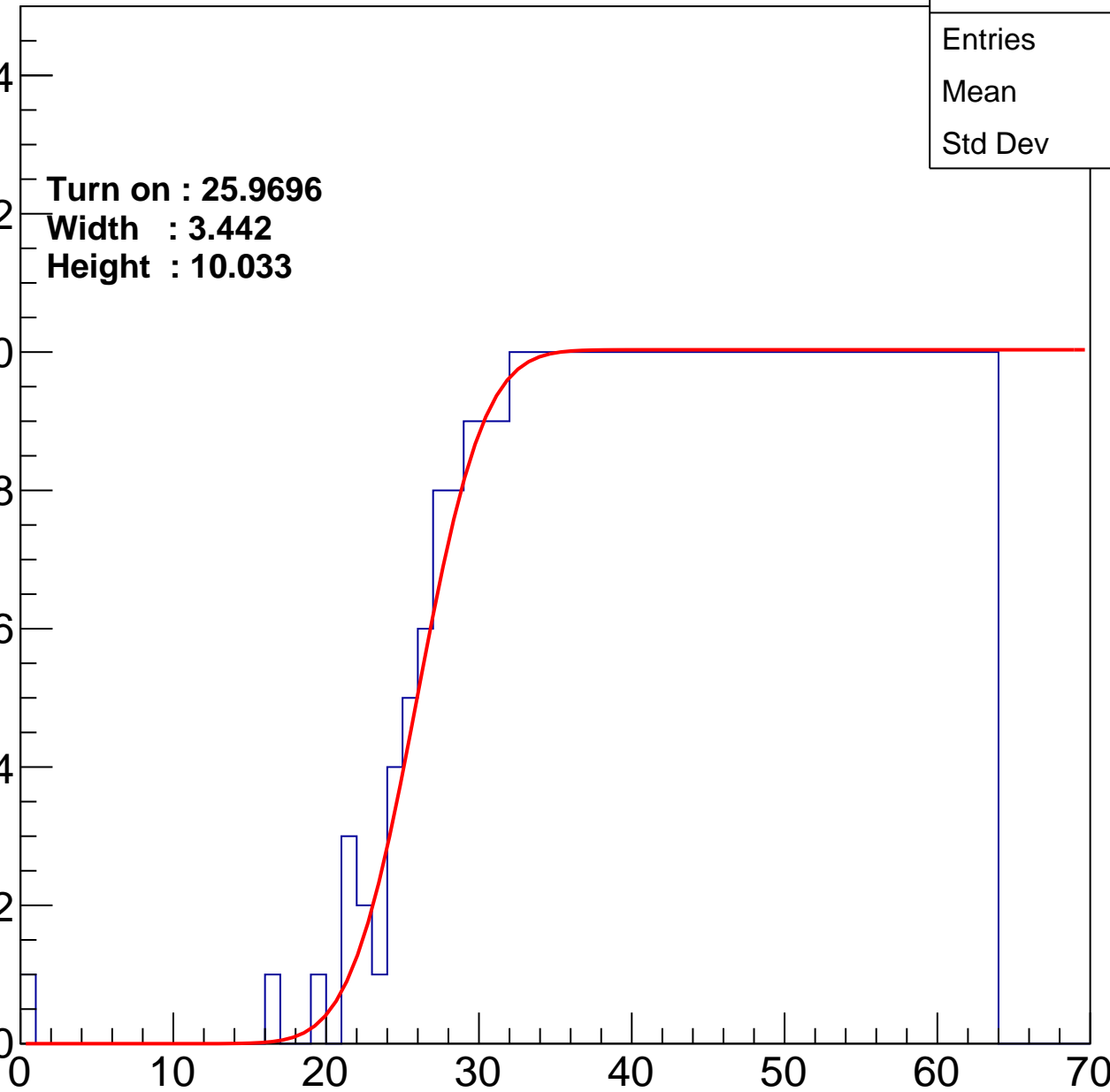
Width : 3.442

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch117

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.59
Std Dev	11.51

Turn on : 27.2583

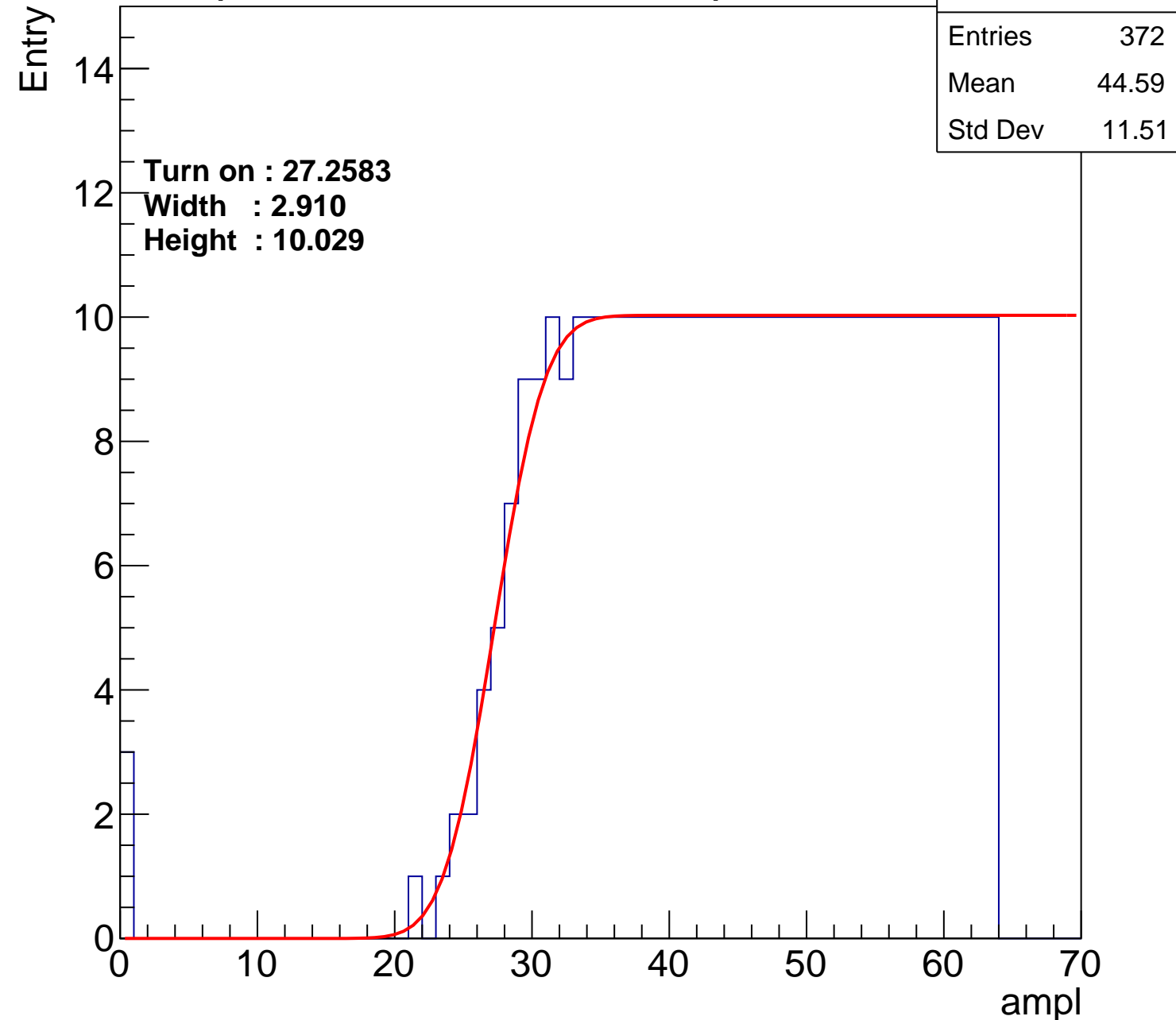
Width : 2.910

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch118

calib_packv5_042523_0143.root, FC#11, port A2

Entries	395
Mean	43.39
Std Dev	12.24

Turn on : 25.5117

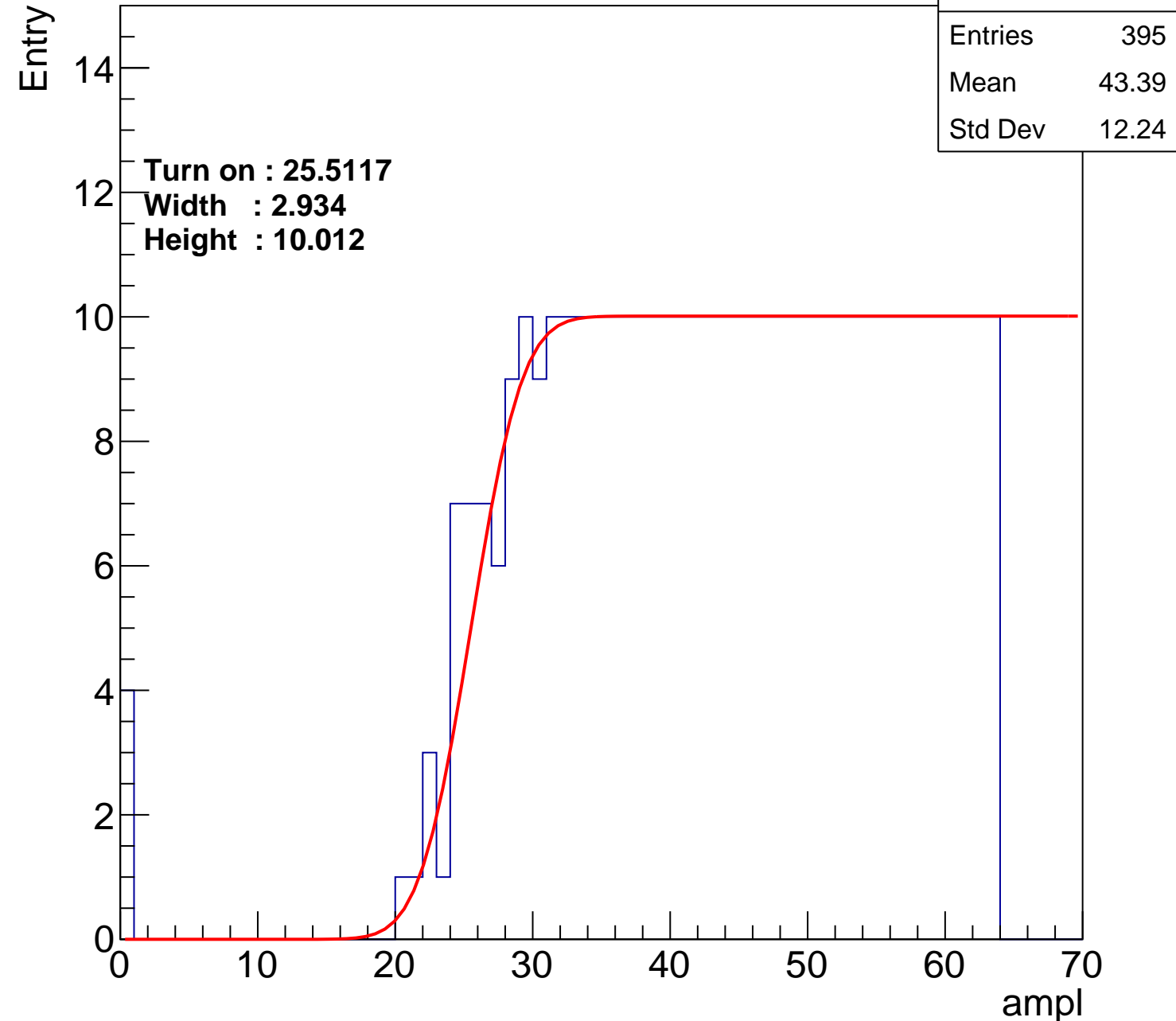
Width : 2.934

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch119

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.29
Std Dev	11.83

Turn on : 26.6194

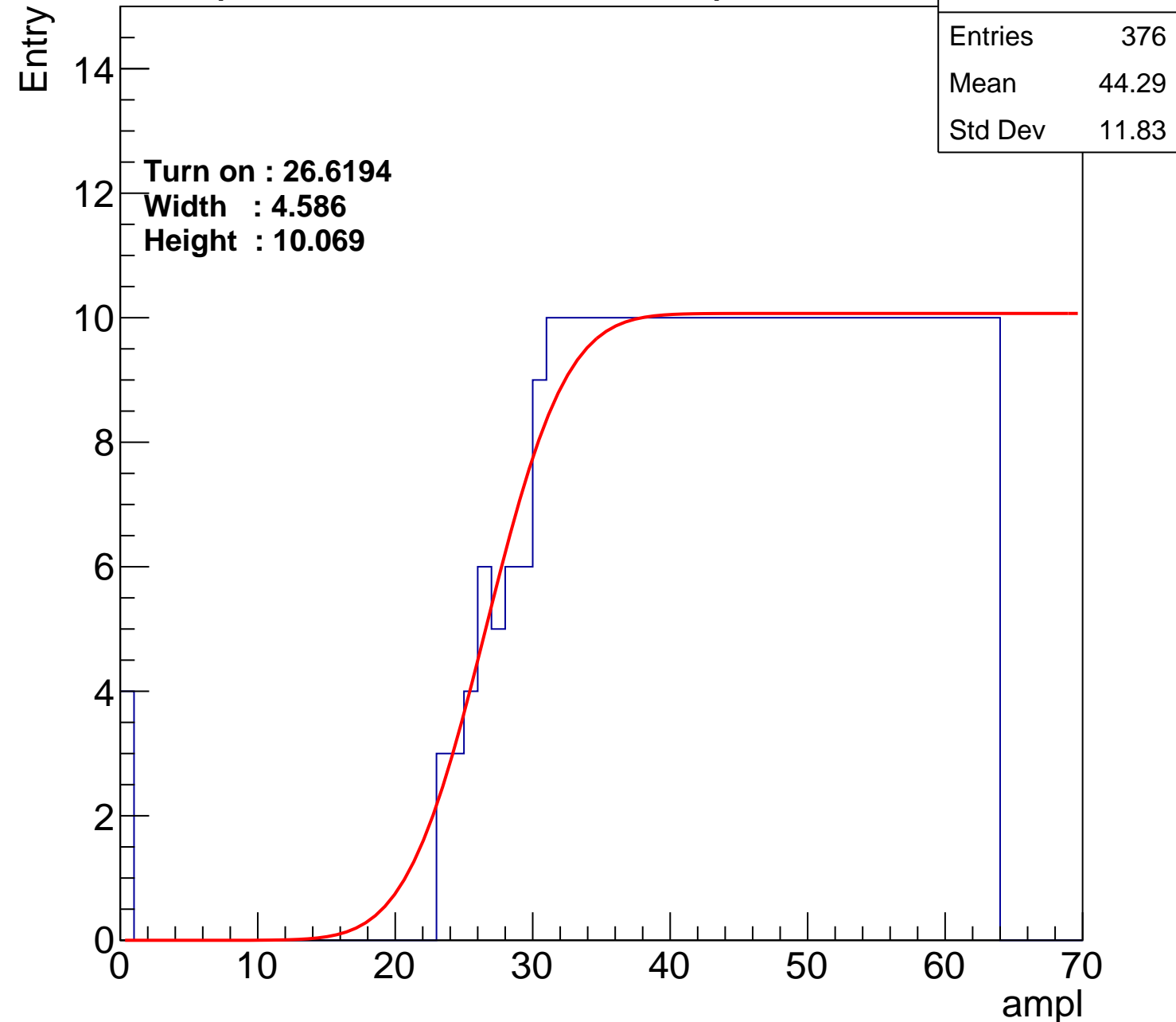
Width : 4.586

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch120

calib_packv5_042523_0143.root, FC#11, port A2

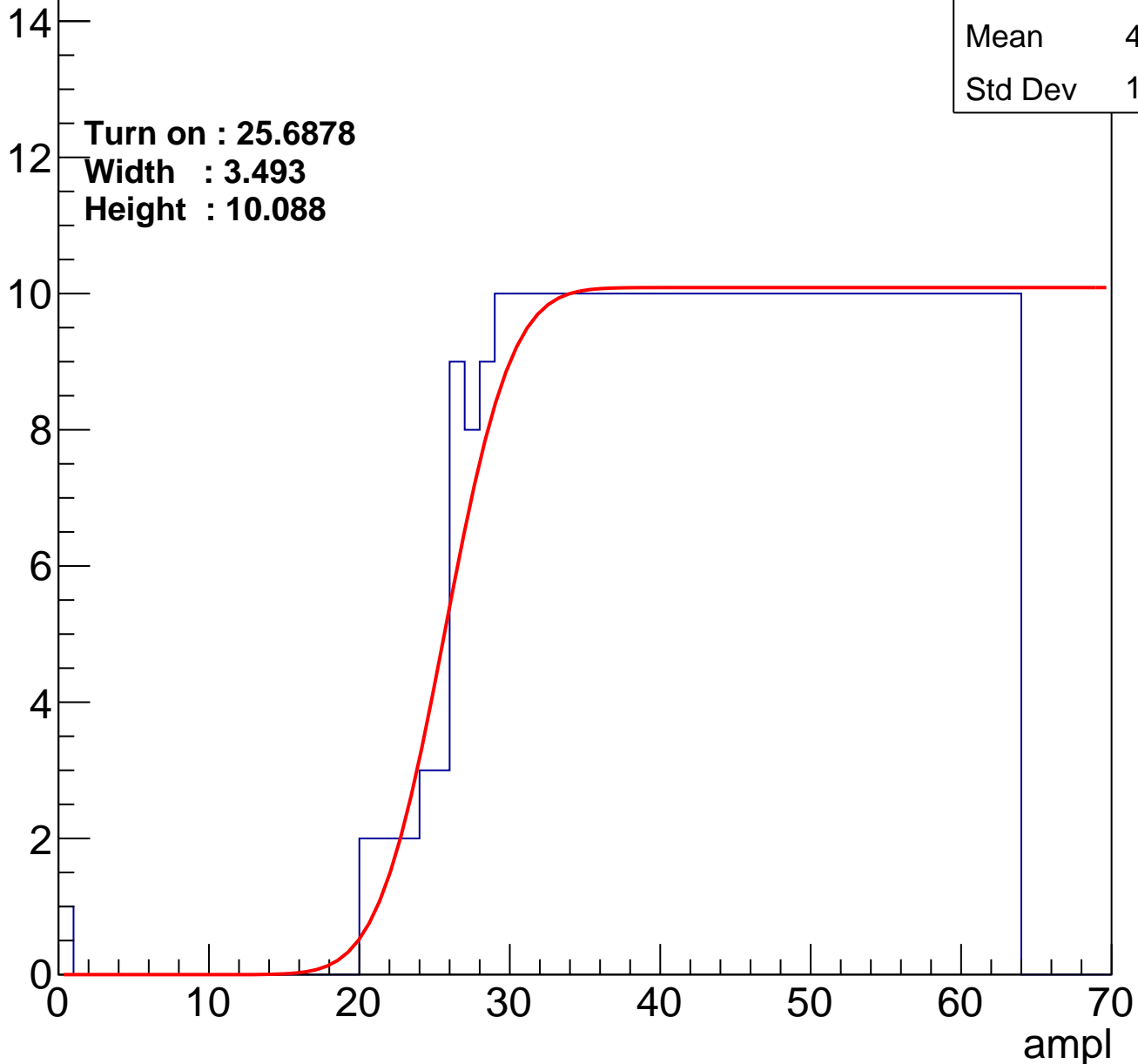
Entries	391
Mean	43.79
Std Dev	11.63

Turn on : 25.6878

Width : 3.493

Height : 10.088

Entry



B1L102S, U9-ch121

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.2
Std Dev	12.28

Turn on : 24.1357

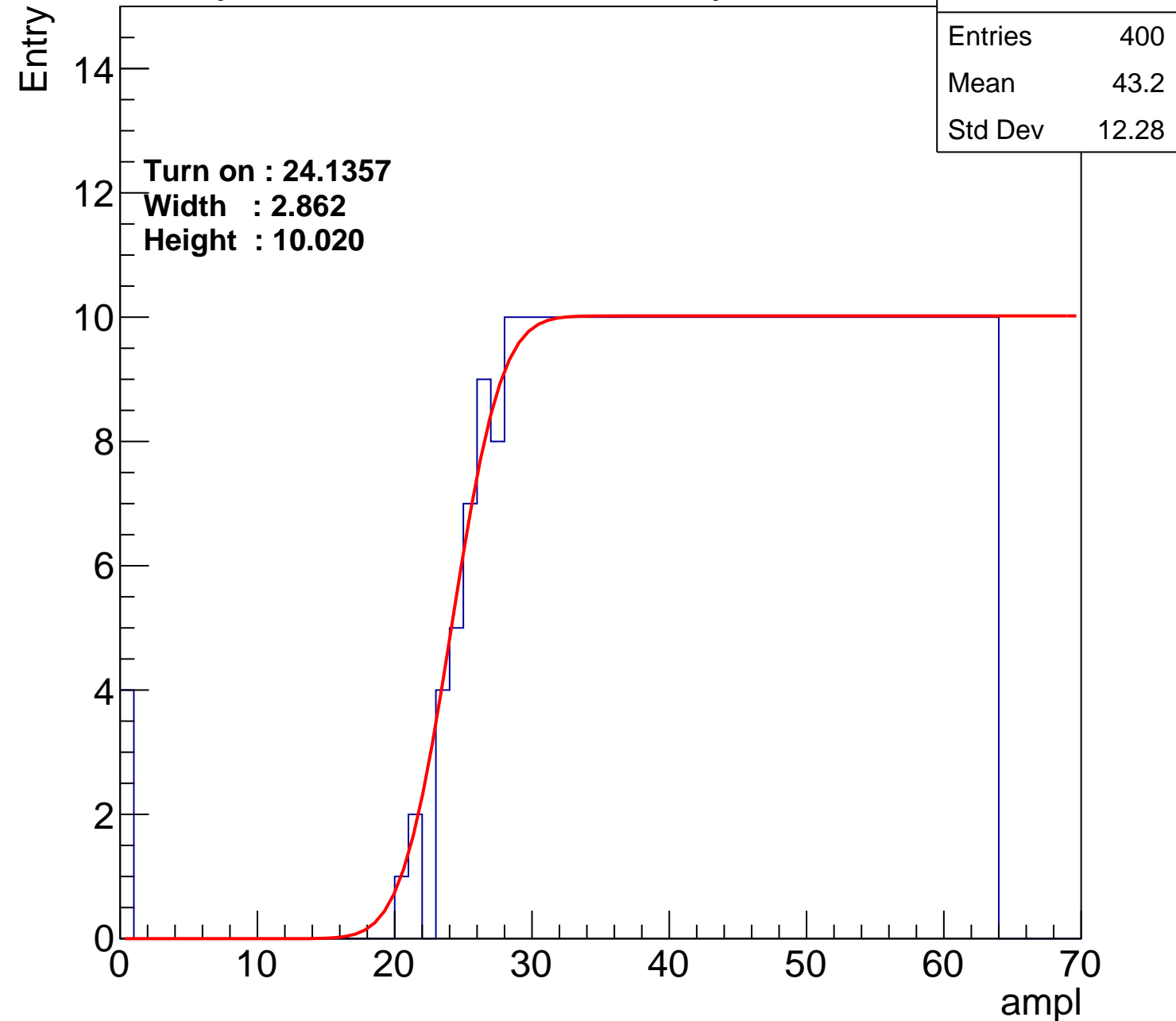
Width : 2.862

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch122

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.36
Std Dev	11.64

Turn on : 26.4522

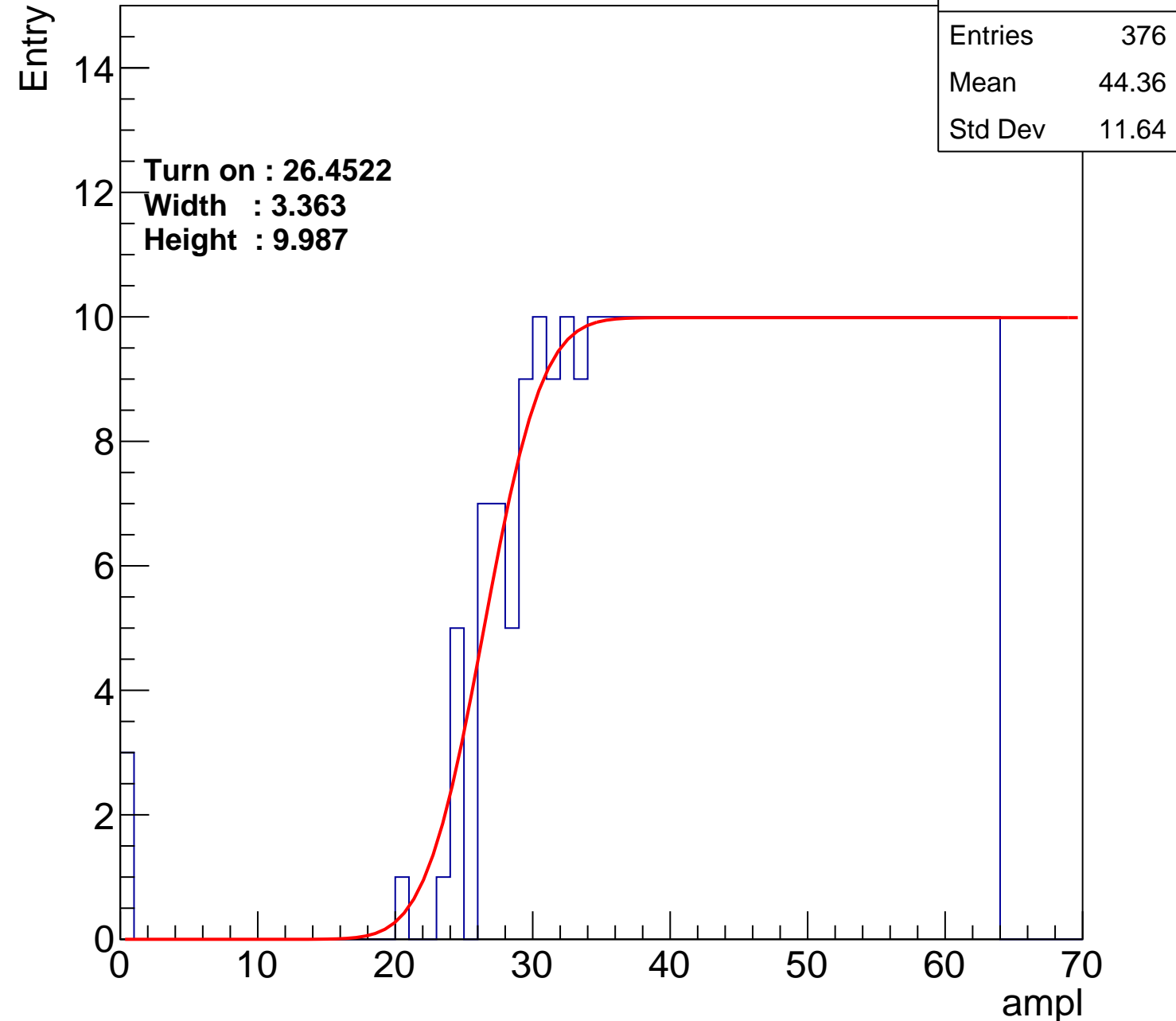
Width : 3.363

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch123

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	44.08
Std Dev	11.46

Turn on : 25.8773

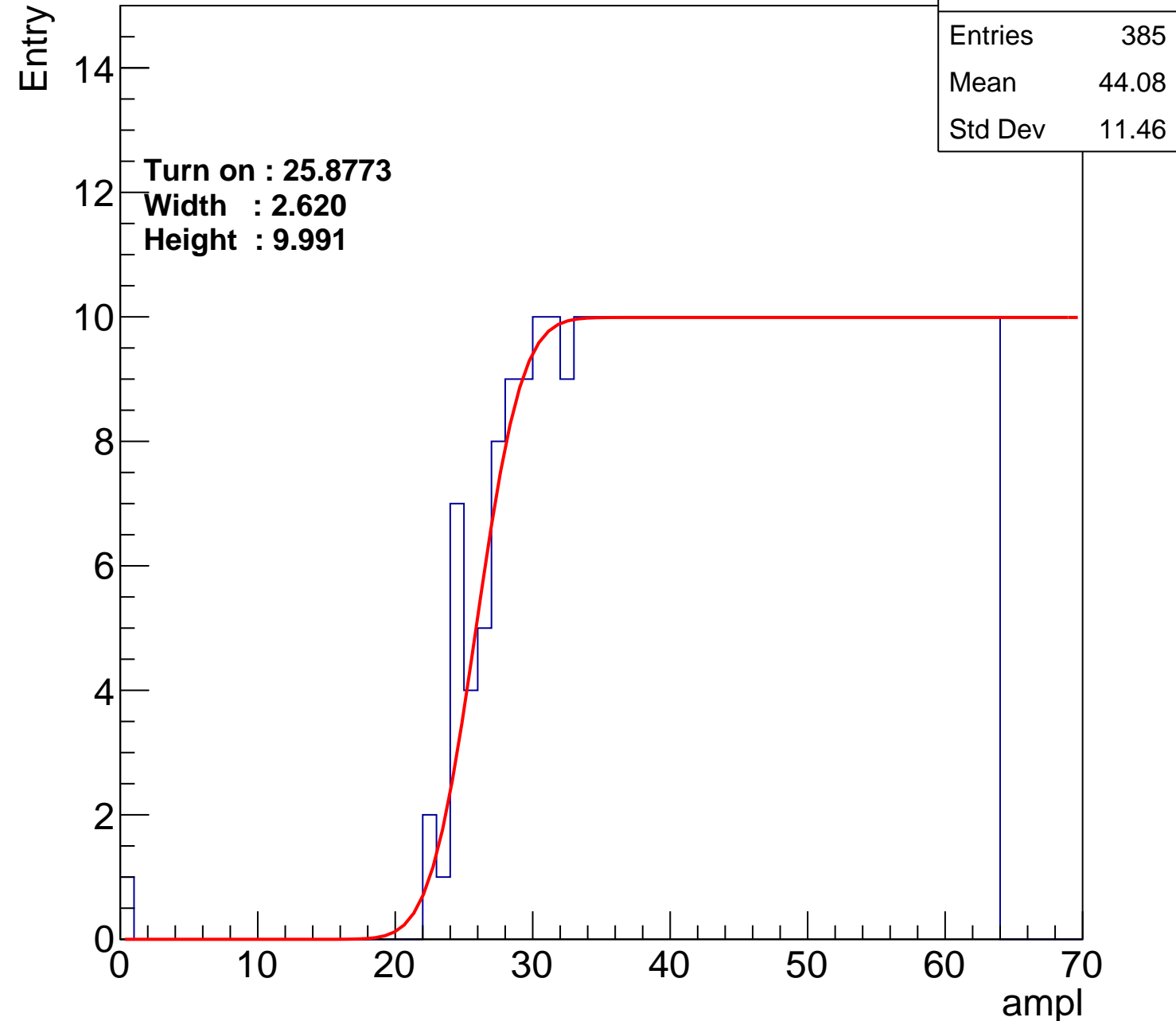
Width : 2.620

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch124

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.7711

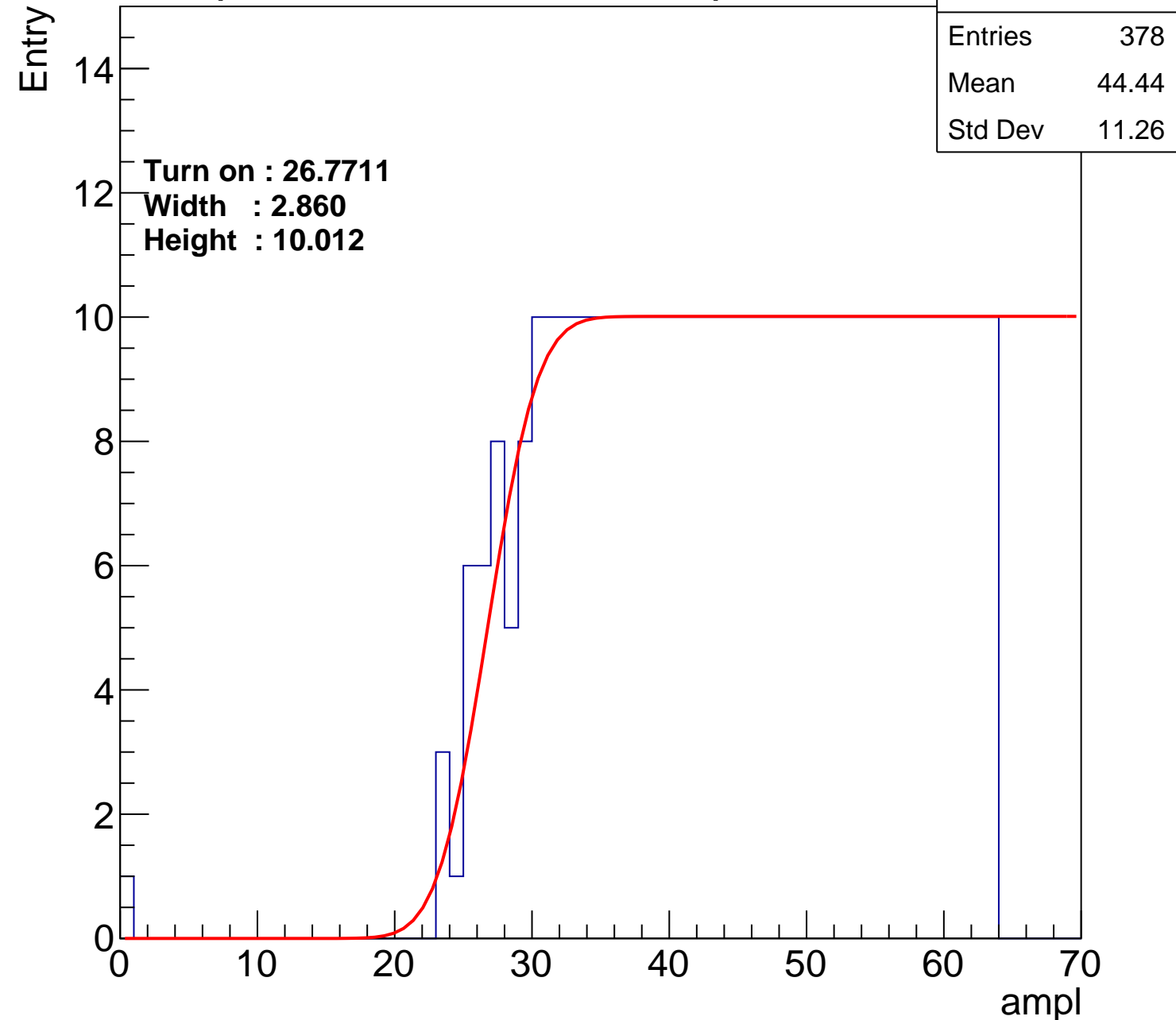
Width : 2.860

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch125

calib_packv5_042523_0143.root, FC#11, port A2

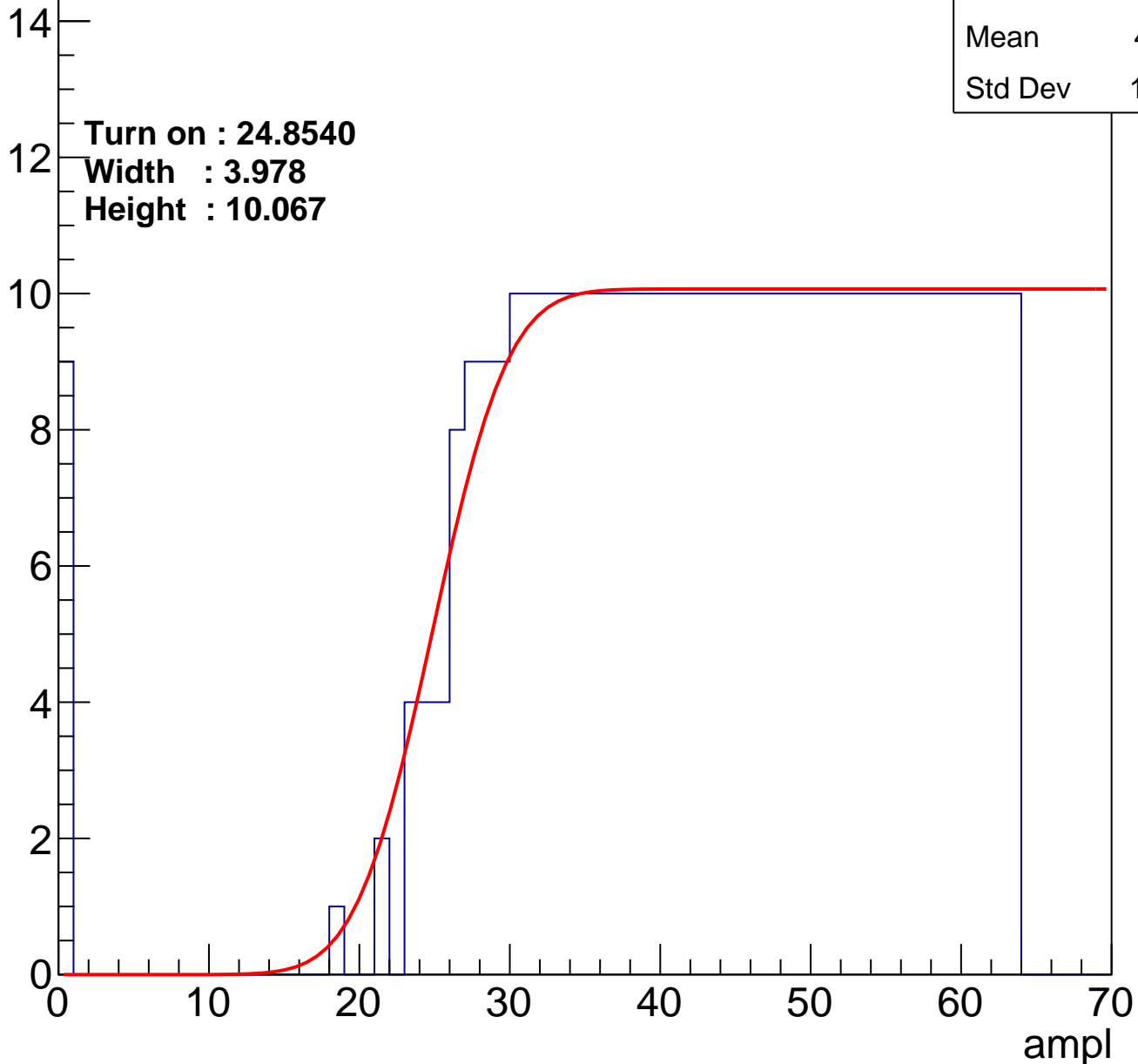
Entries	399
Mean	42.91
Std Dev	13.04

Turn on : 24.8540

Width : 3.978

Height : 10.067

Entry



B1L102S, U9-ch126

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	43.05
Std Dev	12.08

Turn on : 23.8419

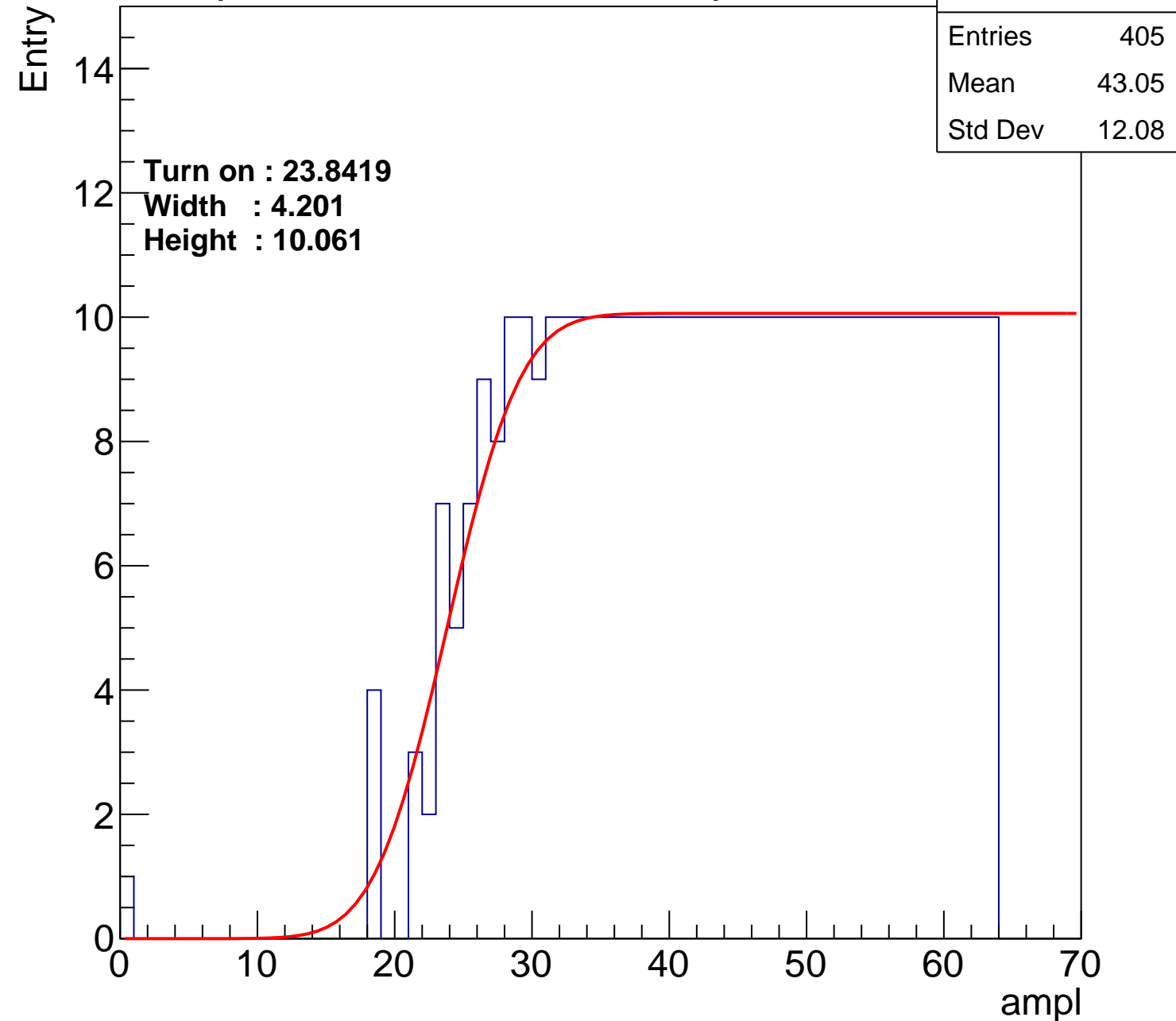
Width : 4.201

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.32
Std Dev	11.71

Turn on : 26.9671

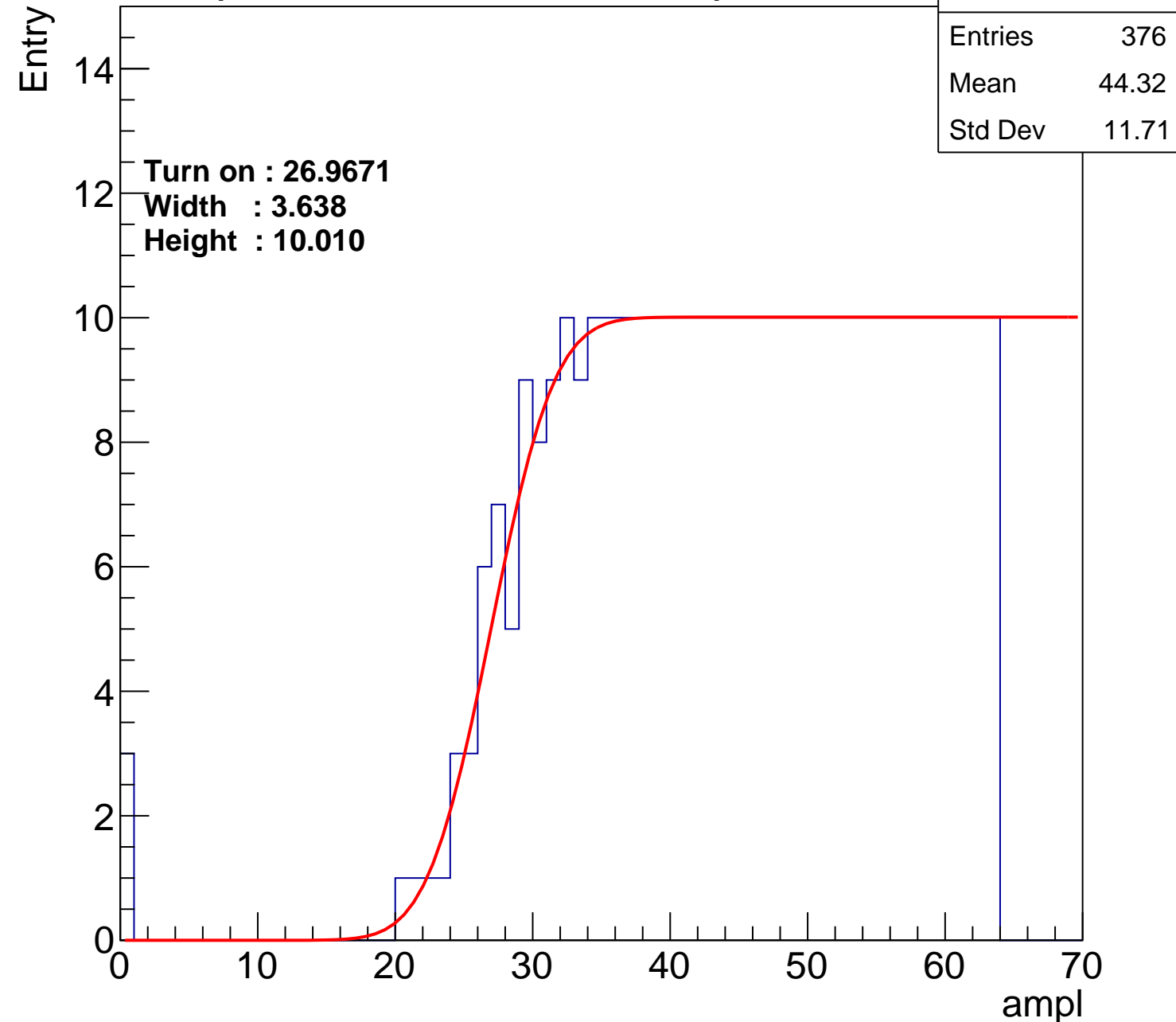
Width : 3.638

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U9-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.32
Std Dev	11.71

Turn on : 26.9671

Width : 3.638

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl

