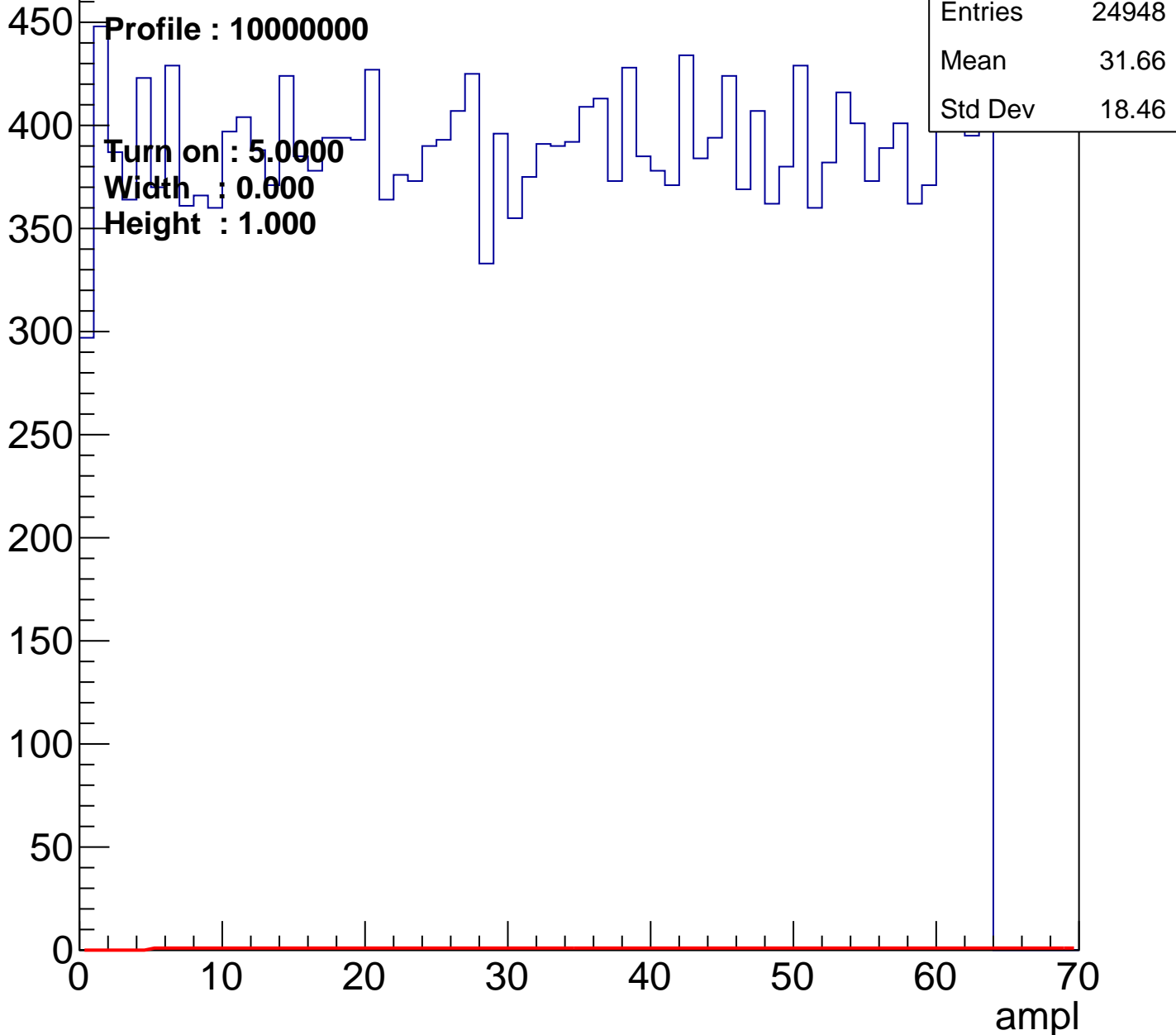


B1L003S, U11-ch96

calib_packv5_042523_0143.root, FC#13, Port D2

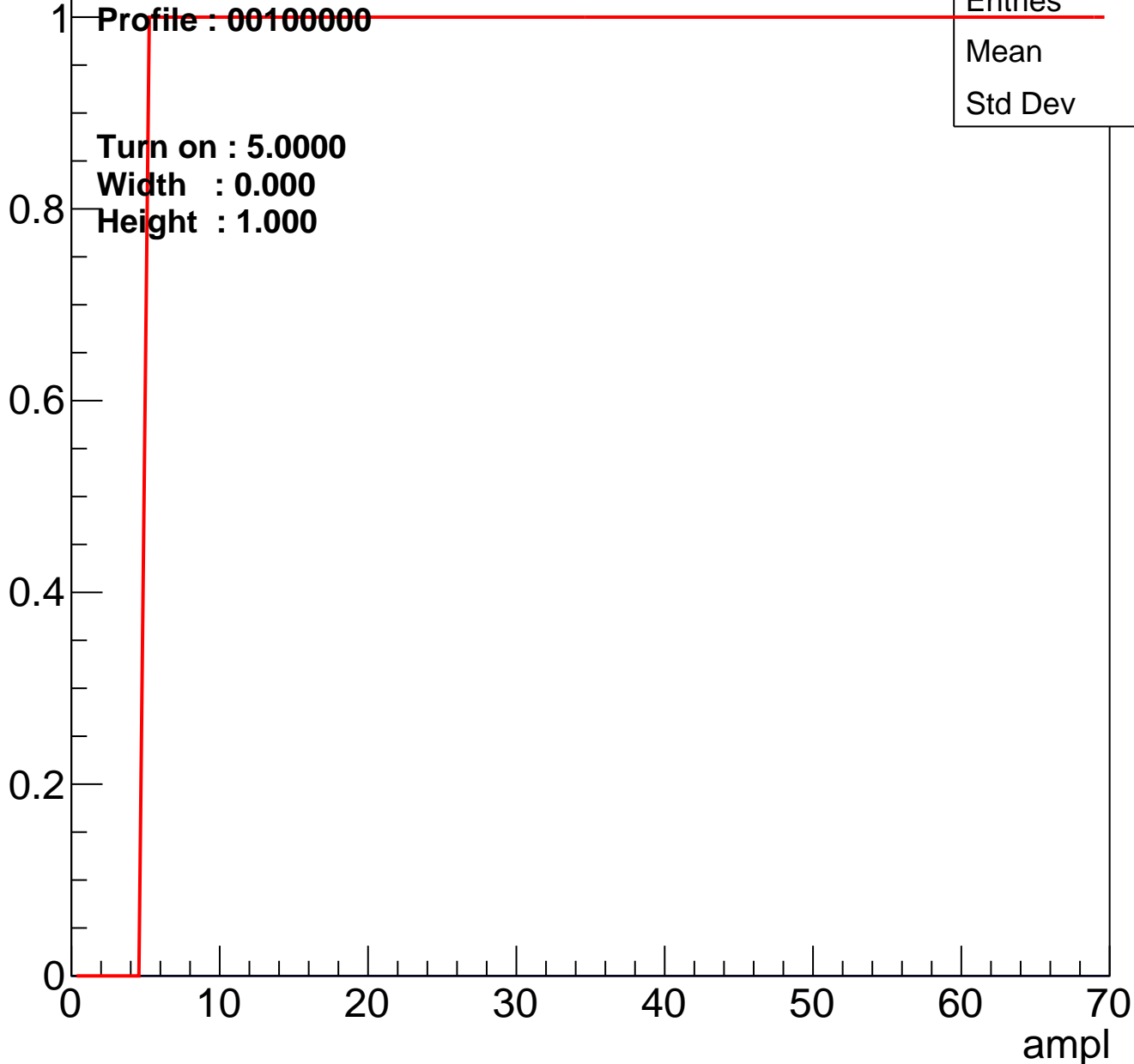
Entry



B1L003S, U14-ch0

calib_packv5_042523_0143.root, FC#13, Port D2

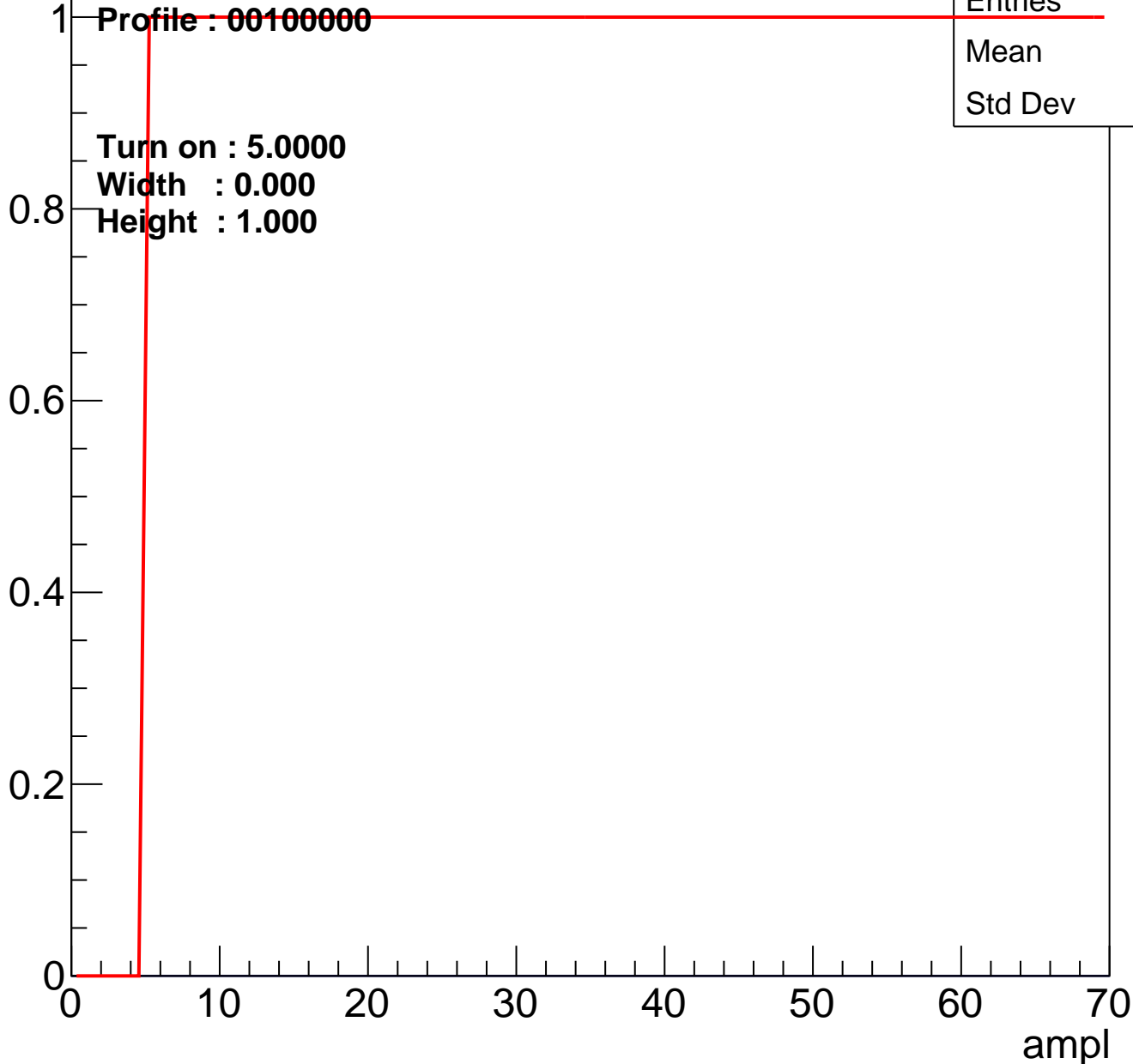
Entry



B1L003S, U14-ch1

calib_packv5_042523_0143.root, FC#13, Port D2

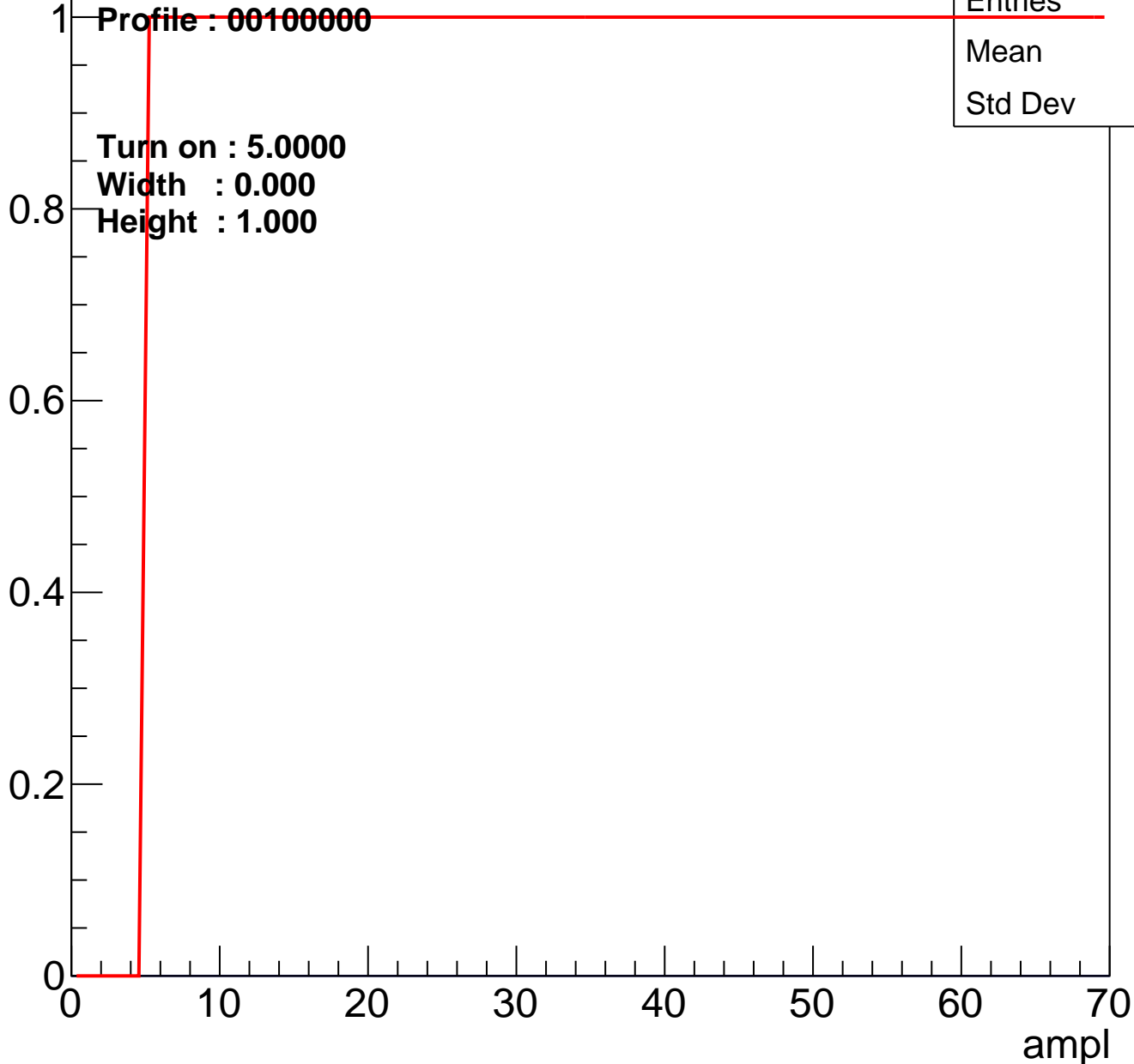
Entry



B1L003S, U14-ch2

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

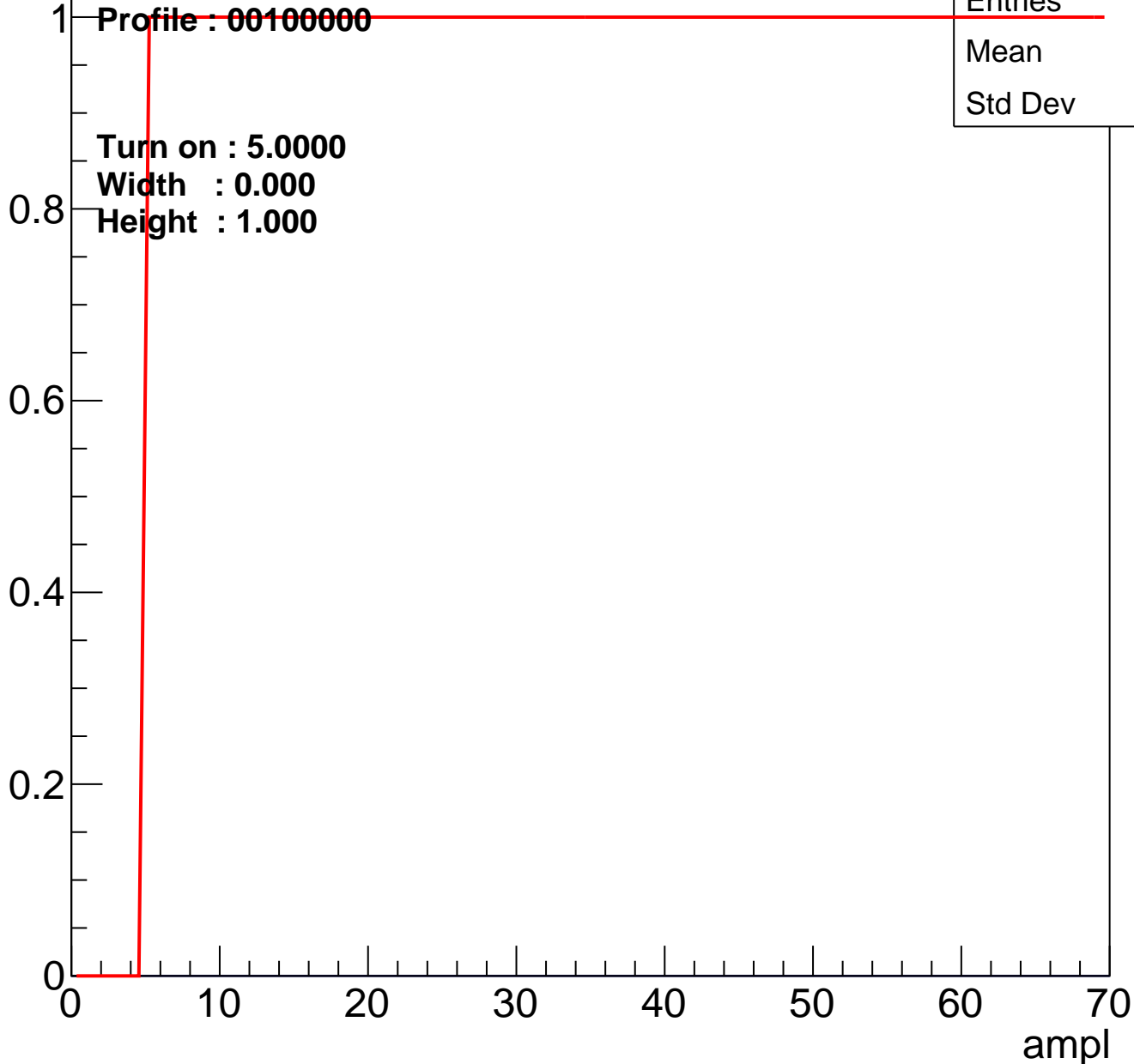


Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch3

calib_packv5_042523_0143.root, FC#13, Port D2

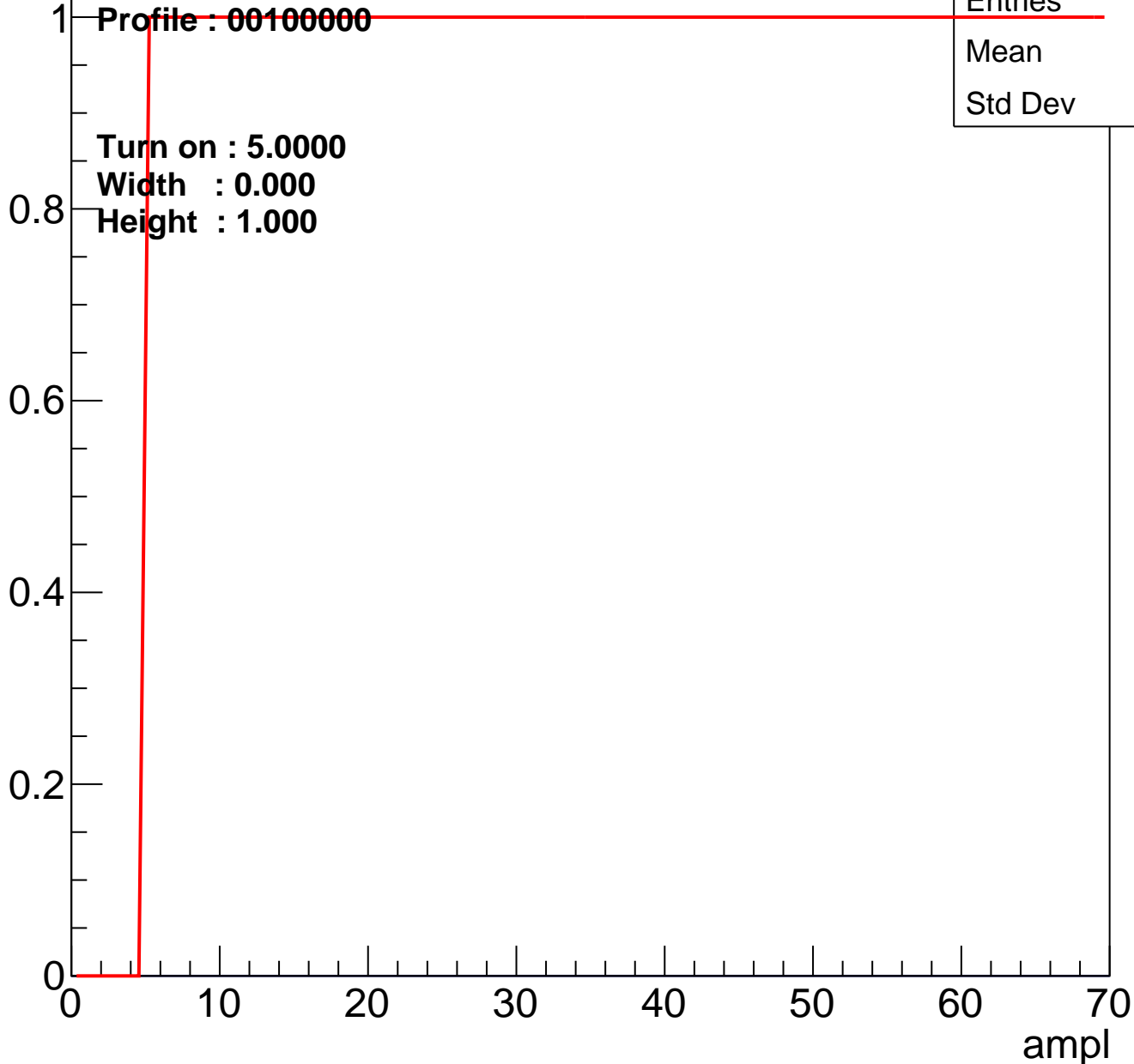
Entry



B1L003S, U14-ch4

calib_packv5_042523_0143.root, FC#13, Port D2

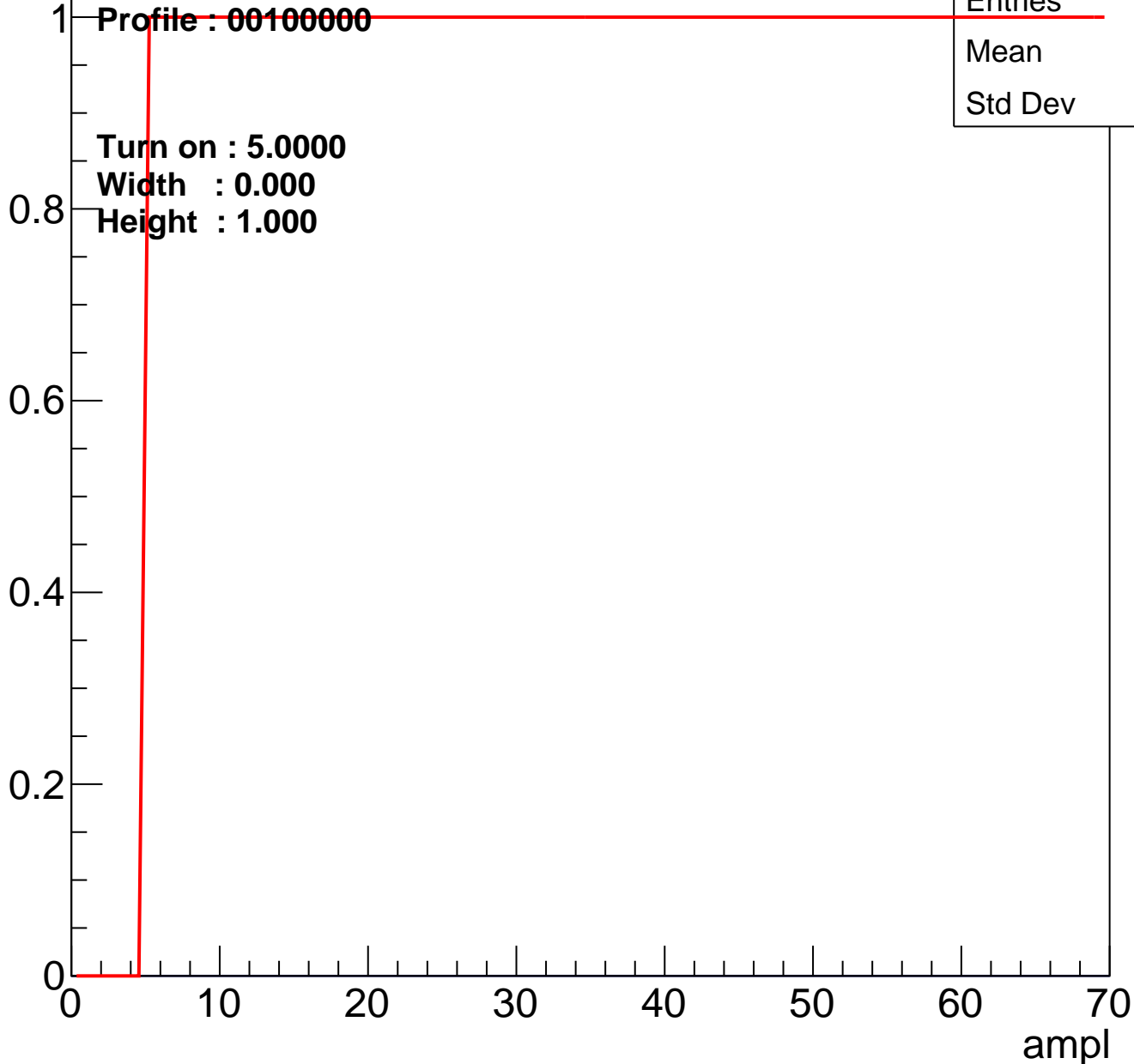
Entry



B1L003S, U14-ch5

calib_packv5_042523_0143.root, FC#13, Port D2

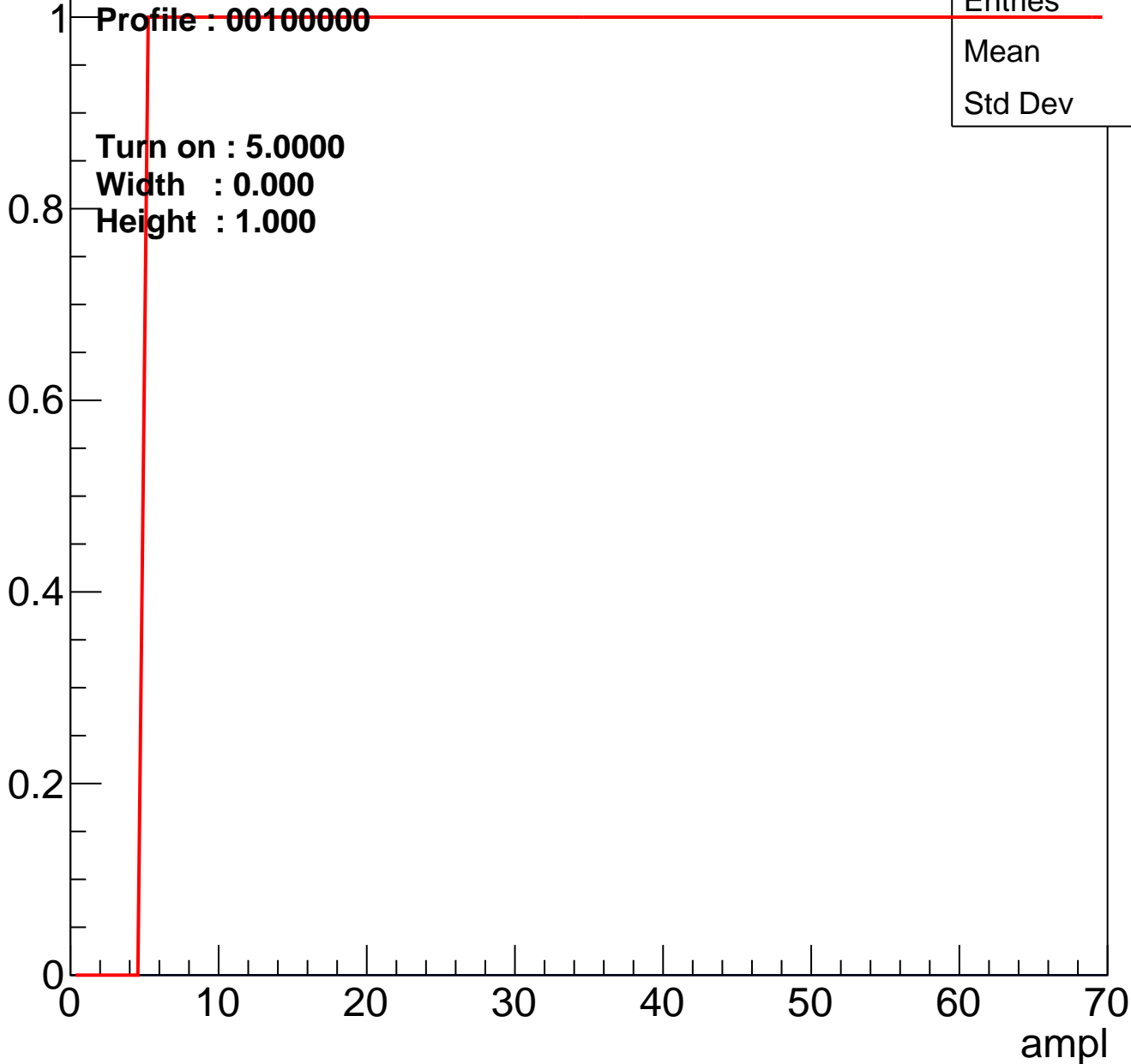
Entry



B1L003S, U14-ch6

calib_packv5_042523_0143.root, FC#13, Port D2

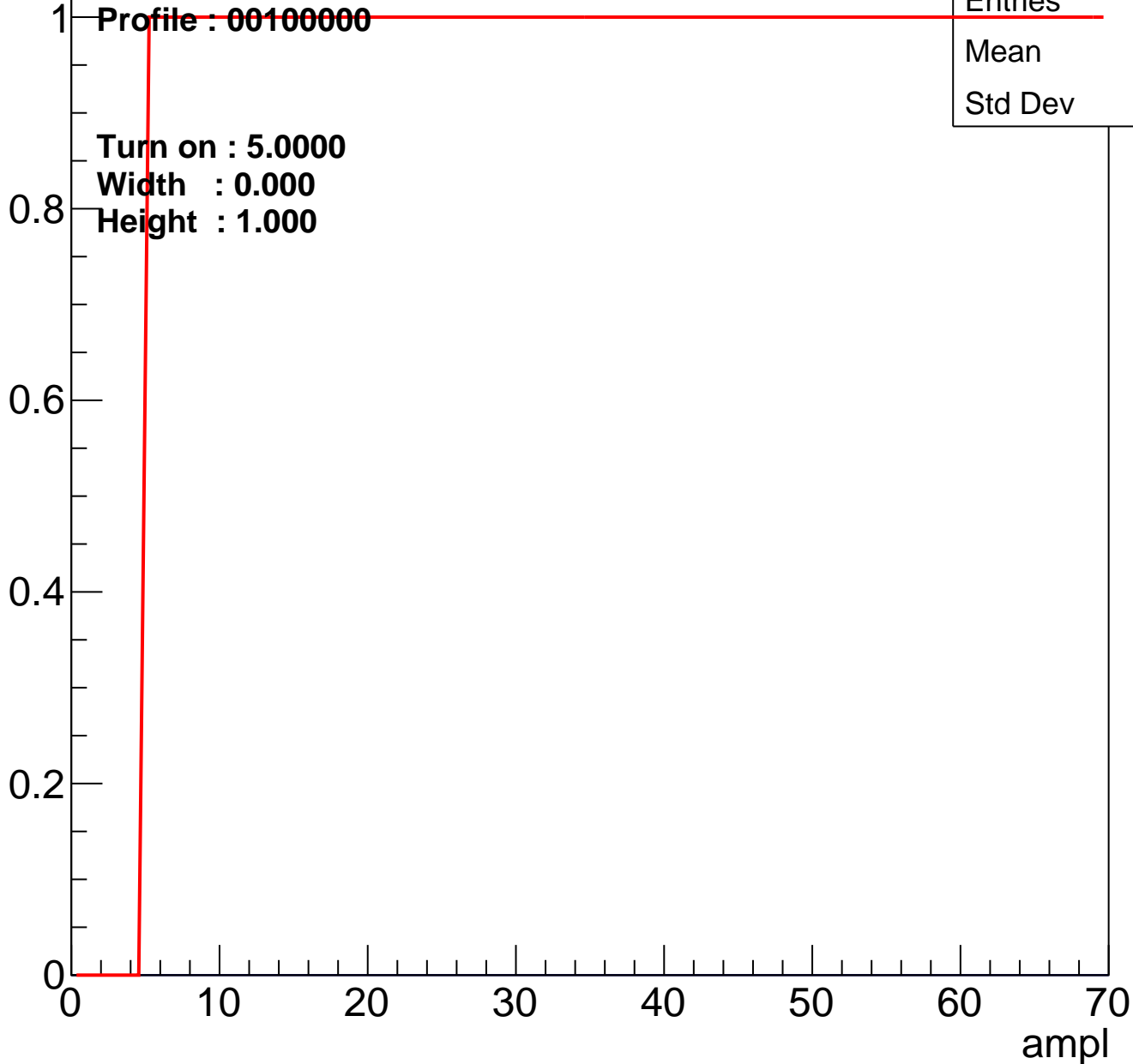
Entry



B1L003S, U14-ch7

calib_packv5_042523_0143.root, FC#13, Port D2

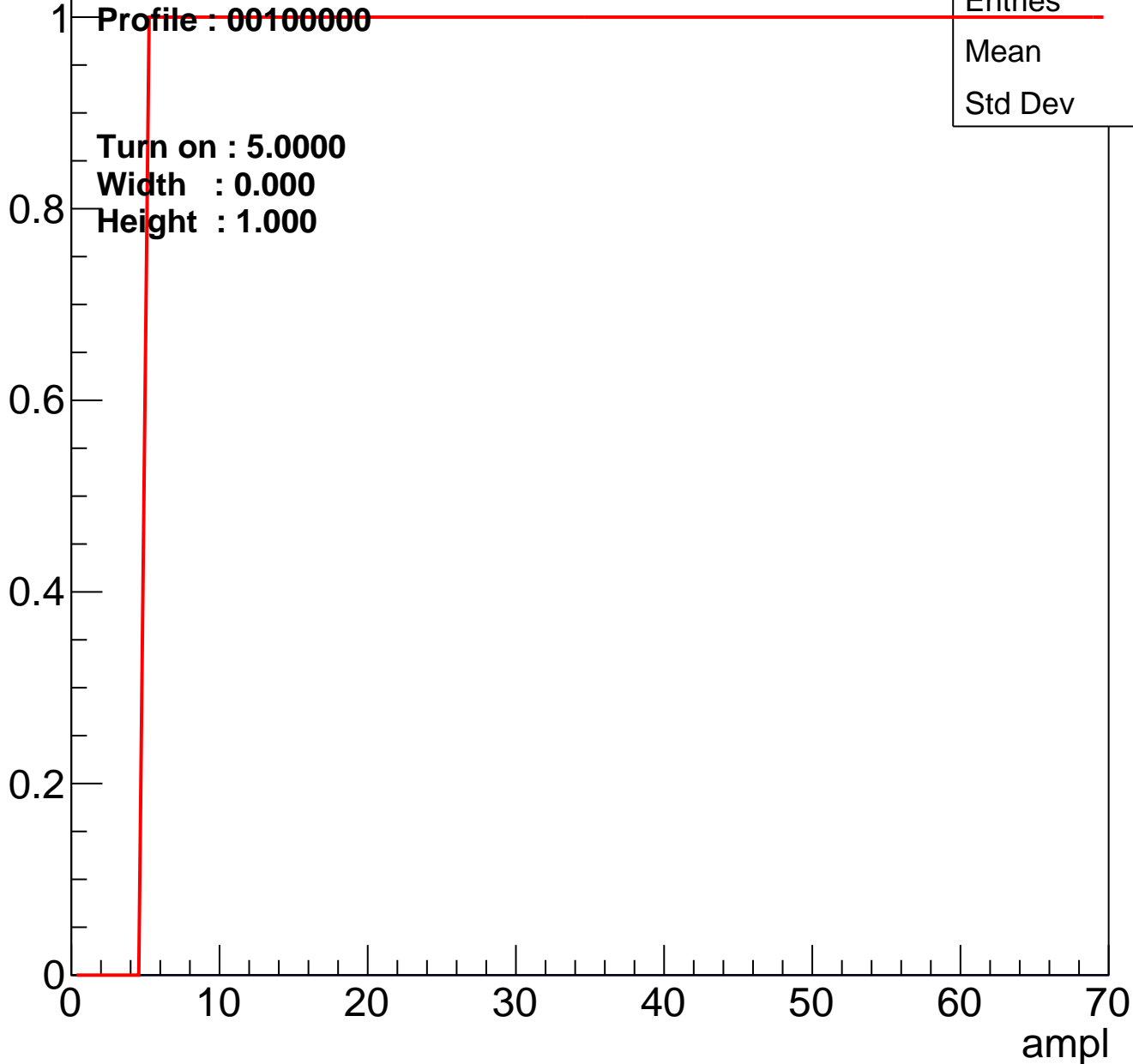
Entry



B1L003S, U14-ch8

calib_packv5_042523_0143.root, FC#13, Port D2

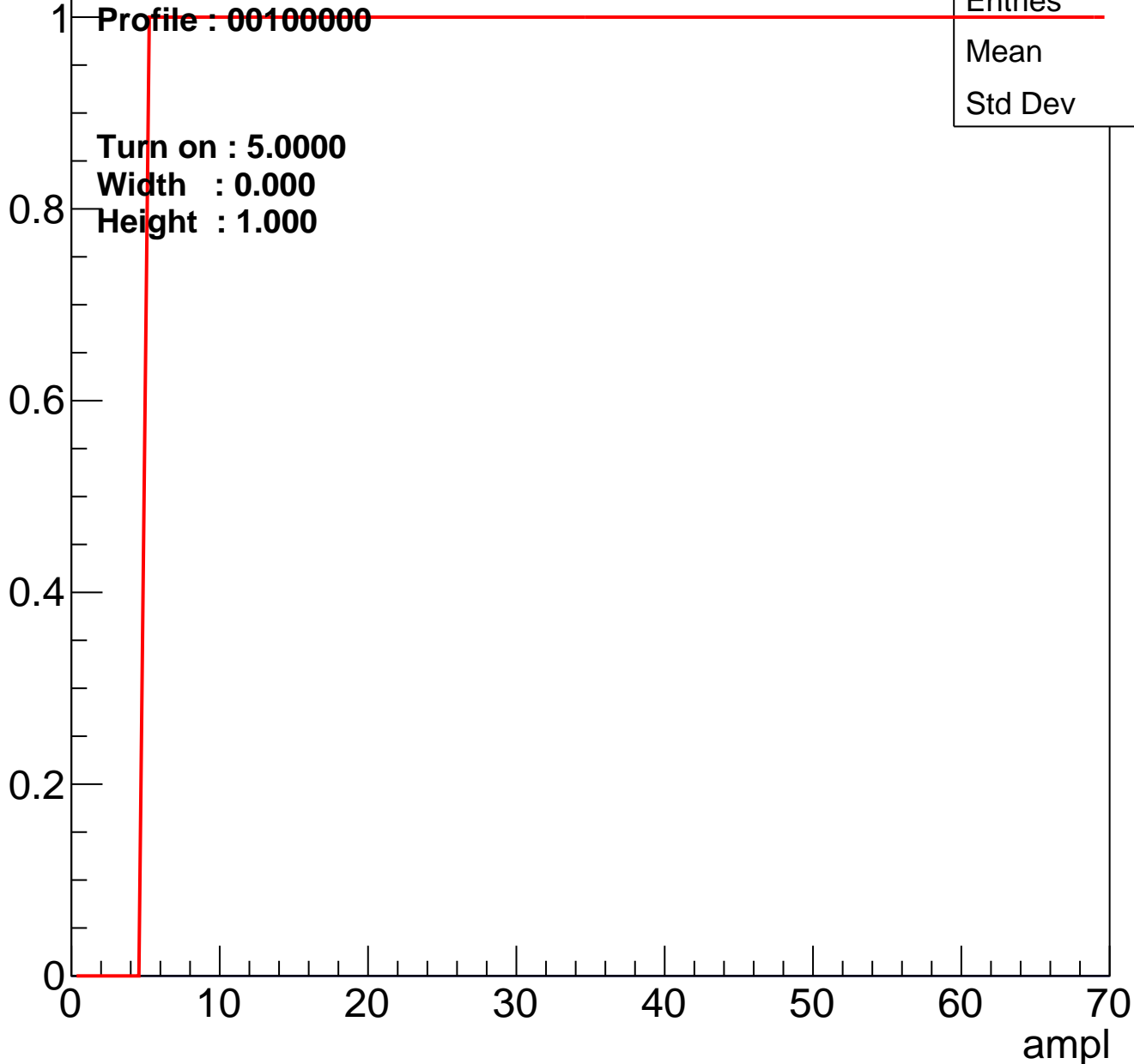
Entry



B1L003S, U14-ch9

calib_packv5_042523_0143.root, FC#13, Port D2

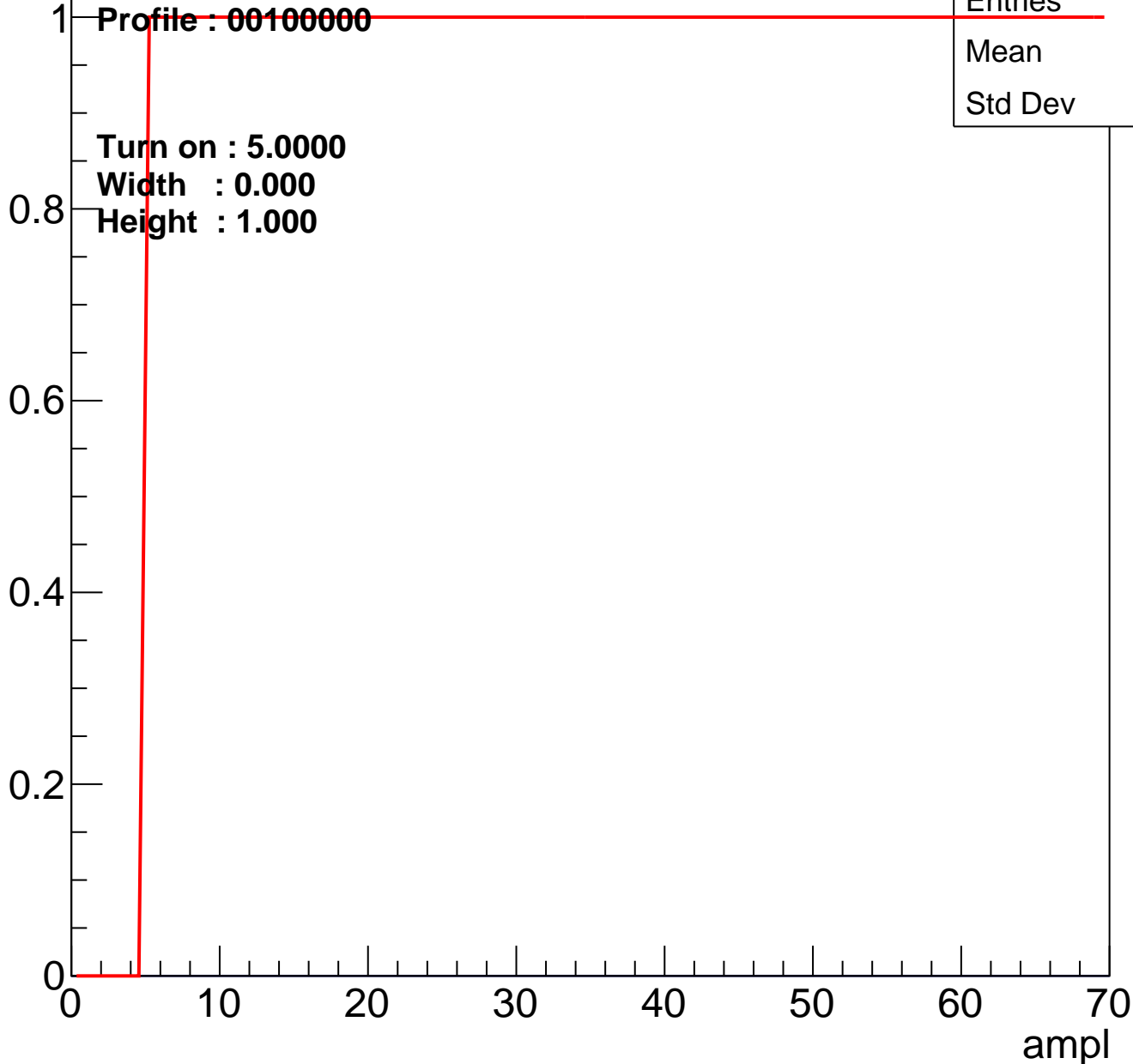
Entry



B1L003S, U14-ch10

calib_packv5_042523_0143.root, FC#13, Port D2

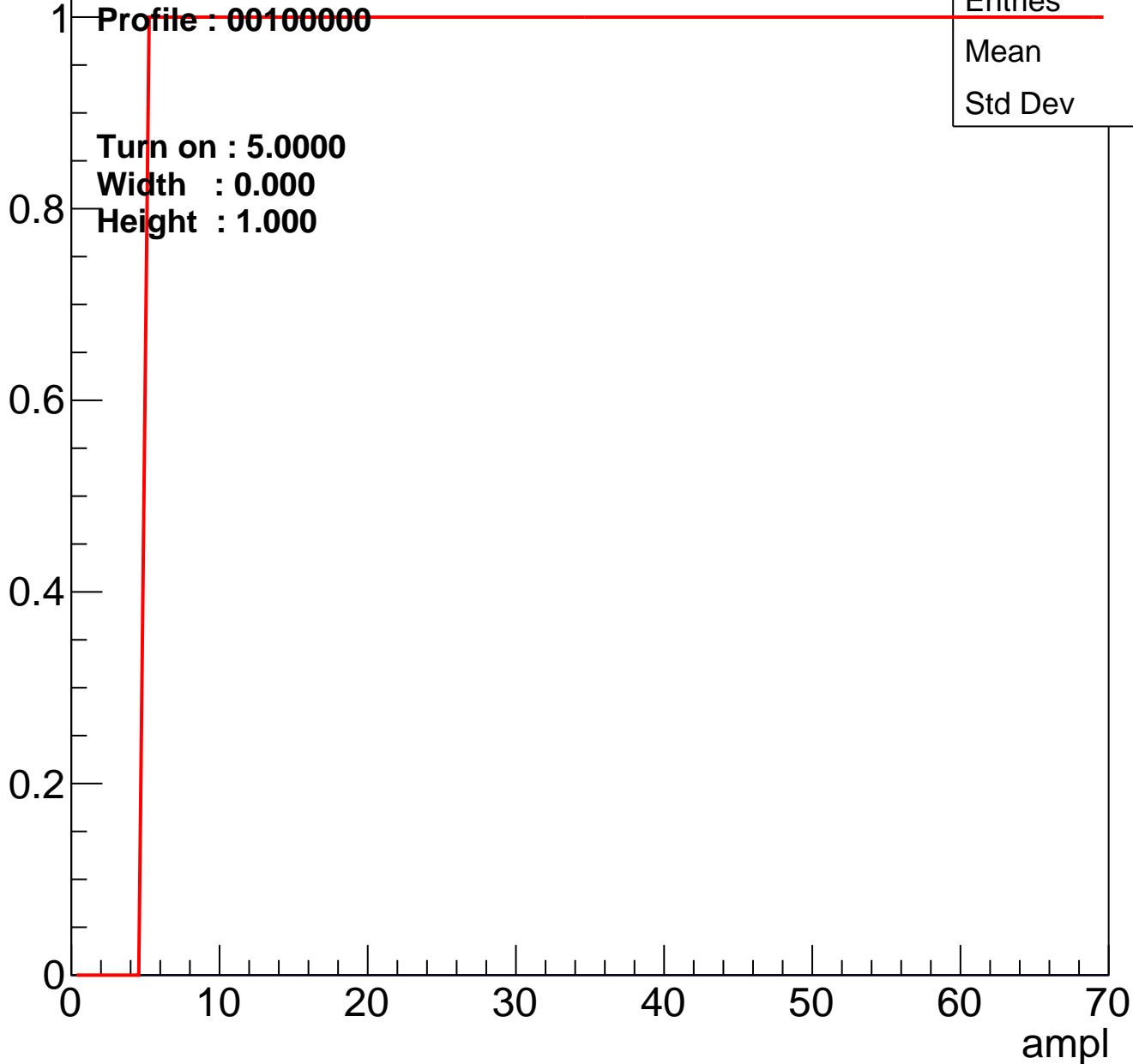
Entry



B1L003S, U14-ch11

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U14-ch12

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

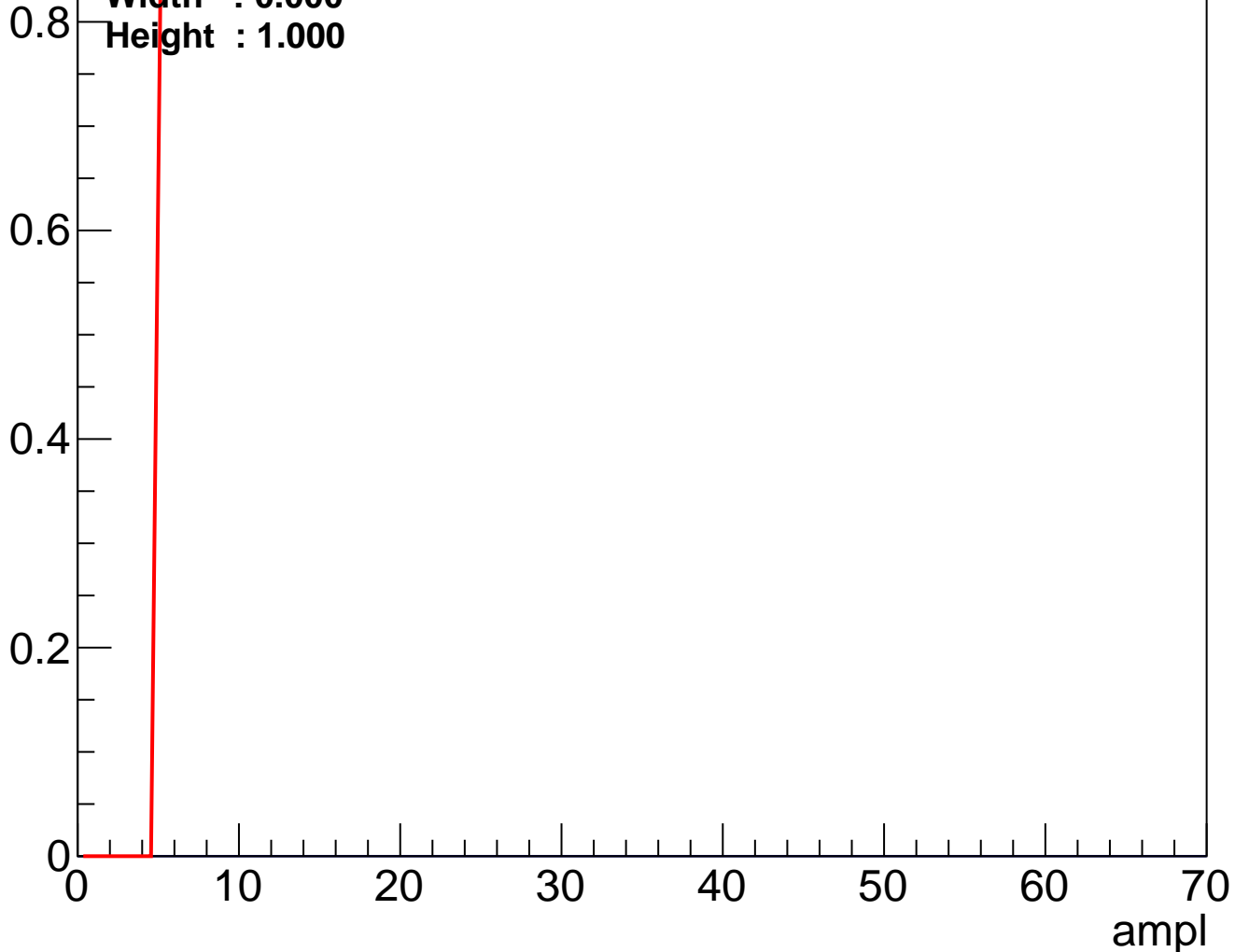
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

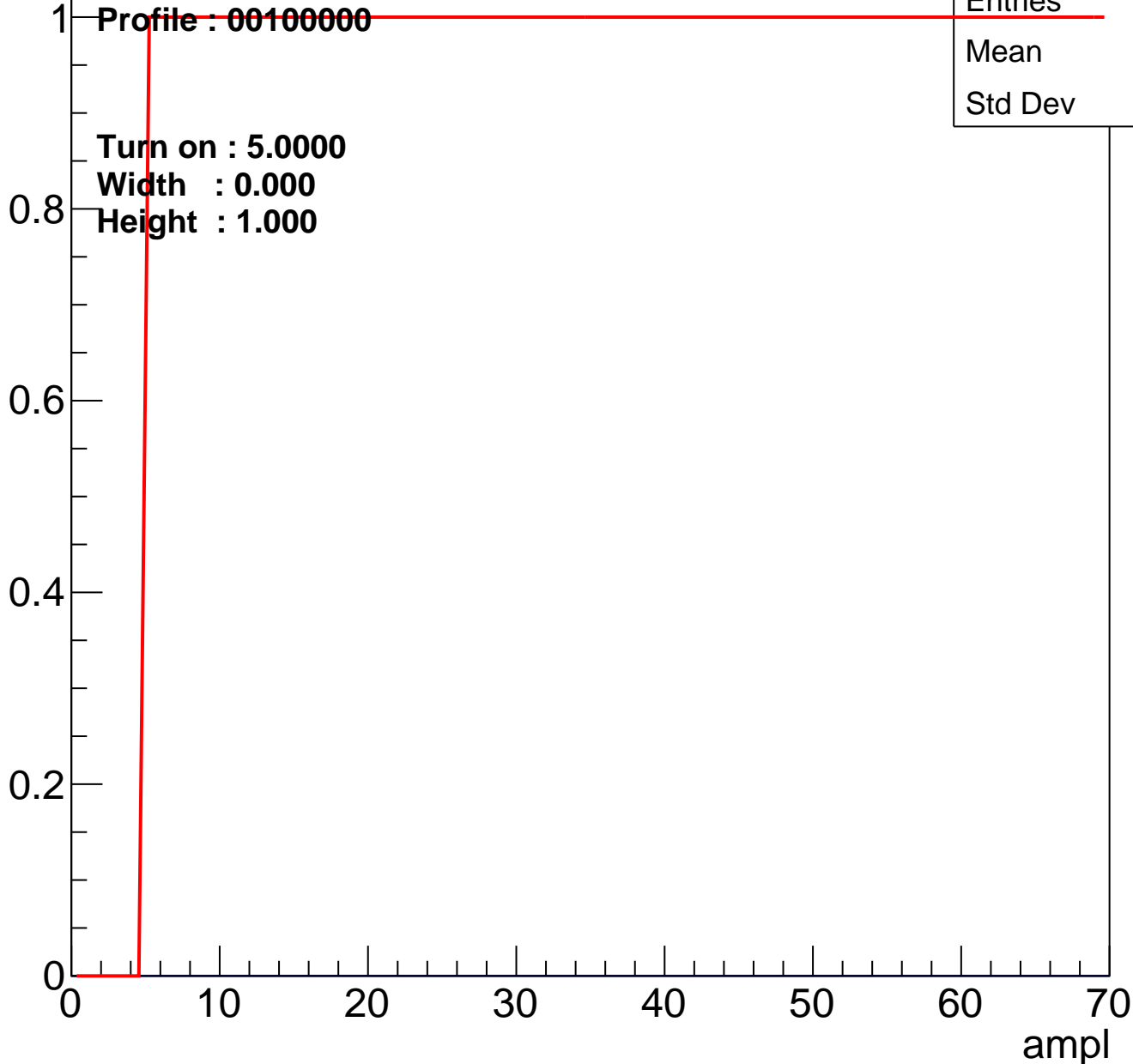
Entries	0
Mean	0
Std Dev	0



B1L003S, U14-ch13

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

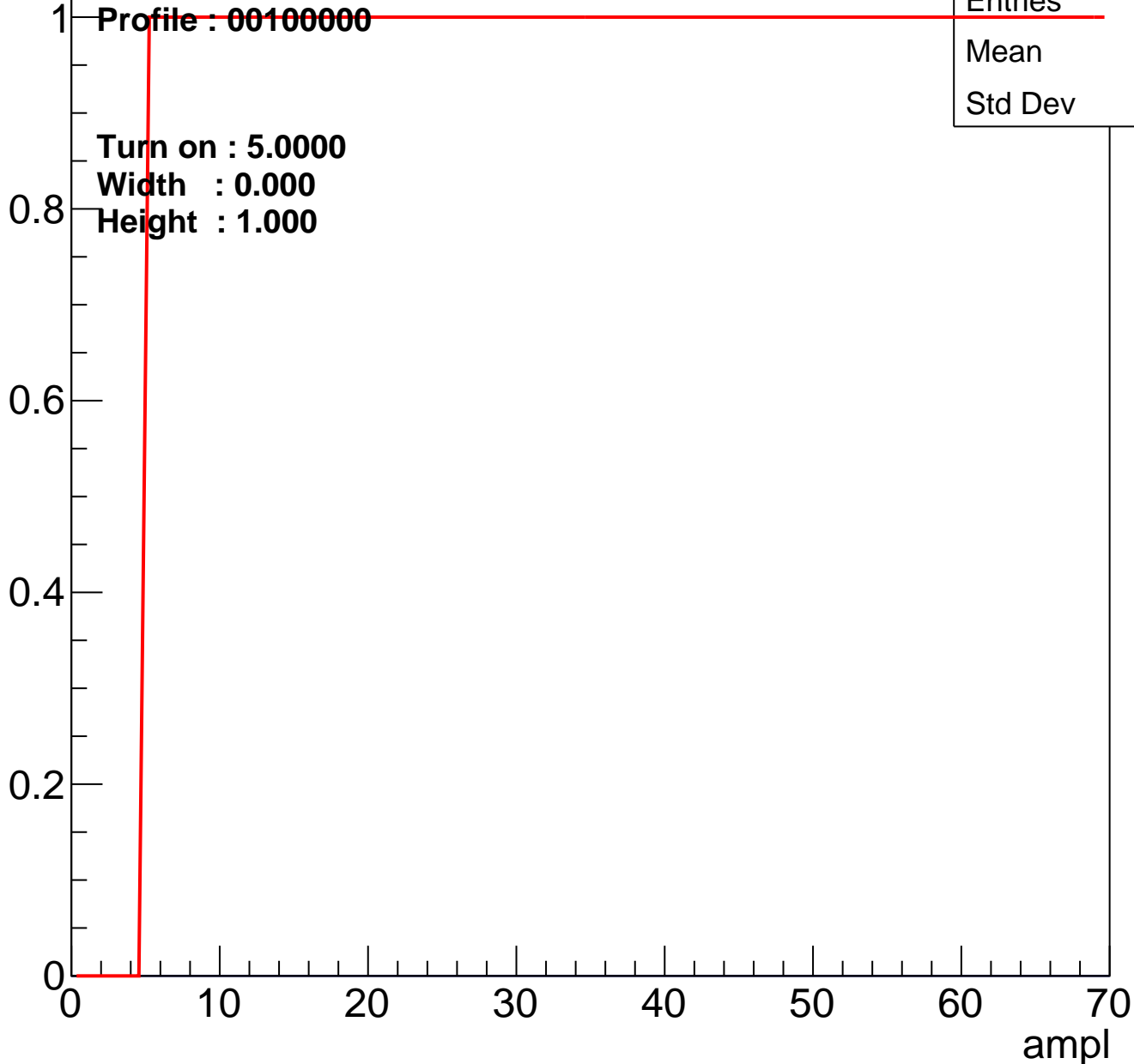


Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch14

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U14-ch15

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

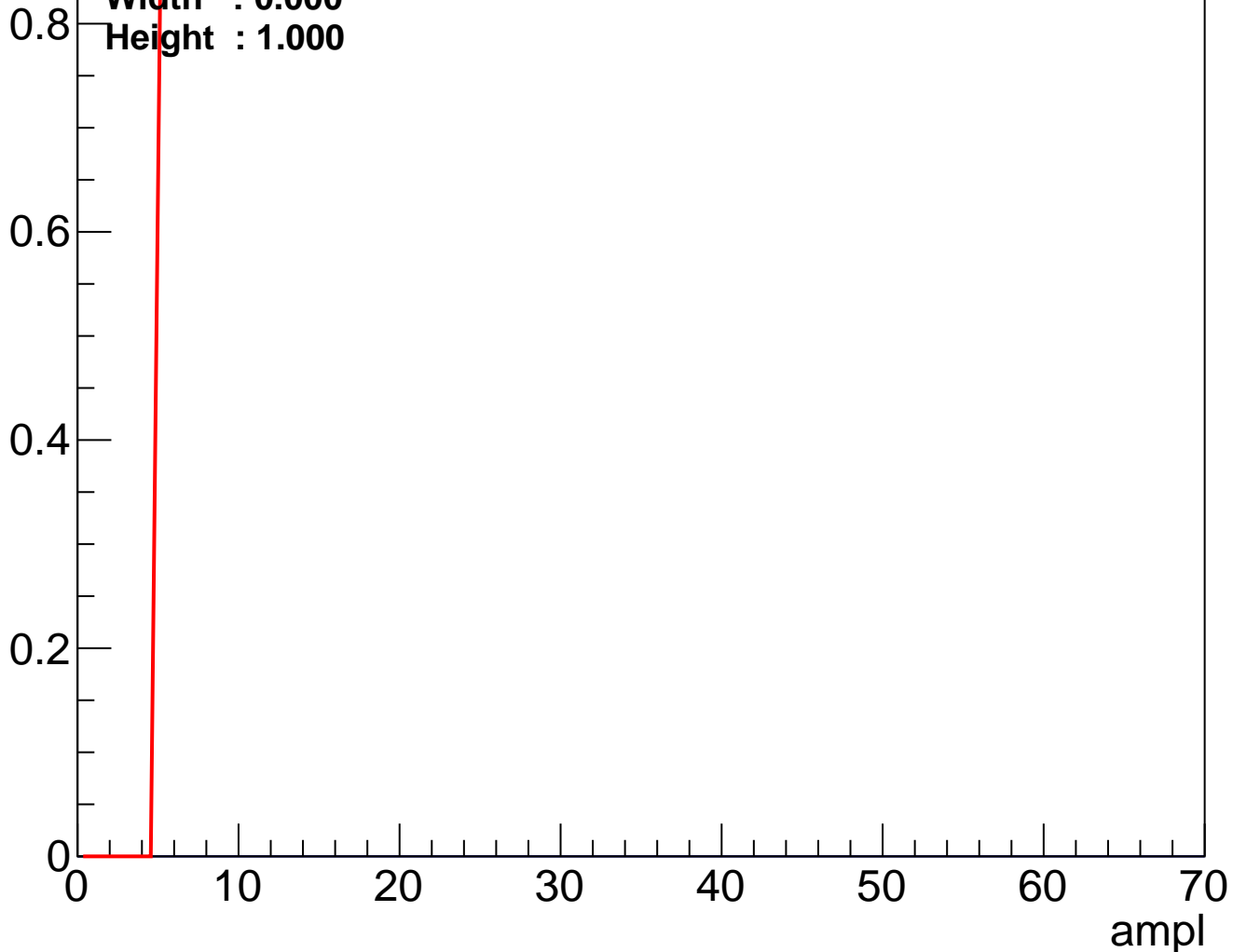
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

Entries	0
Mean	0
Std Dev	0



B1L003S, U14-ch16

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

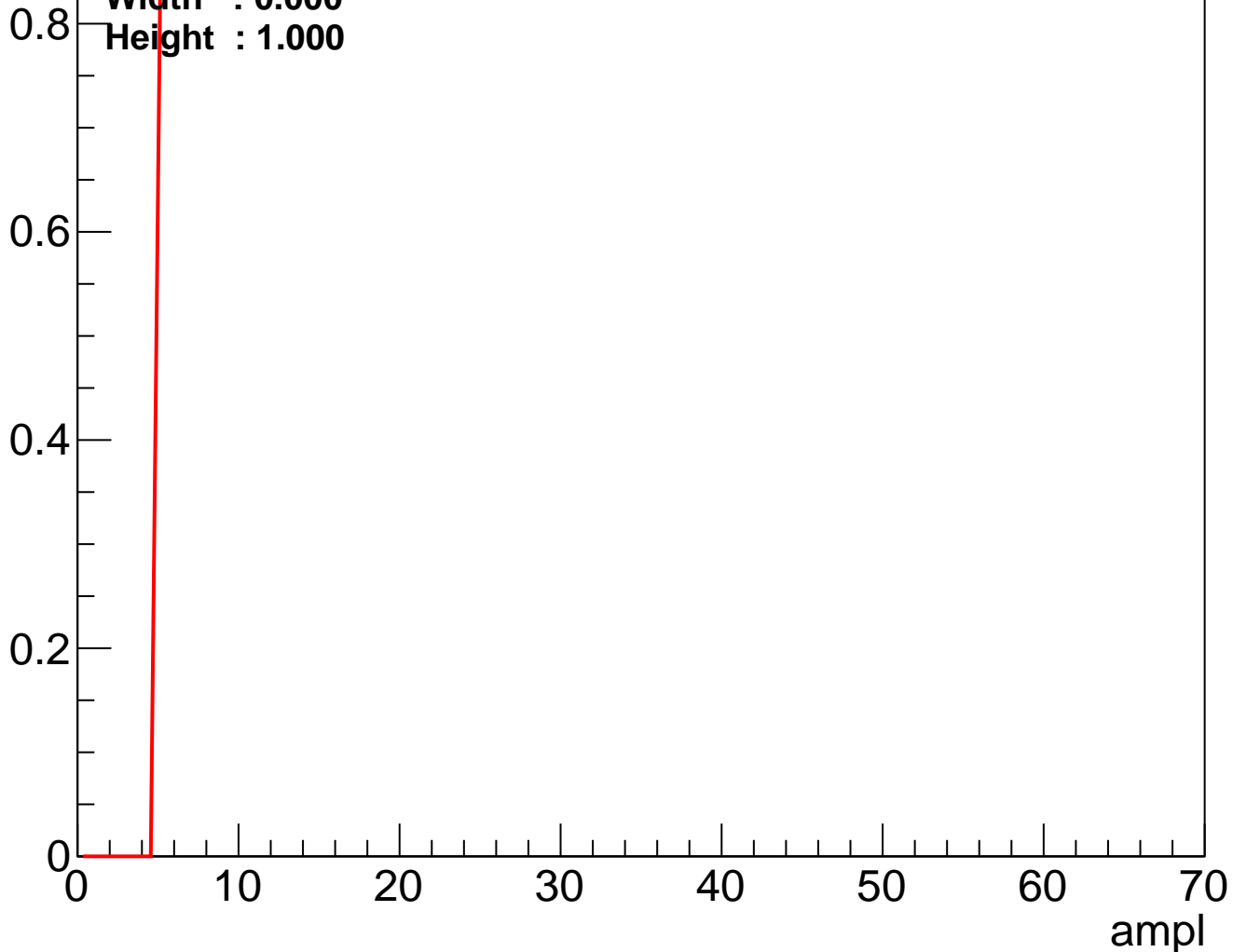
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

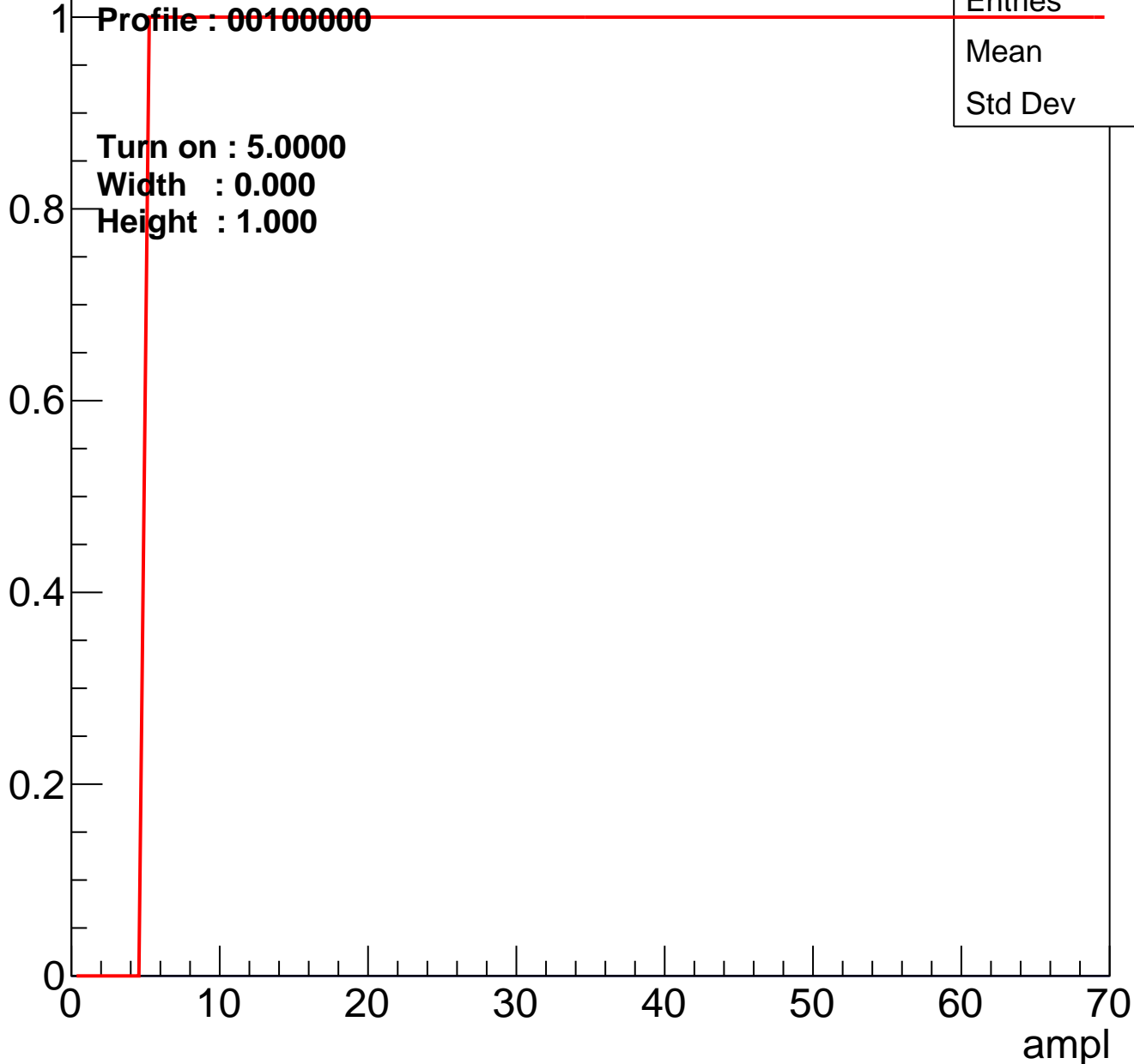
Entries	0
Mean	0
Std Dev	0



B1L003S, U14-ch17

calib_packv5_042523_0143.root, FC#13, Port D2

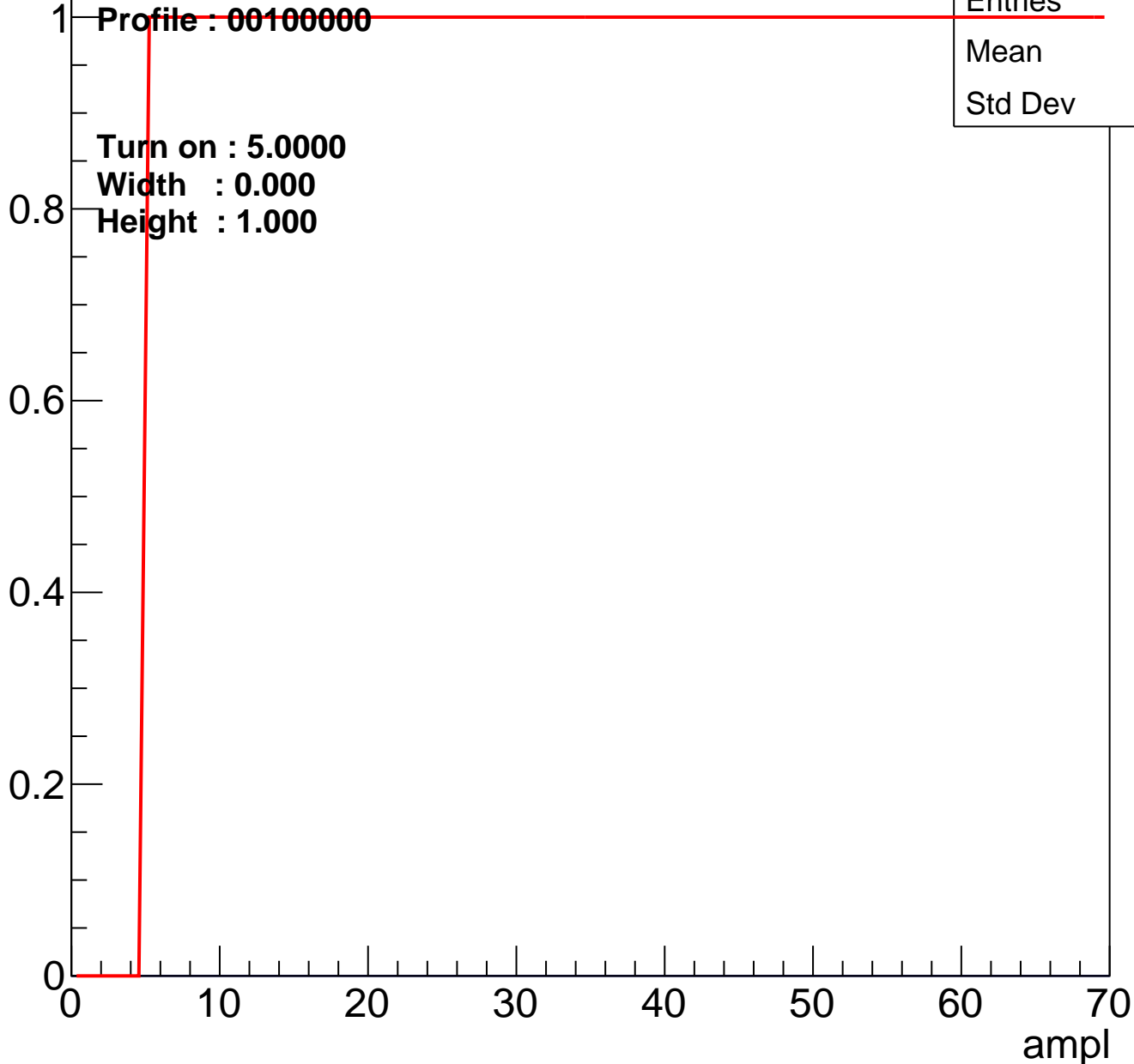
Entry



B1L003S, U14-ch18

calib_packv5_042523_0143.root, FC#13, Port D2

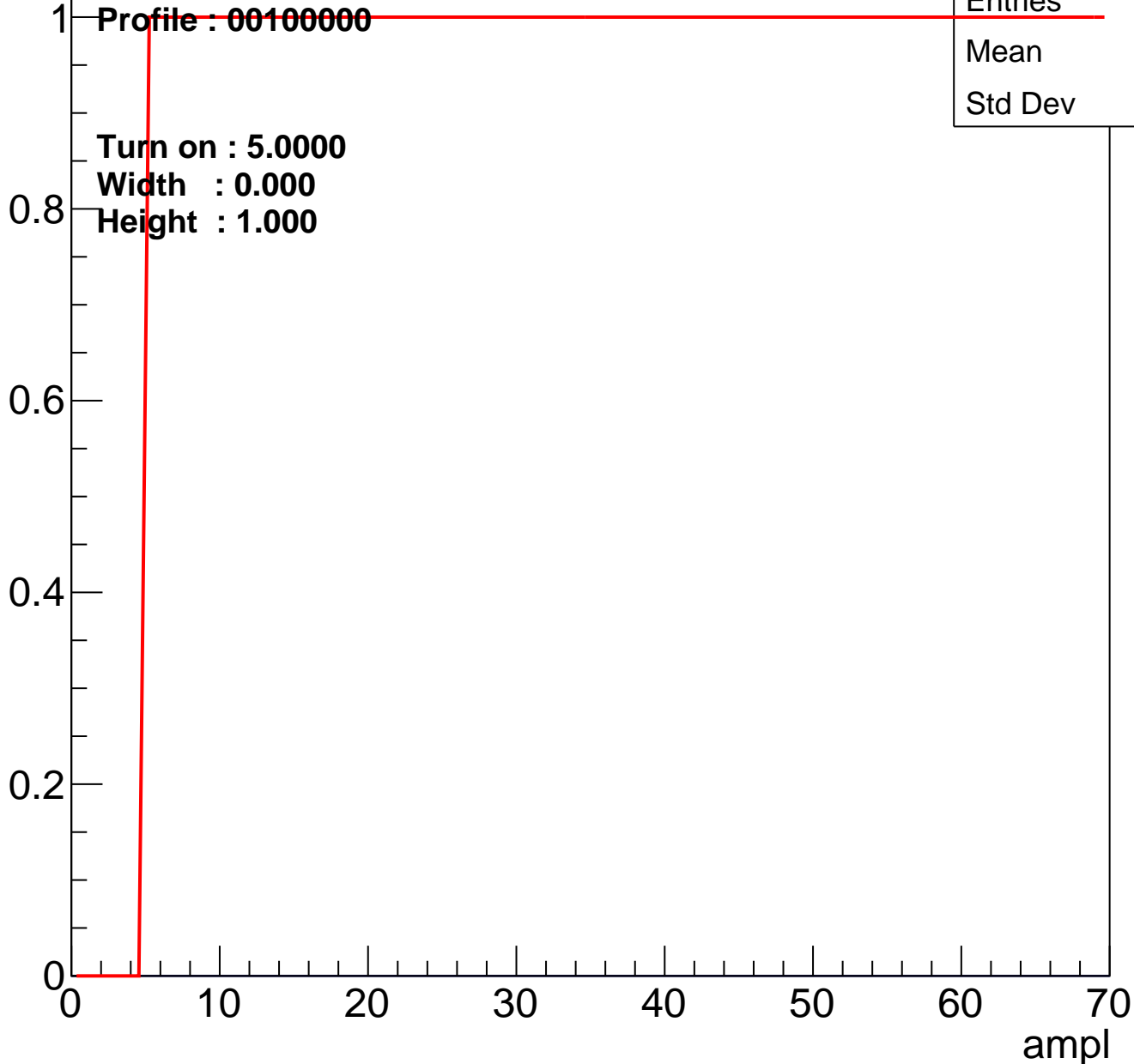
Entry



B1L003S, U14-ch19

calib_packv5_042523_0143.root, FC#13, Port D2

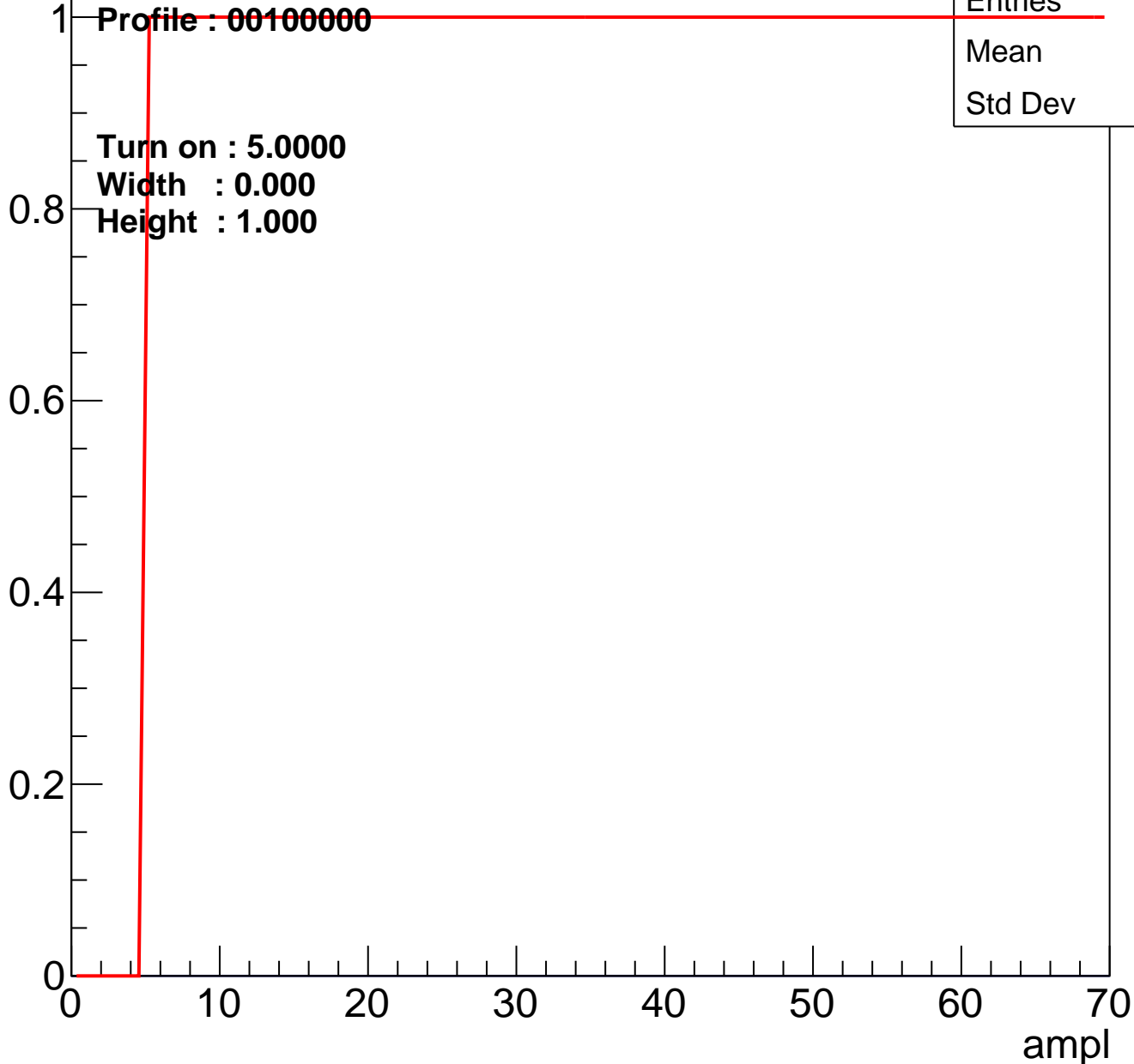
Entry



B1L003S, U14-ch20

calib_packv5_042523_0143.root, FC#13, Port D2

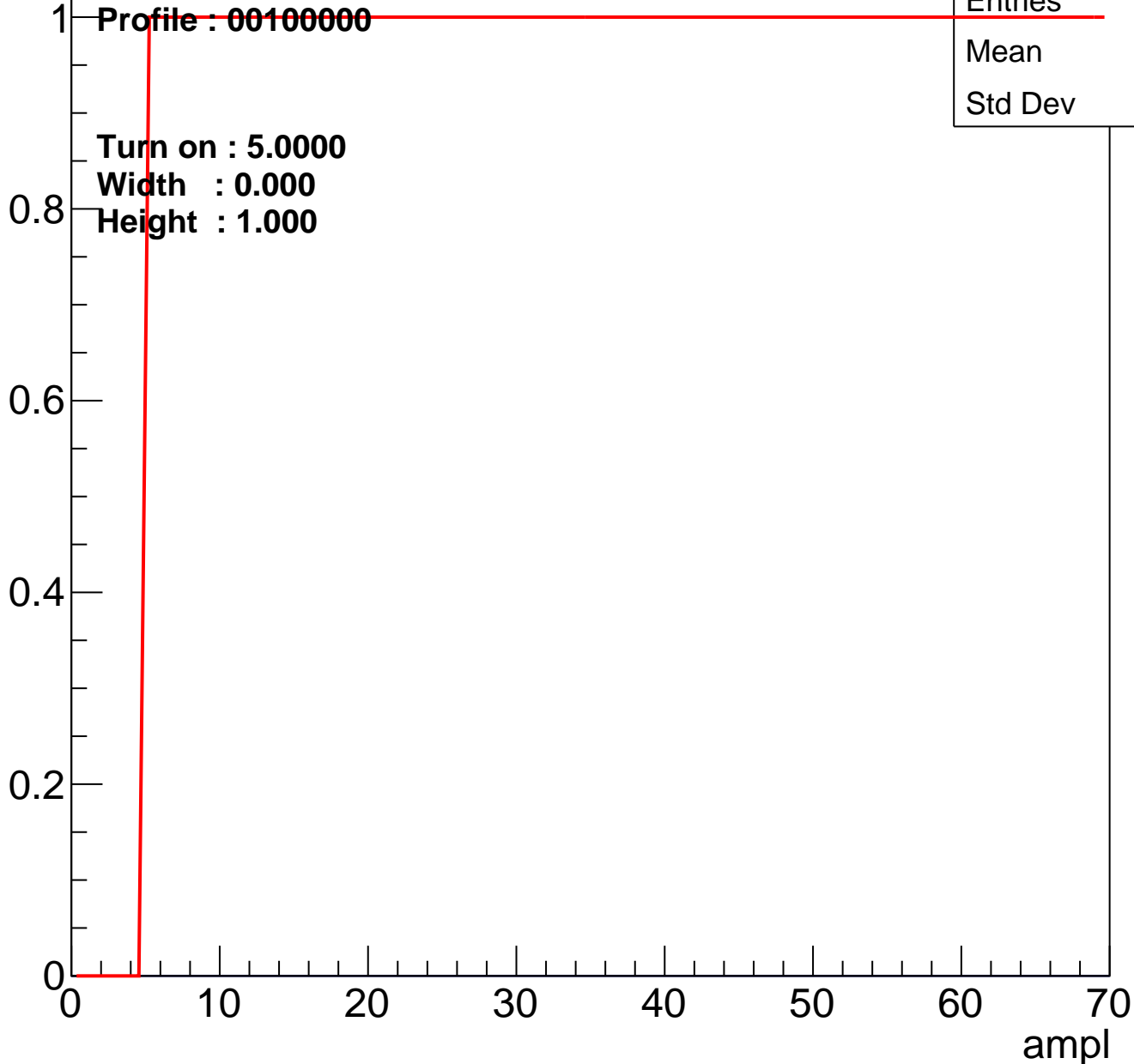
Entry



B1L003S, U14-ch21

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U14-ch22

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

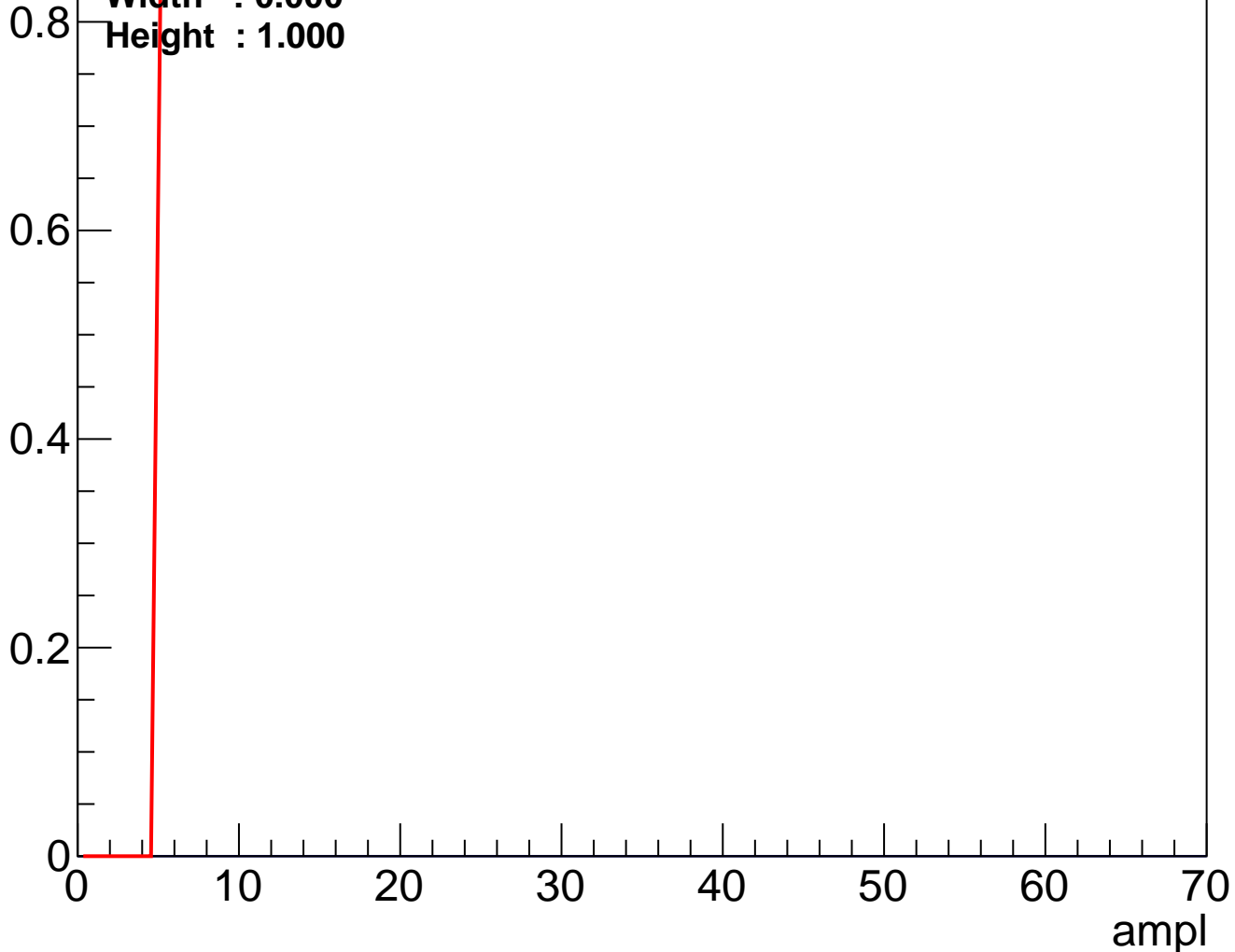
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

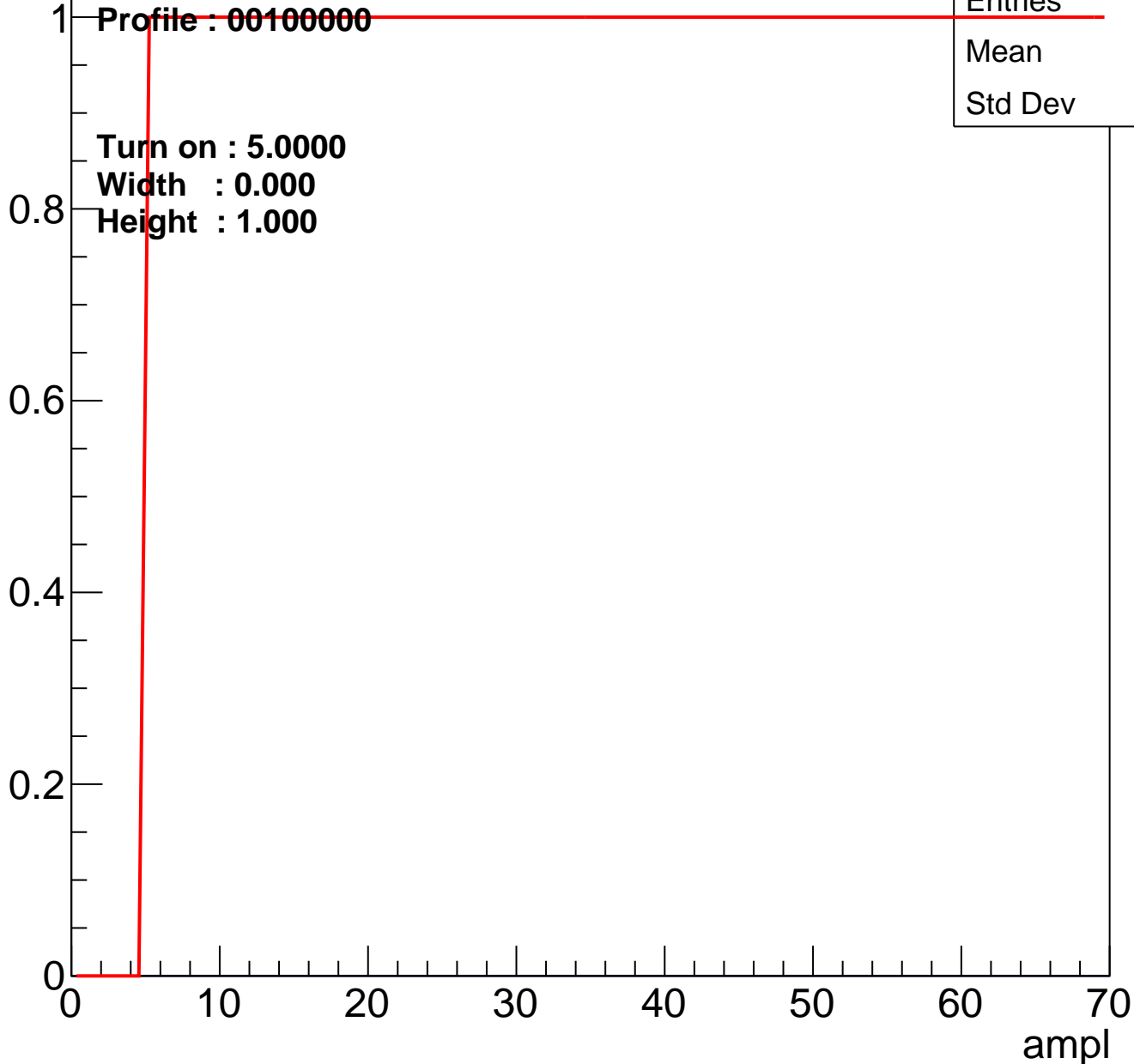
Entries	0
Mean	0
Std Dev	0



B1L003S, U14-ch23

calib_packv5_042523_0143.root, FC#13, Port D2

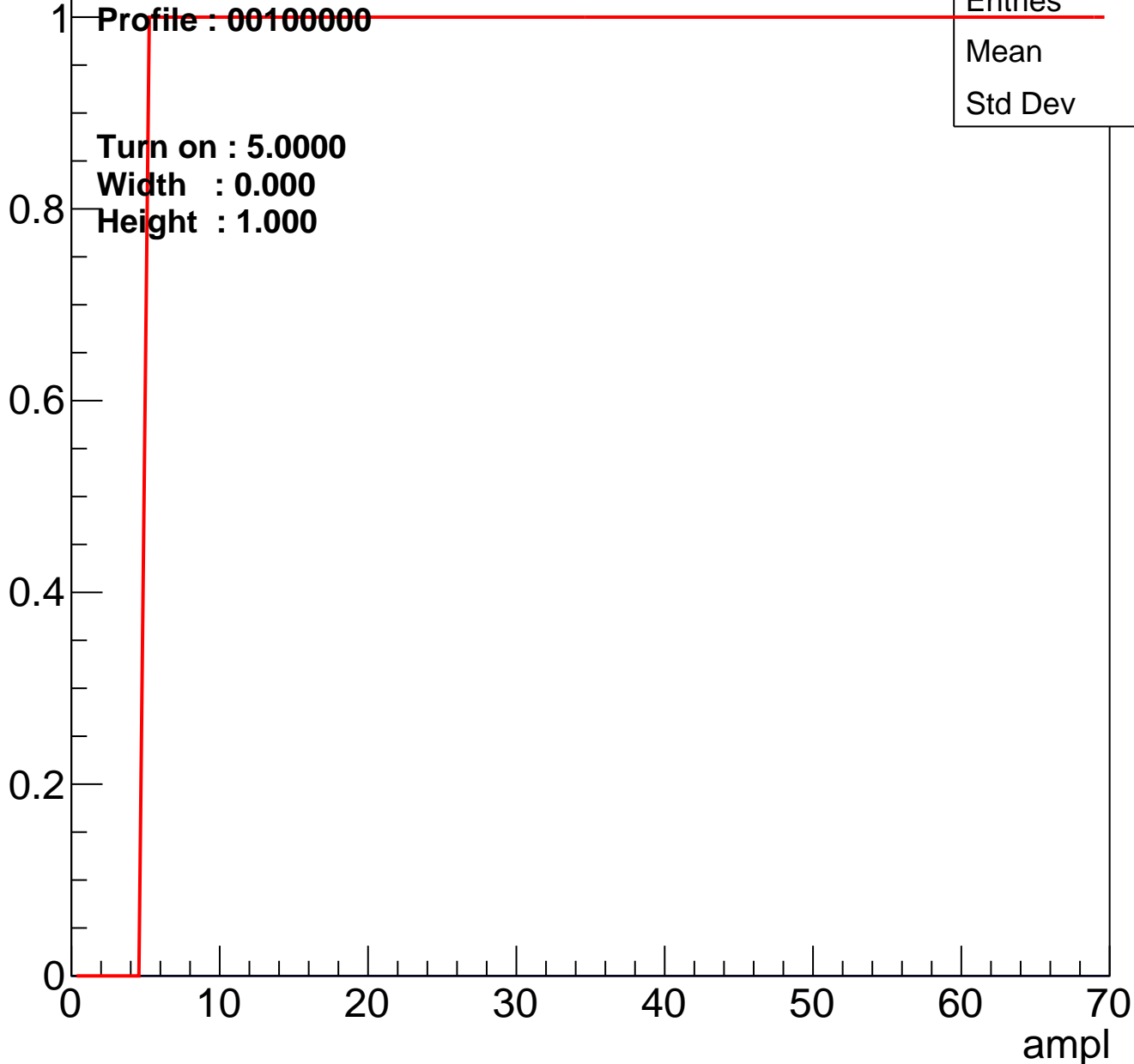
Entry



B1L003S, U14-ch24

calib_packv5_042523_0143.root, FC#13, Port D2

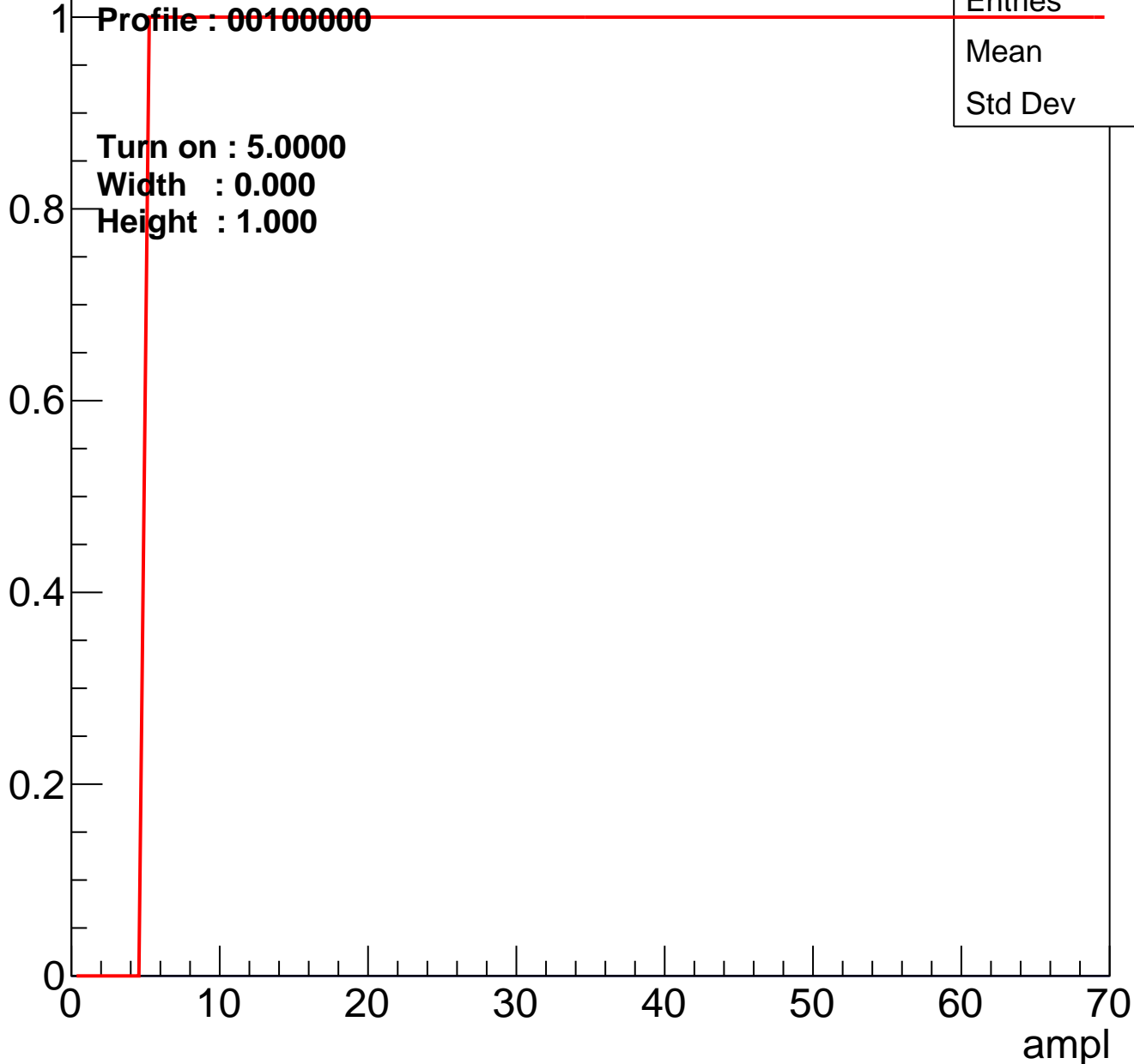
Entry



B1L003S, U14-ch25

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

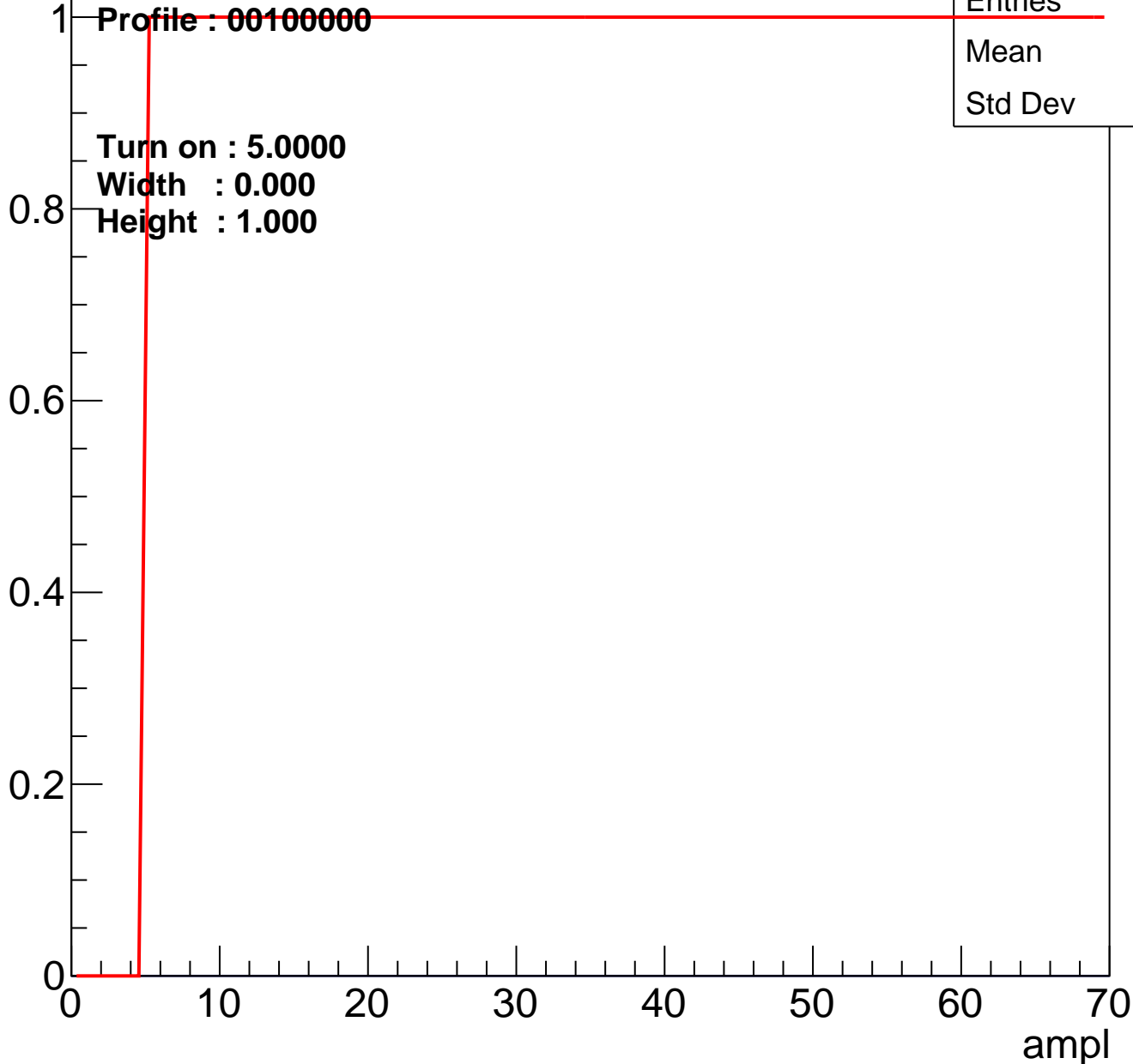


Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch26

calib_packv5_042523_0143.root, FC#13, Port D2

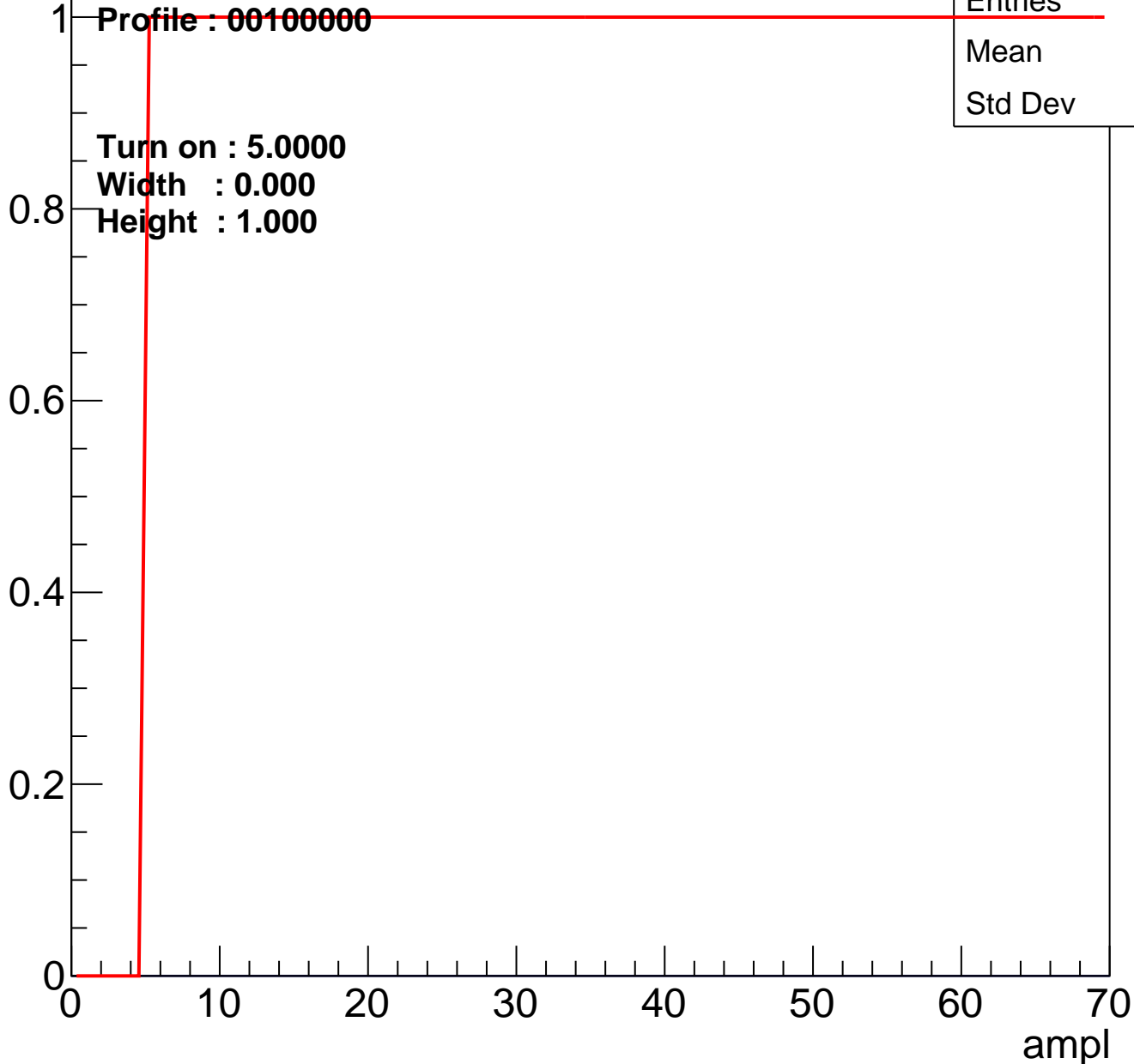
Entry



B1L003S, U14-ch27

calib_packv5_042523_0143.root, FC#13, Port D2

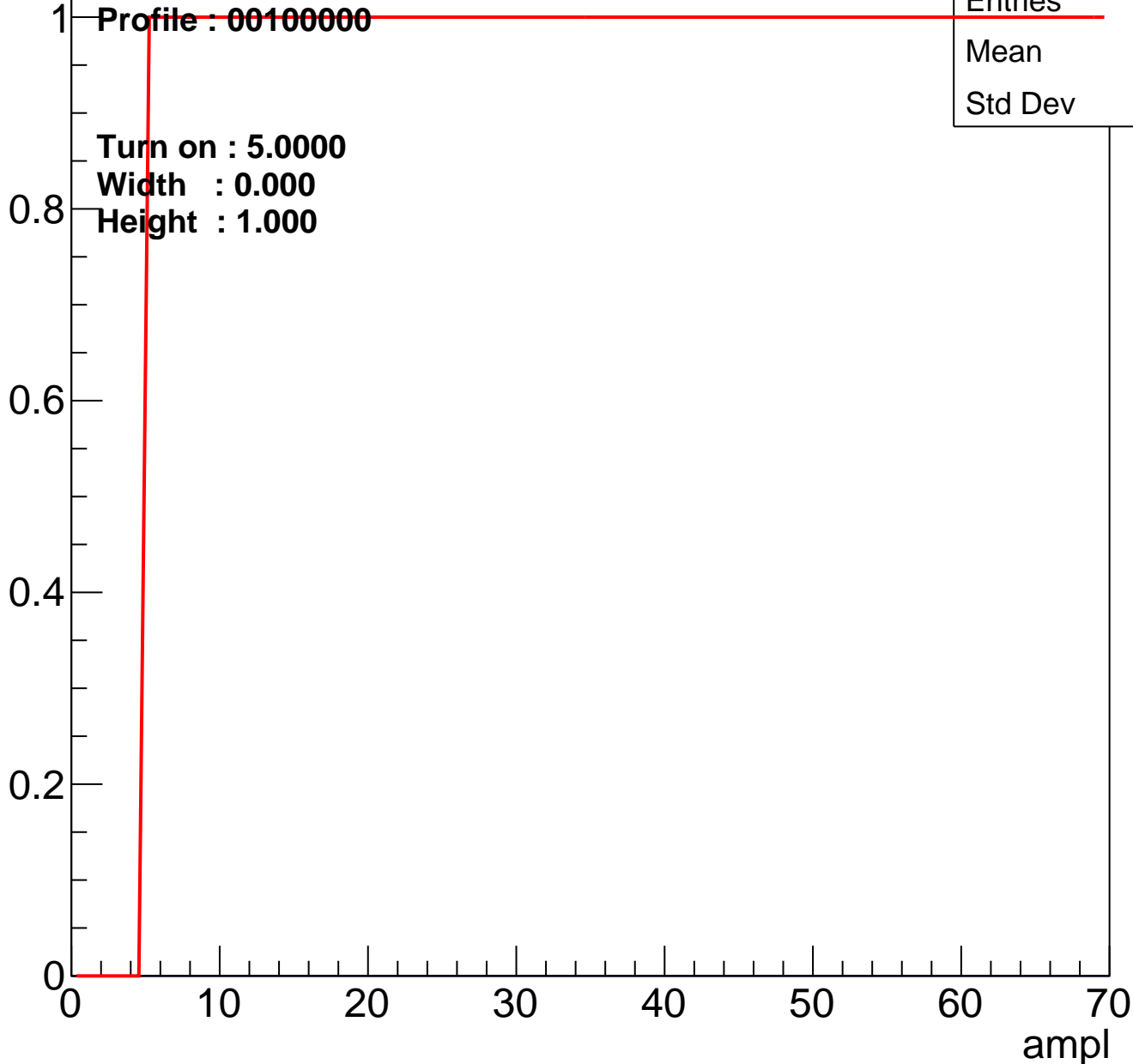
Entry



B1L003S, U14-ch28

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

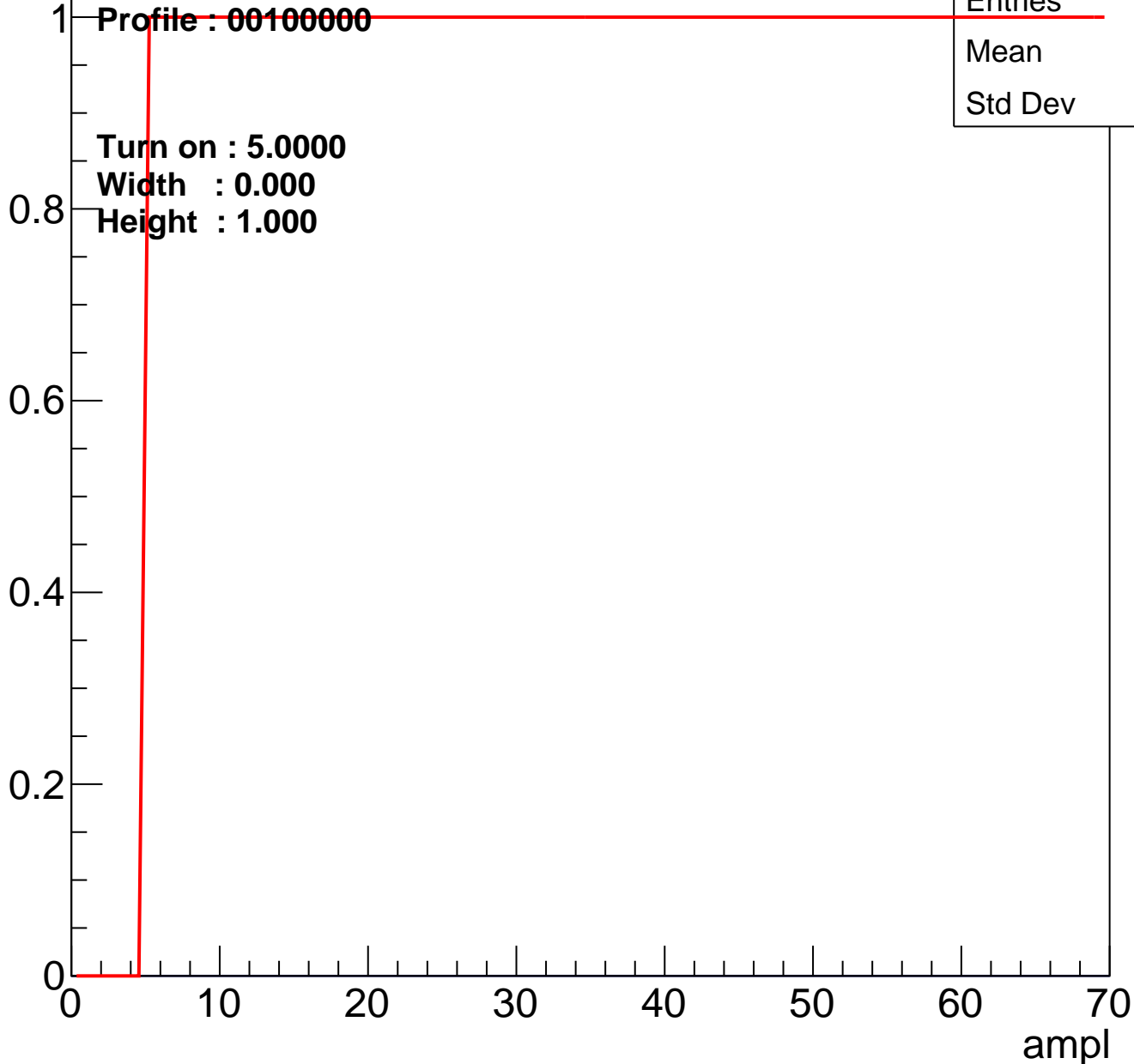


Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch29

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

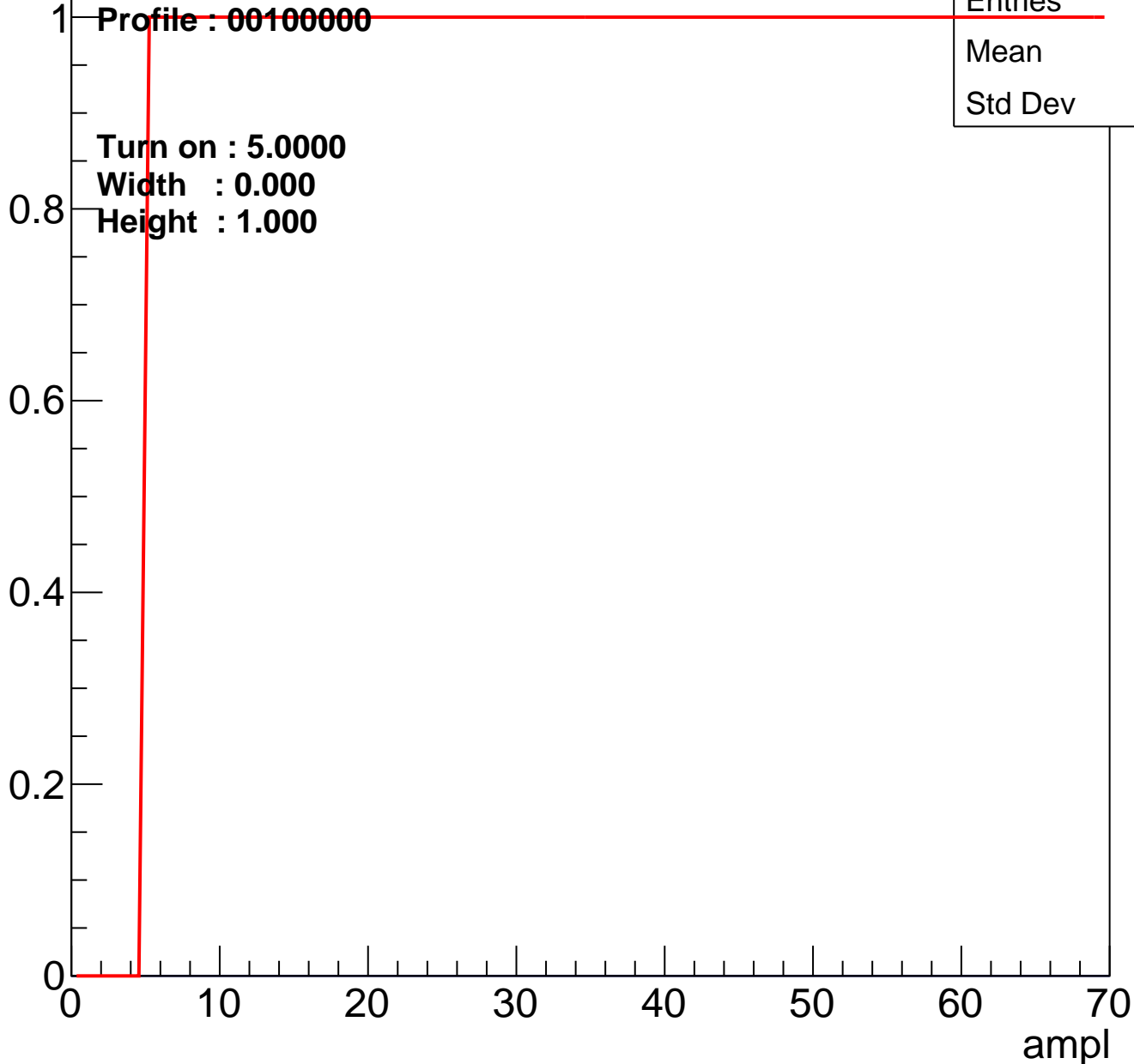


Entries	0
Mean	0
Std Dev	0

B1L003S, U14-ch30

calib_packv5_042523_0143.root, FC#13, Port D2

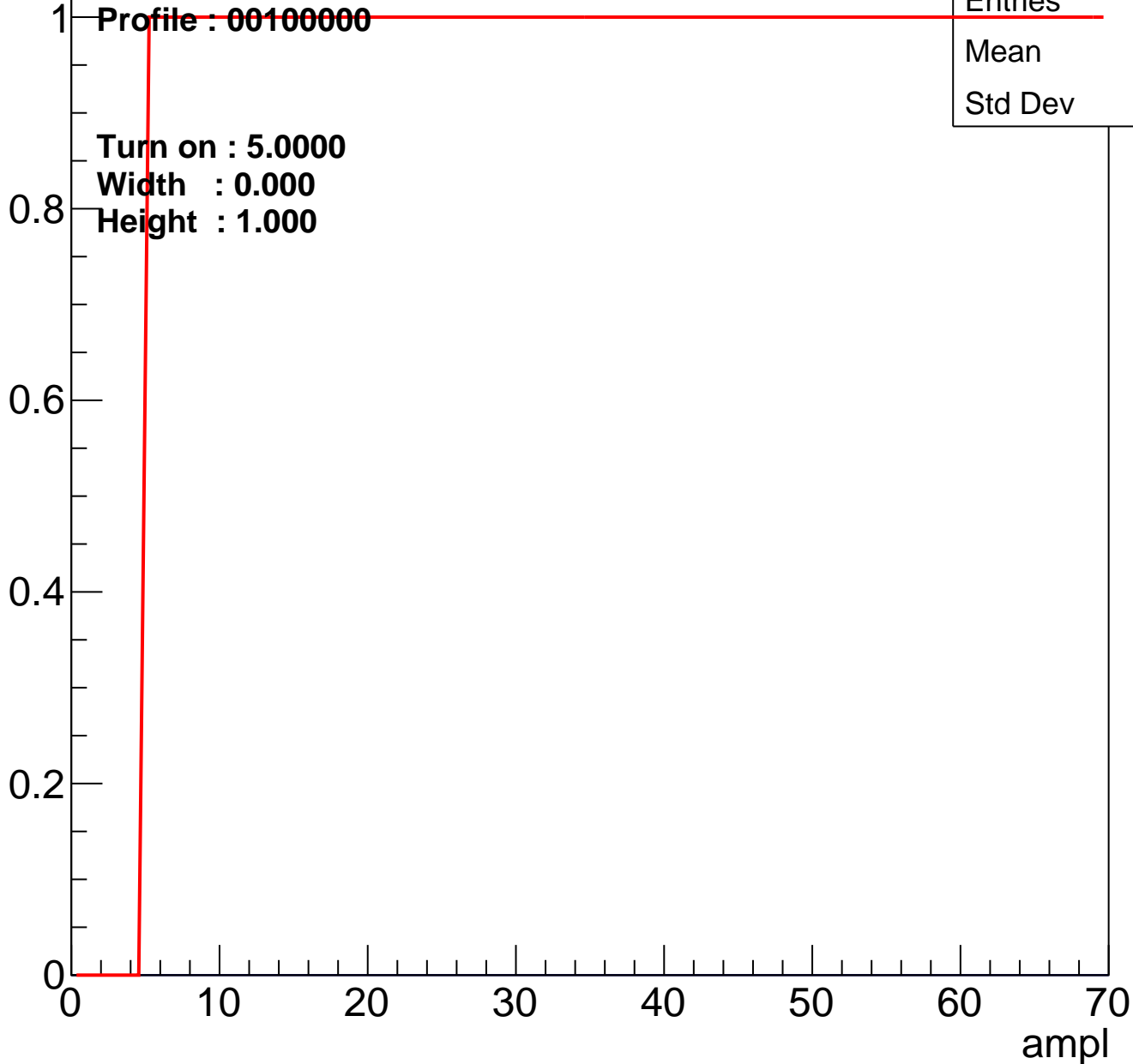
Entry



B1L003S, U14-ch31

calib_packv5_042523_0143.root, FC#13, Port D2

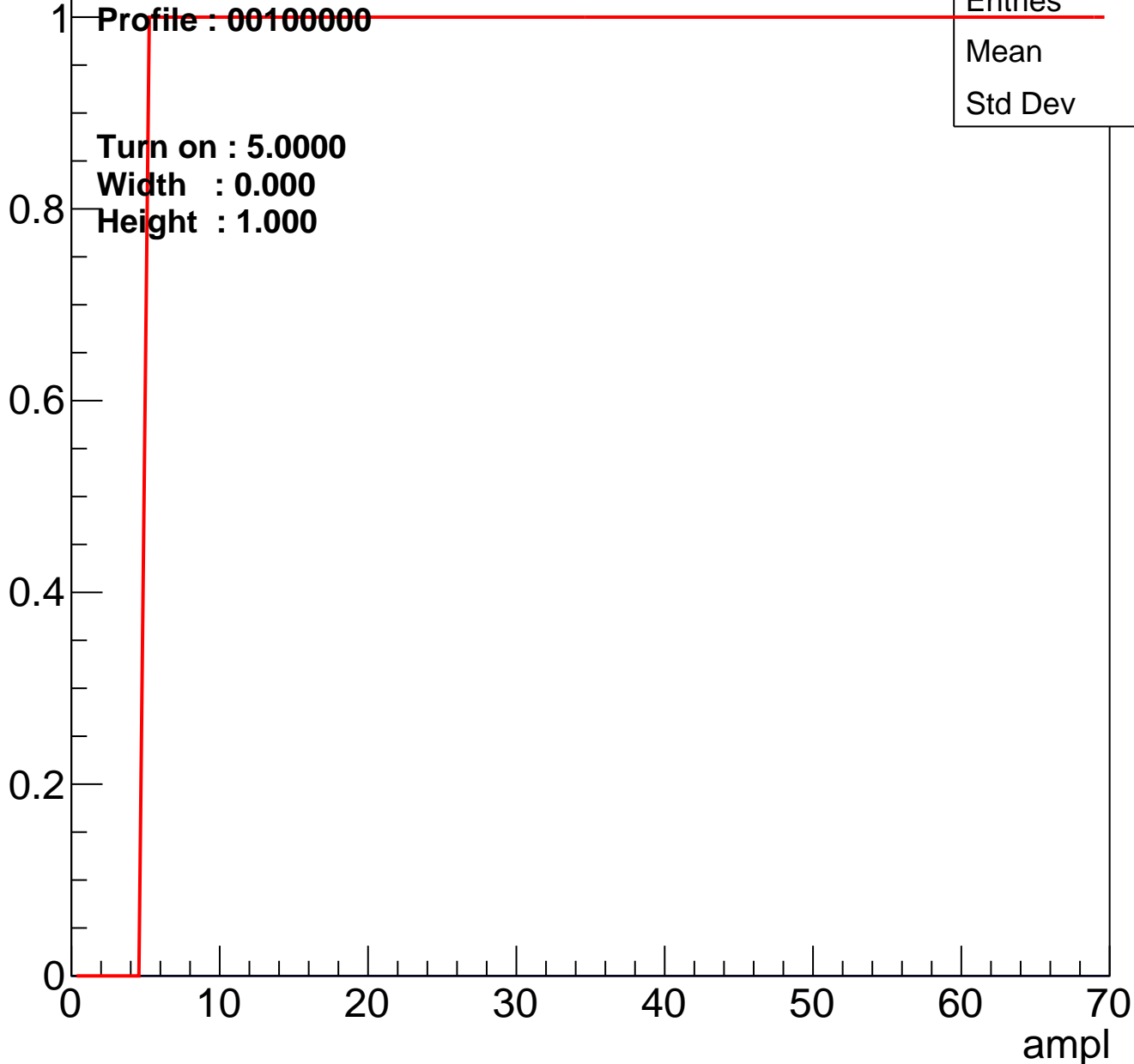
Entry



B1L003S, U15-ch0

calib_packv5_042523_0143.root, FC#13, Port D2

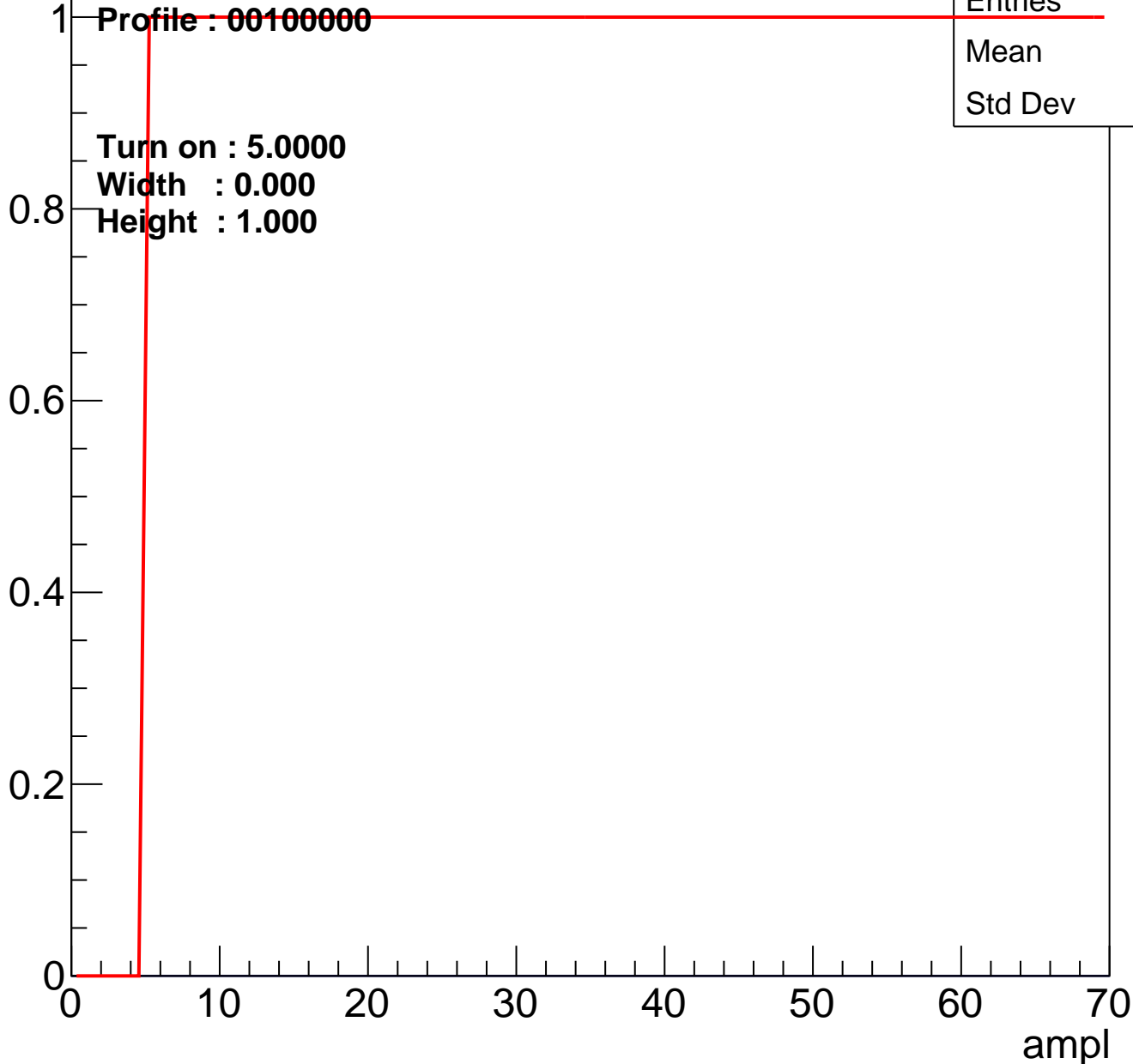
Entry



B1L003S, U15-ch1

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

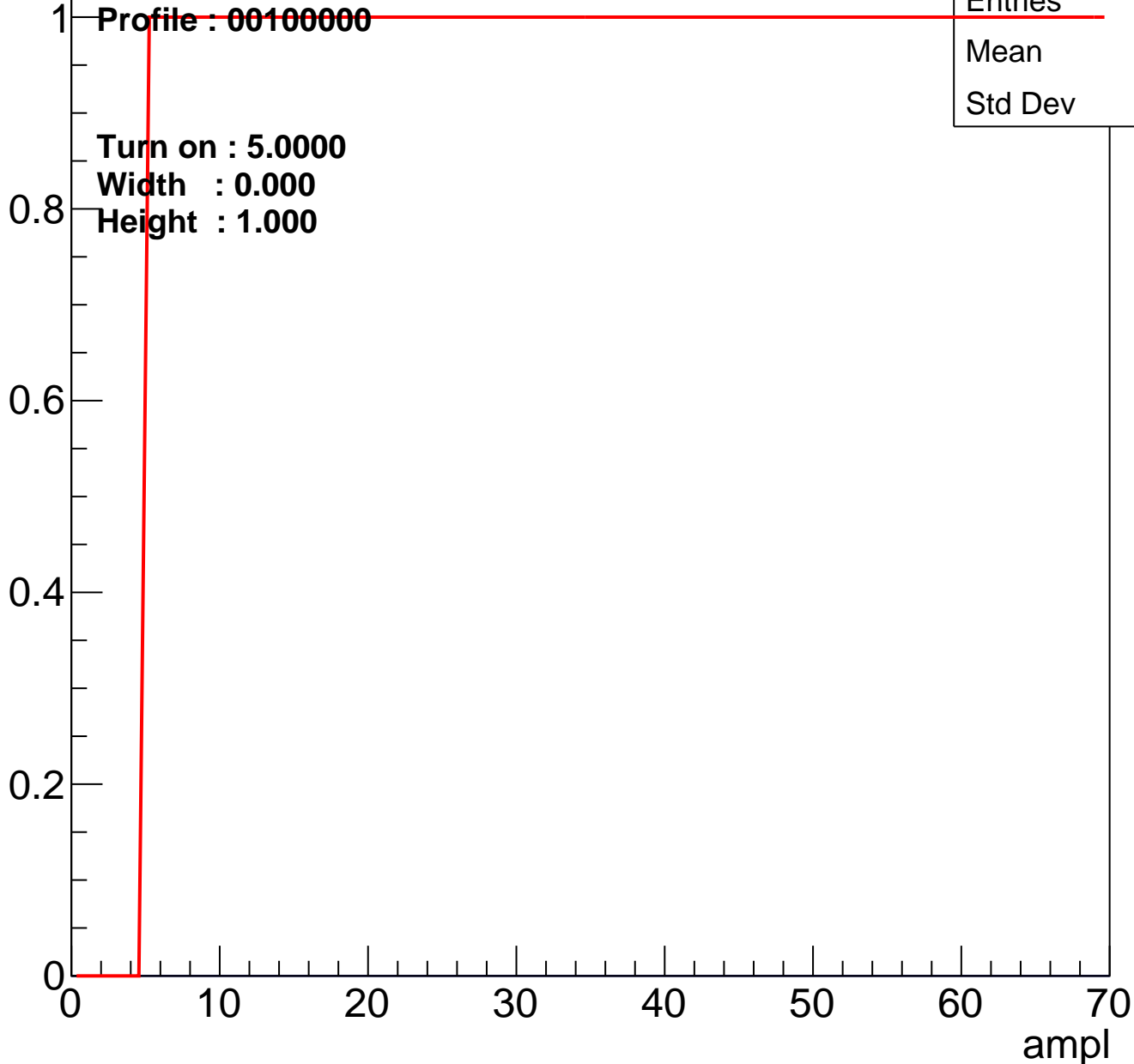


Entries	0
Mean	0
Std Dev	0

B1L003S, U15-ch2

calib_packv5_042523_0143.root, FC#13, Port D2

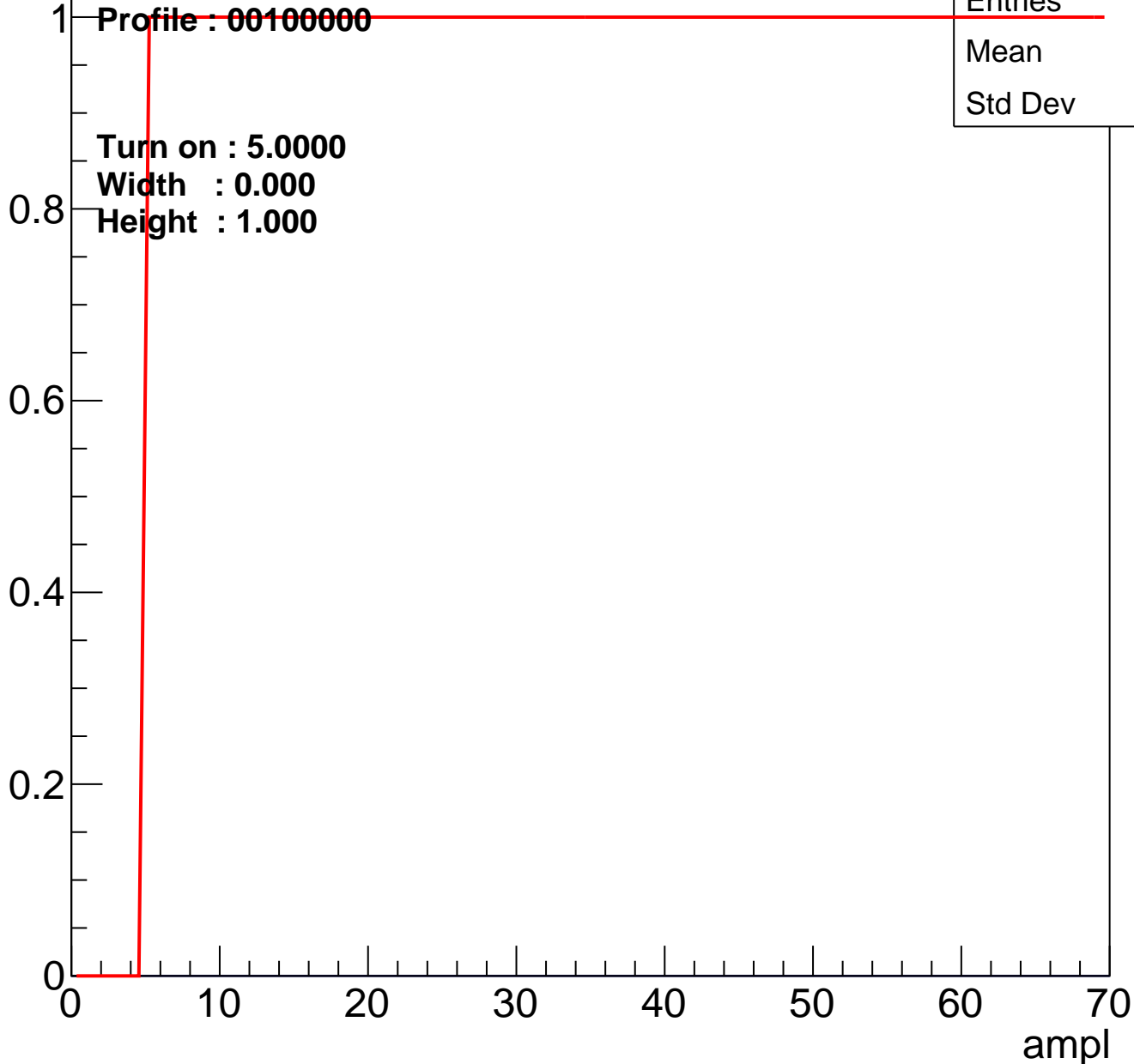
Entry



B1L003S, U15-ch3

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

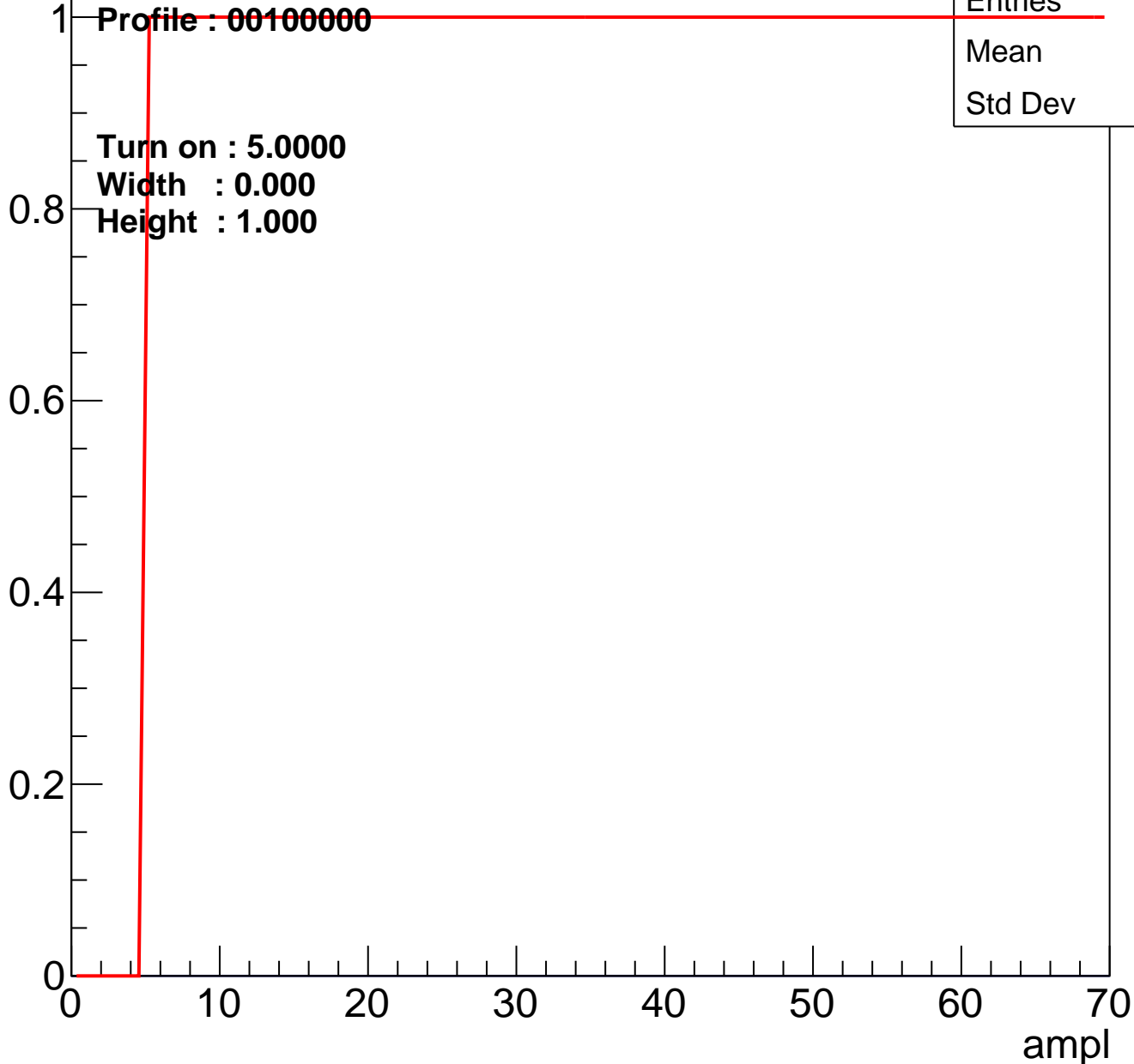


Entries	0
Mean	0
Std Dev	0

B1L003S, U15-ch4

calib_packv5_042523_0143.root, FC#13, Port D2

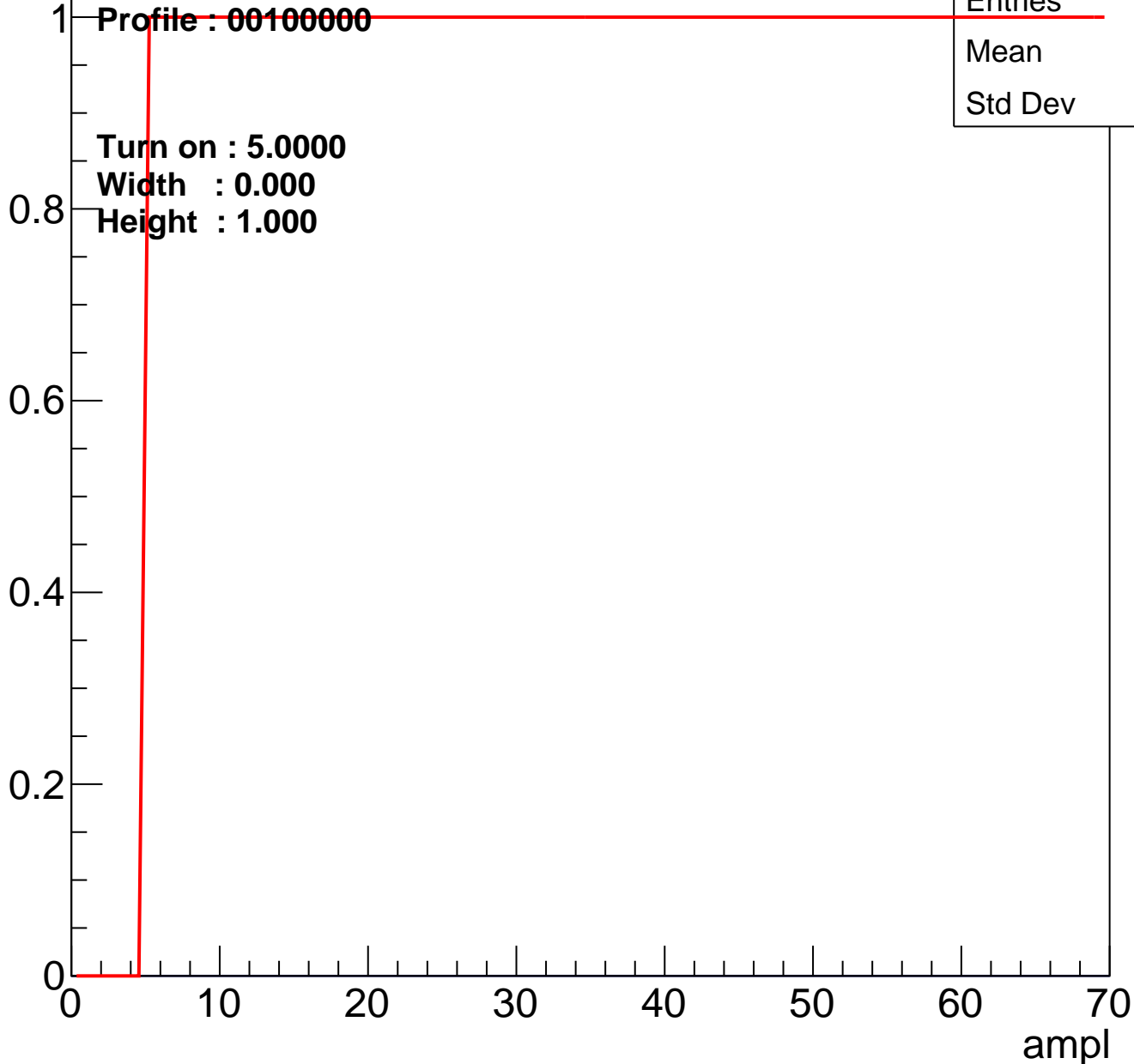
Entry



B1L003S, U15-ch5

calib_packv5_042523_0143.root, FC#13, Port D2

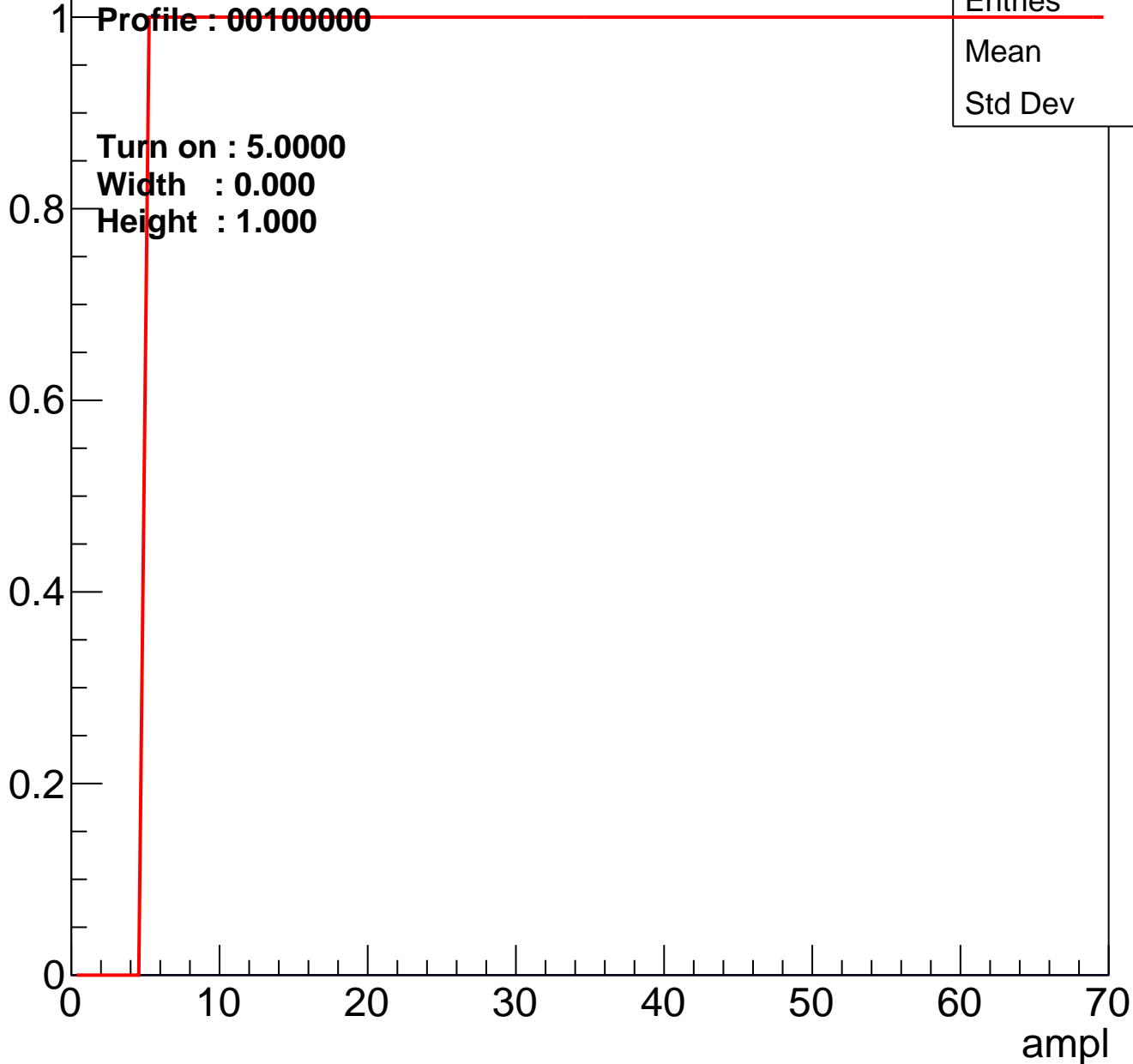
Entry



B1L003S, U15-ch6

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U15-ch7

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

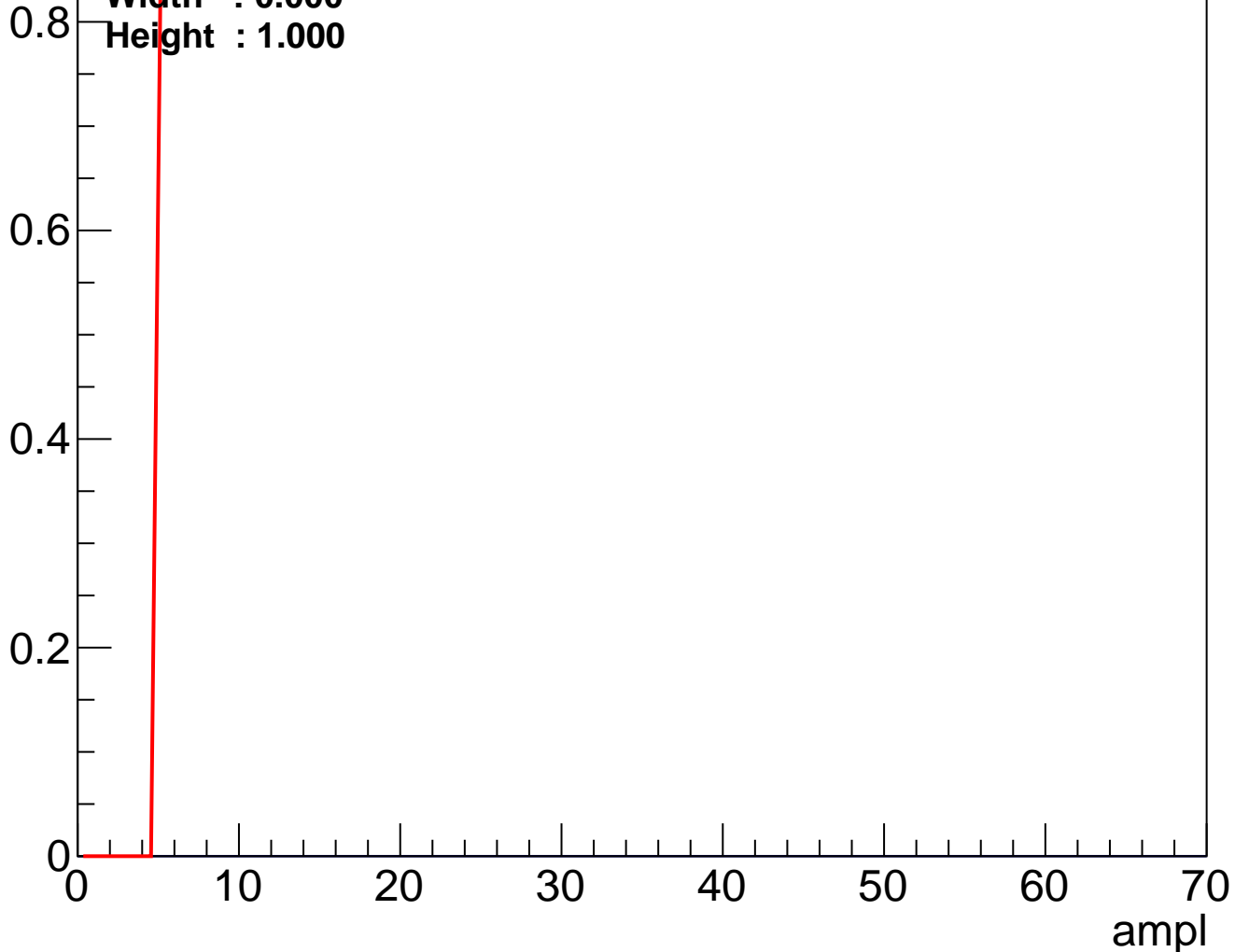
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

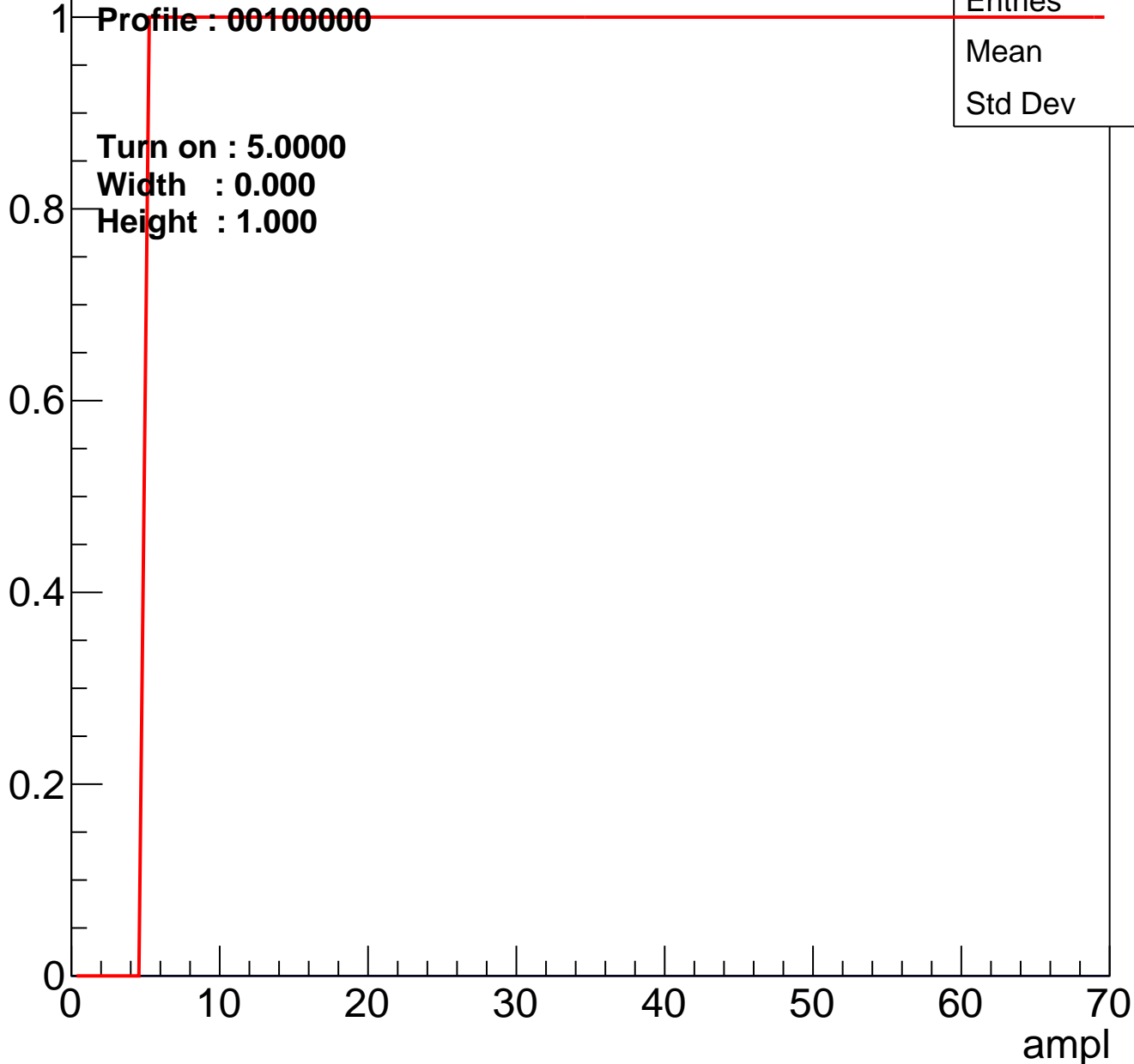
Entries	0
Mean	0
Std Dev	0



B1L003S, U15-ch8

calib_packv5_042523_0143.root, FC#13, Port D2

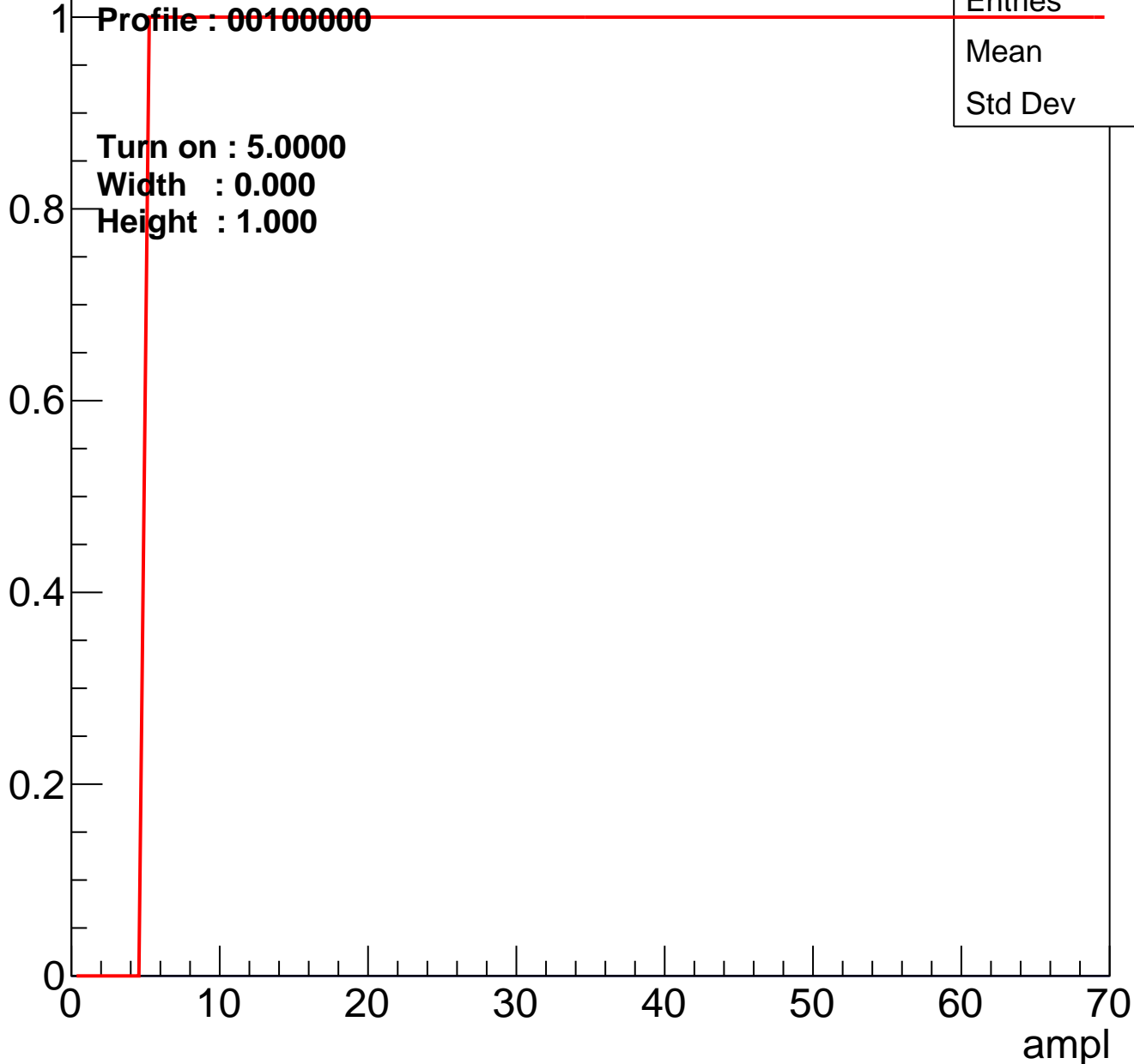
Entry



B1L003S, U15-ch9

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U15-ch10

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

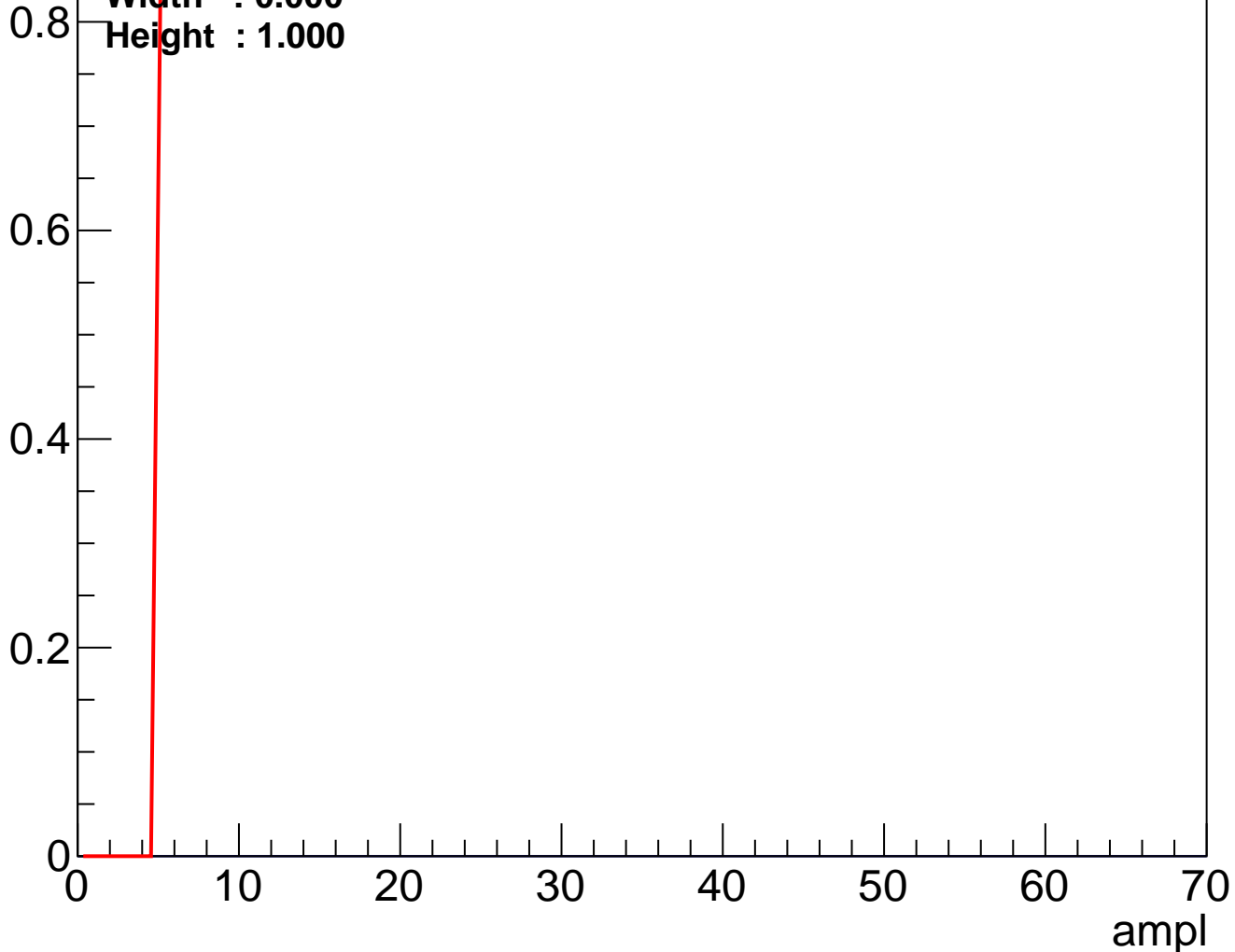
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

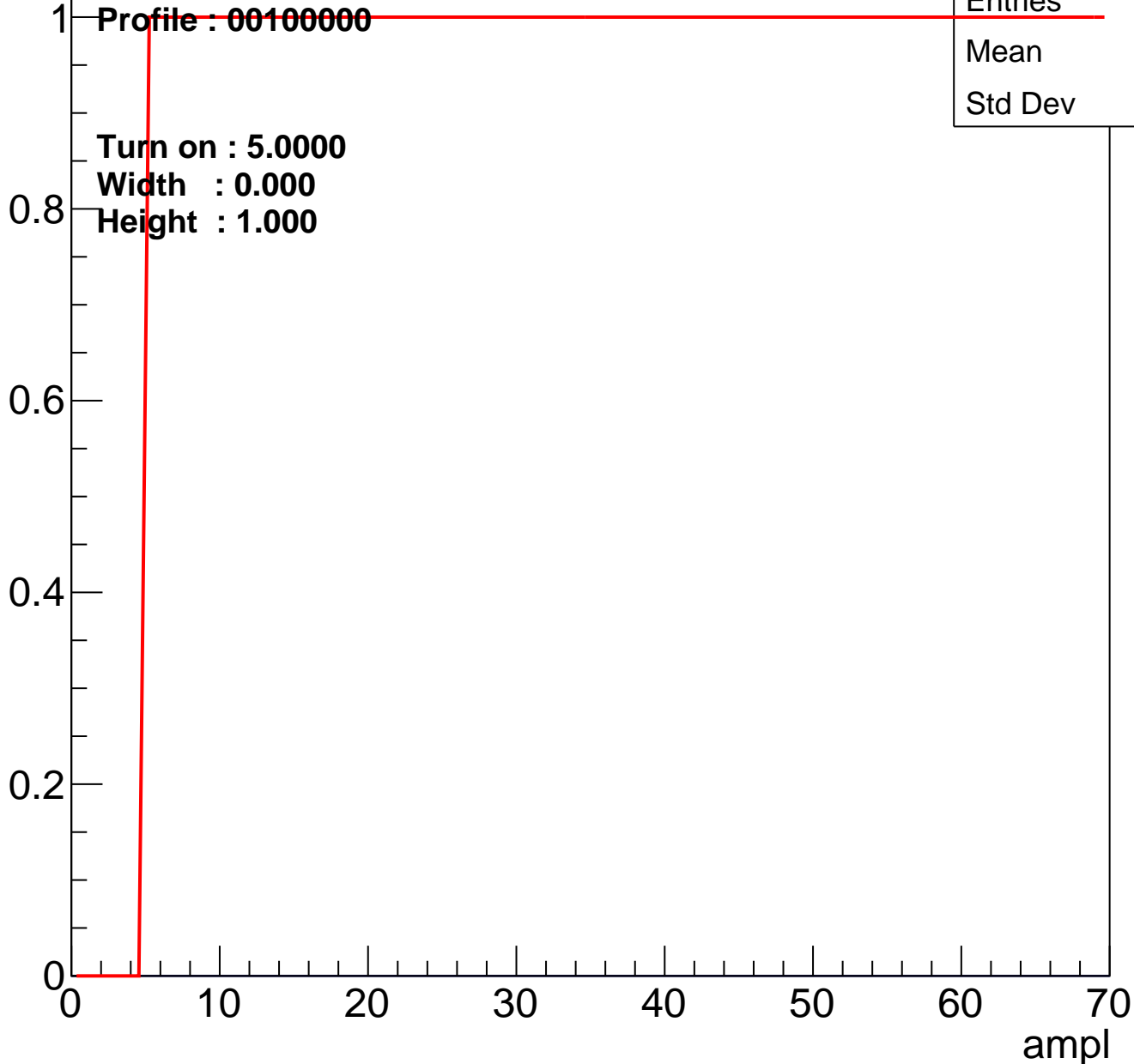
Entries	0
Mean	0
Std Dev	0



B1L003S, U15-ch11

calib_packv5_042523_0143.root, FC#13, Port D2

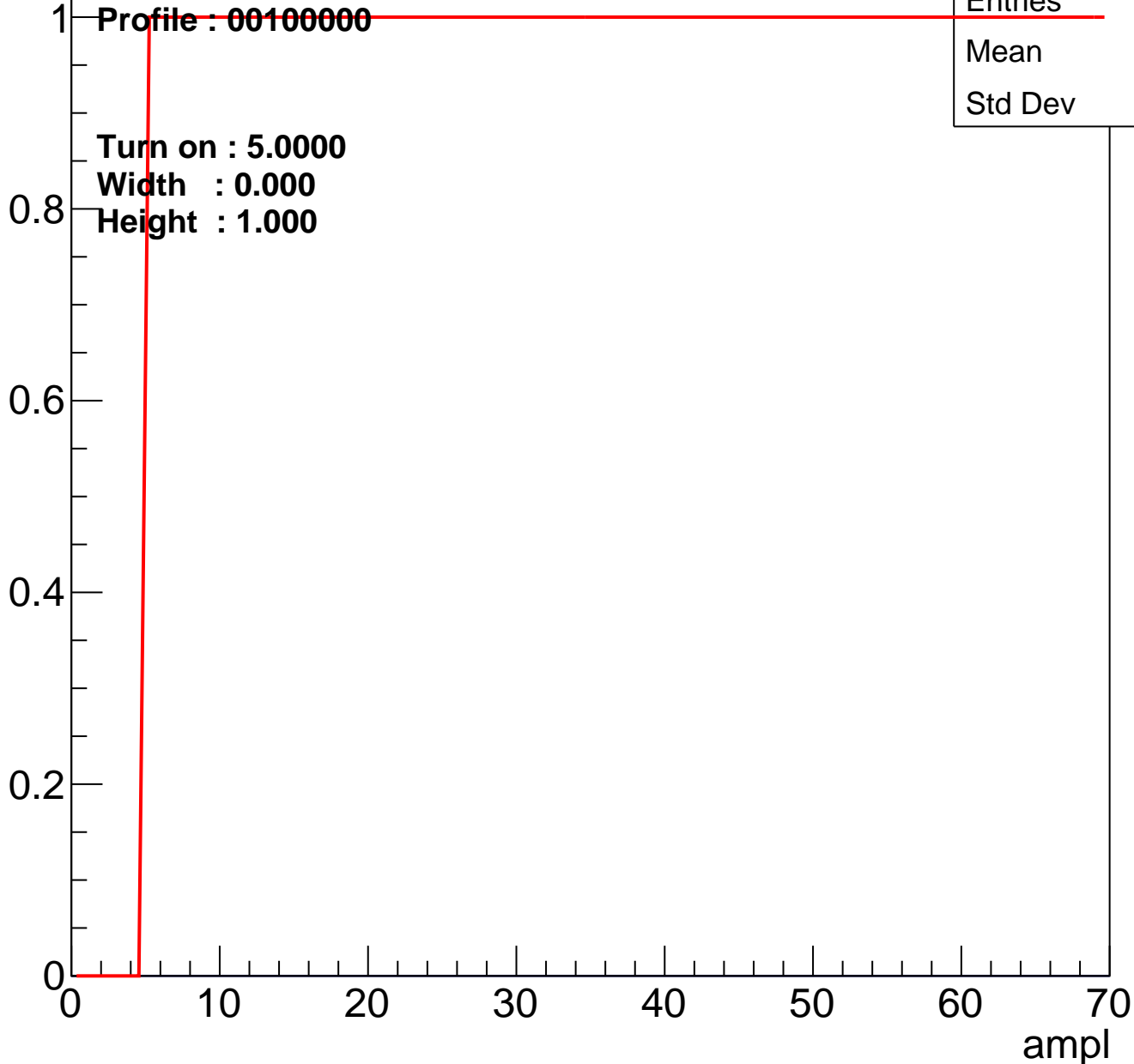
Entry



B1L003S, U15-ch12

calib_packv5_042523_0143.root, FC#13, Port D2

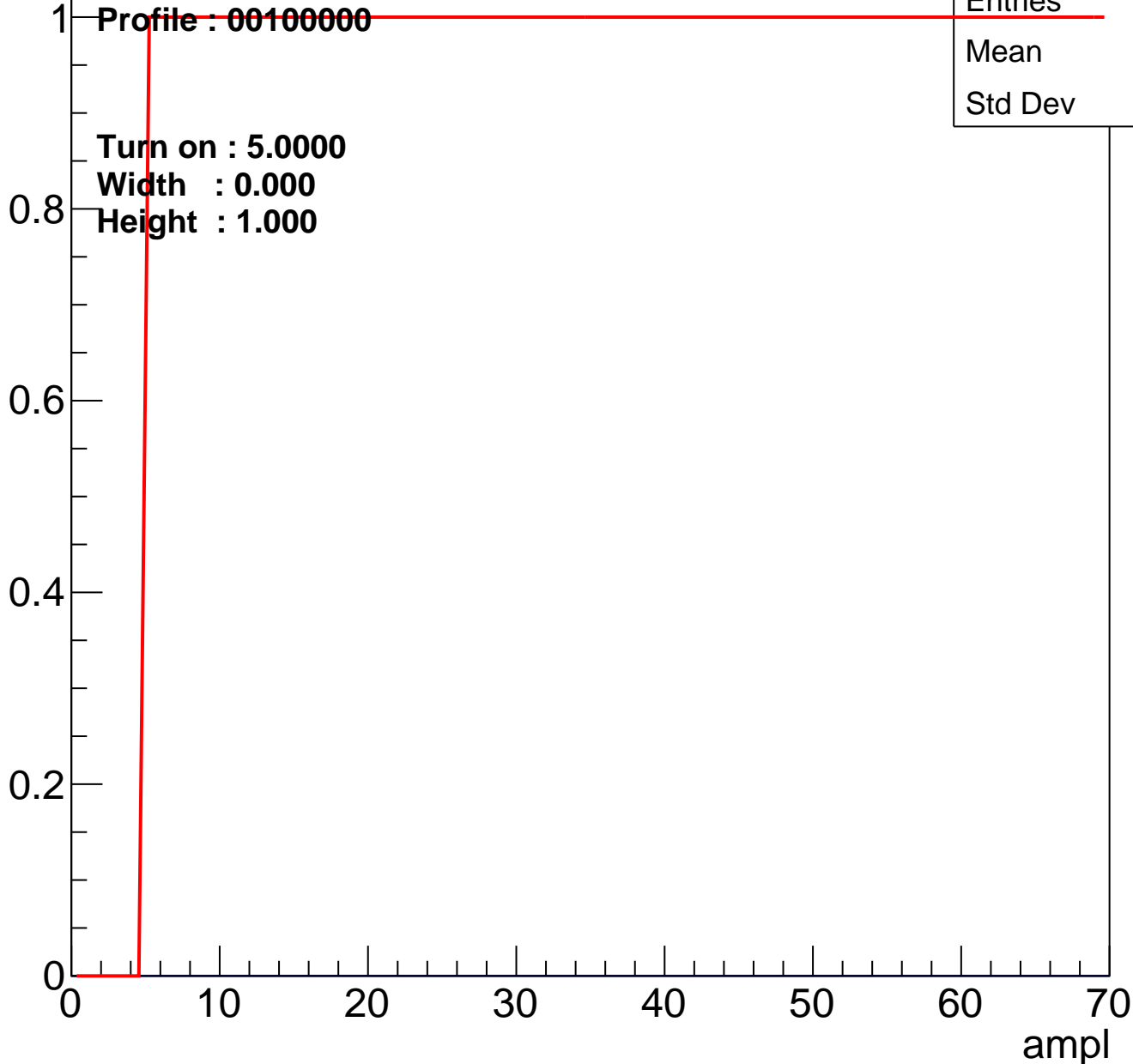
Entry



B1L003S, U15-ch13

calib_packv5_042523_0143.root, FC#13, Port D2

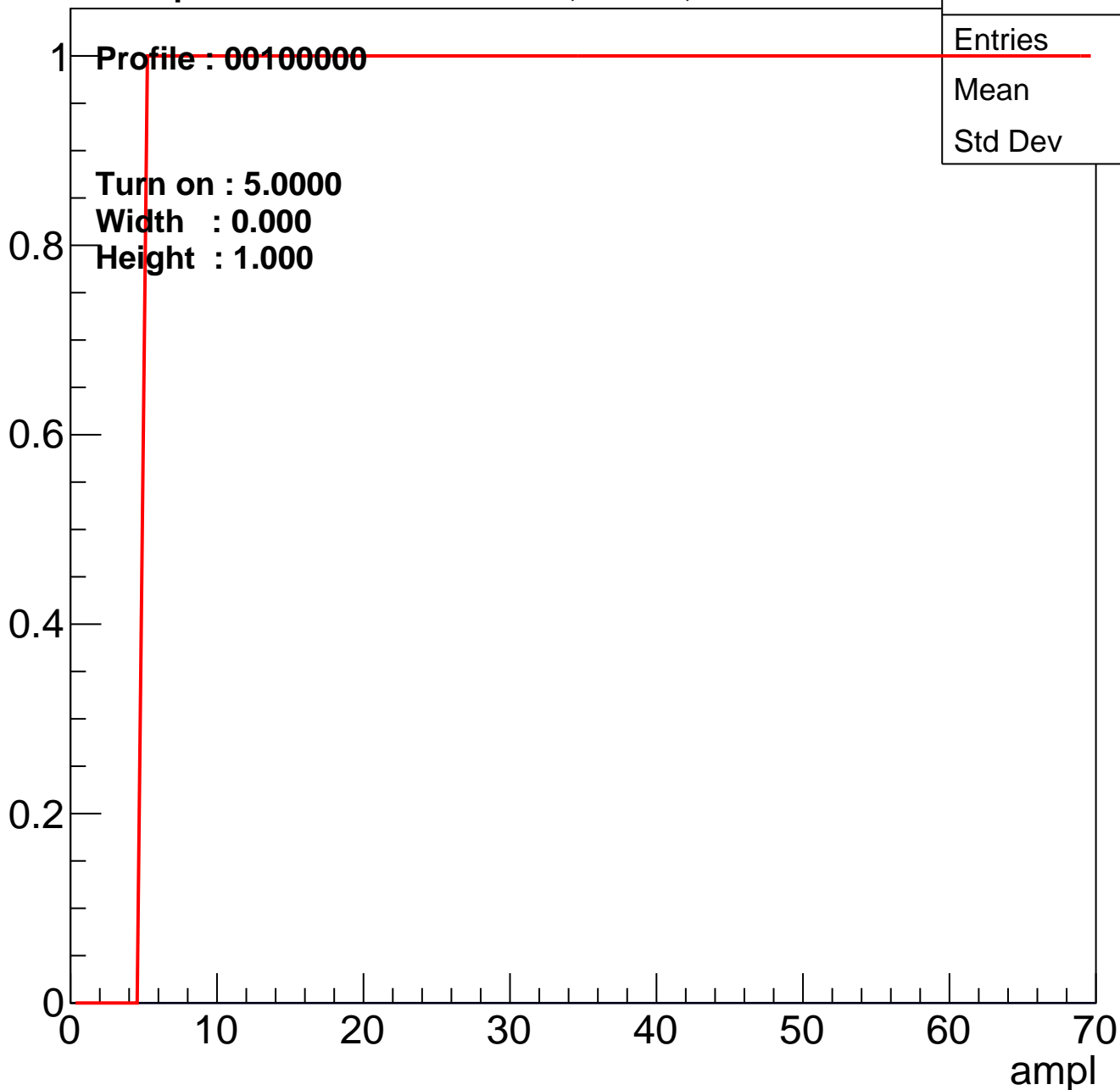
Entry



B1L003S, U15-ch14

calib_packv5_042523_0143.root, FC#13, Port D2

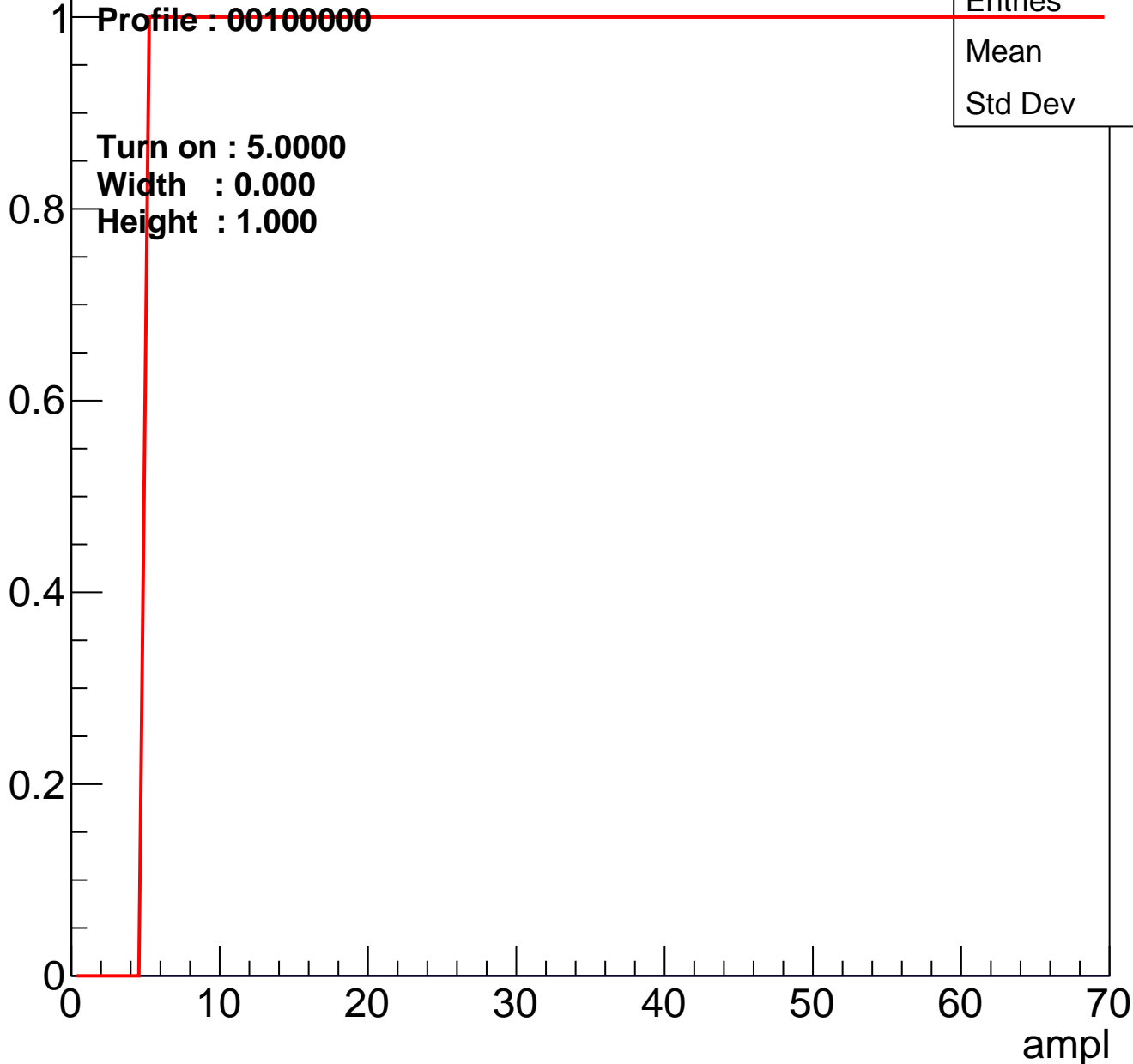
Entry



B1L003S, U15-ch15

calib_packv5_042523_0143.root, FC#13, Port D2

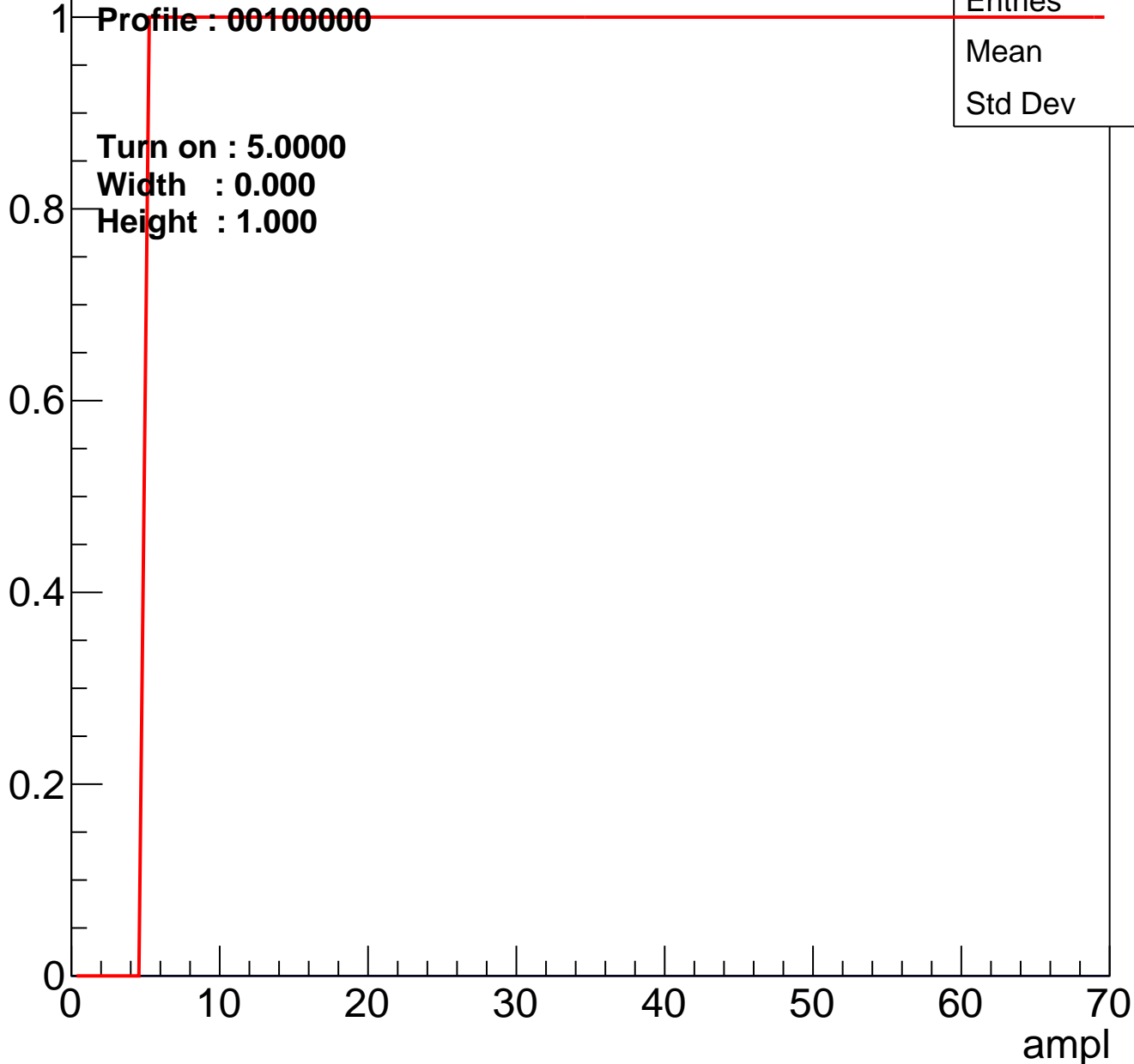
Entry



B1L003S, U15-ch16

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U15-ch17

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

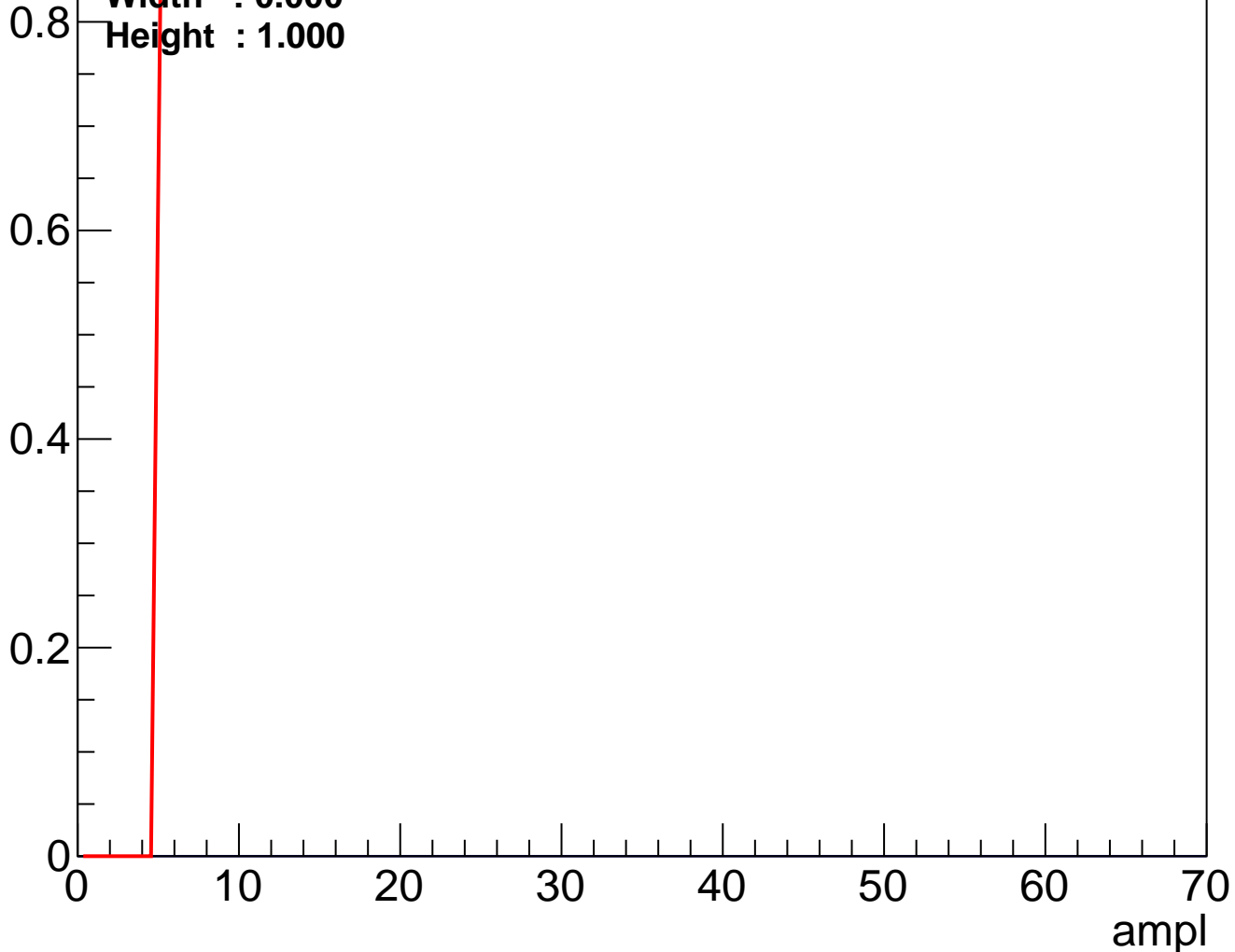
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

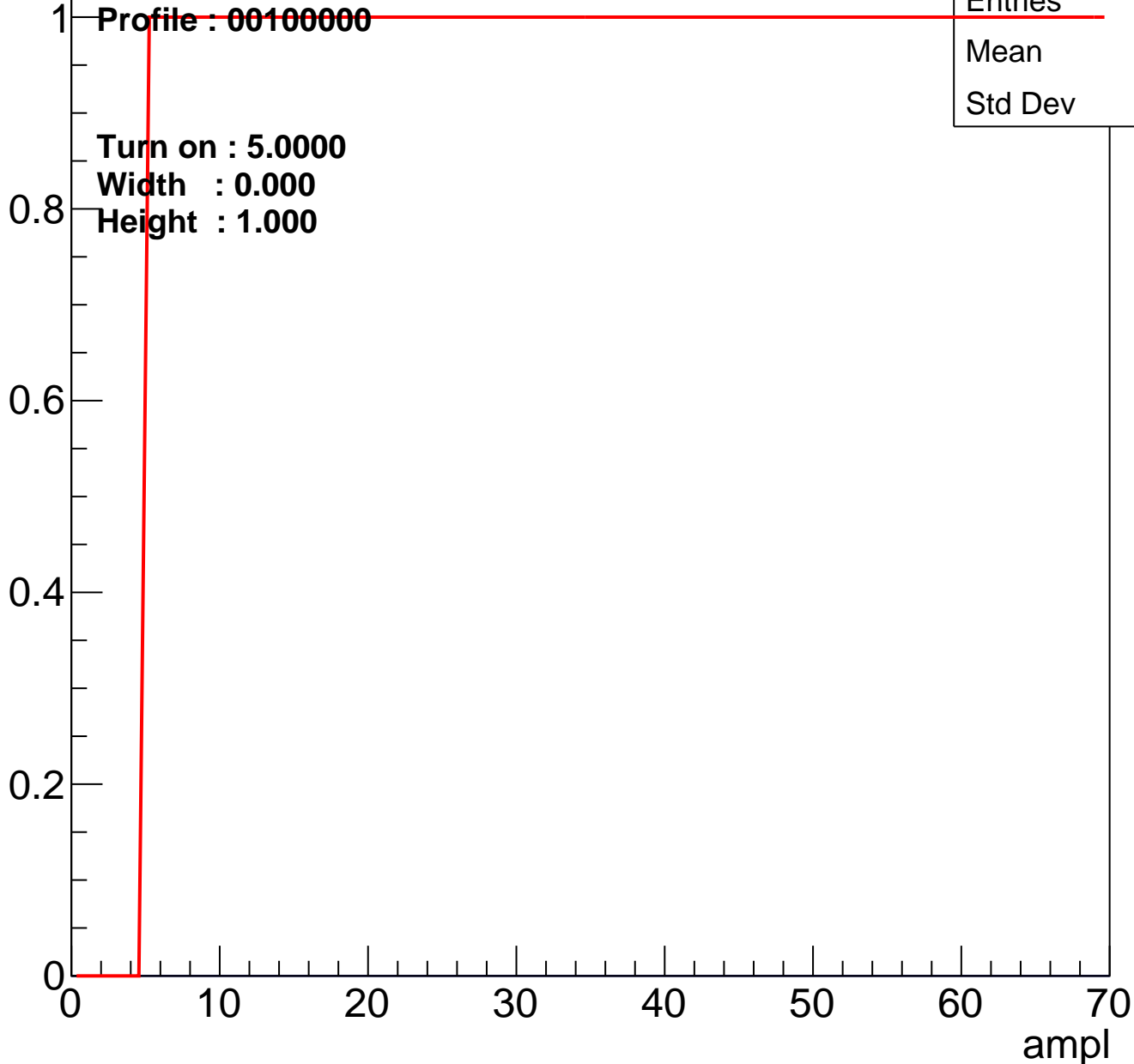
Entries	0
Mean	0
Std Dev	0



B1L003S, U15-ch18

calib_packv5_042523_0143.root, FC#13, Port D2

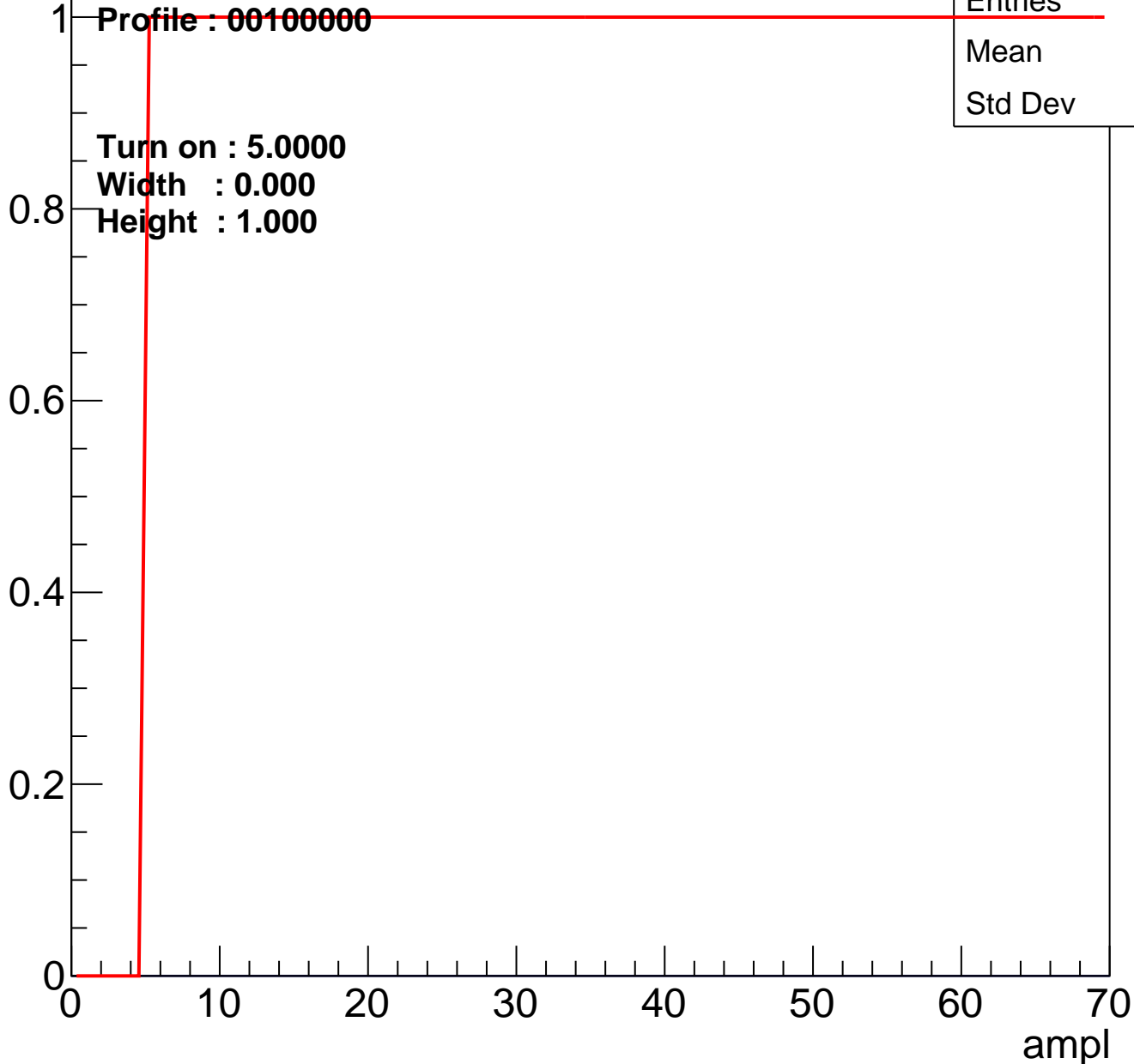
Entry



B1L003S, U15-ch19

calib_packv5_042523_0143.root, FC#13, Port D2

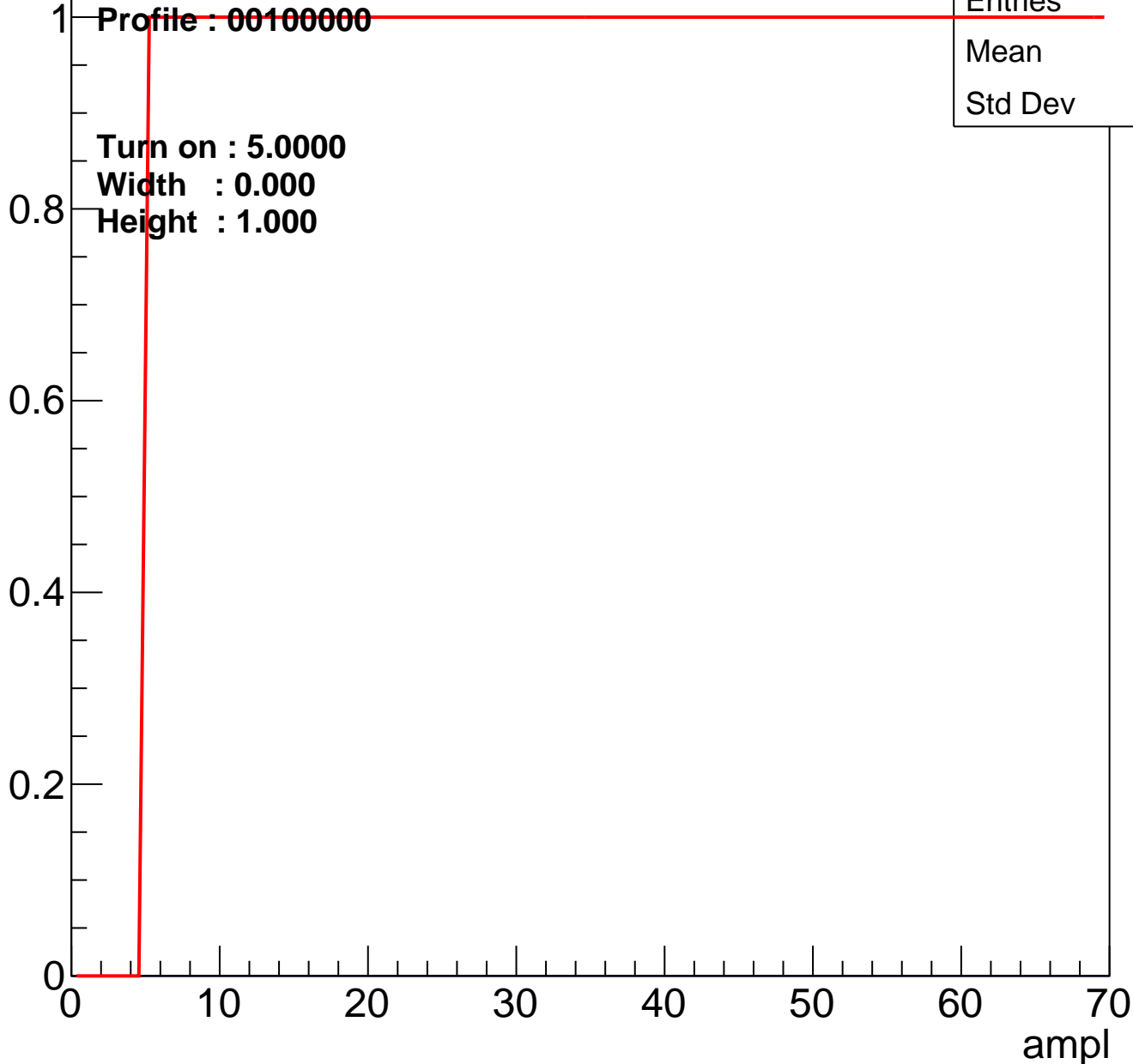
Entry



B1L003S, U15-ch20

calib_packv5_042523_0143.root, FC#13, Port D2

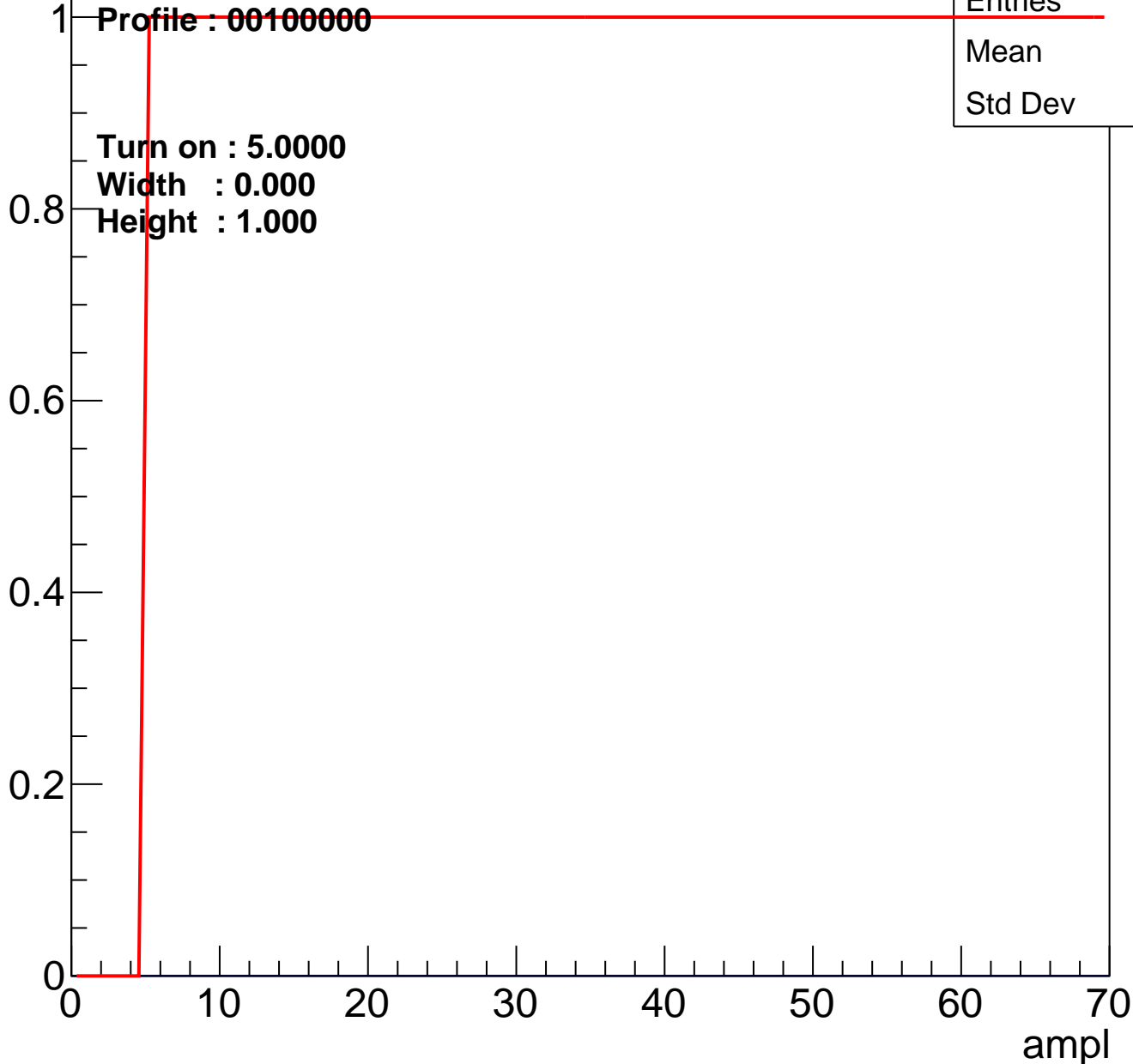
Entry



B1L003S, U15-ch21

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

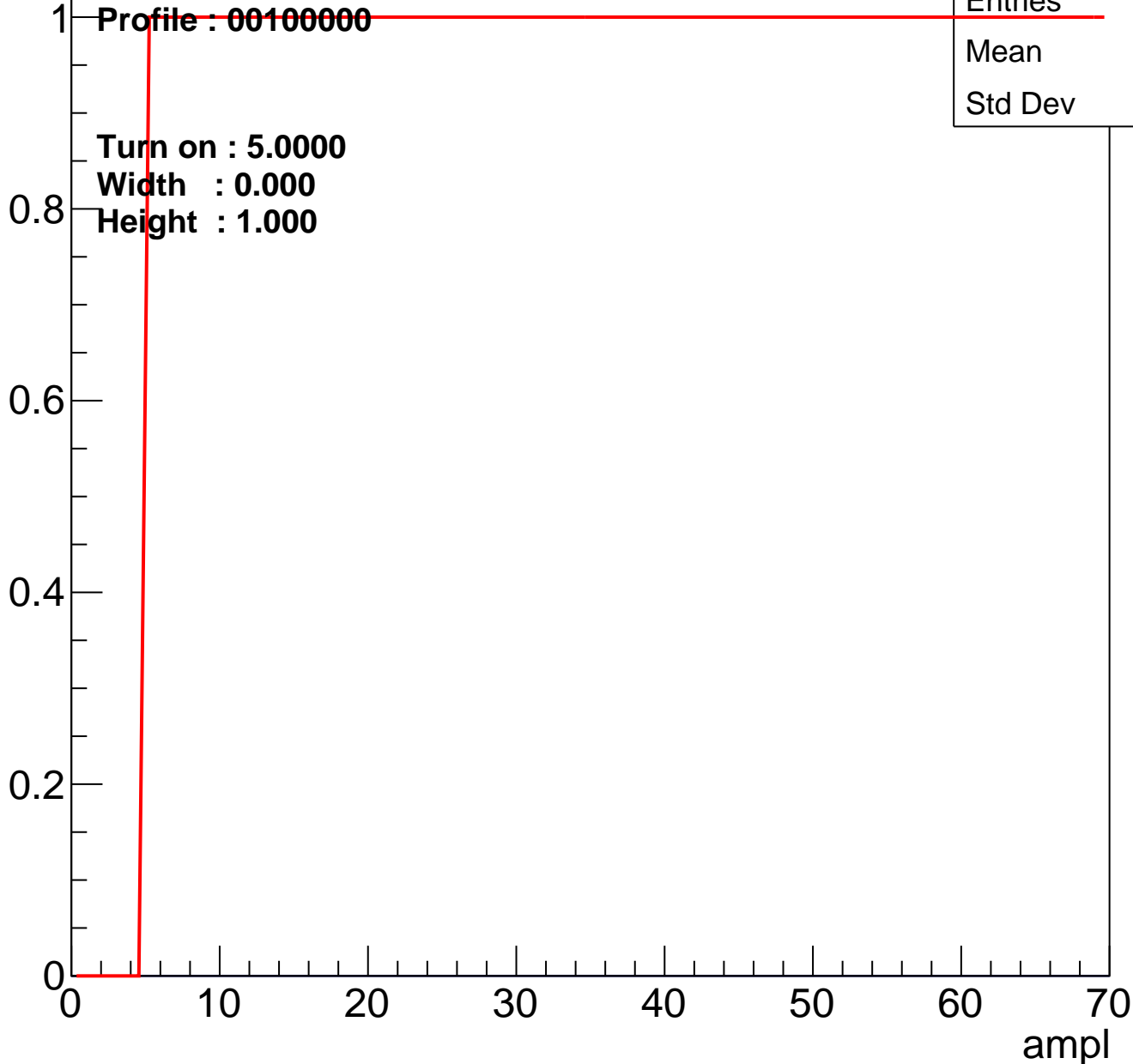


Entries	0
Mean	0
Std Dev	0

B1L003S, U15-ch22

calib_packv5_042523_0143.root, FC#13, Port D2

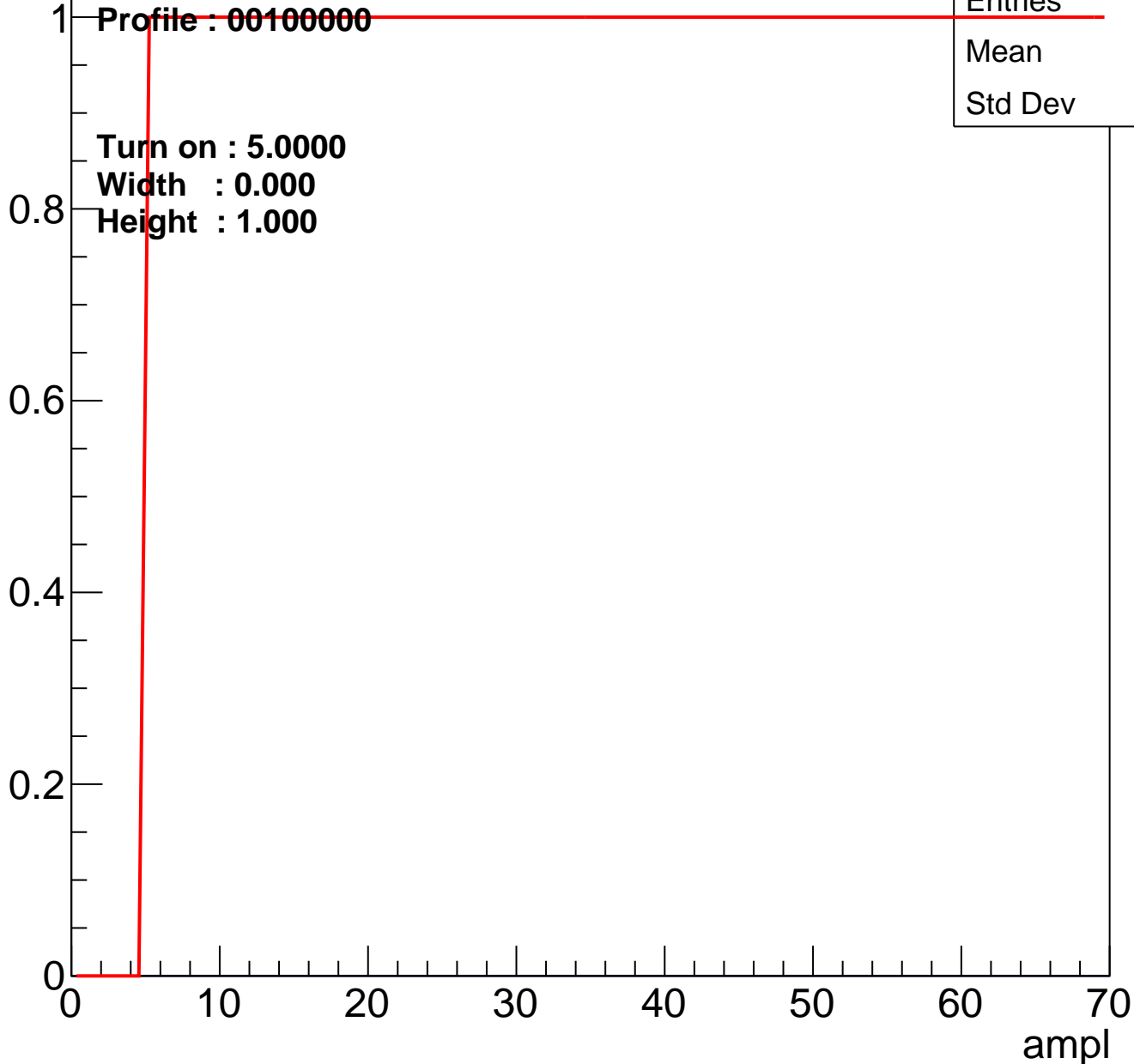
Entry



B1L003S, U15-ch23

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U15-ch24

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

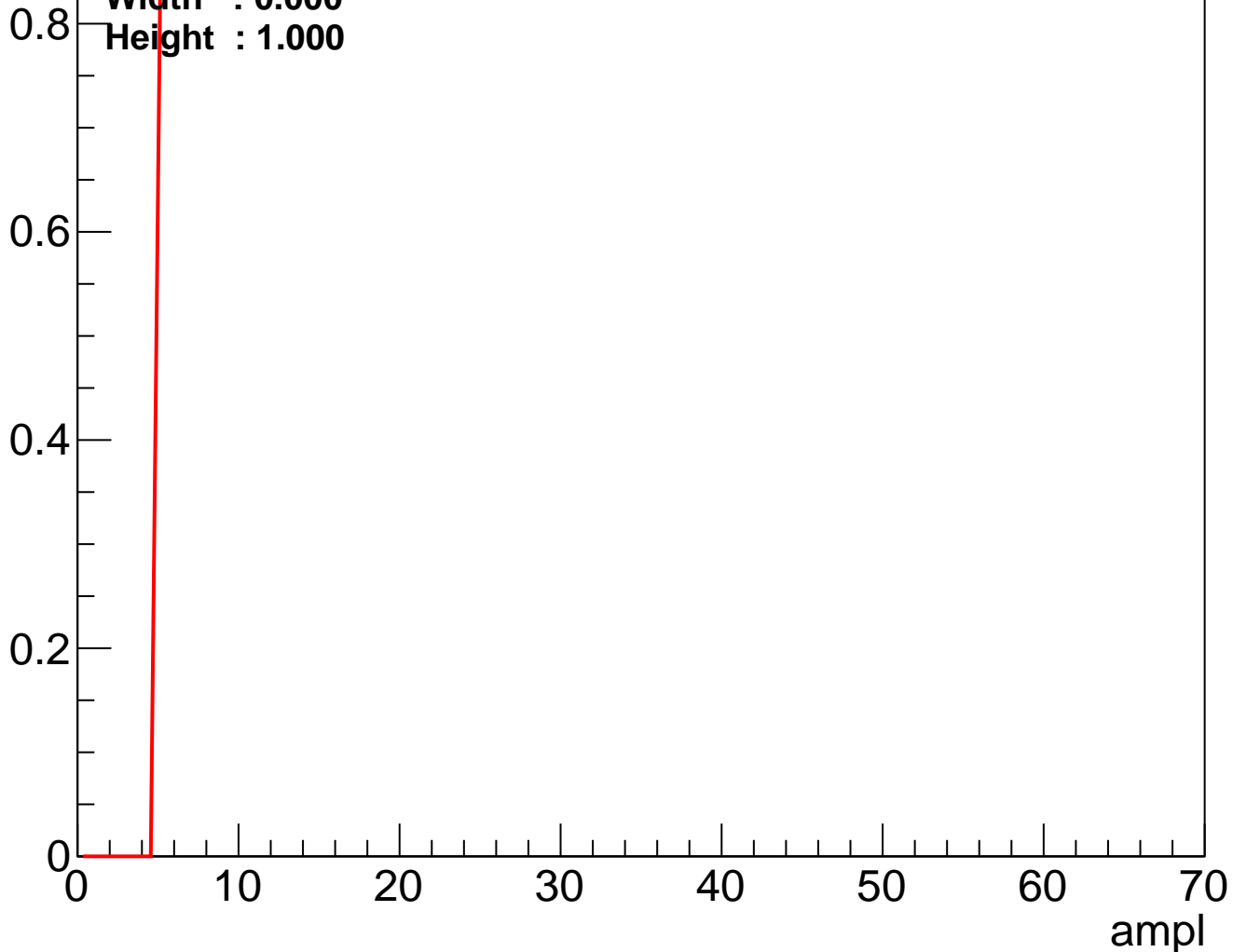
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

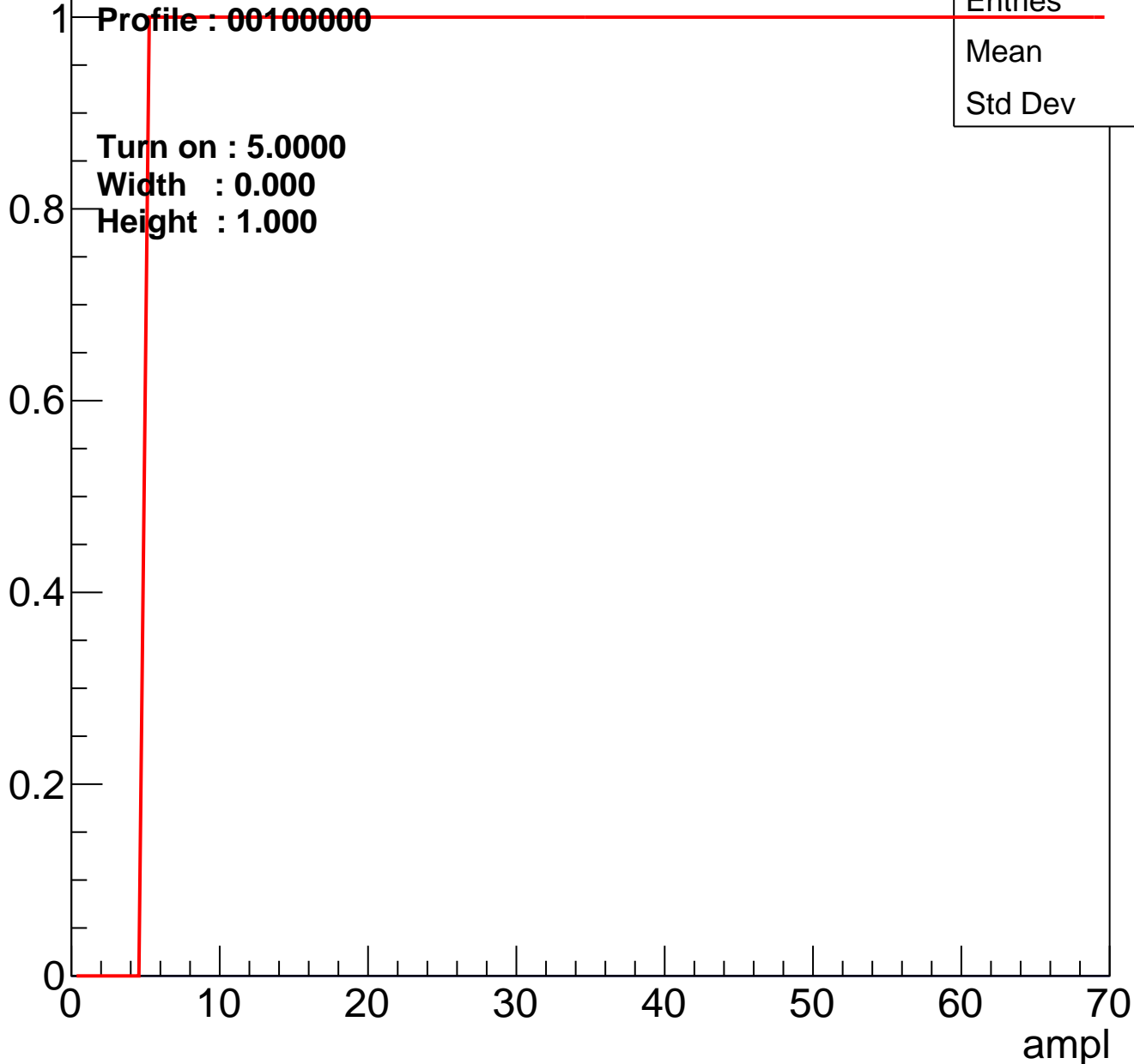
Entries	0
Mean	0
Std Dev	0



B1L003S, U15-ch25

calib_packv5_042523_0143.root, FC#13, Port D2

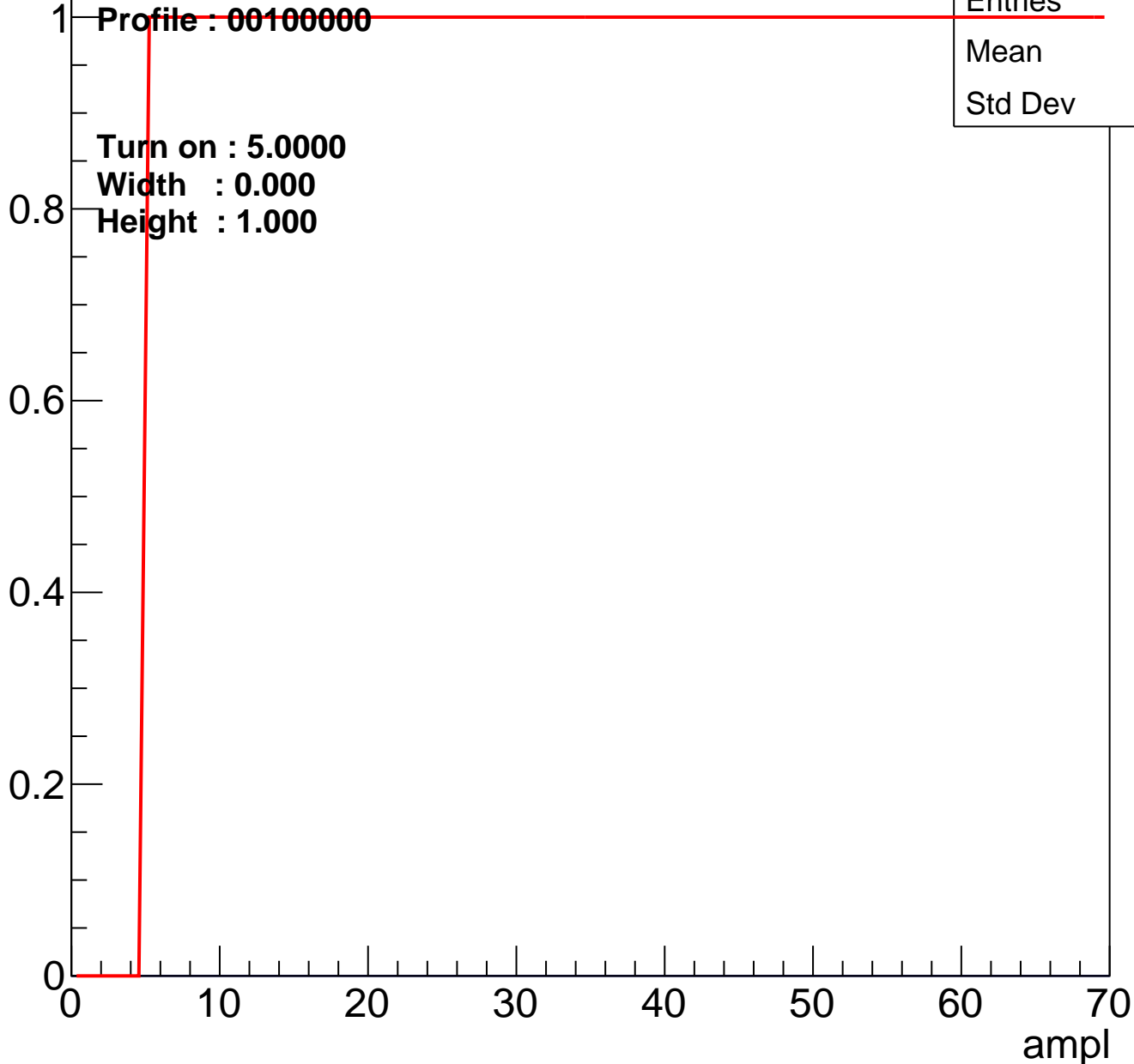
Entry



B1L003S, U15-ch26

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

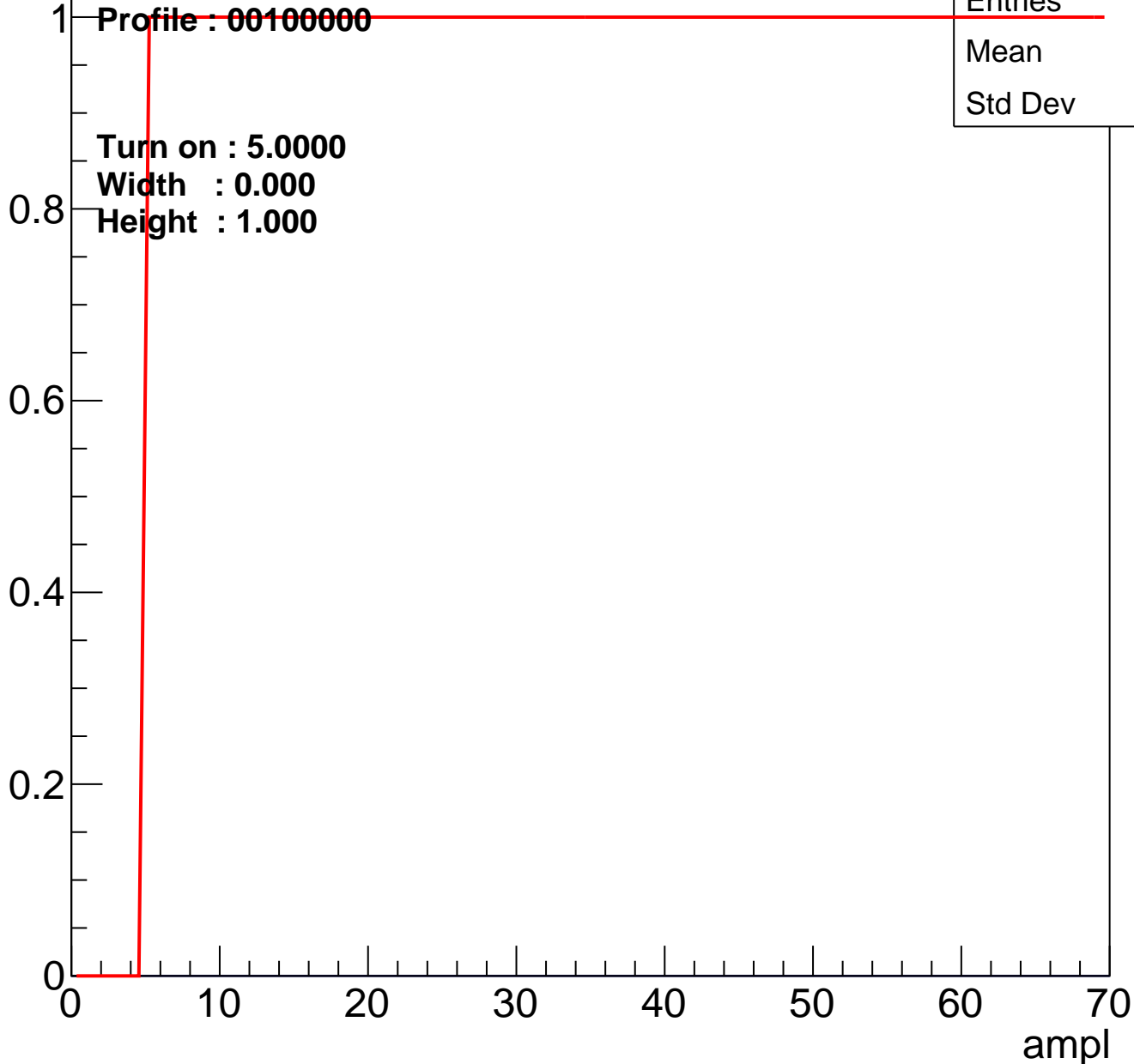


Entries	0
Mean	0
Std Dev	0

B1L003S, U15-ch27

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

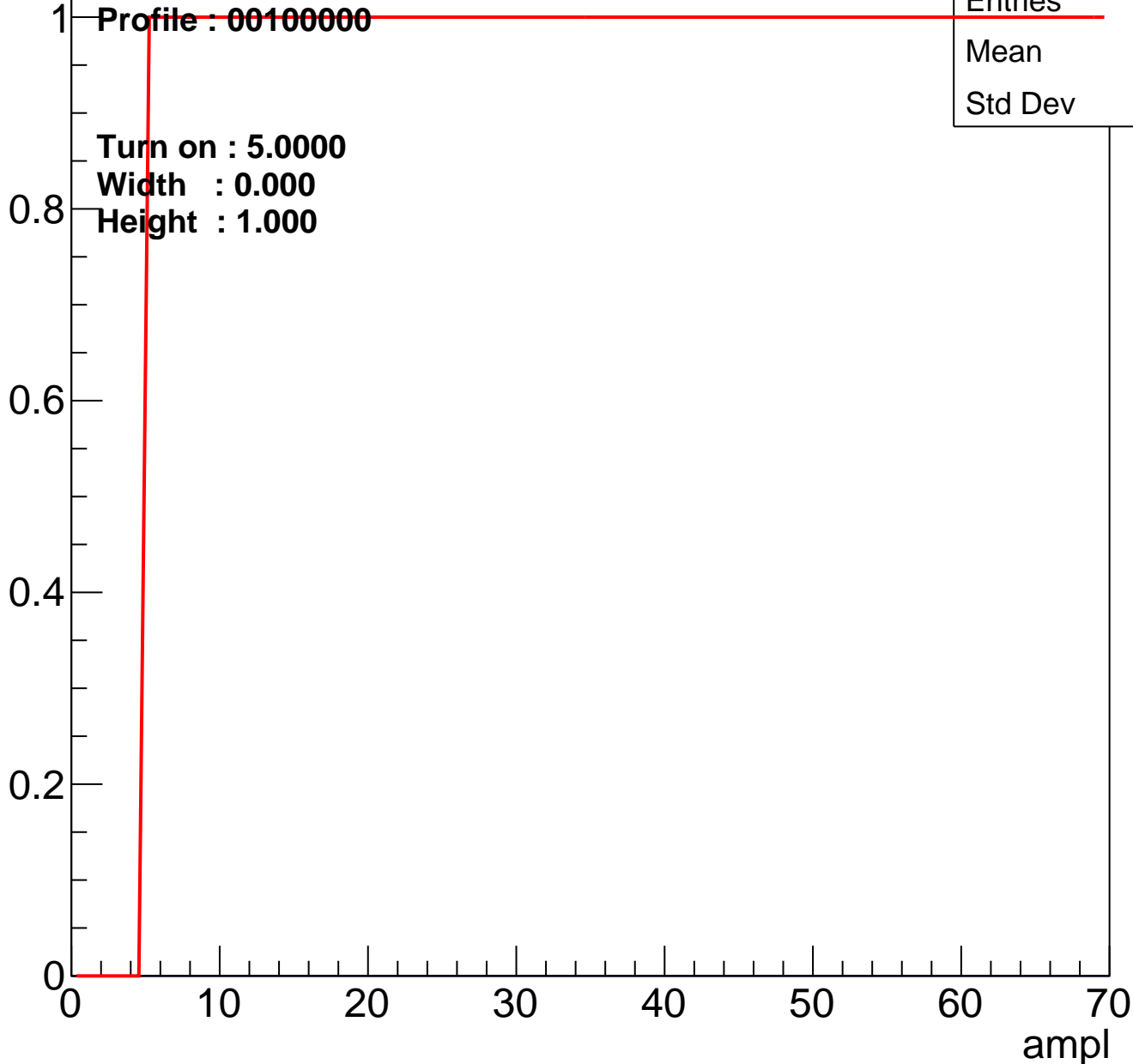


Entries	0
Mean	0
Std Dev	0

B1L003S, U15-ch28

calib_packv5_042523_0143.root, FC#13, Port D2

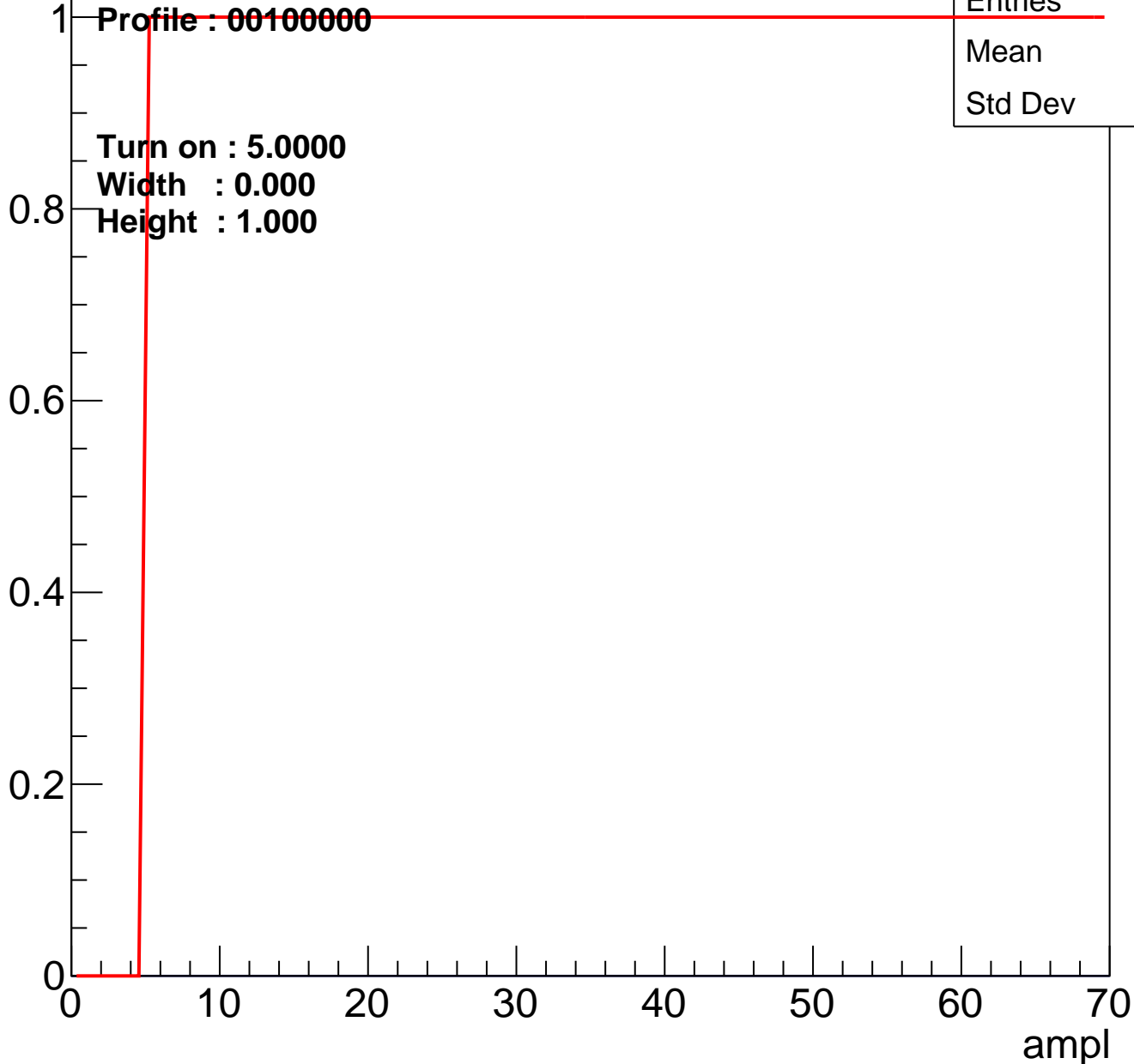
Entry



B1L003S, U15-ch29

calib_packv5_042523_0143.root, FC#13, Port D2

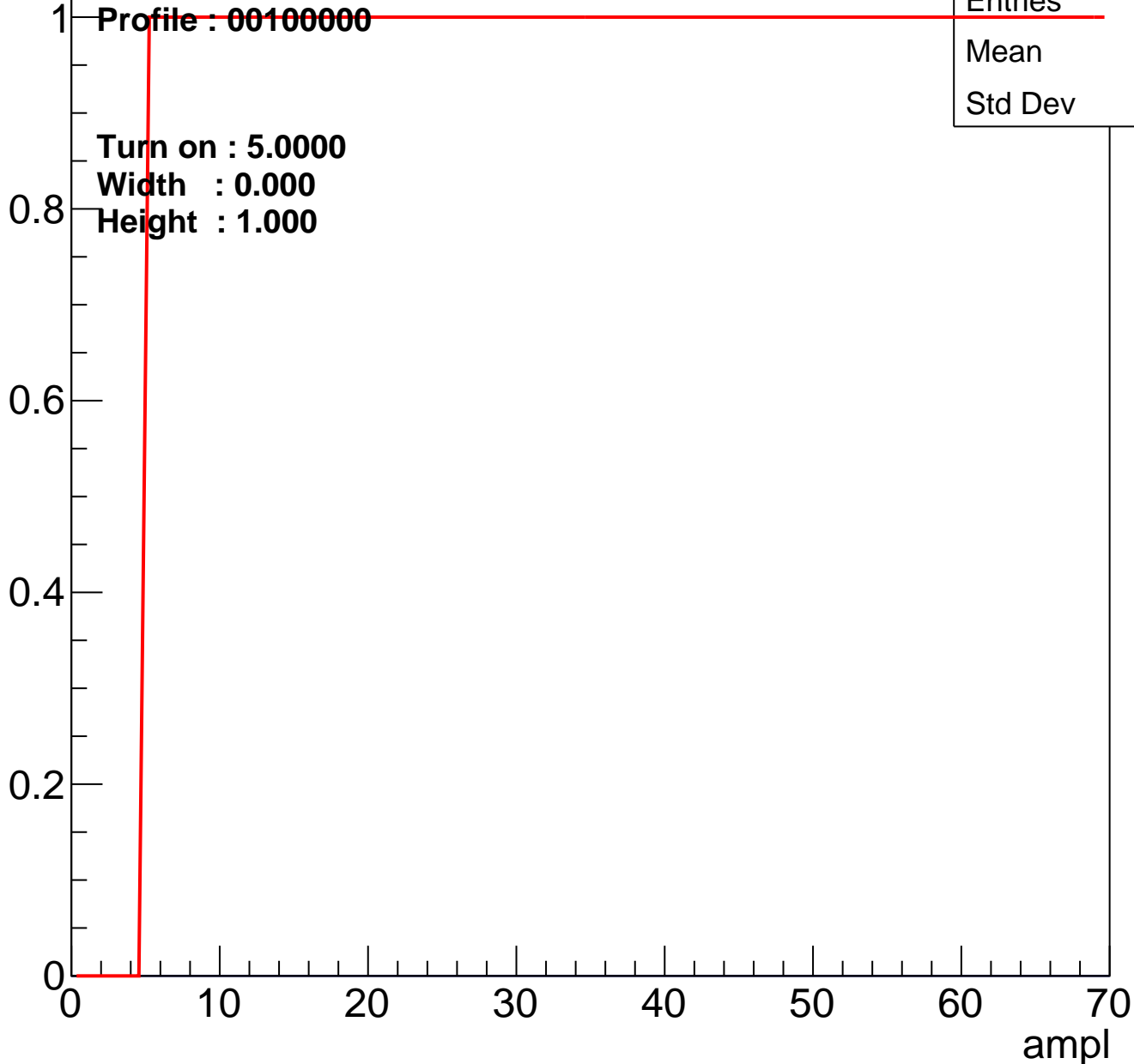
Entry



B1L003S, U15-ch30

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

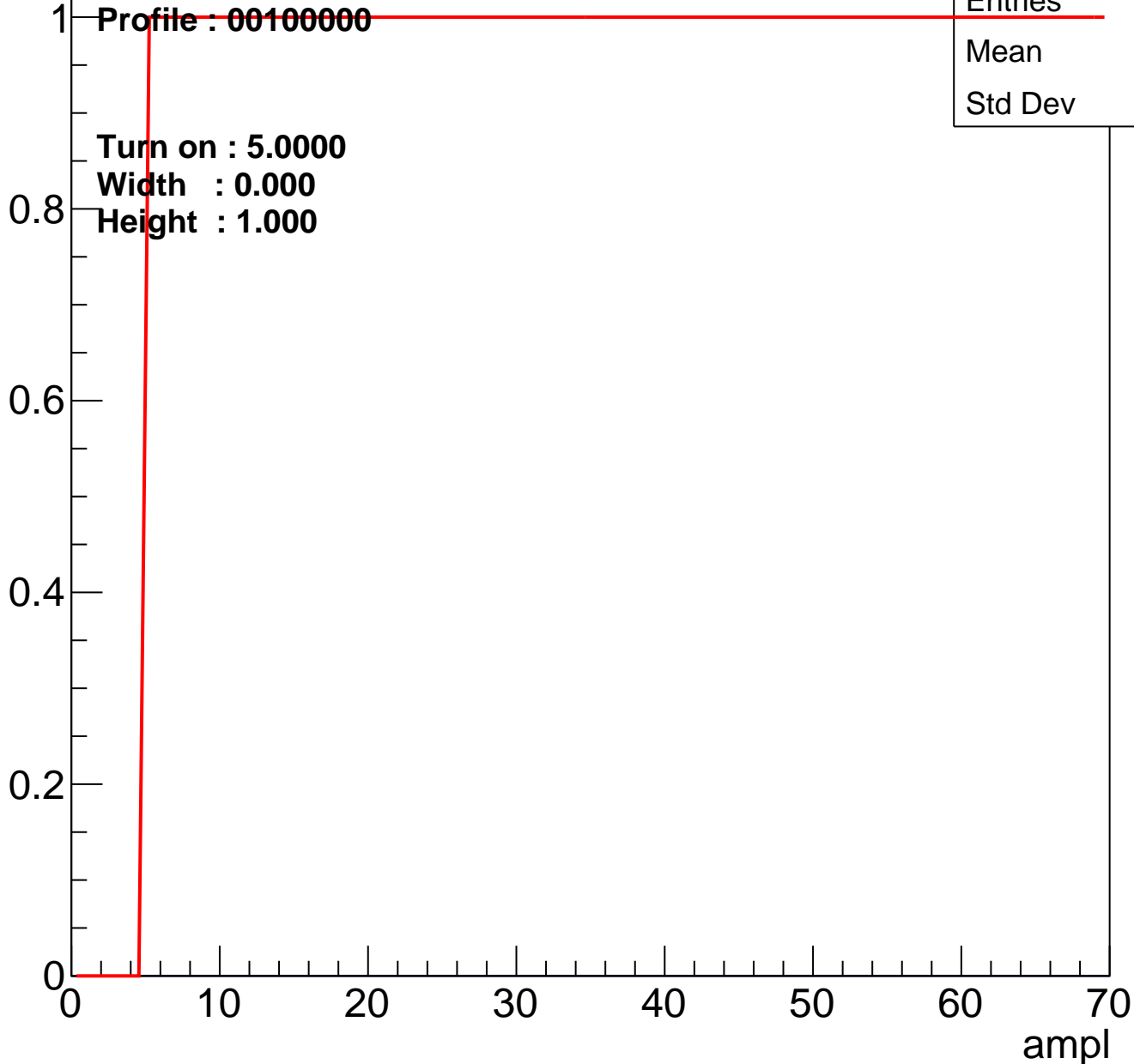


Entries	0
Mean	0
Std Dev	0

B1L003S, U15-ch31

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U17-ch0

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

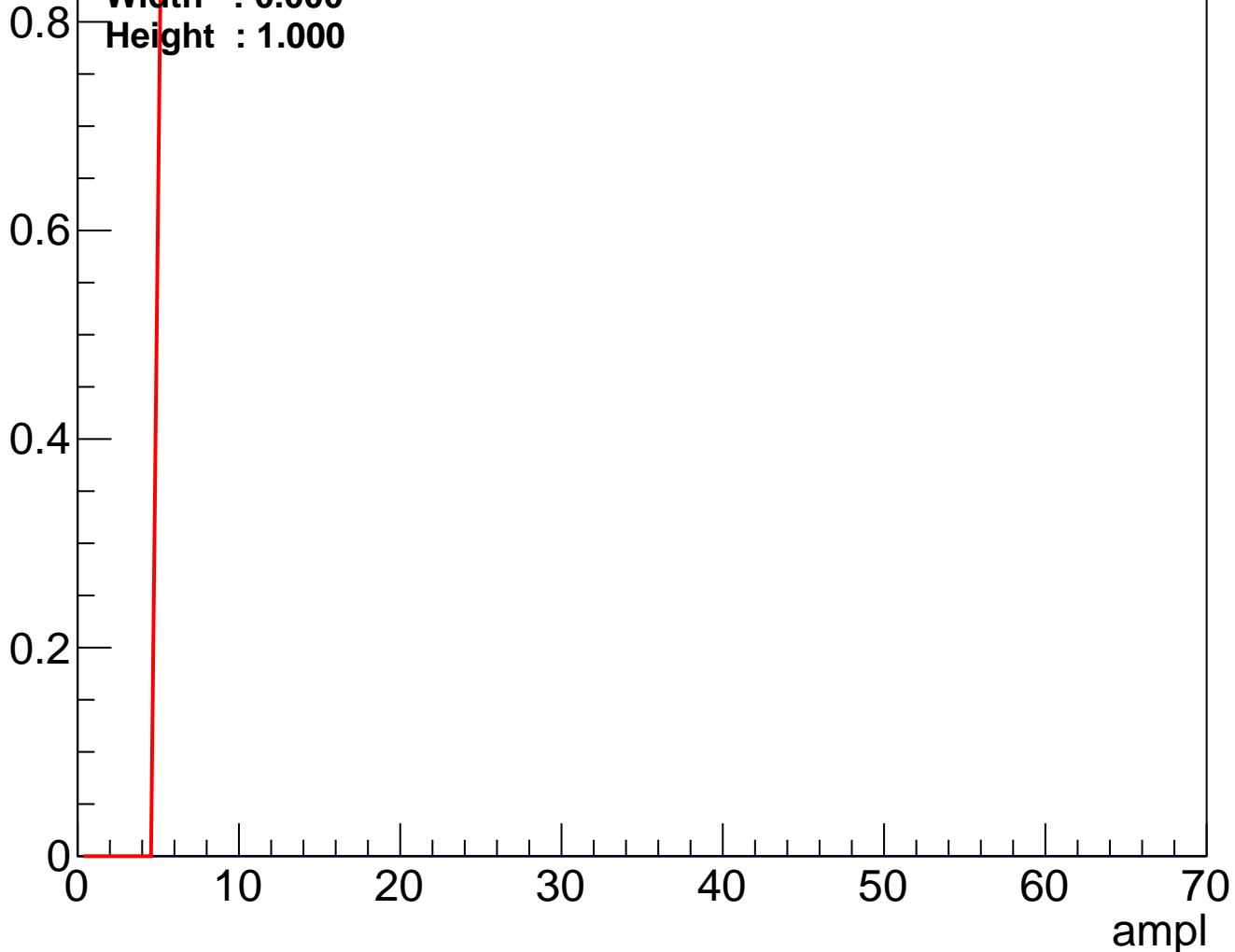
Profile : 00100000

Turn on : 5.0000

Width : 0.000

Height : 1.000

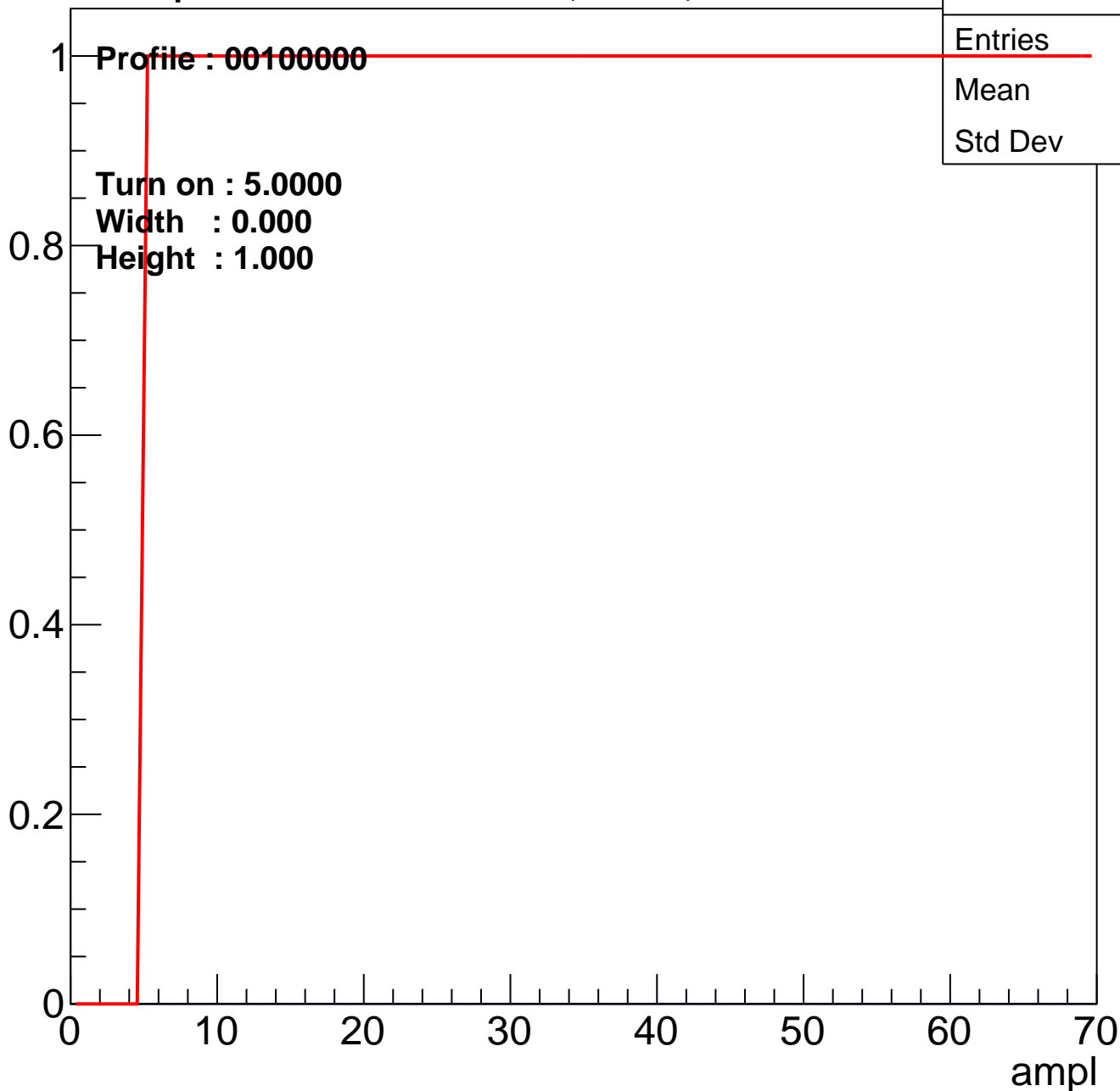
Entries	0
Mean	0
Std Dev	0



B1L003S, U17-ch1

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

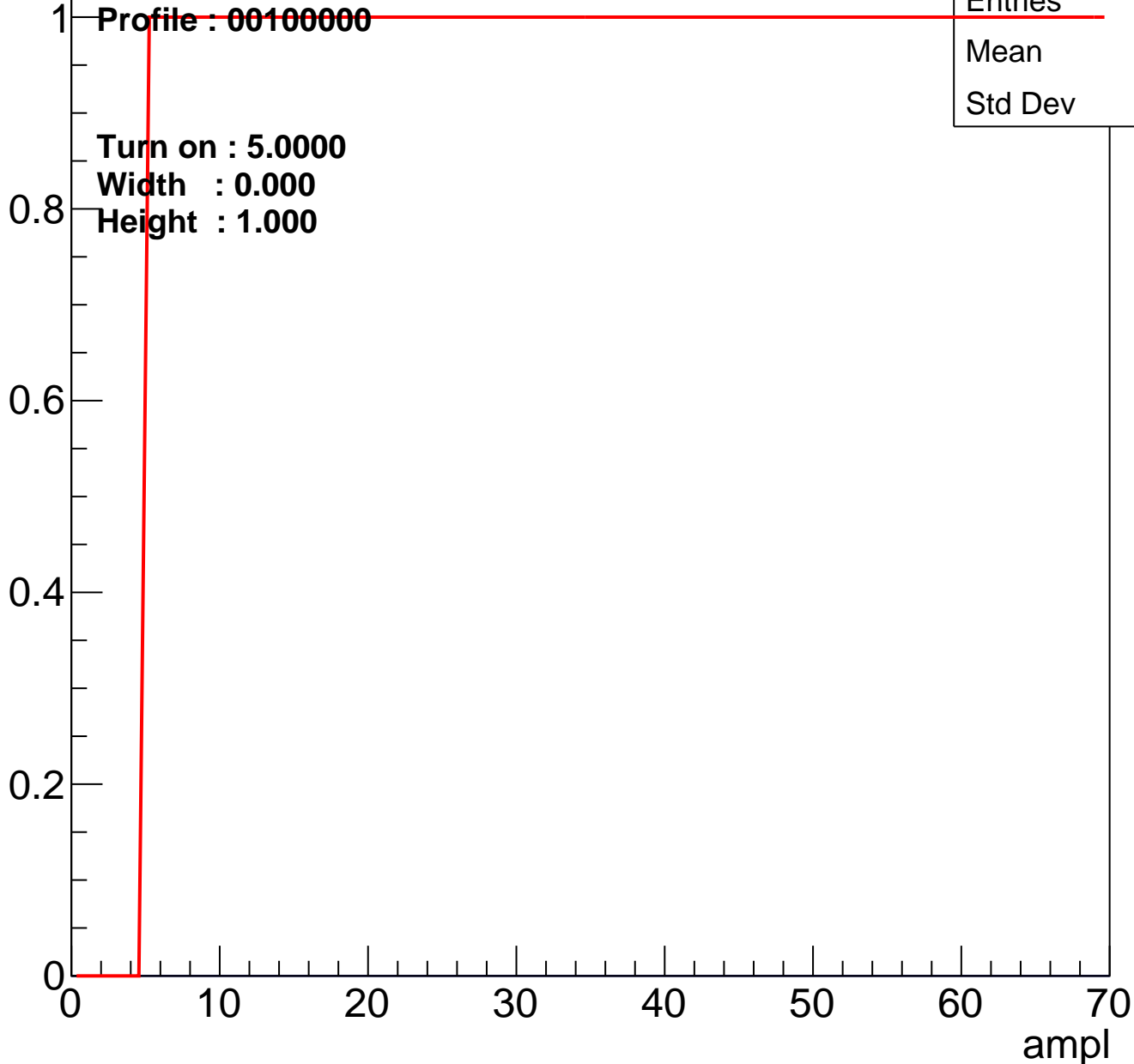


Entries	0
Mean	0
Std Dev	0

B1L003S, U17-ch2

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U17-ch3

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

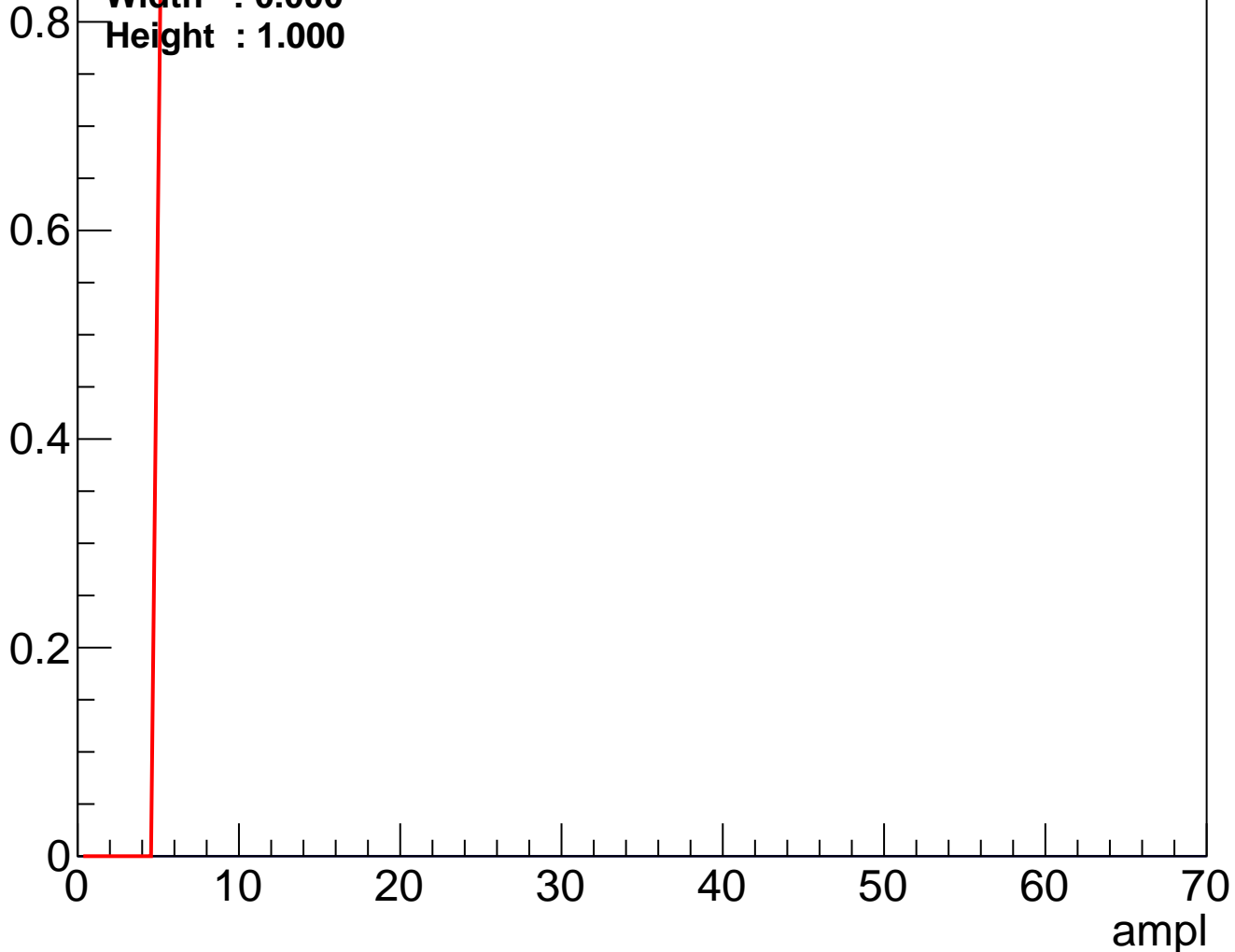
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

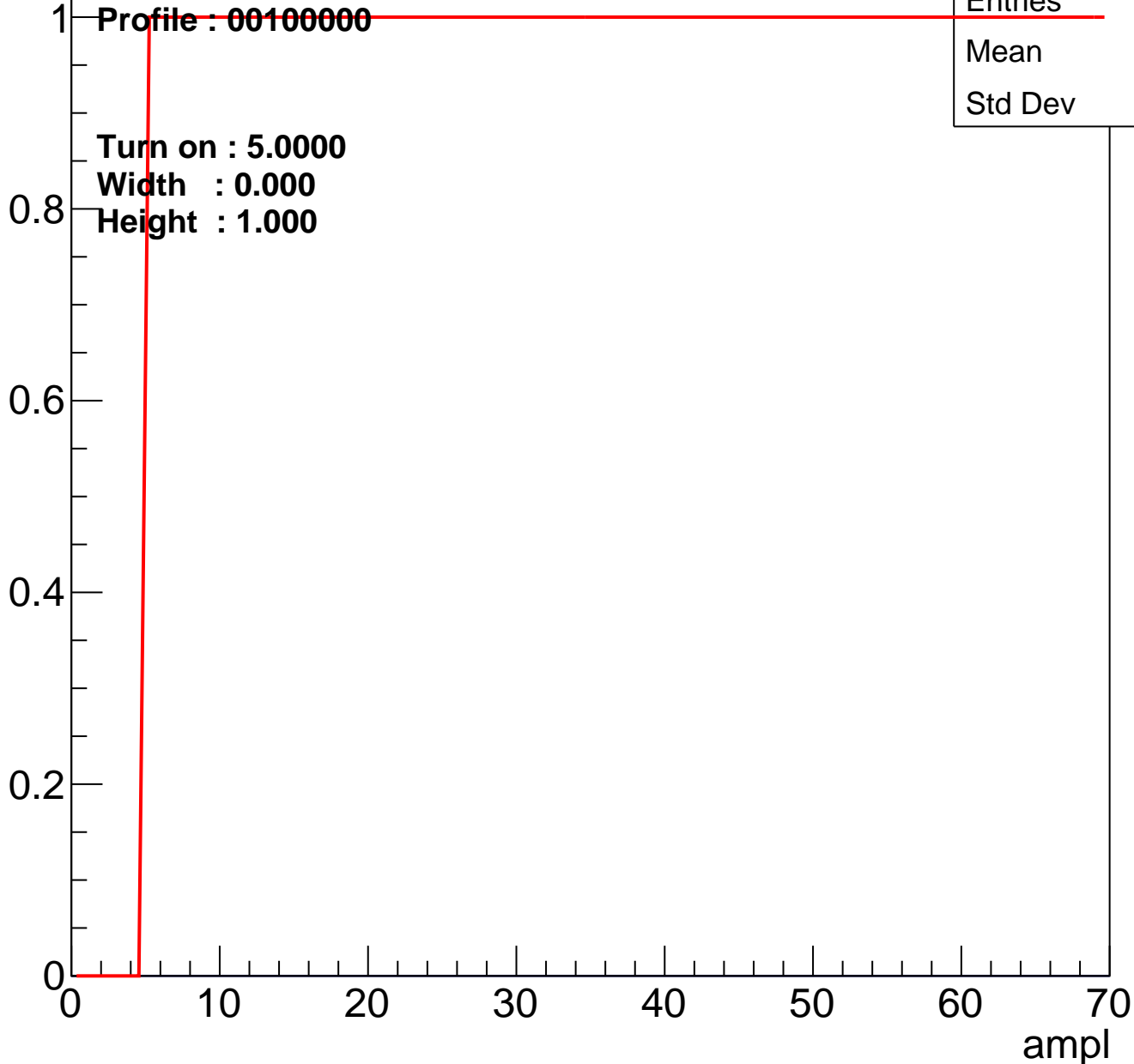
Entries	0
Mean	0
Std Dev	0



B1L003S, U17-ch4

calib_packv5_042523_0143.root, FC#13, Port D2

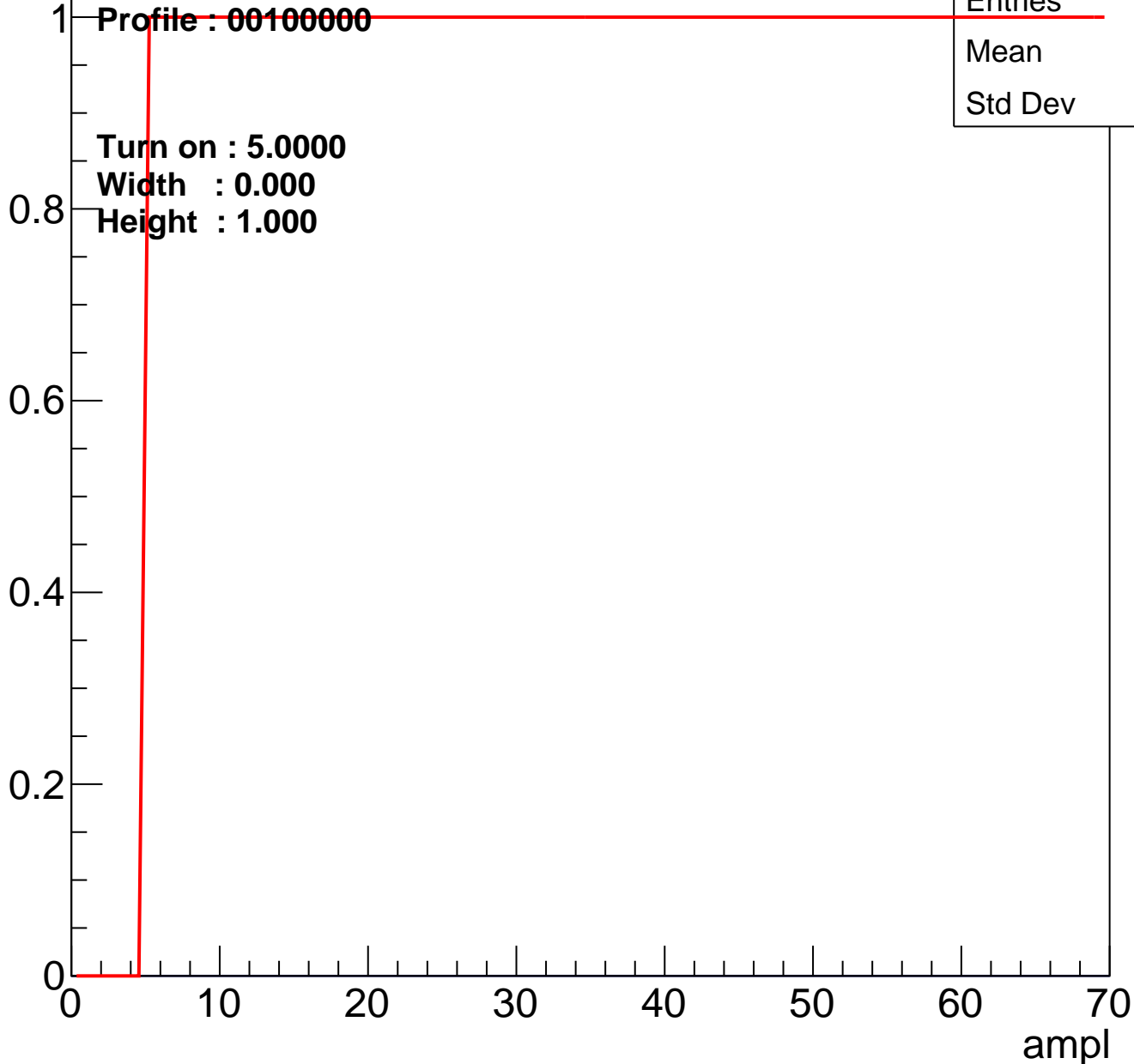
Entry



B1L003S, U17-ch5

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U17-ch6

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

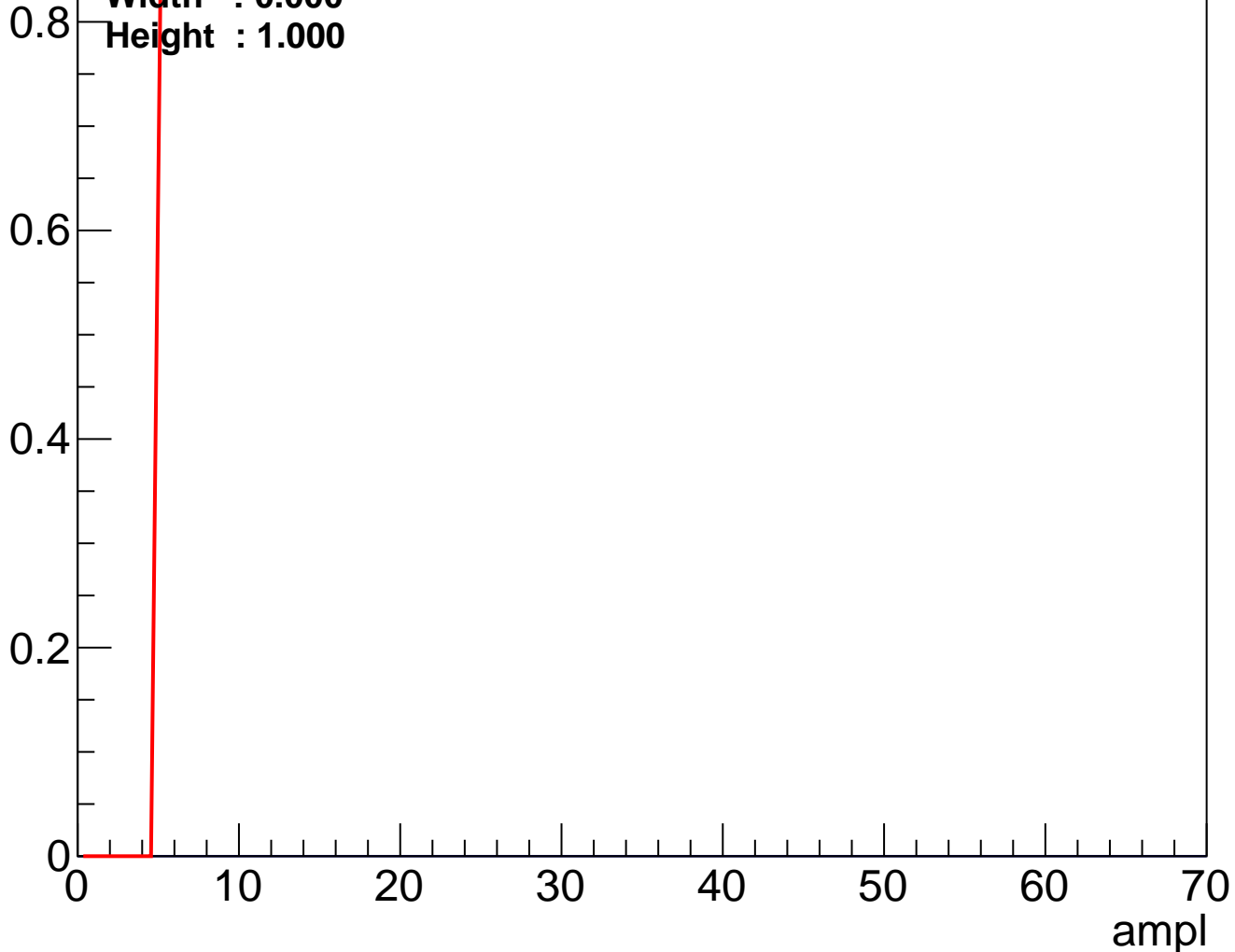
Profile : 00100000

Turn on : 5.0000

Width : 0.000

Height : 1.000

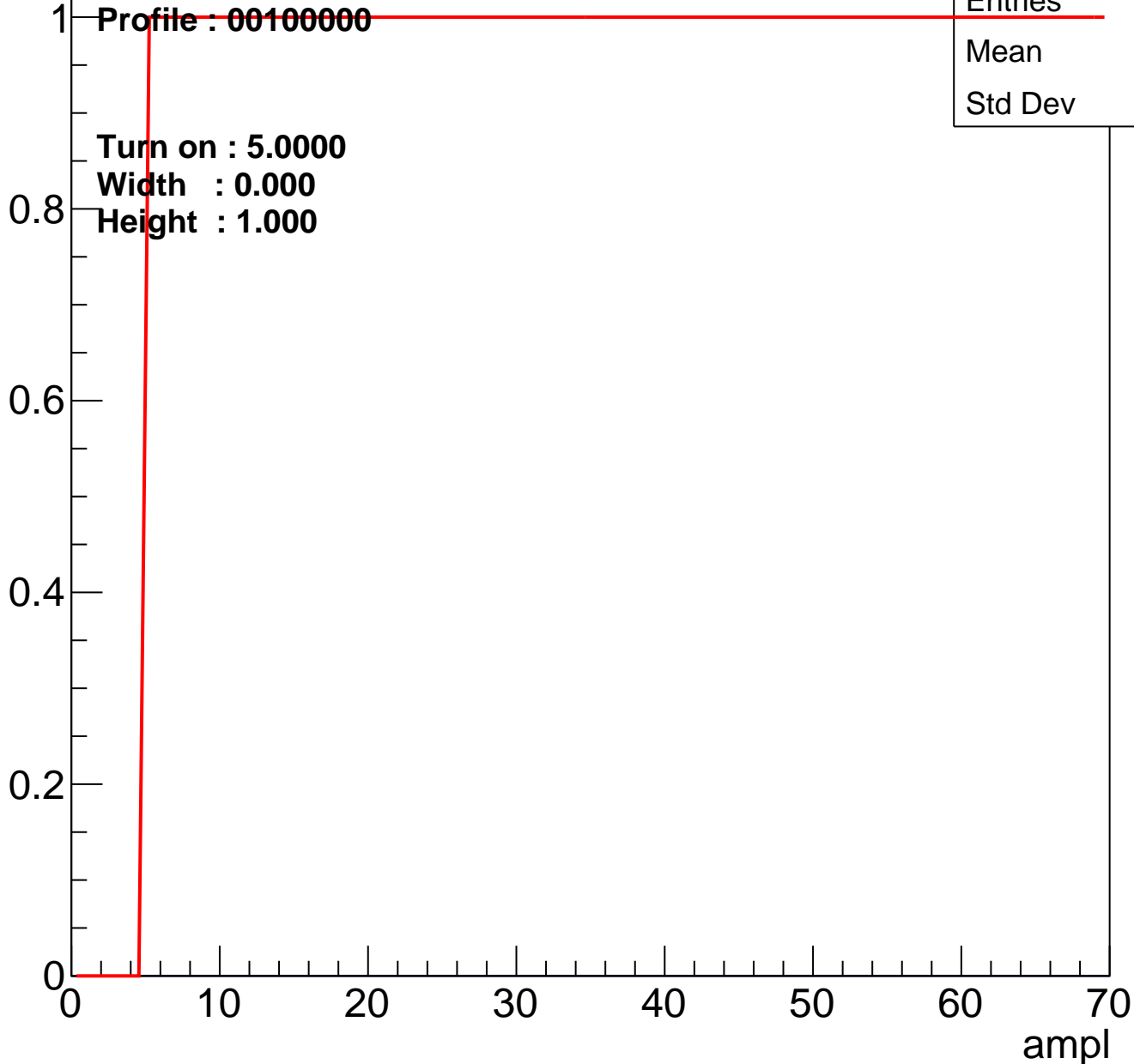
Entries	0
Mean	0
Std Dev	0



B1L003S, U17-ch7

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

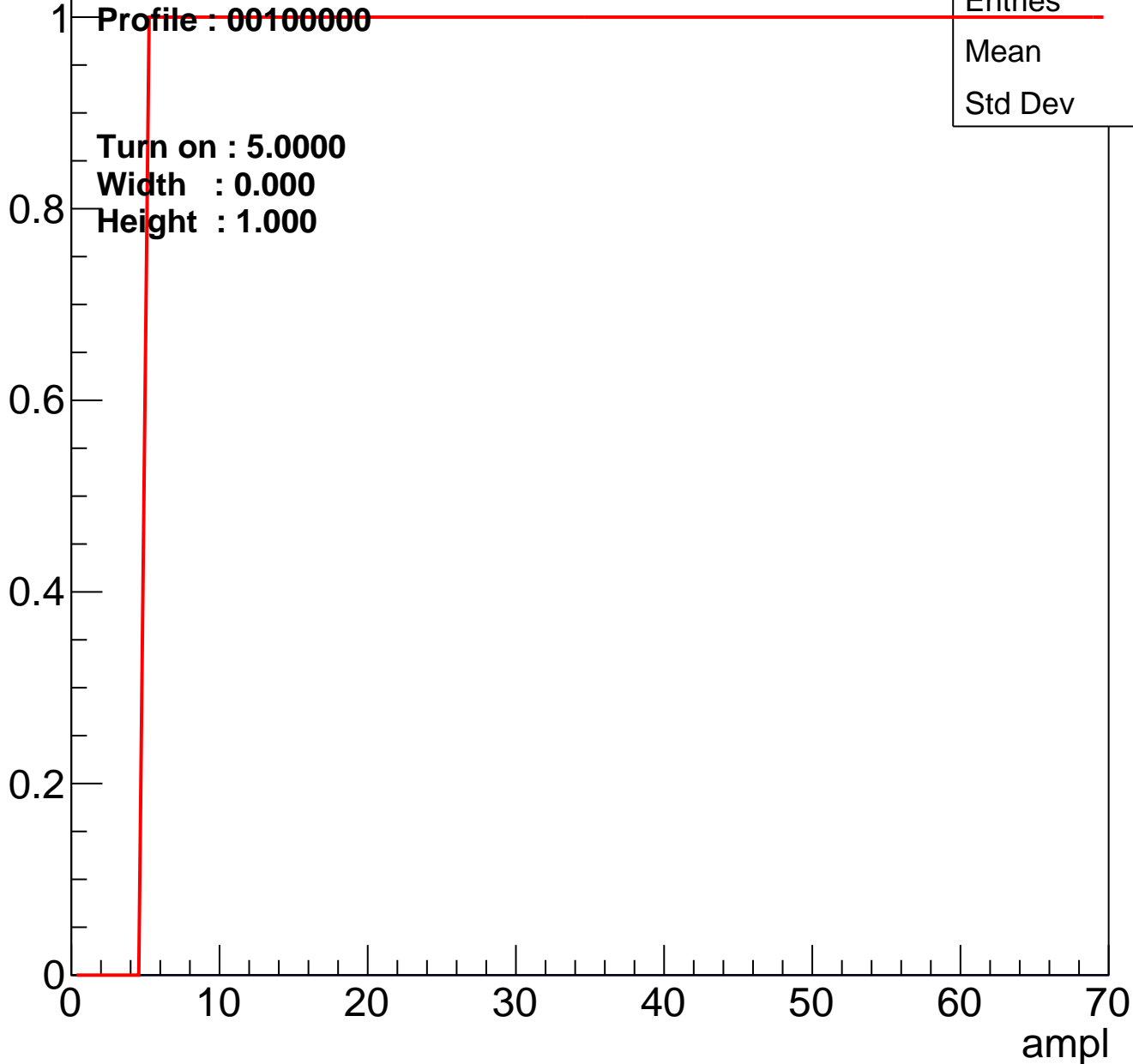


Entries	0
Mean	0
Std Dev	0

B1L003S, U17-ch8

calib_packv5_042523_0143.root, FC#13, Port D2

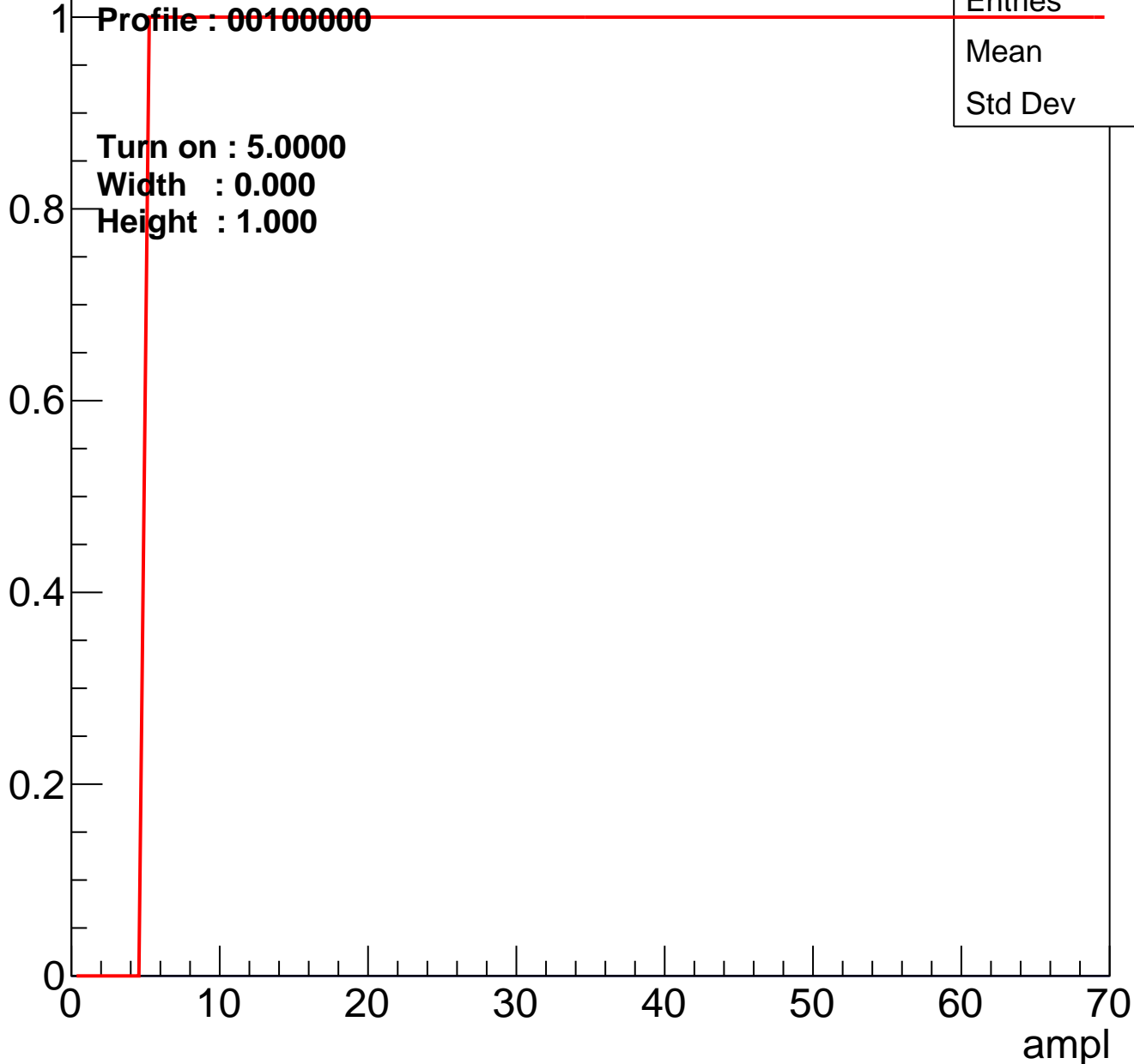
Entry



B1L003S, U17-ch9

calib_packv5_042523_0143.root, FC#13, Port D2

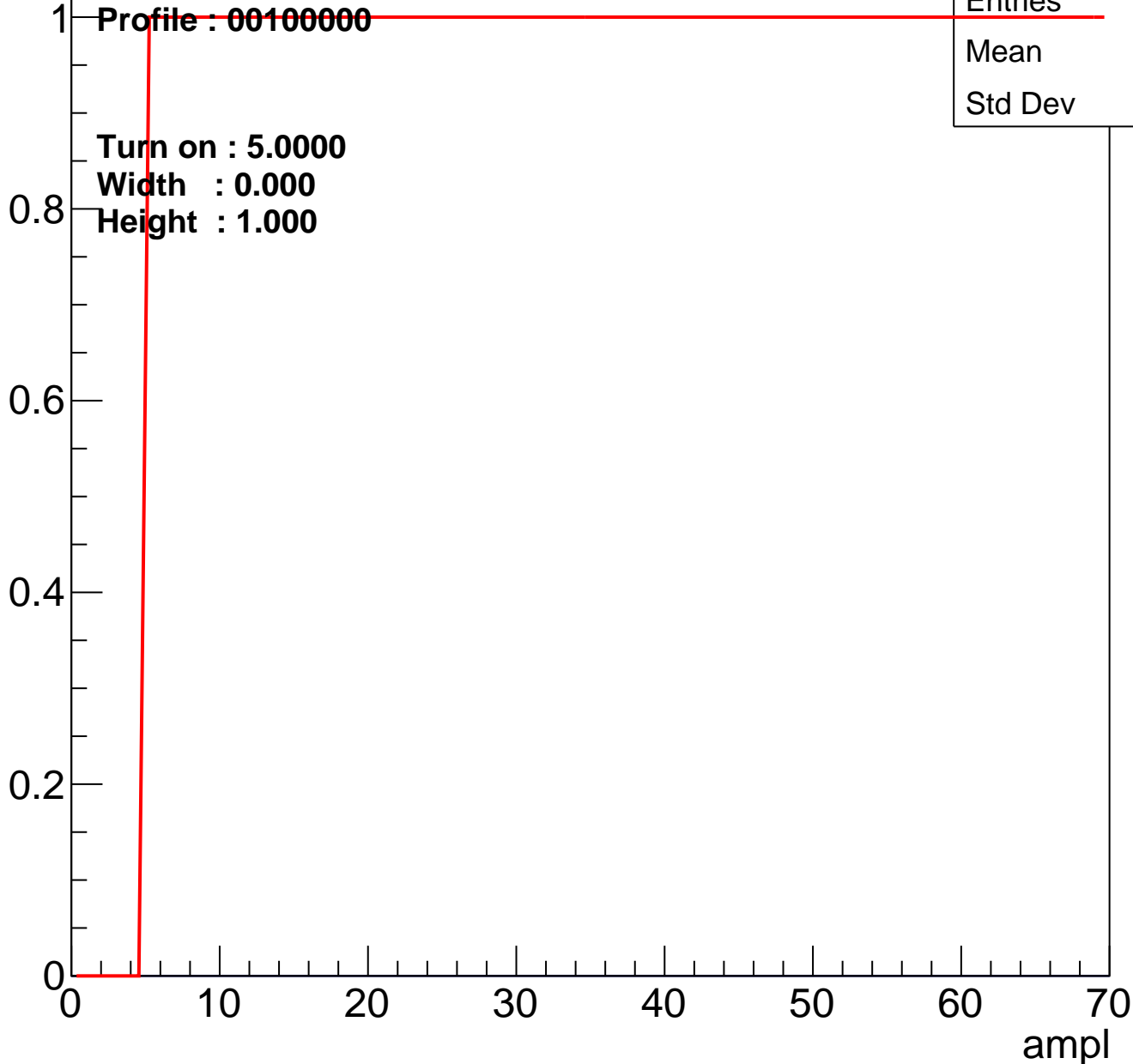
Entry



B1L003S, U17-ch10

calib_packv5_042523_0143.root, FC#13, Port D2

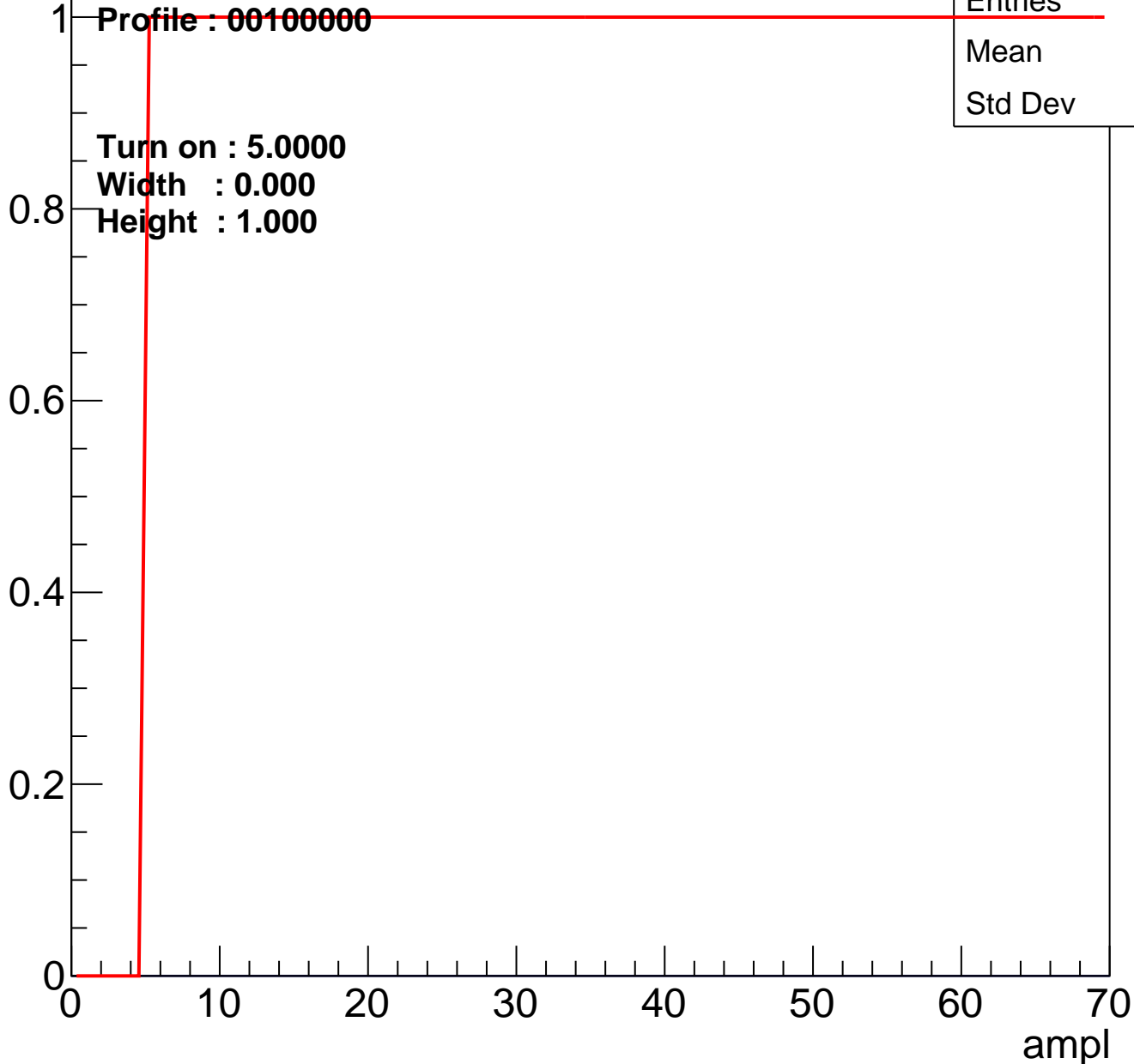
Entry



B1L003S, U17-ch11

calib_packv5_042523_0143.root, FC#13, Port D2

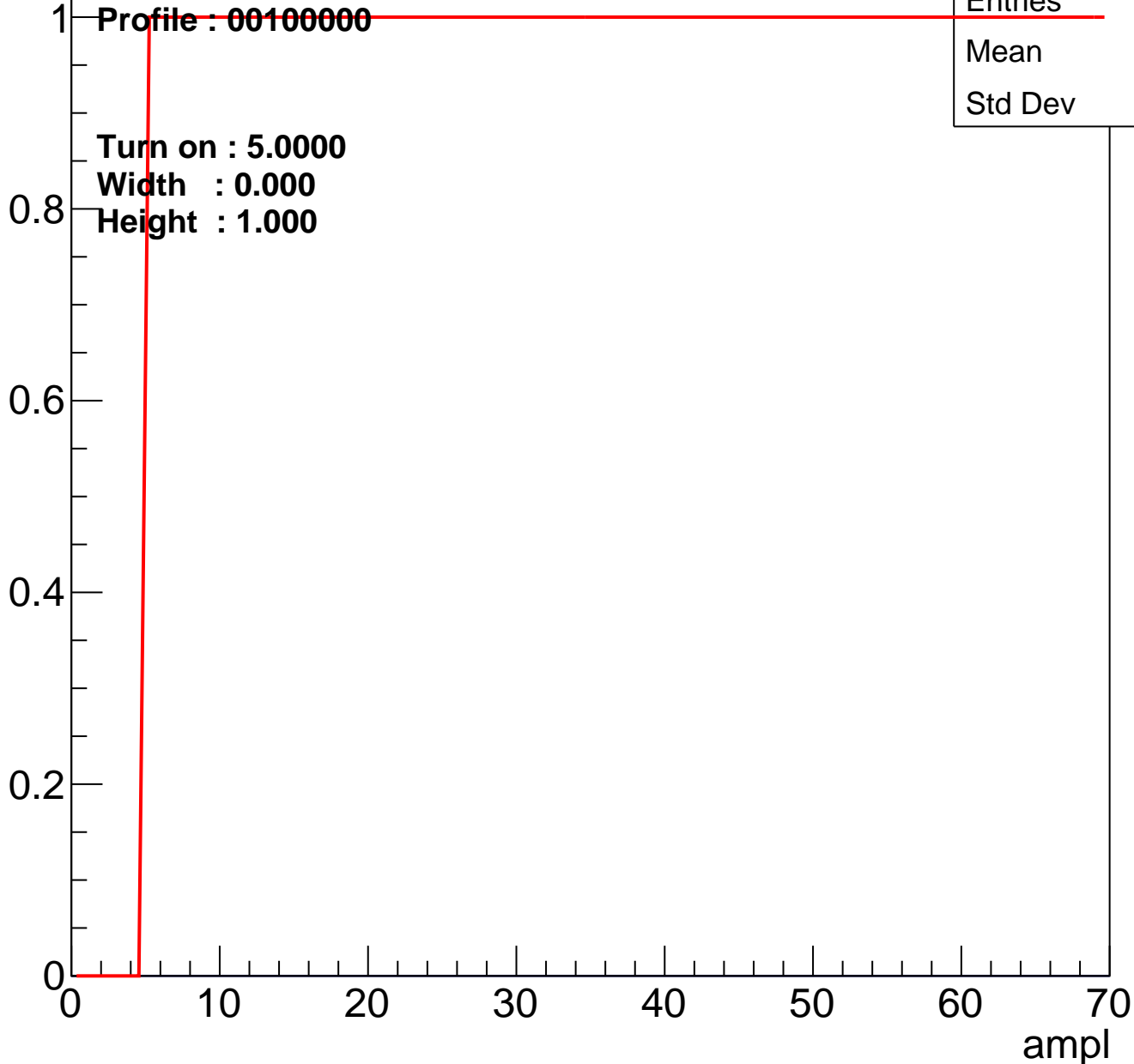
Entry



B1L003S, U17-ch12

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U17-ch13

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

Entries	0
Mean	0
Std Dev	0

0.8

0.6

0.4

0.2

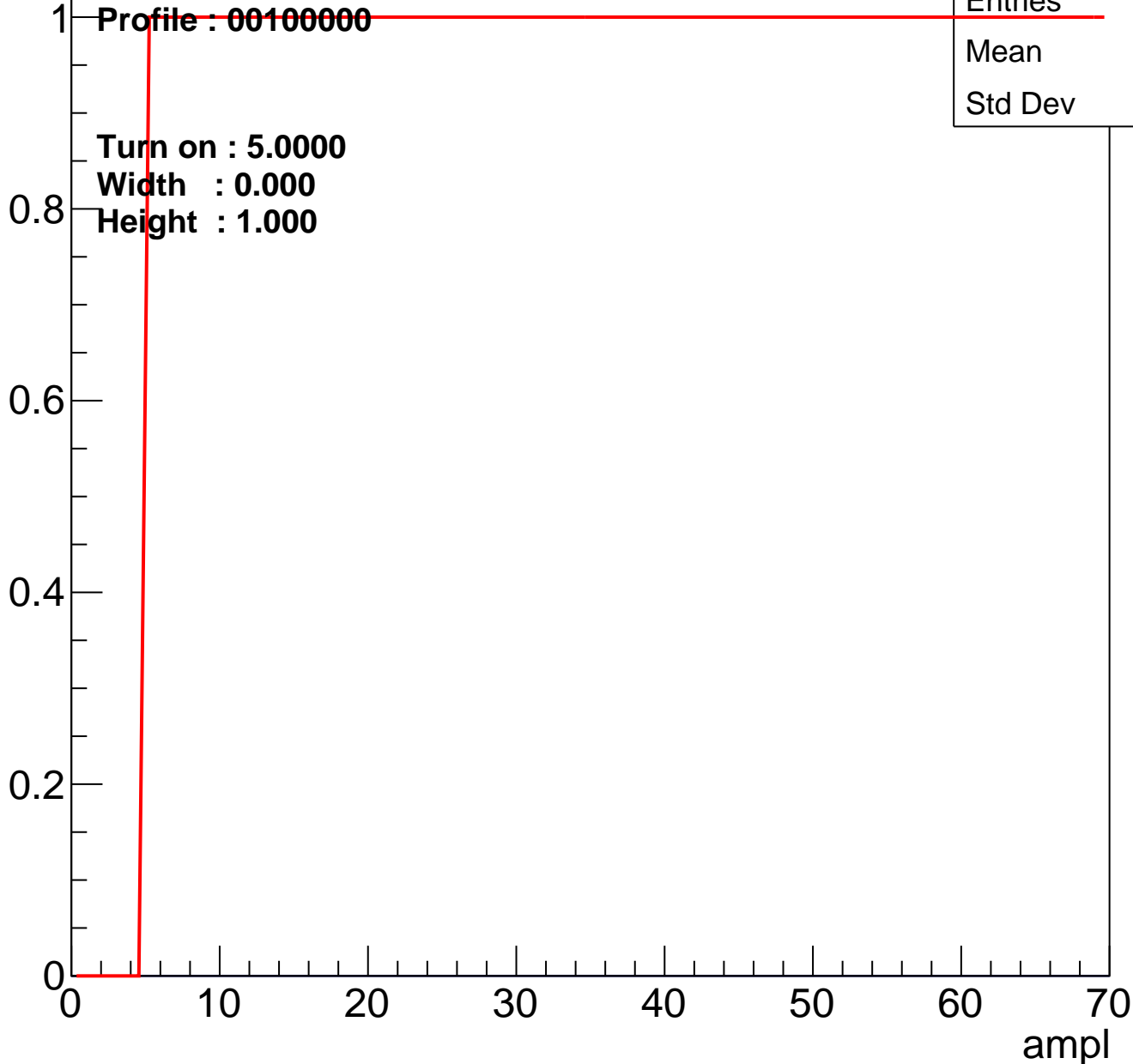
0

ampl

B1L003S, U17-ch14

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U17-ch15

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

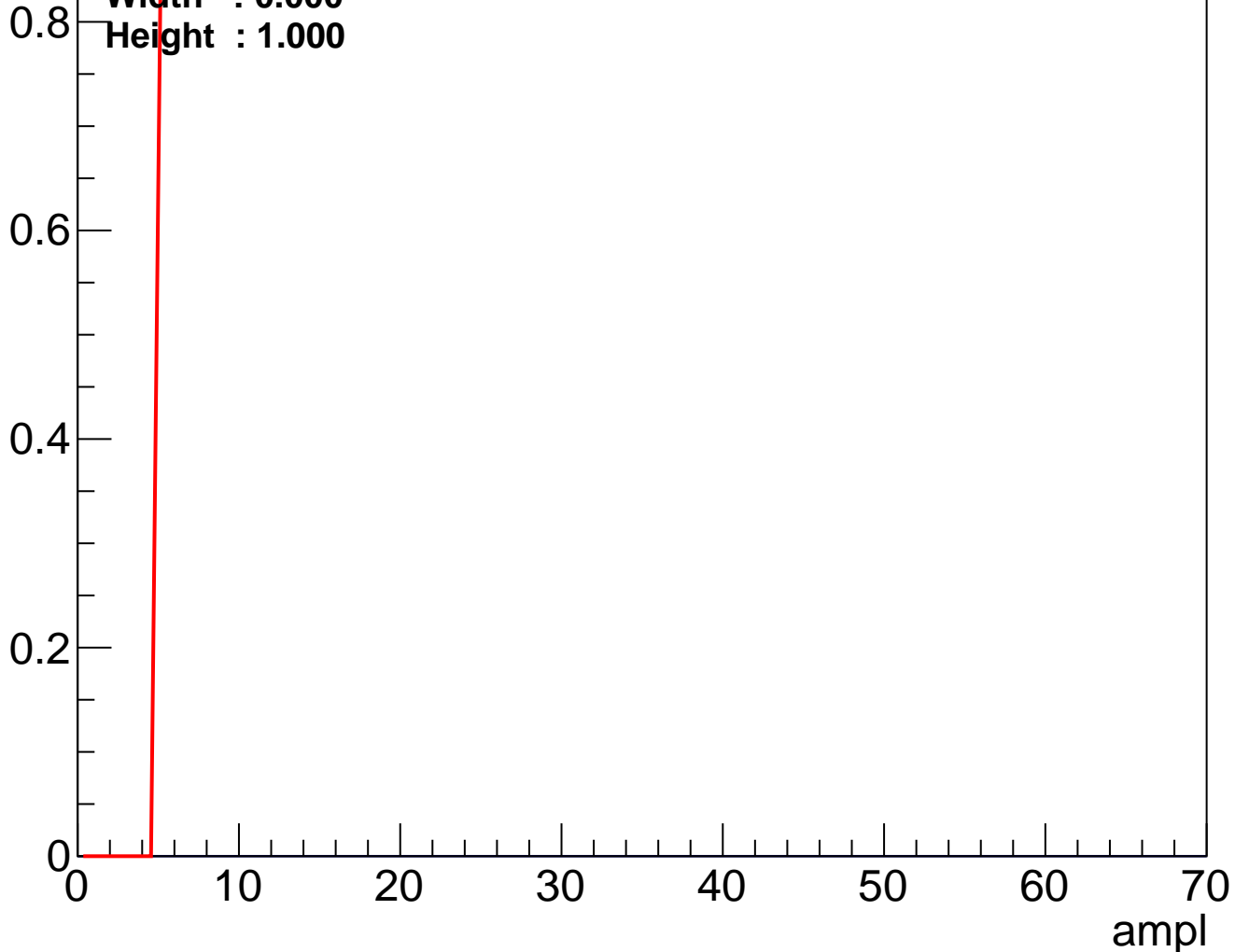
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

Entries	0
Mean	0
Std Dev	0



B1L003S, U17-ch16

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

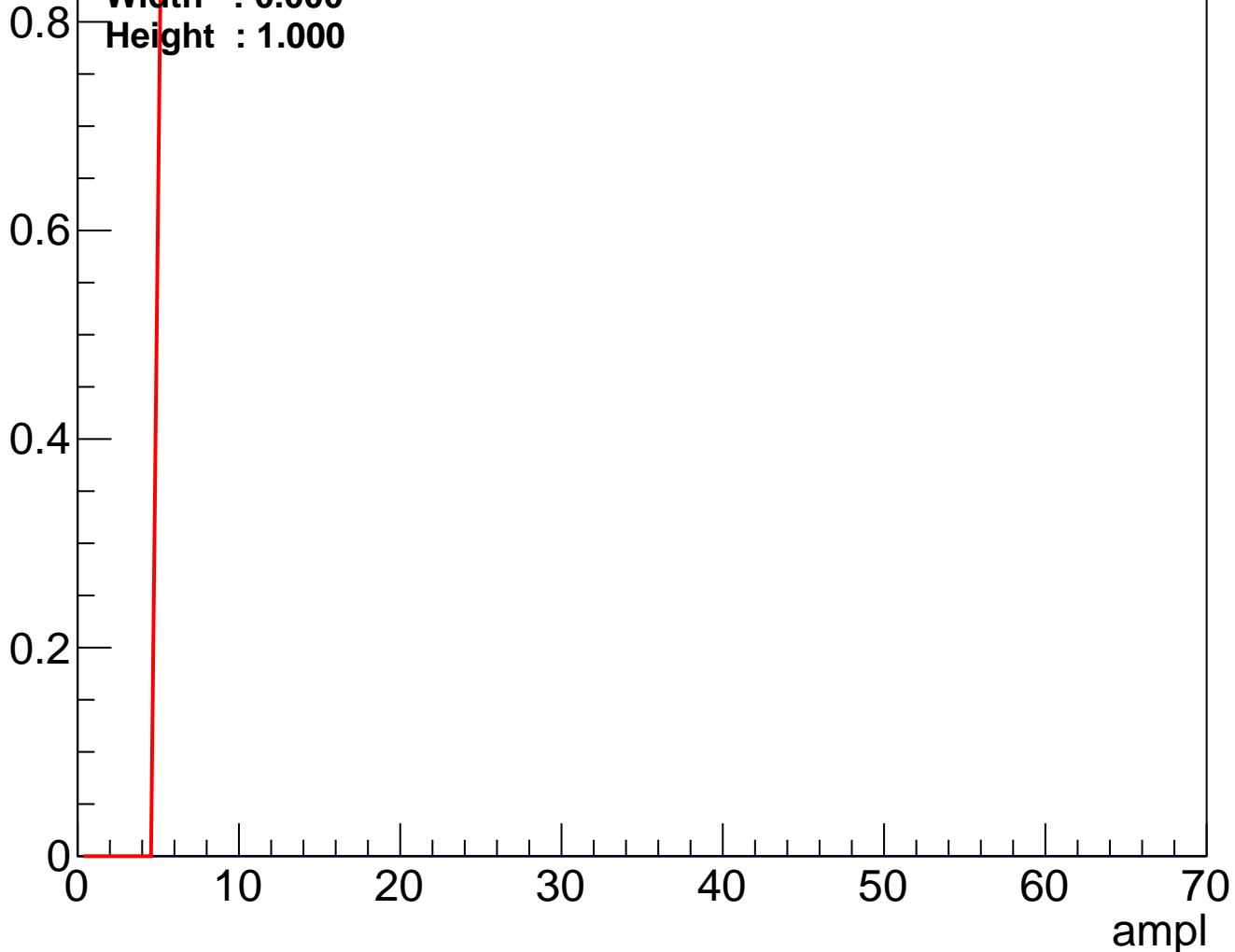
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

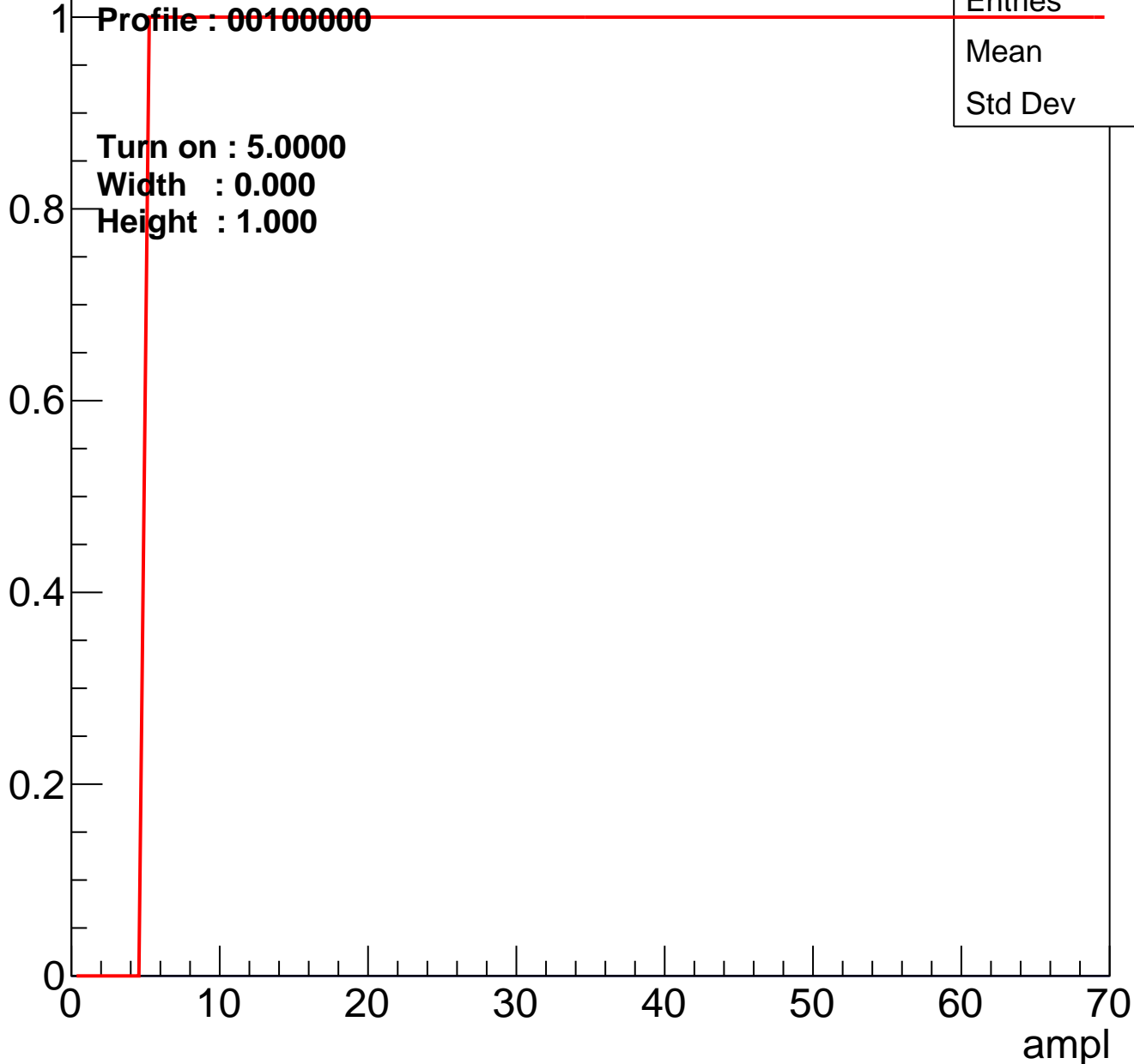
Entries	0
Mean	0
Std Dev	0



B1L003S, U17-ch17

calib_packv5_042523_0143.root, FC#13, Port D2

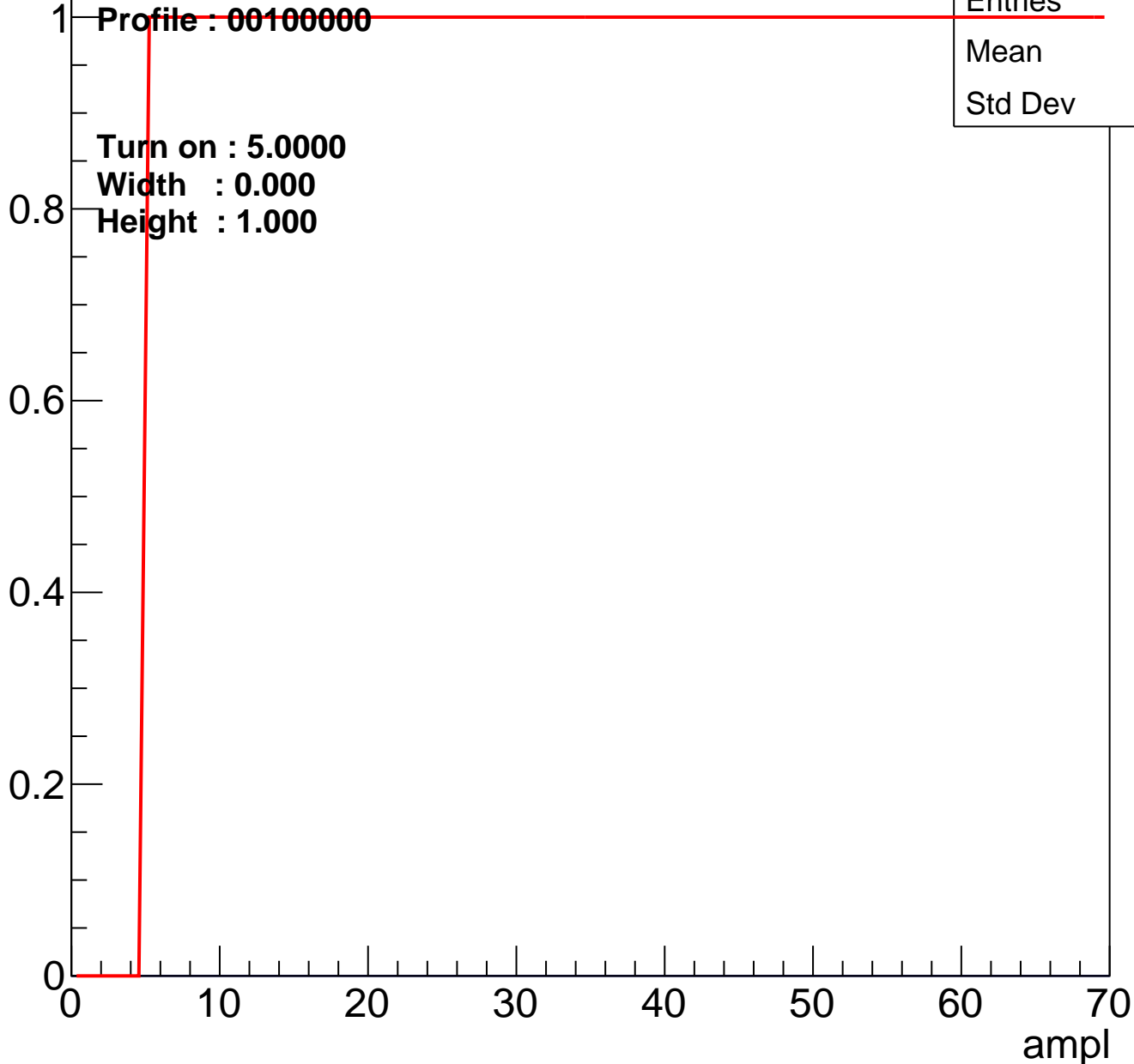
Entry



B1L003S, U17-ch18

calib_packv5_042523_0143.root, FC#13, Port D2

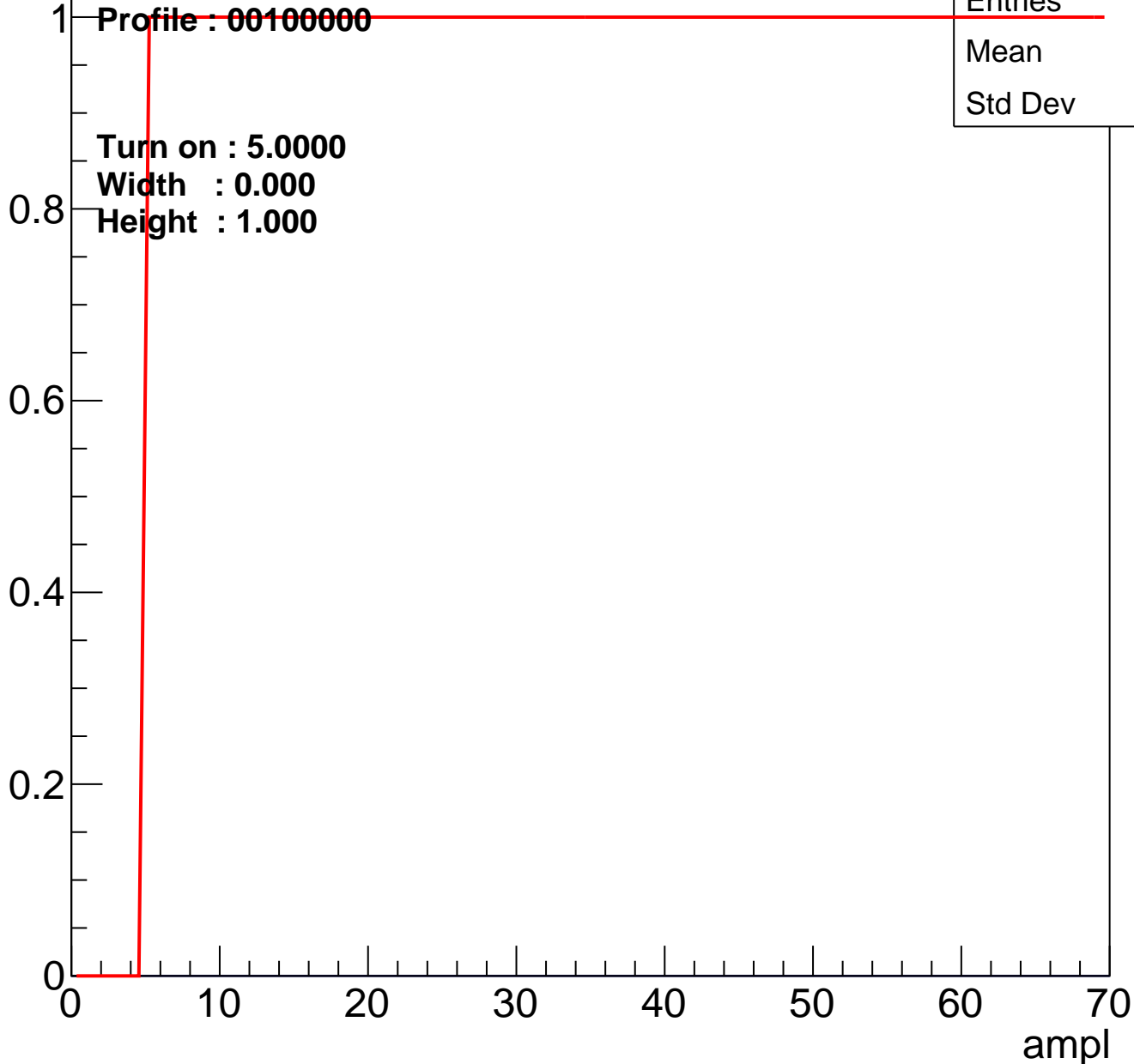
Entry



B1L003S, U17-ch19

calib_packv5_042523_0143.root, FC#13, Port D2

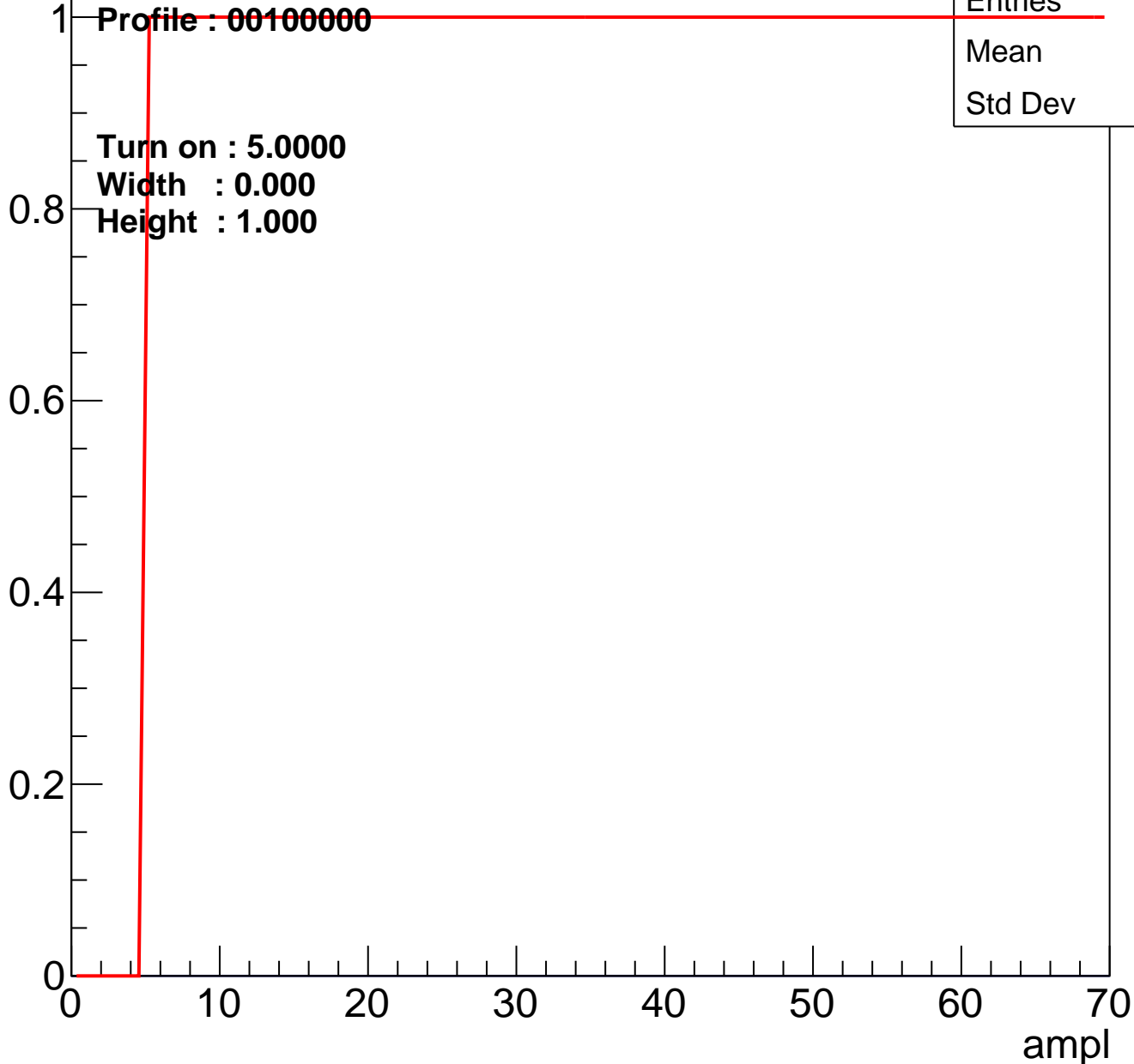
Entry



B1L003S, U17-ch20

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

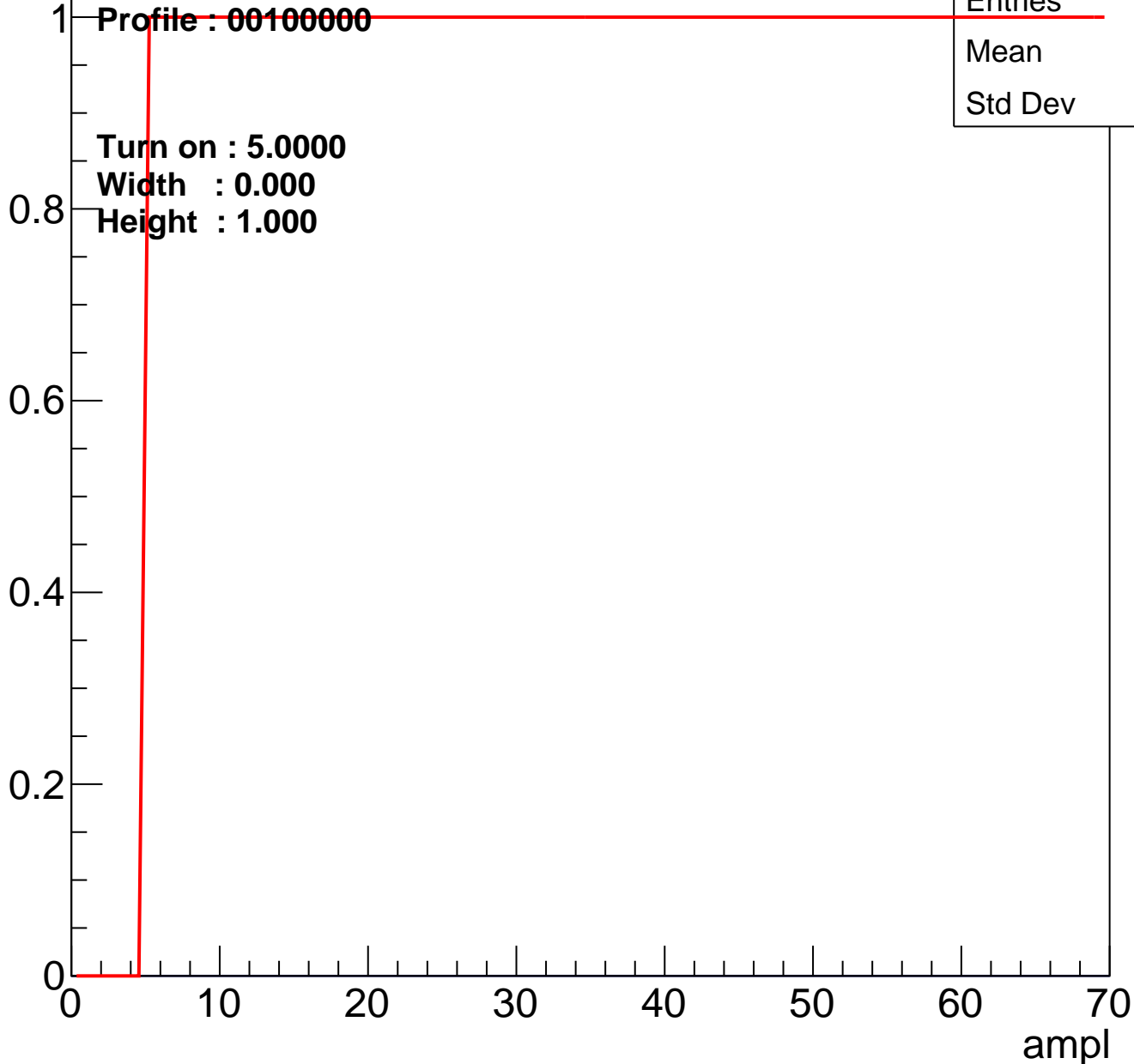


Entries	0
Mean	0
Std Dev	0

B1L003S, U17-ch21

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U17-ch22

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

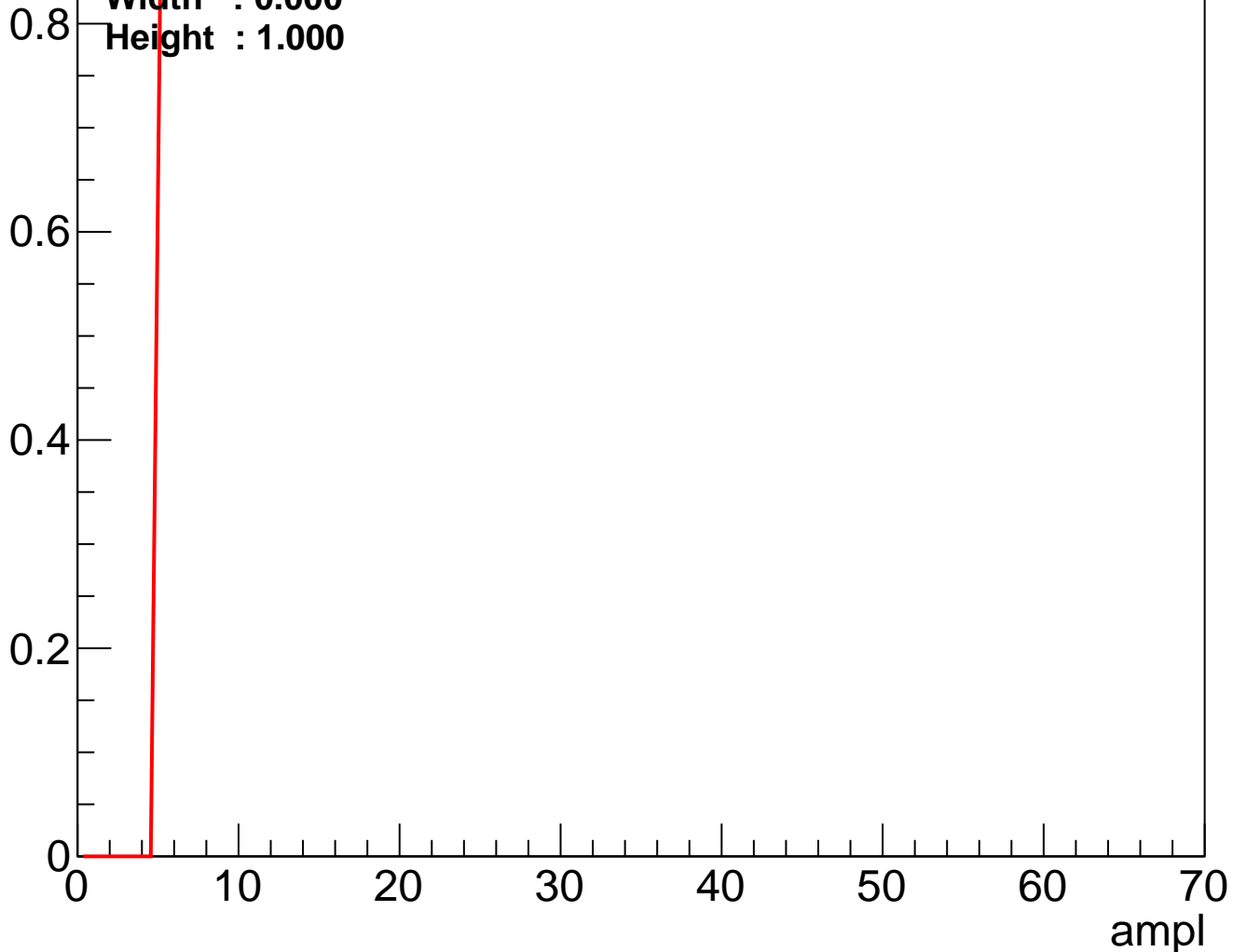
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

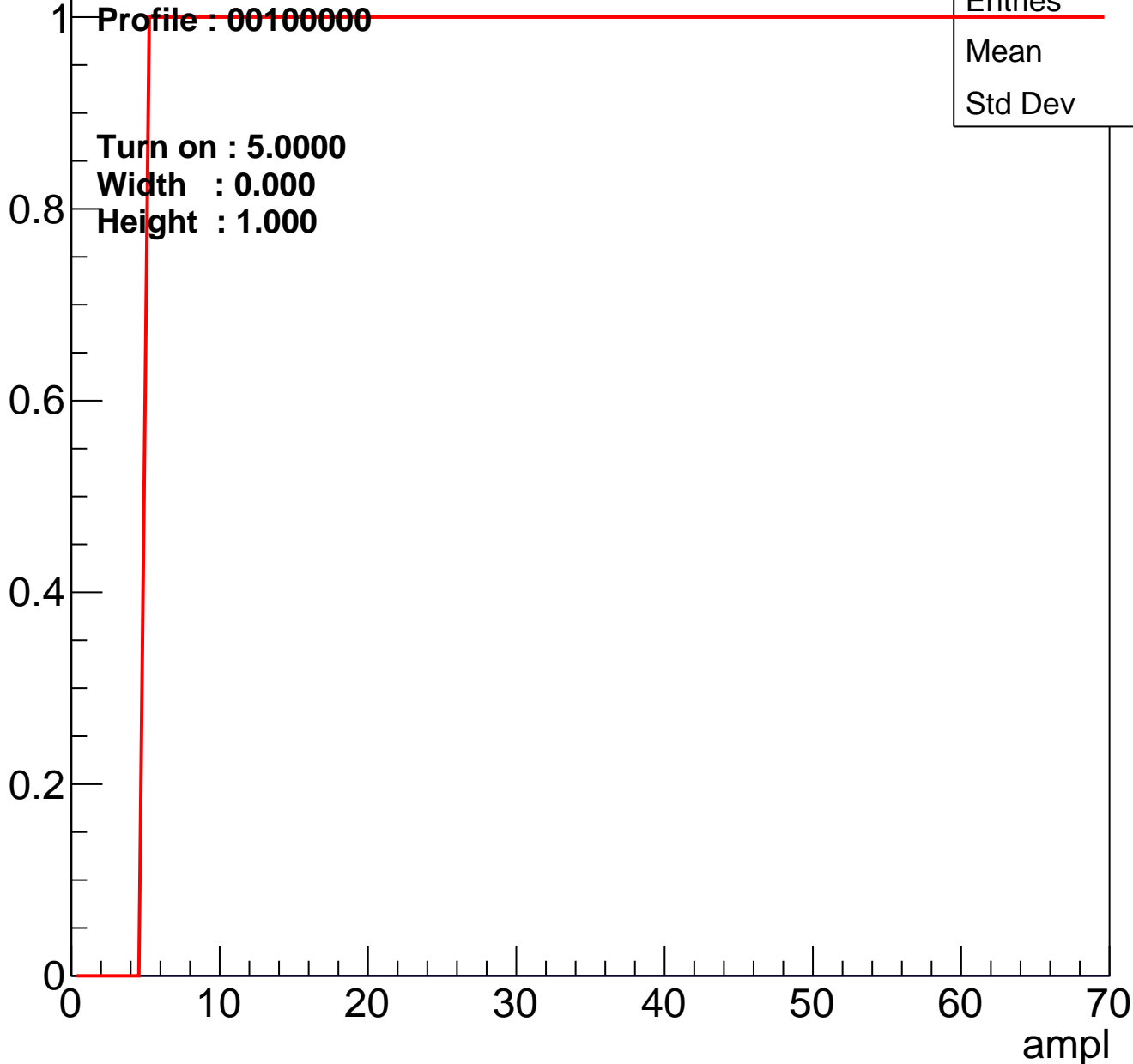
Entries	0
Mean	0
Std Dev	0



B1L003S, U17-ch23

calib_packv5_042523_0143.root, FC#13, Port D2

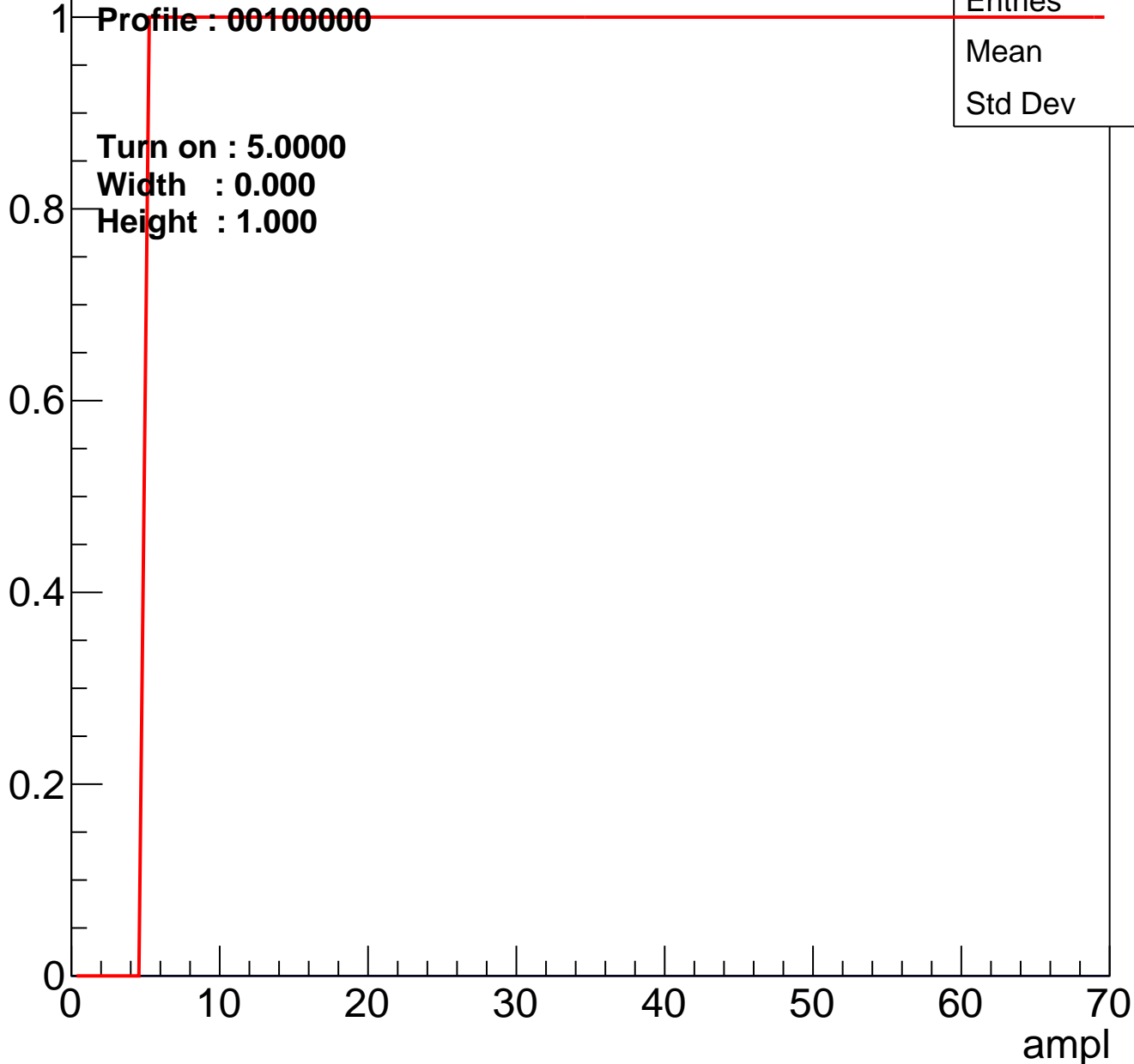
Entry



B1L003S, U17-ch24

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U17-ch25

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

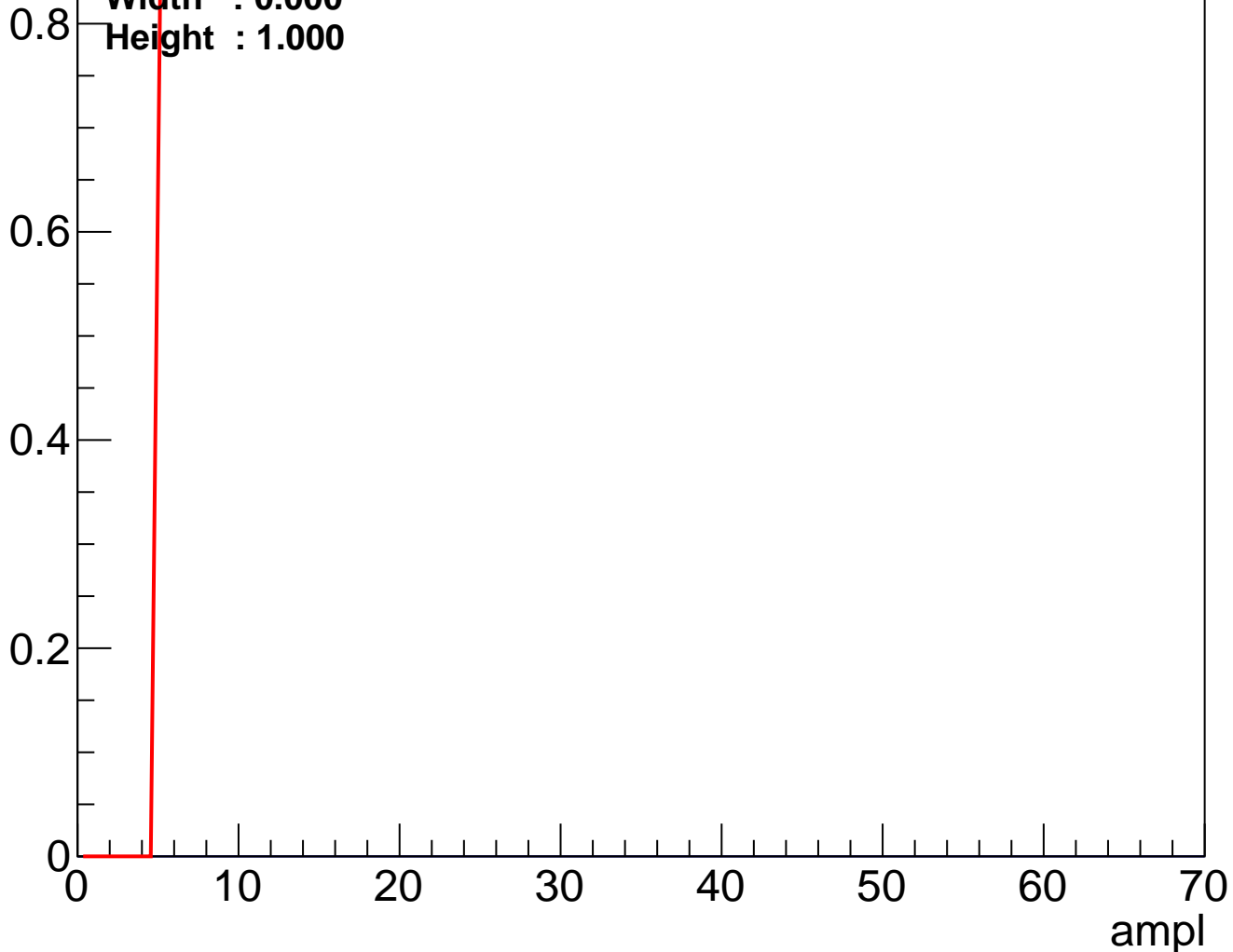
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

Entries	0
Mean	0
Std Dev	0



B1L003S, U17-ch26

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

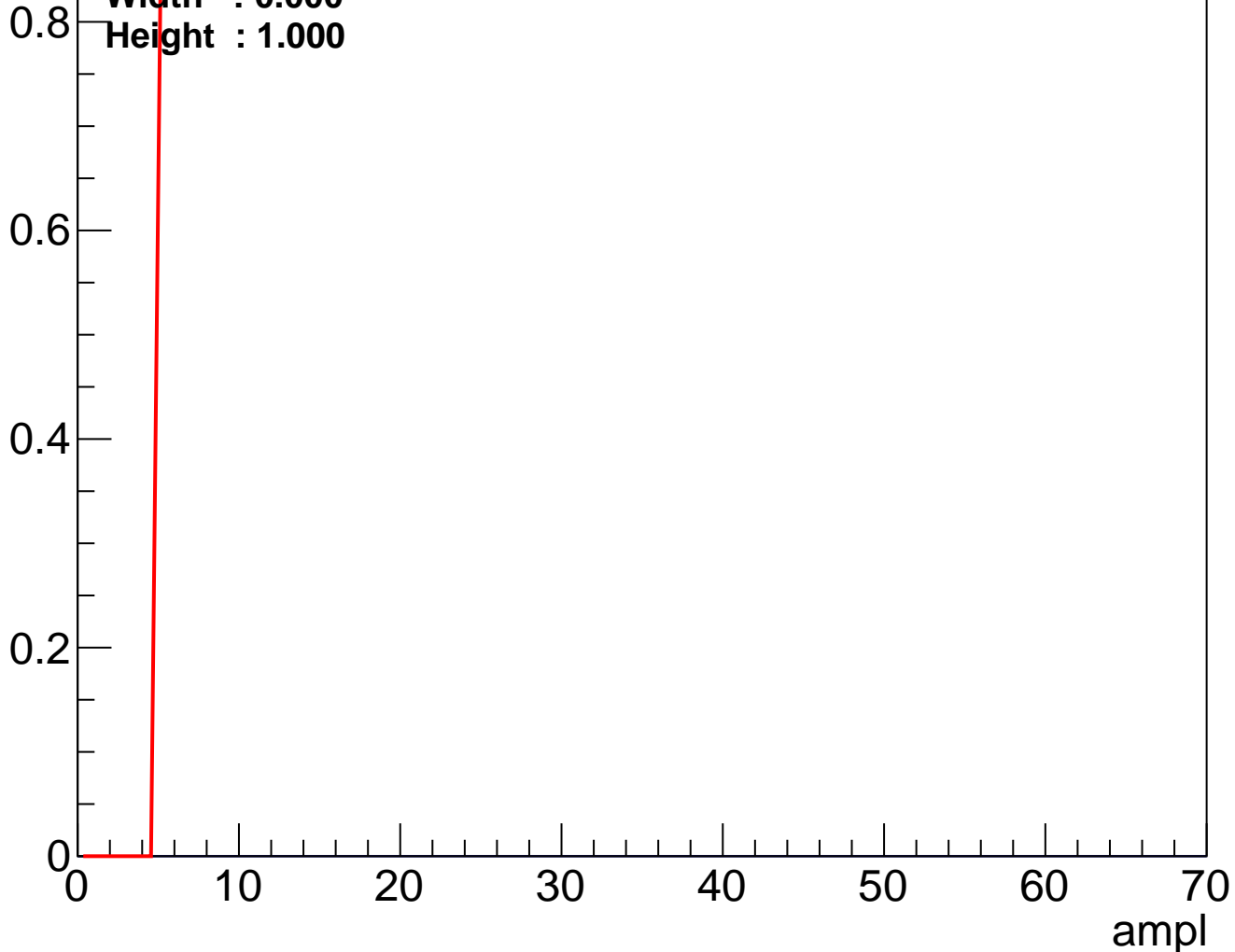
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

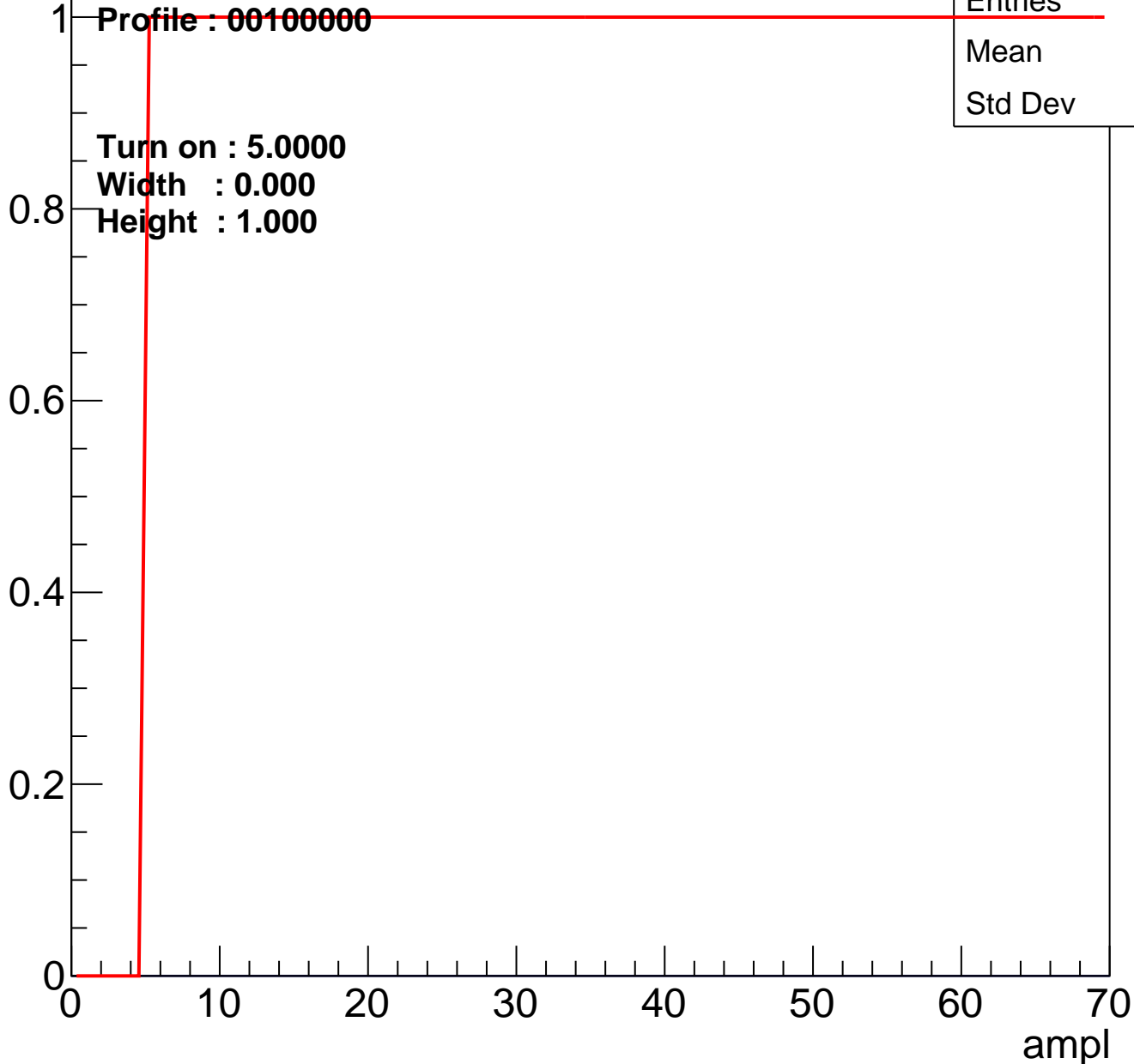
Entries	0
Mean	0
Std Dev	0



B1L003S, U17-ch27

calib_packv5_042523_0143.root, FC#13, Port D2

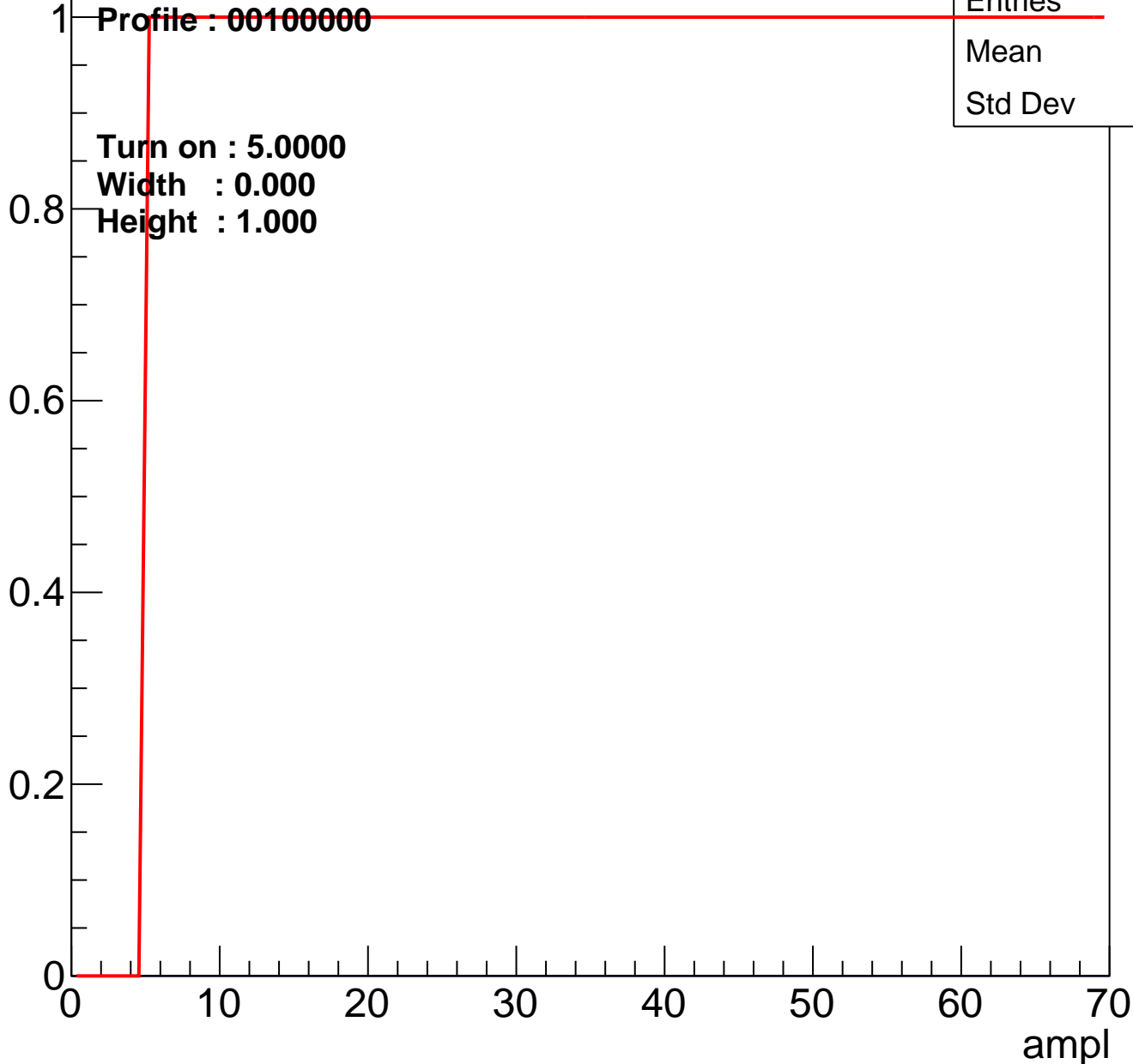
Entry



B1L003S, U17-ch28

calib_packv5_042523_0143.root, FC#13, Port D2

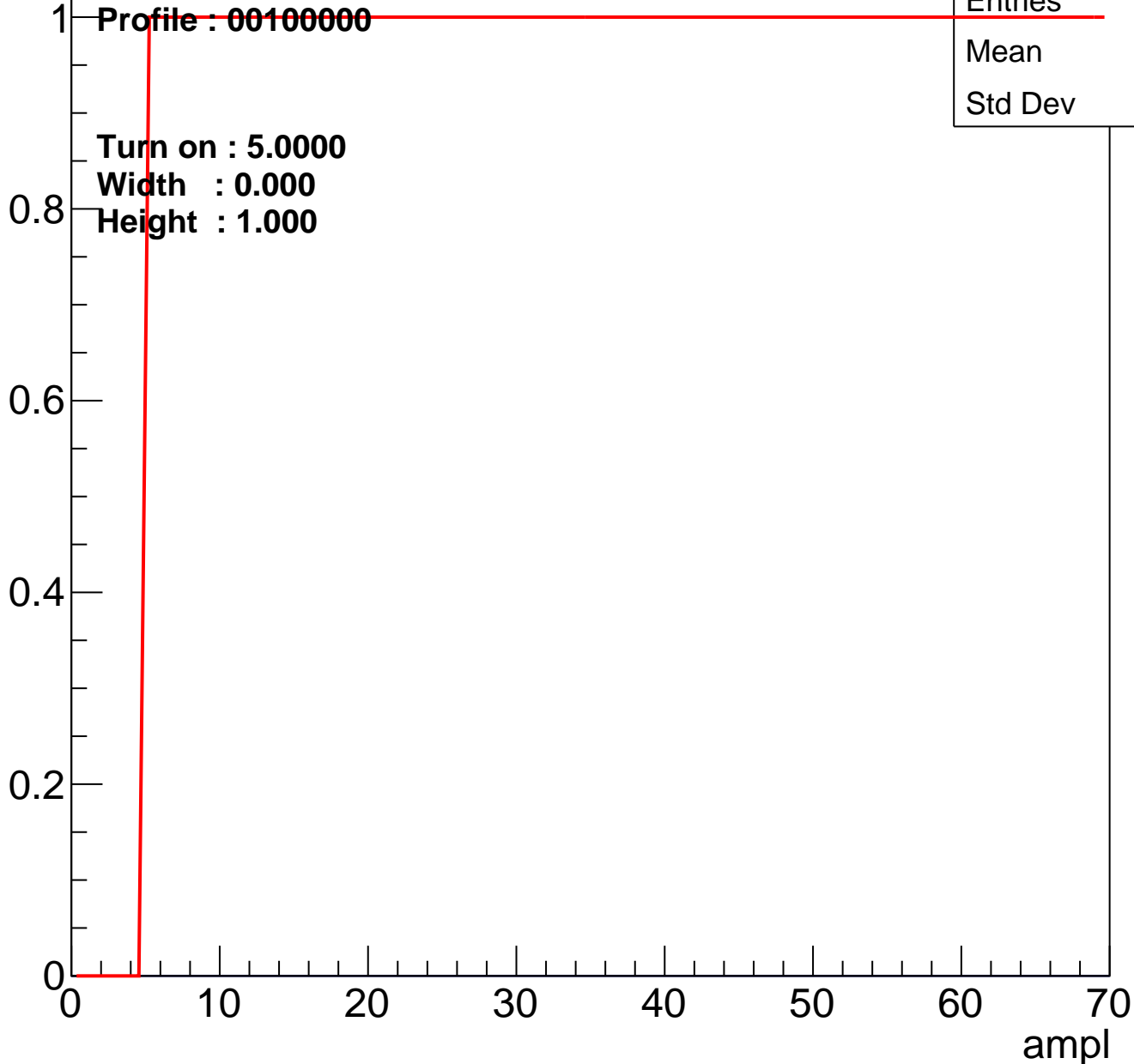
Entry



B1L003S, U17-ch29

calib_packv5_042523_0143.root, FC#13, Port D2

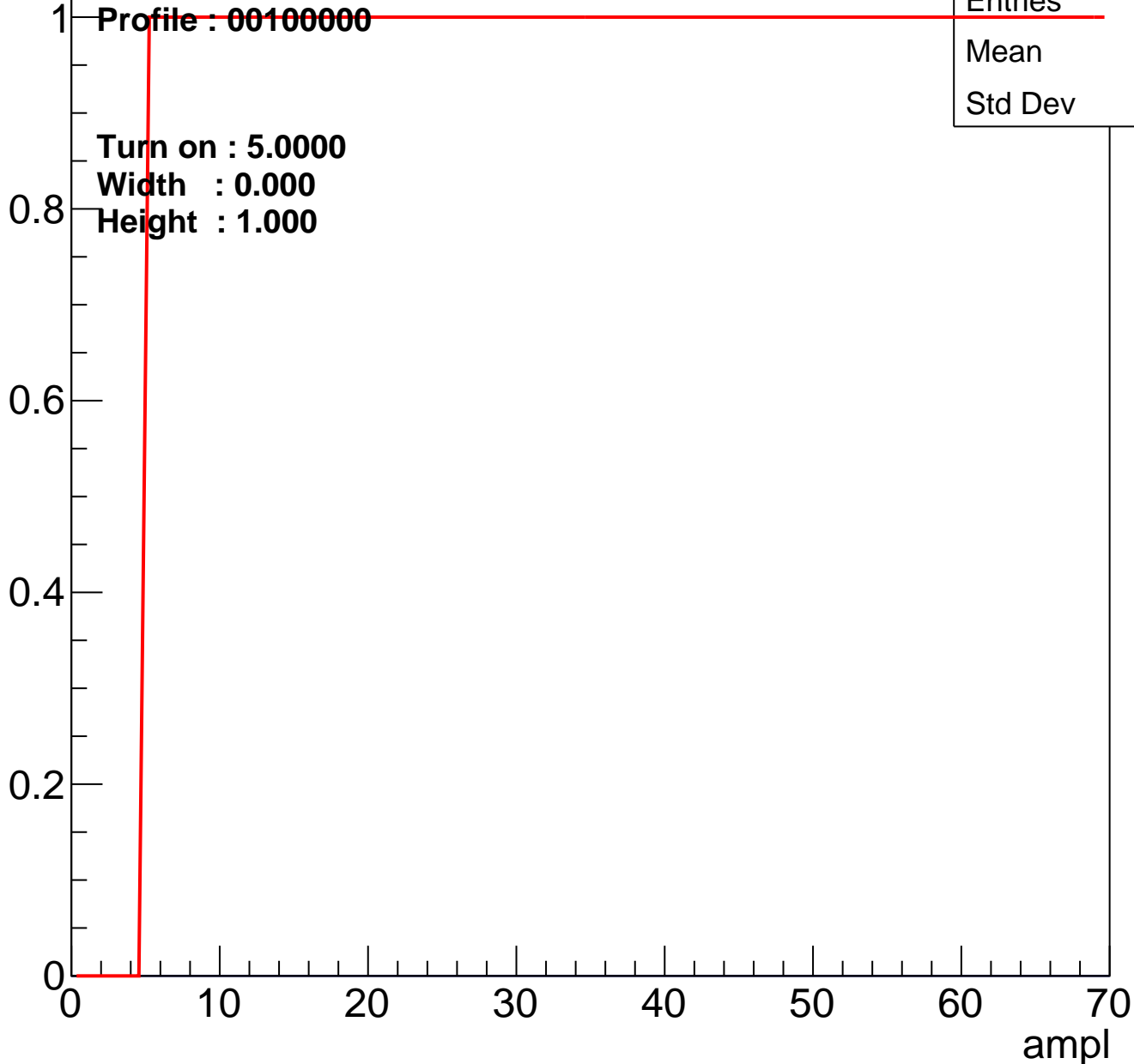
Entry



B1L003S, U17-ch30

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

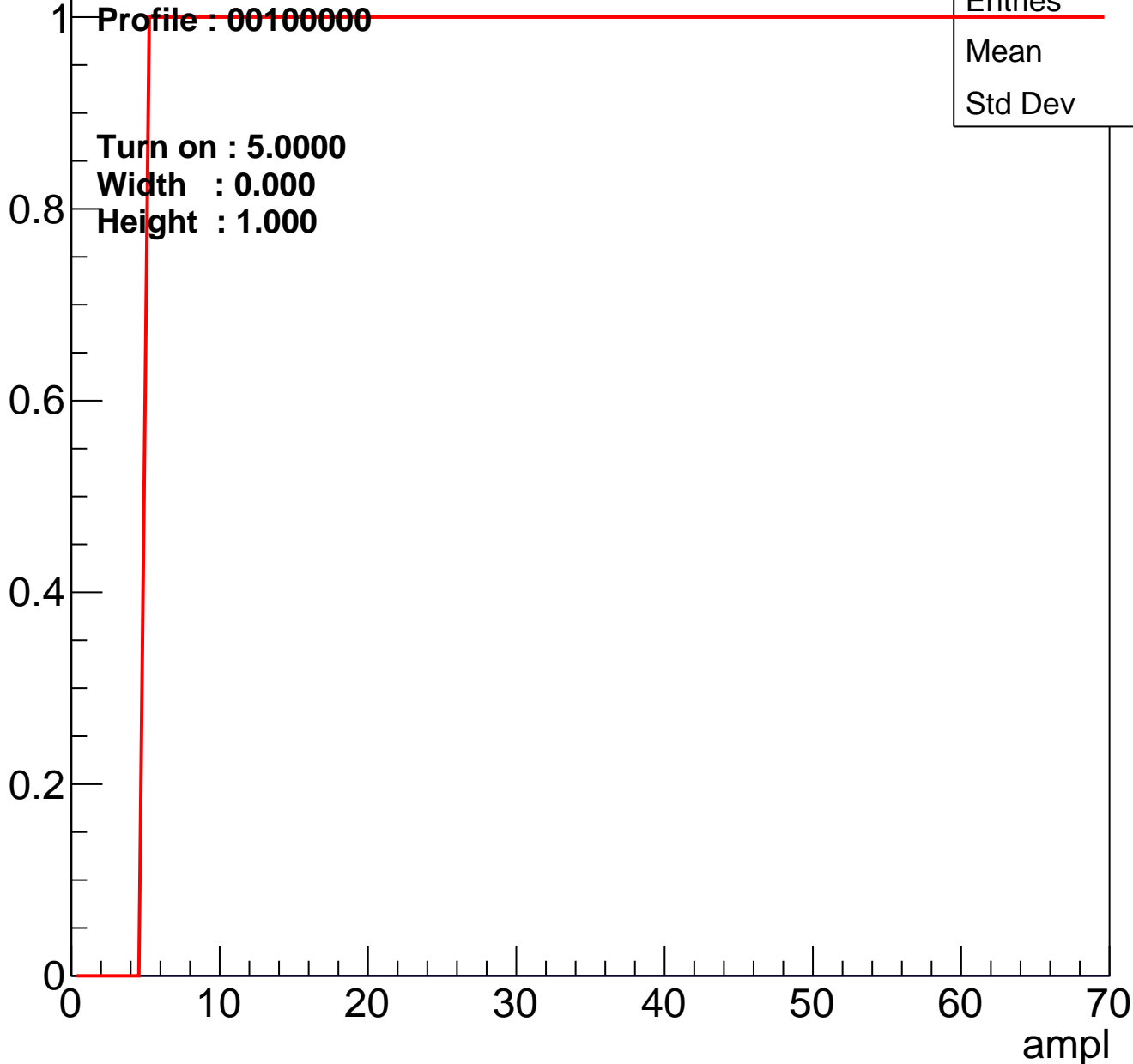


Entries	0
Mean	0
Std Dev	0

B1L003S, U17-ch31

calib_packv5_042523_0143.root, FC#13, Port D2

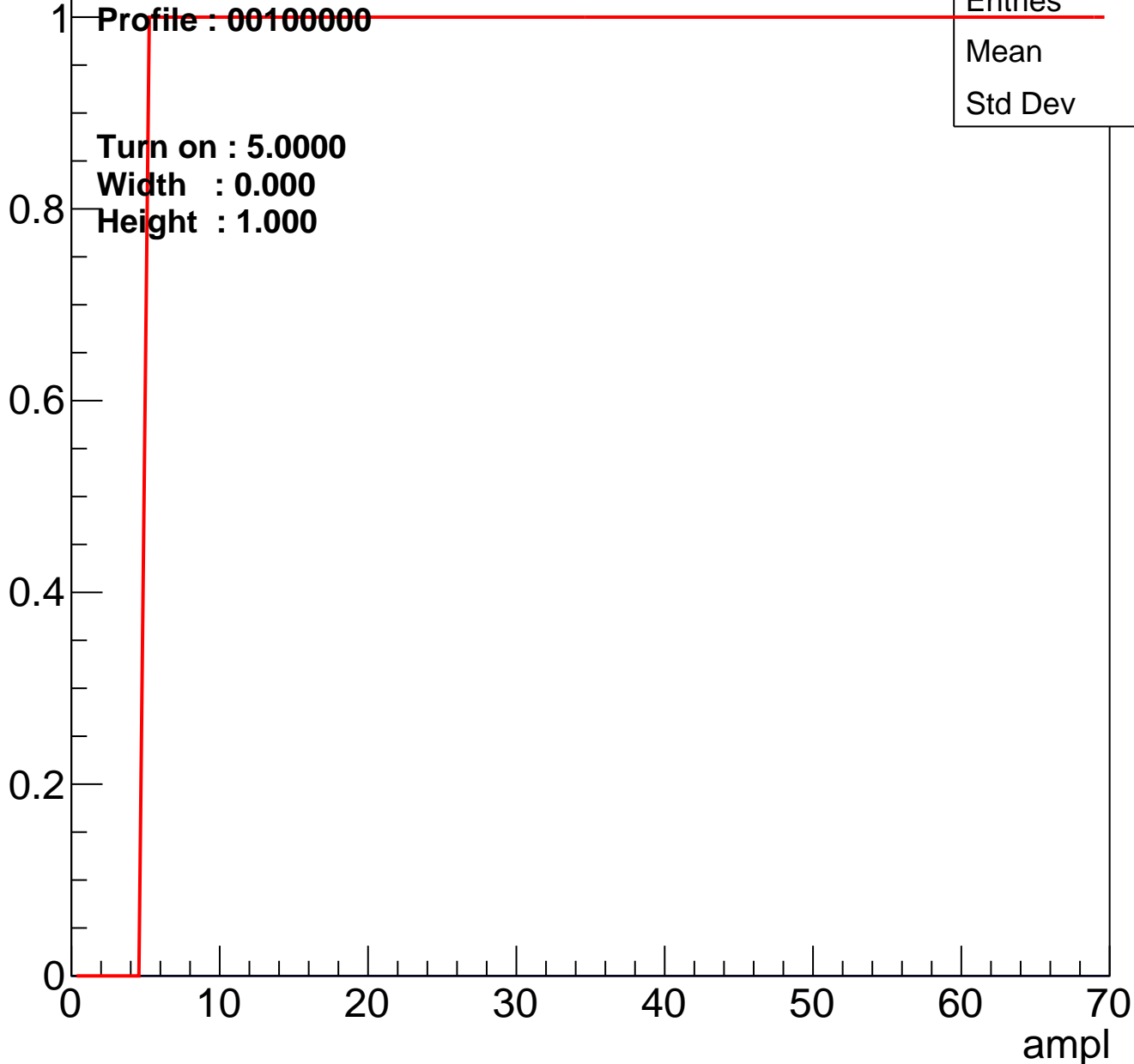
Entry



B1L003S, U18-ch0

calib_packv5_042523_0143.root, FC#13, Port D2

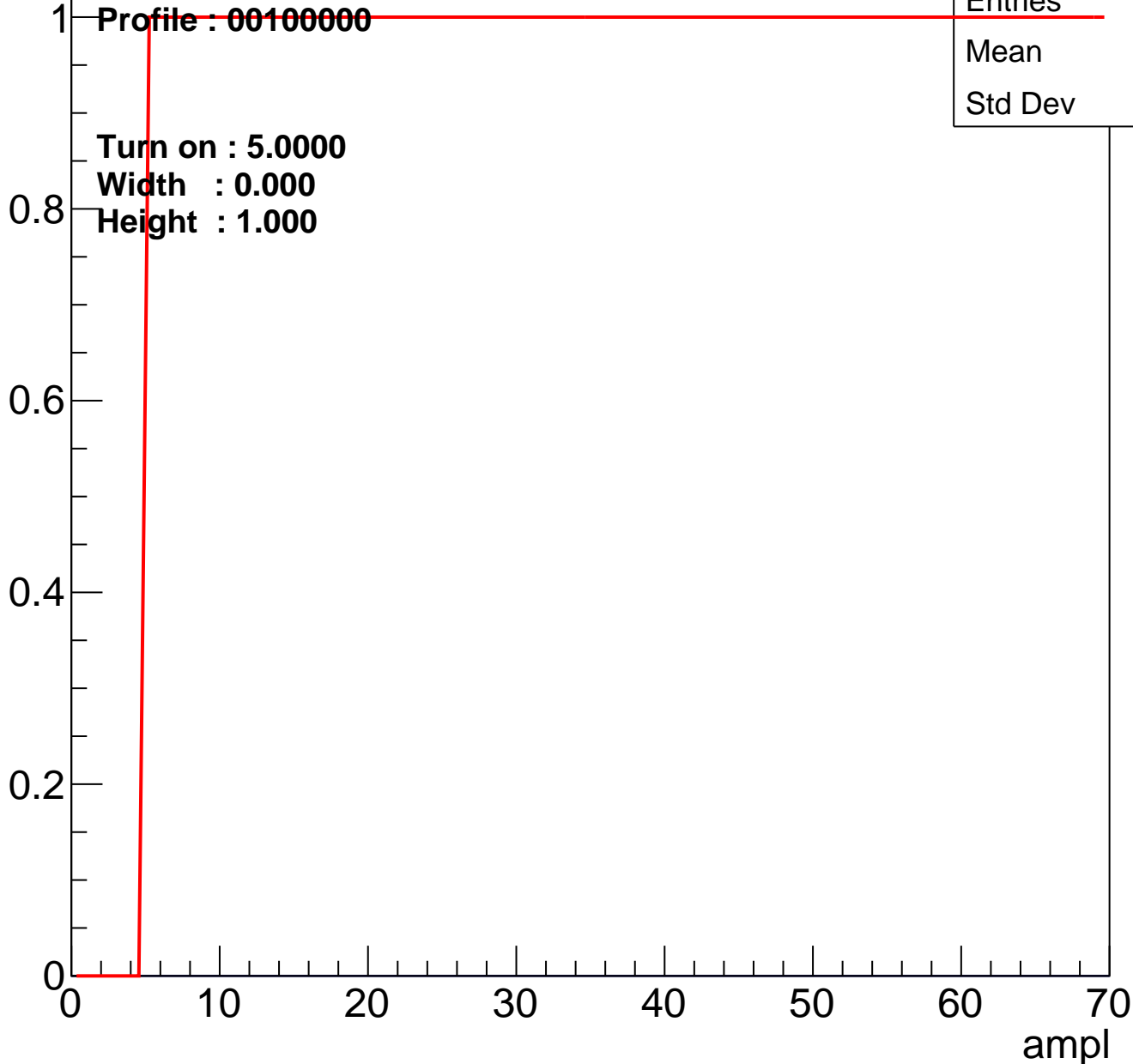
Entry



B1L003S, U18-ch1

calib_packv5_042523_0143.root, FC#13, Port D2

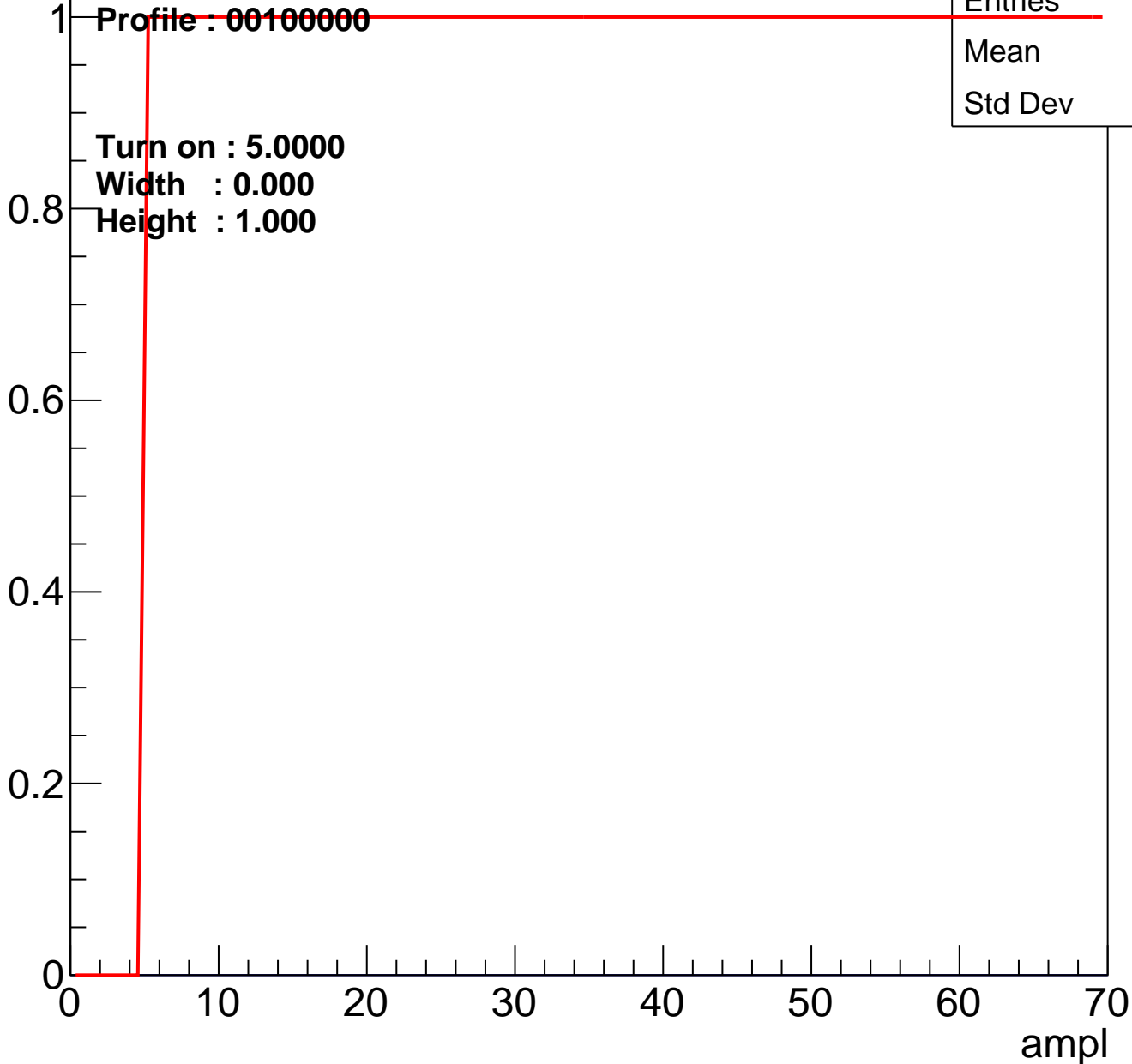
Entry



B1L003S, U18-ch2

calib_packv5_042523_0143.root, FC#13, Port D2

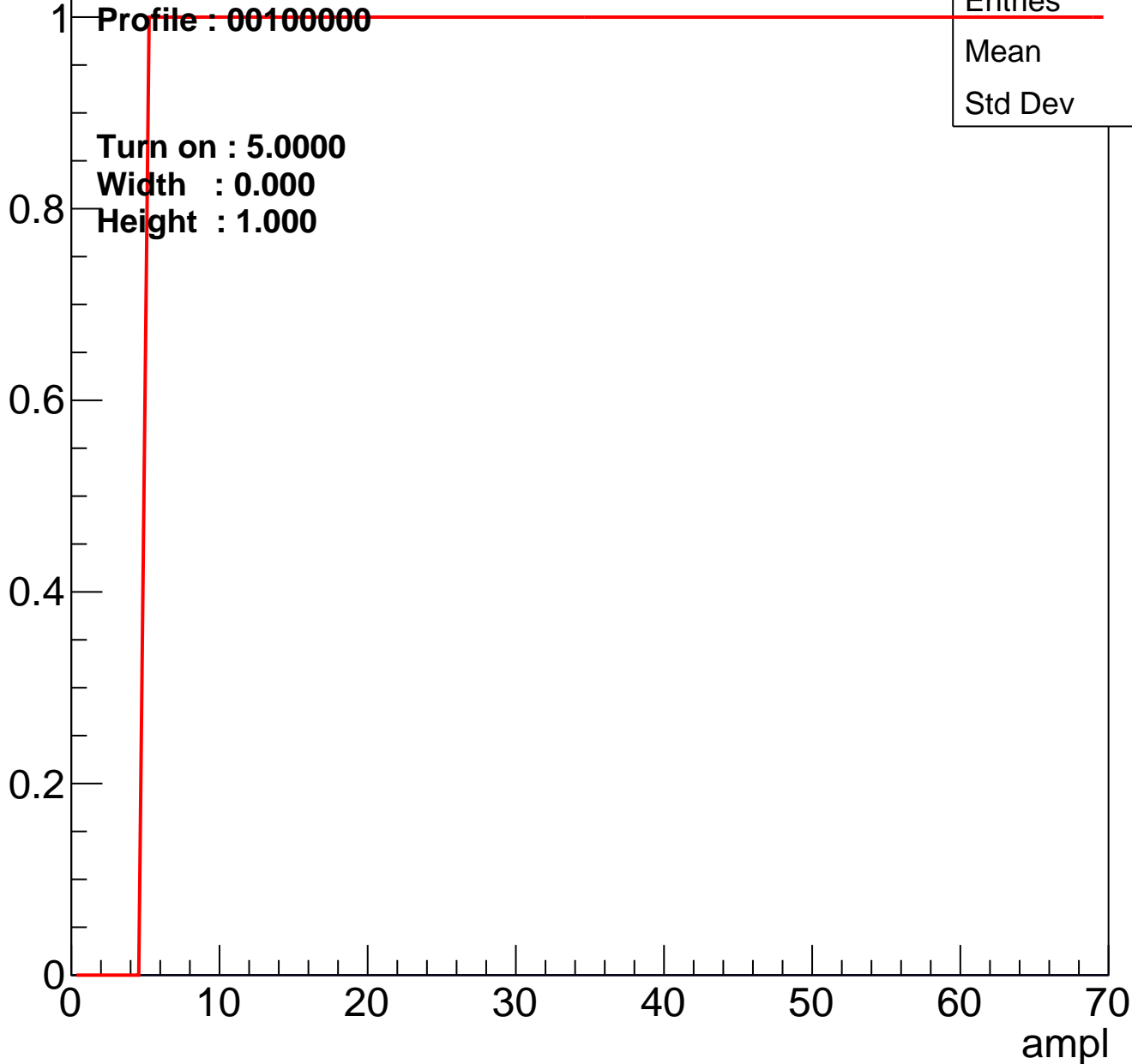
Entry



B1L003S, U18-ch3

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U18-ch4

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

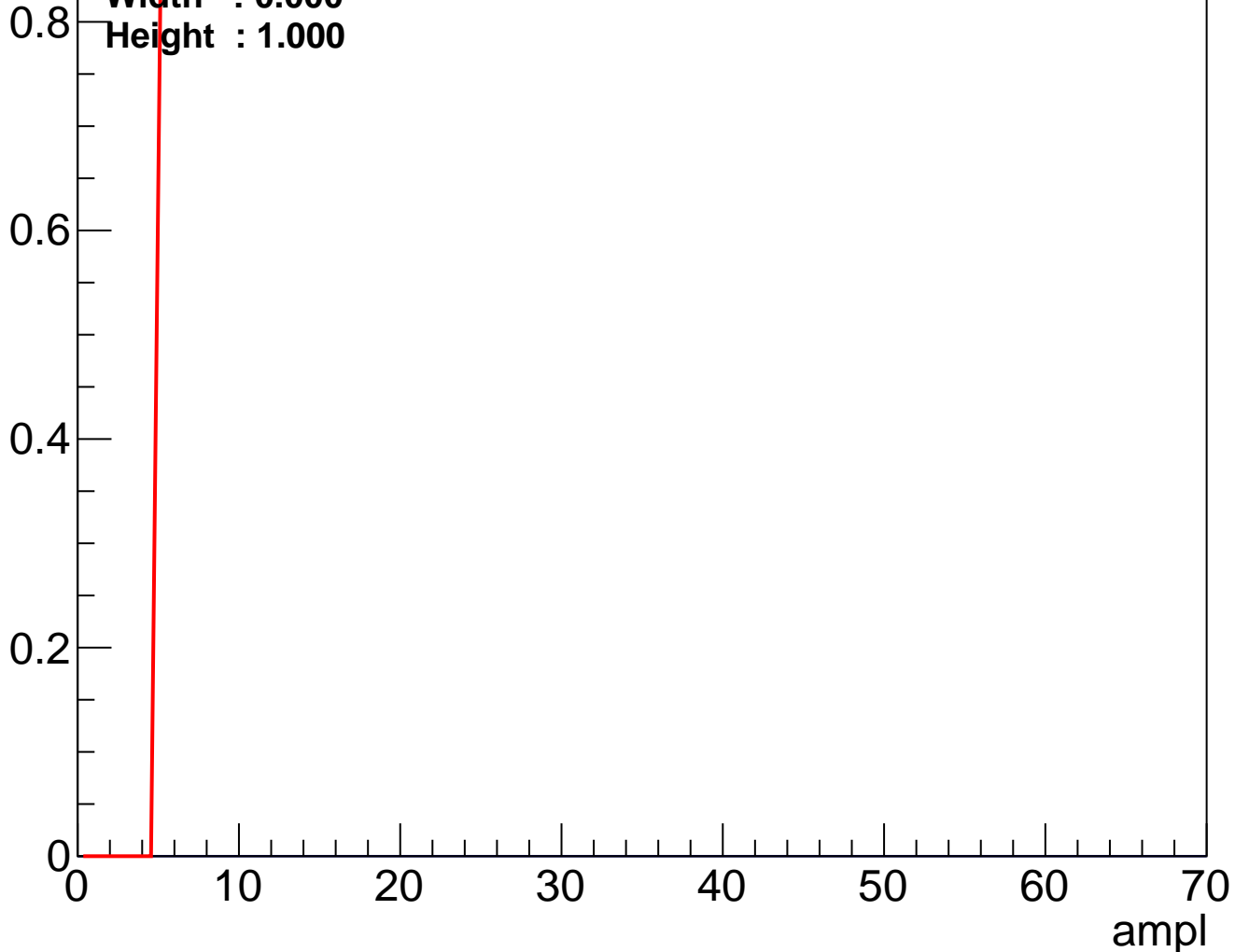
Profile : 00100000

Turn on : 5.0000

Width : 0.000

Height : 1.000

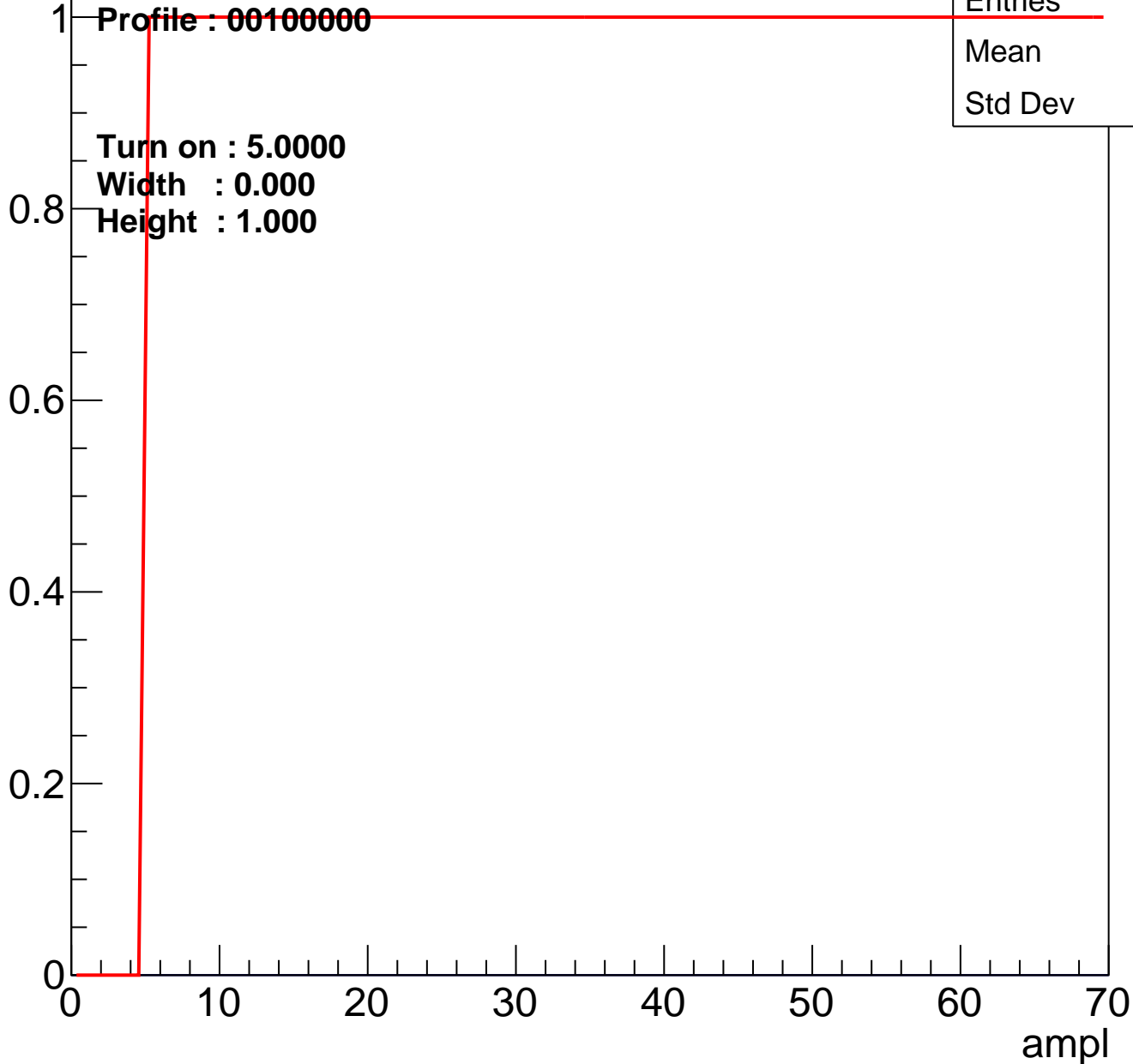
Entries	0
Mean	0
Std Dev	0



B1L003S, U18-ch5

calib_packv5_042523_0143.root, FC#13, Port D2

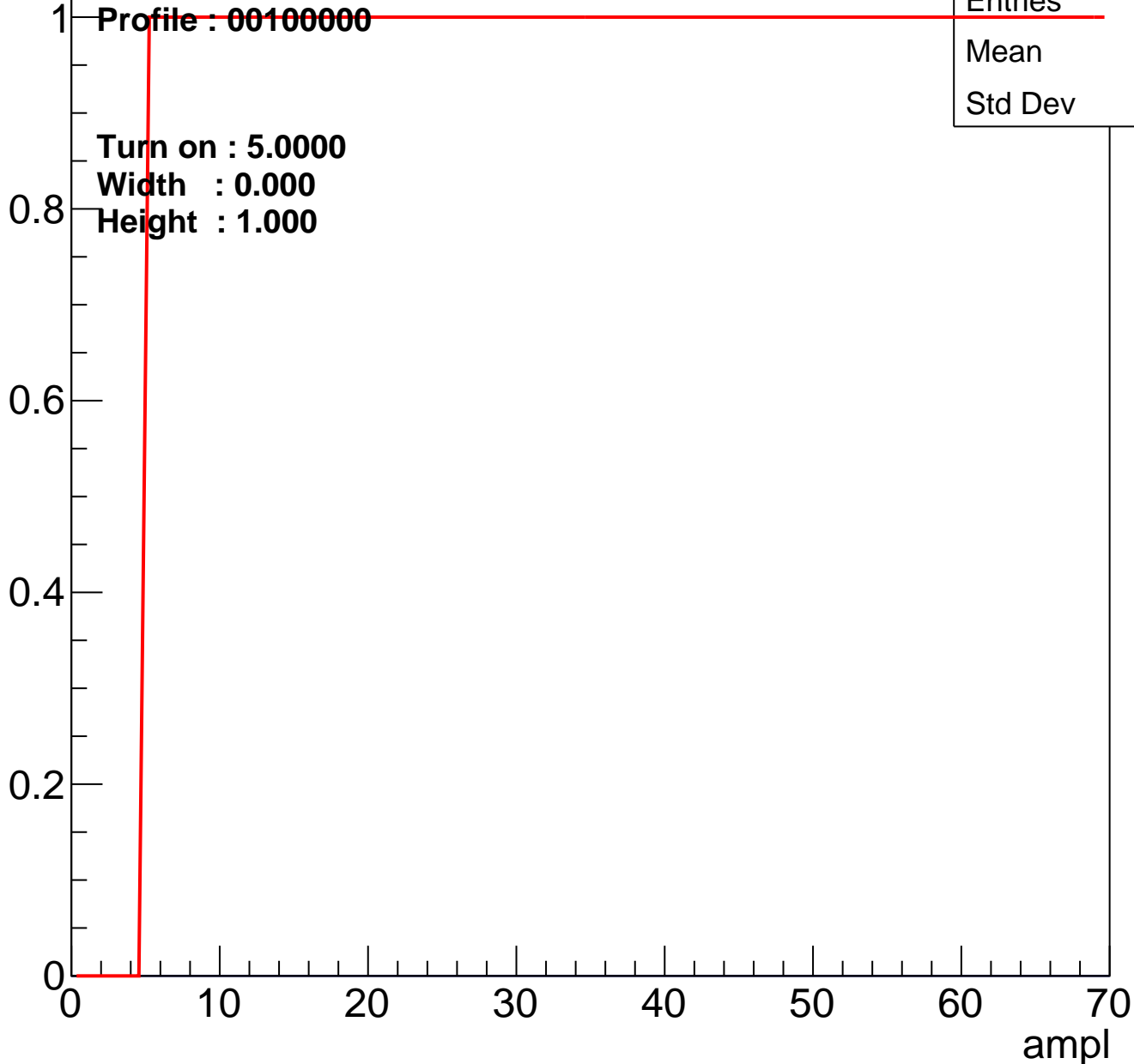
Entry



B1L003S, U18-ch6

calib_packv5_042523_0143.root, FC#13, Port D2

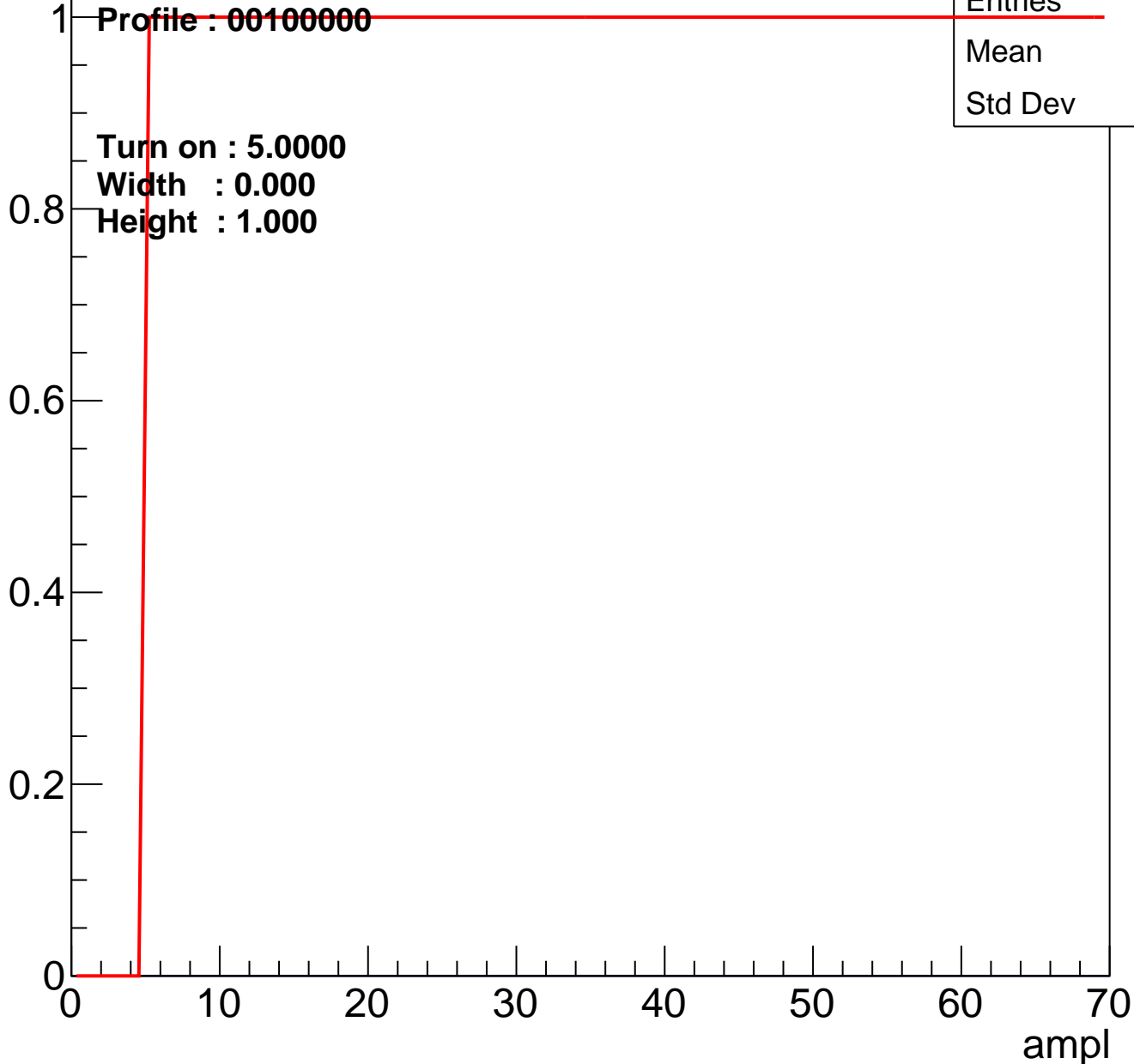
Entry



B1L003S, U18-ch7

calib_packv5_042523_0143.root, FC#13, Port D2

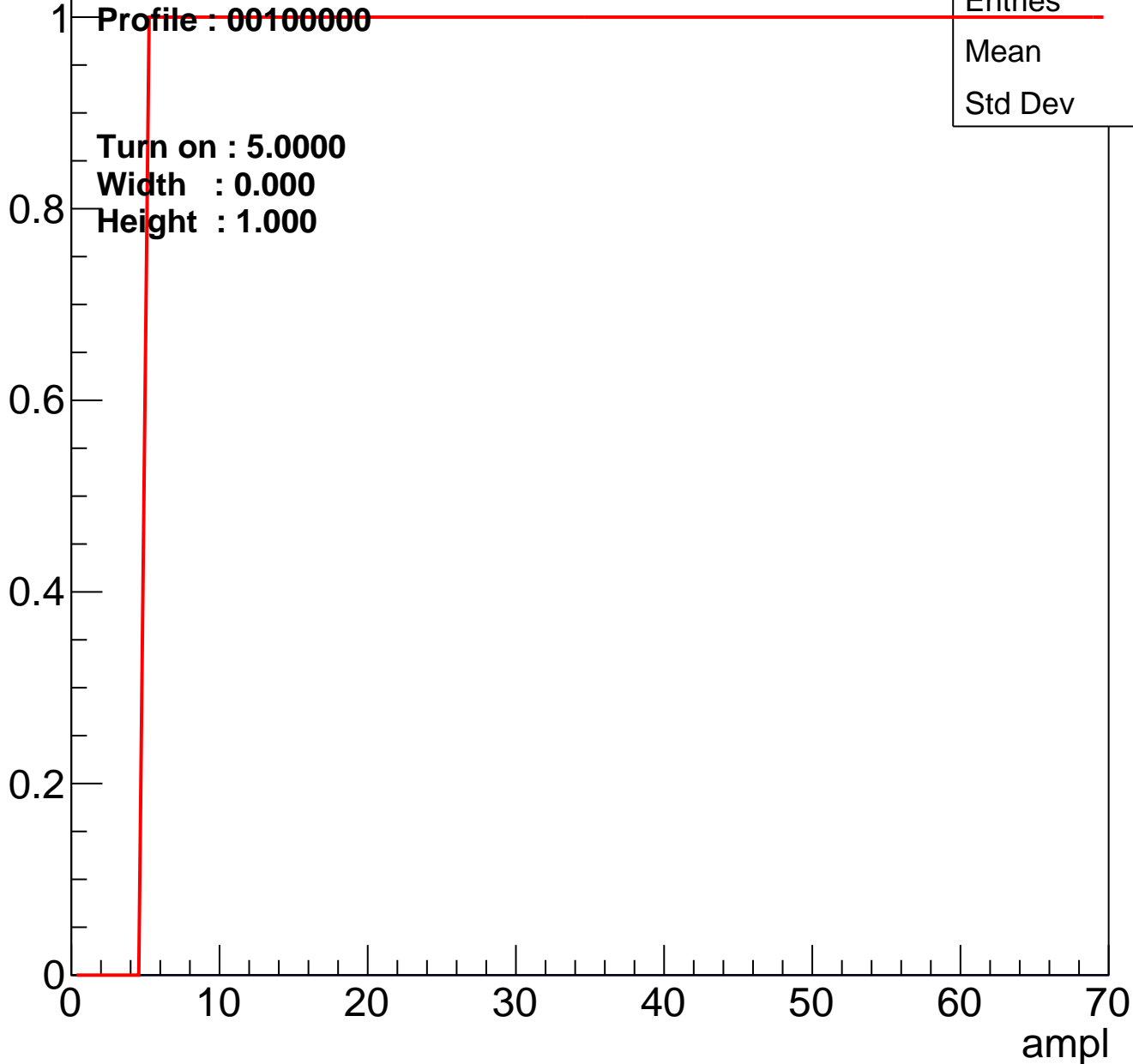
Entry



B1L003S, U18-ch8

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U18-ch9

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

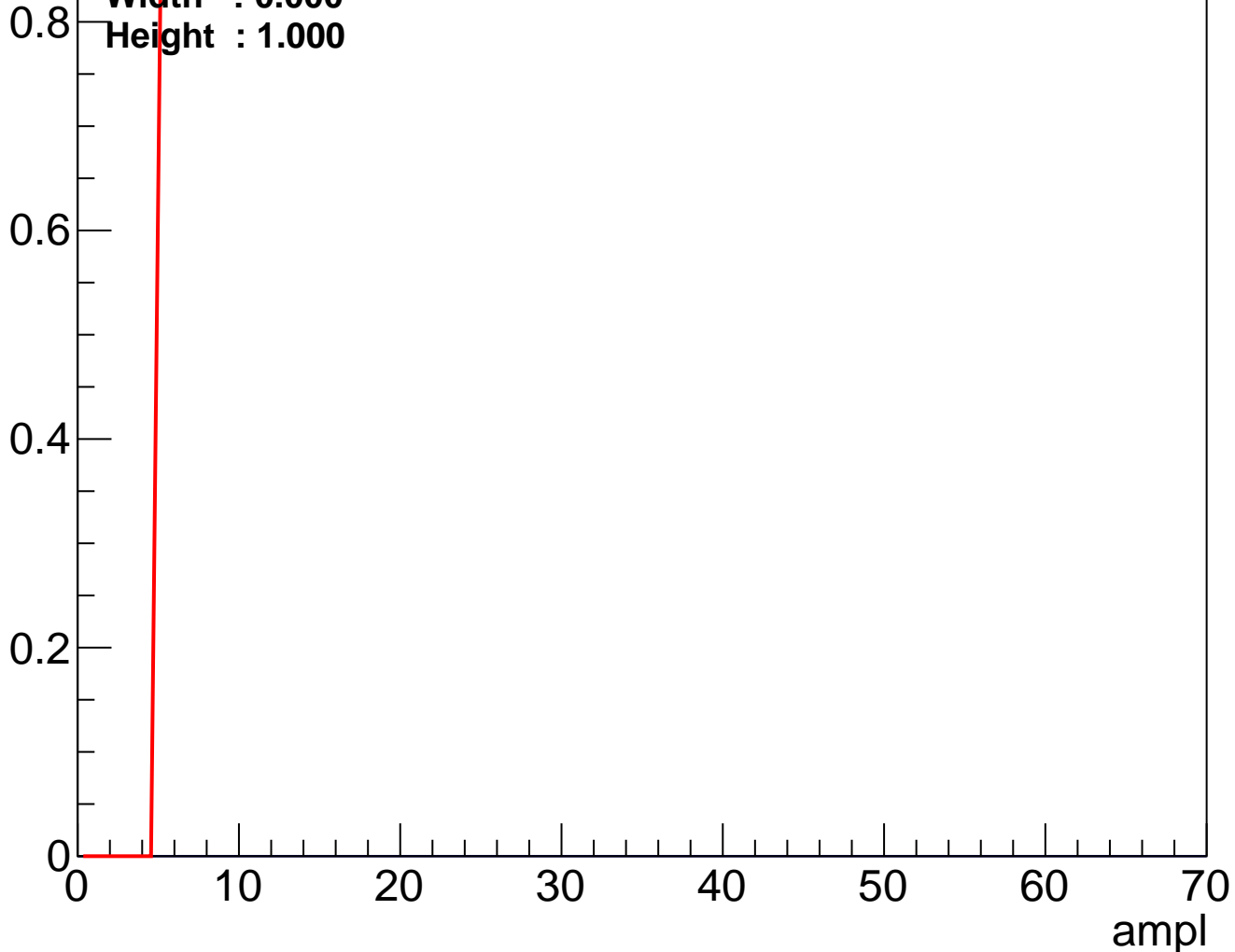
Profile : 00100000

Turn on : 5.0000

Width : 0.000

Height : 1.000

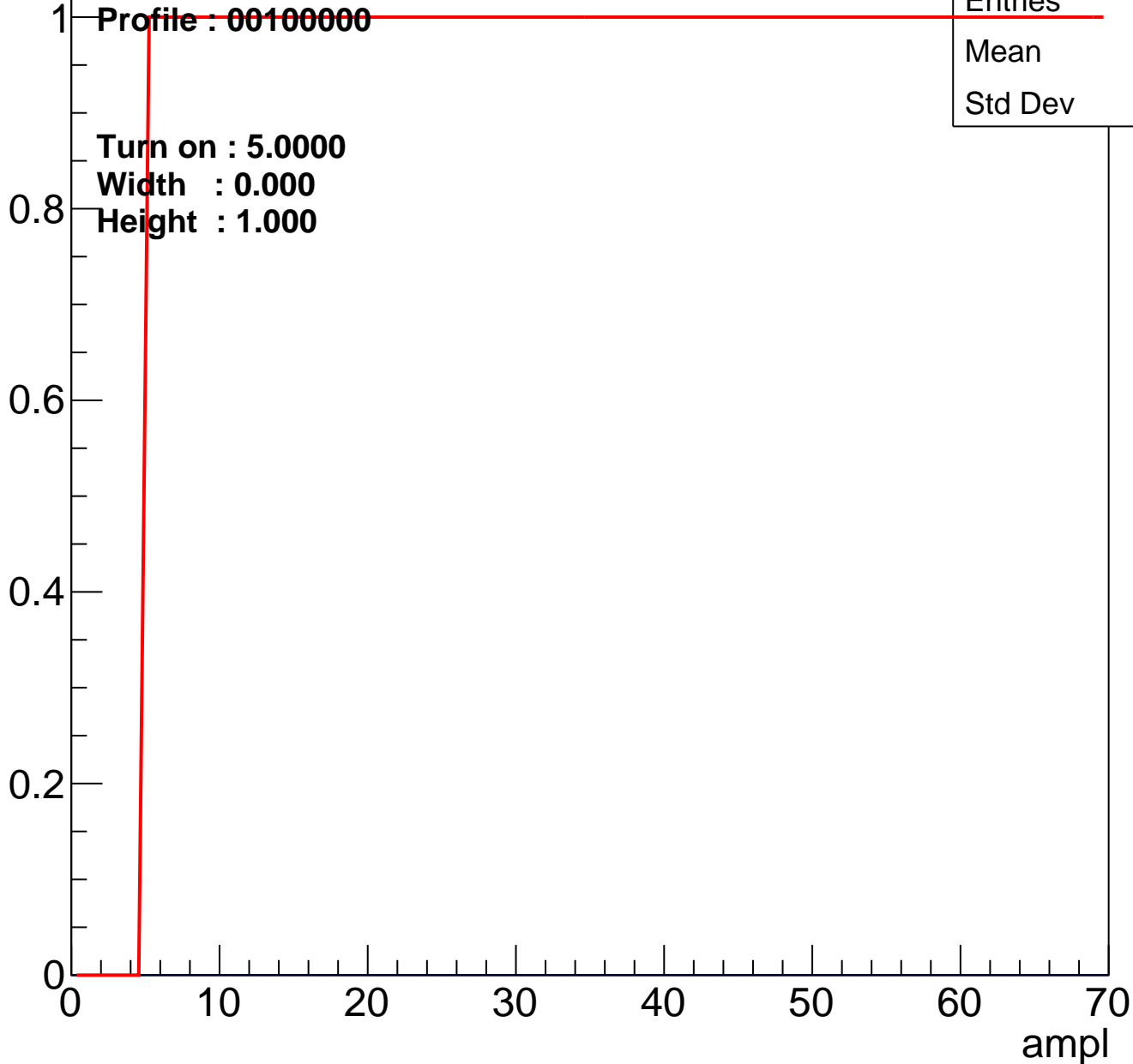
Entries	0
Mean	0
Std Dev	0



B1L003S, U18-ch10

calib_packv5_042523_0143.root, FC#13, Port D2

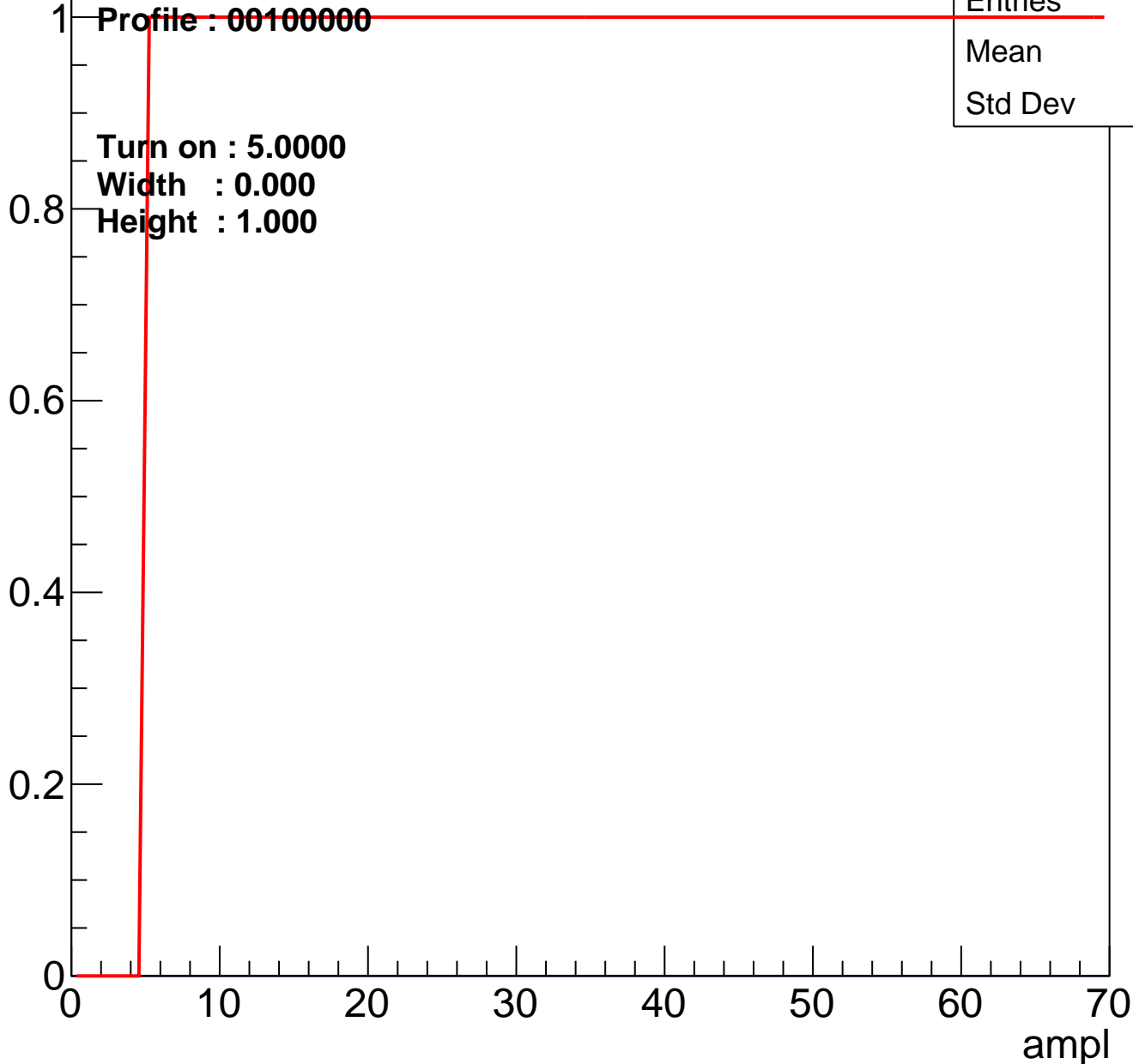
Entry



B1L003S, U18-ch11

calib_packv5_042523_0143.root, FC#13, Port D2

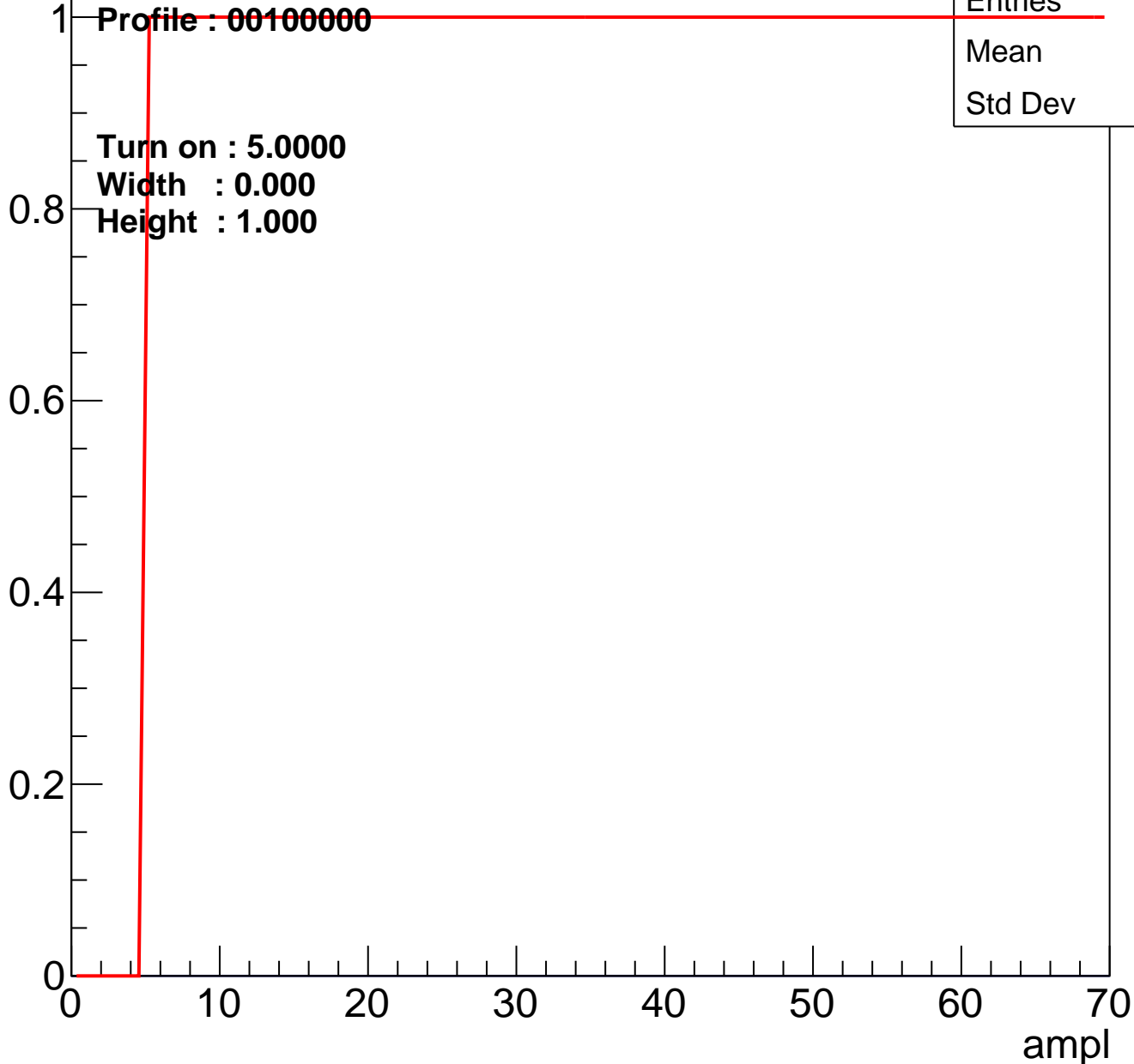
Entry



B1L003S, U18-ch12

calib_packv5_042523_0143.root, FC#13, Port D2

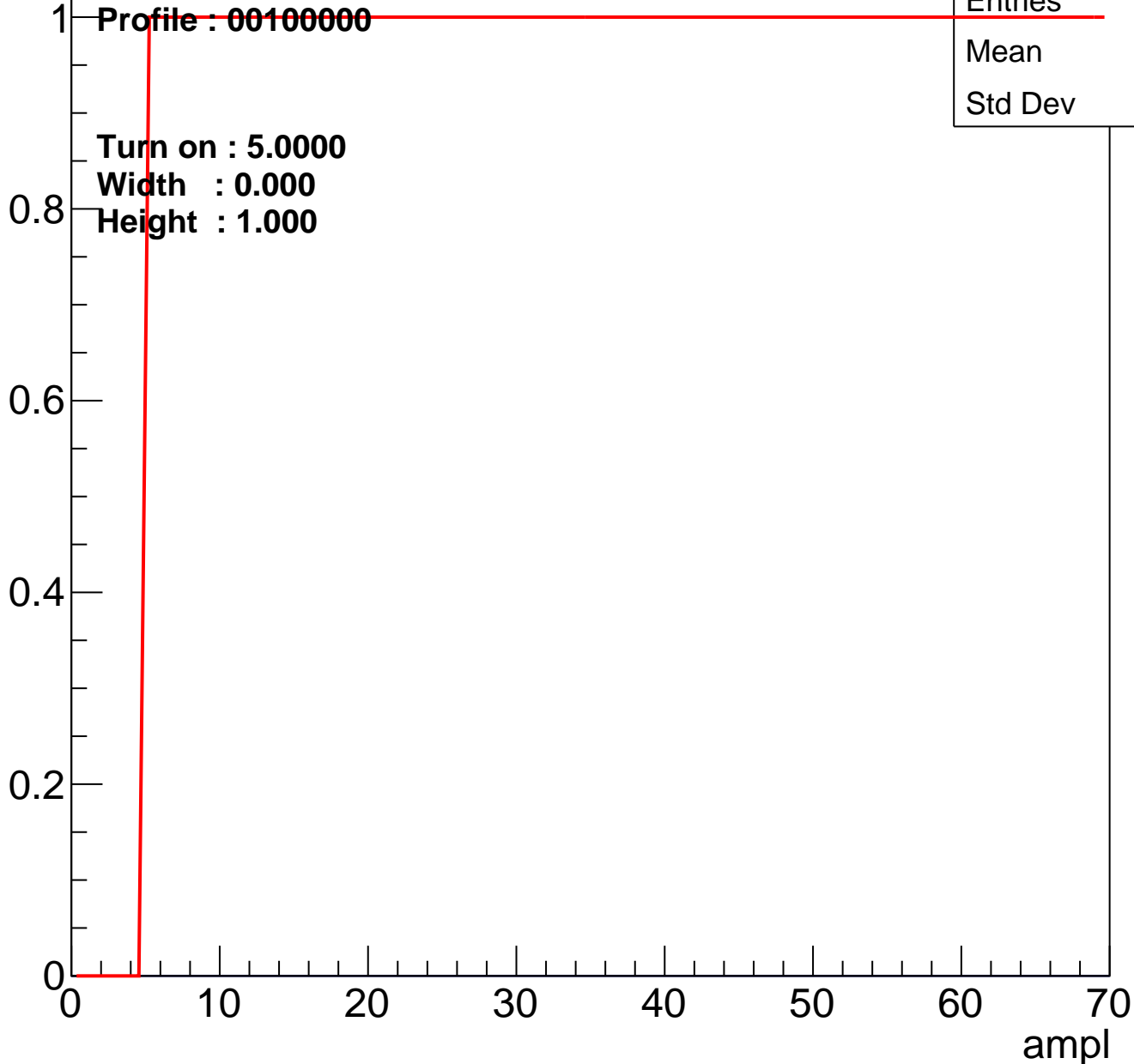
Entry



B1L003S, U18-ch13

calib_packv5_042523_0143.root, FC#13, Port D2

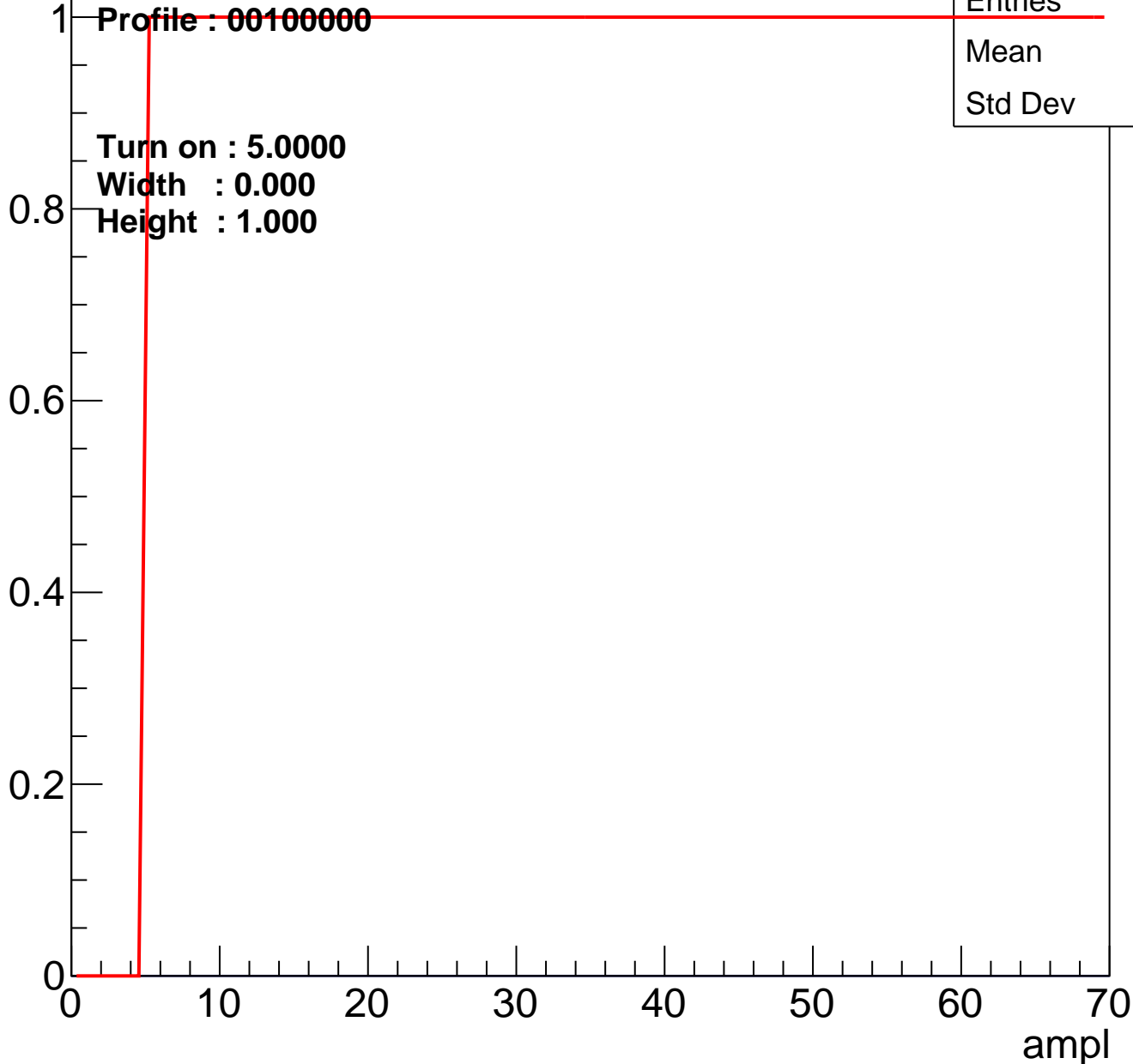
Entry



B1L003S, U18-ch14

calib_packv5_042523_0143.root, FC#13, Port D2

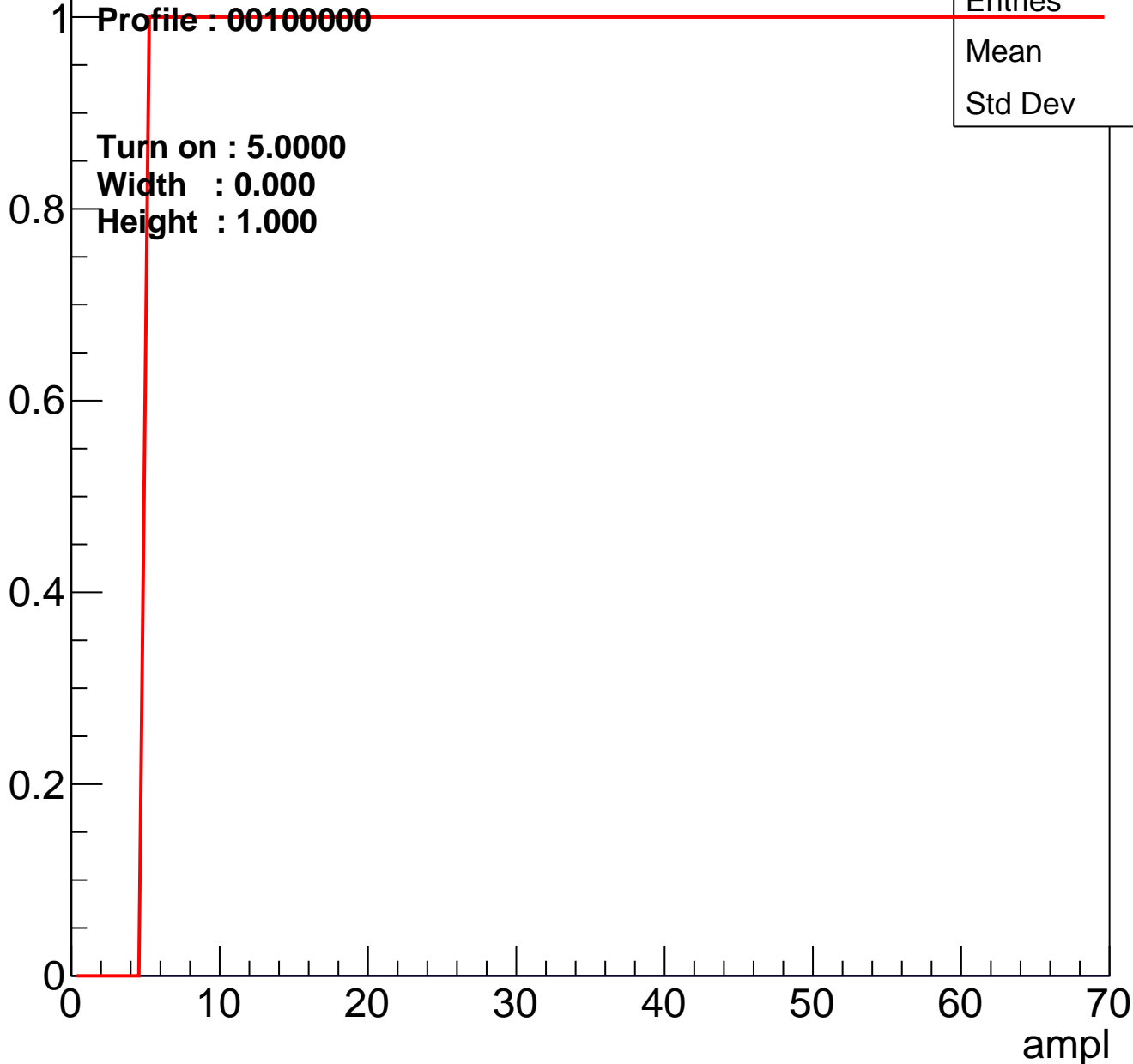
Entry



B1L003S, U18-ch15

calib_packv5_042523_0143.root, FC#13, Port D2

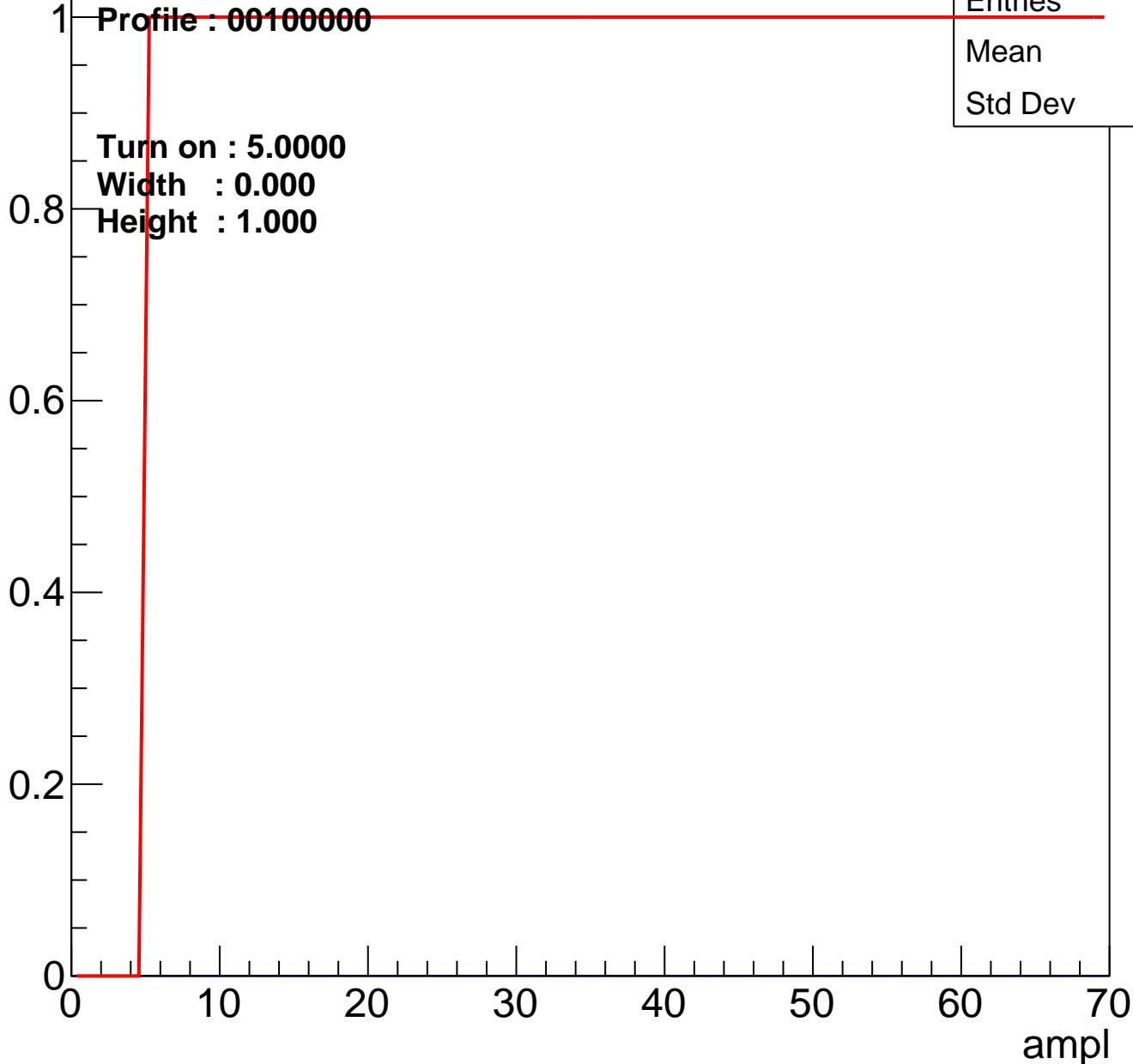
Entry



B1L003S, U18-ch16

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U18-ch17

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

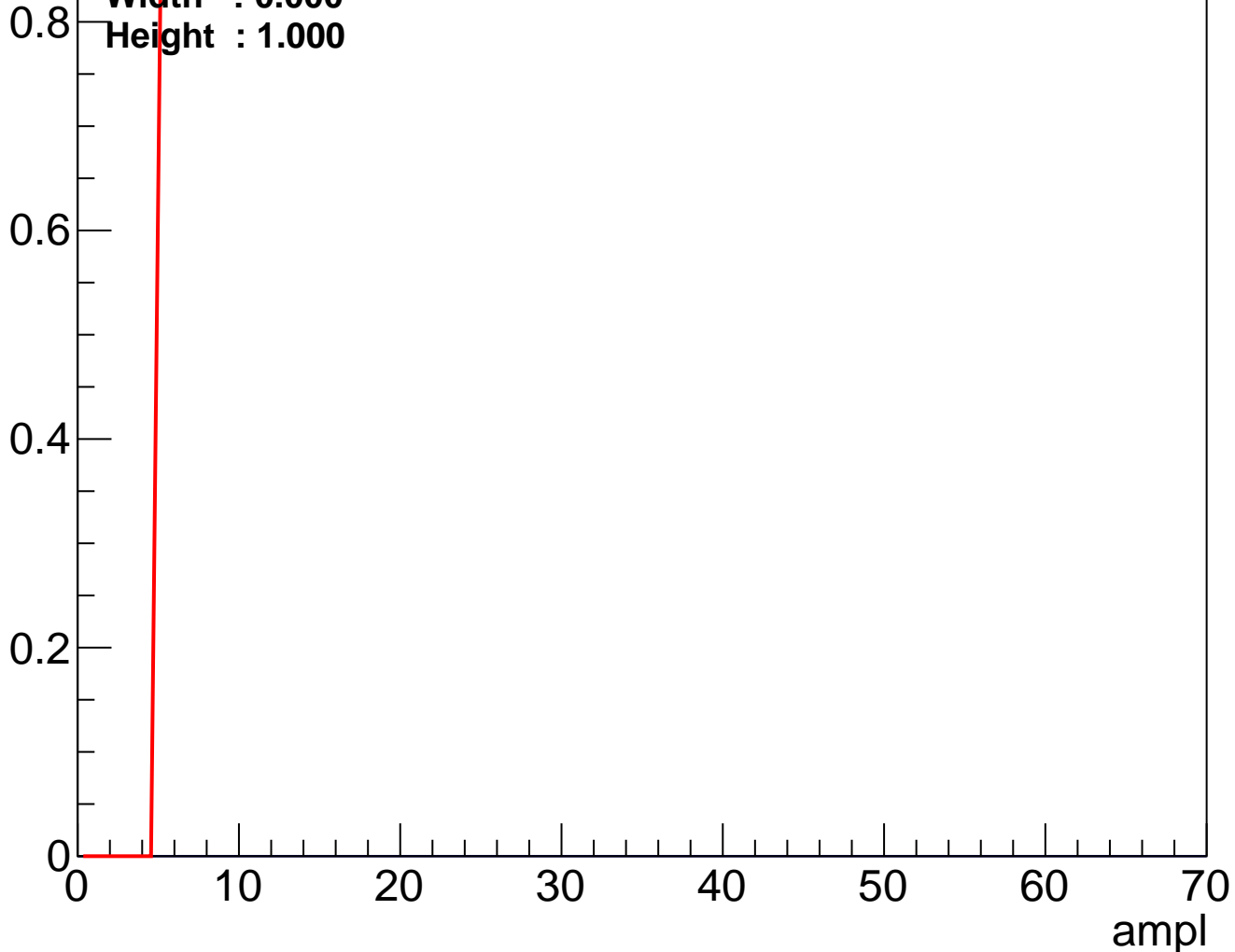
Profile : 00100000

Turn on : 5.0000

Width : 0.000

Height : 1.000

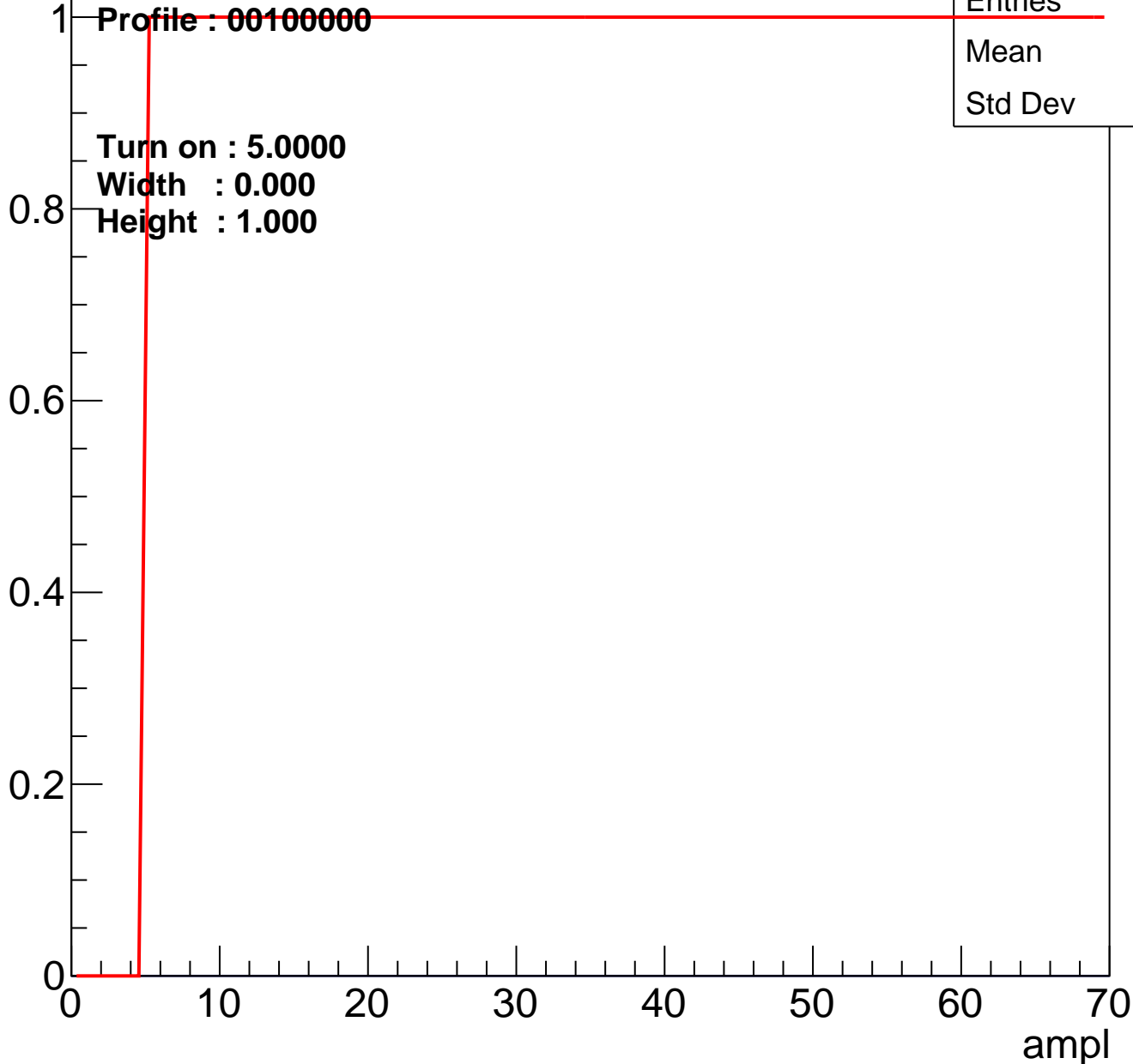
Entries	0
Mean	0
Std Dev	0



B1L003S, U18-ch18

calib_packv5_042523_0143.root, FC#13, Port D2

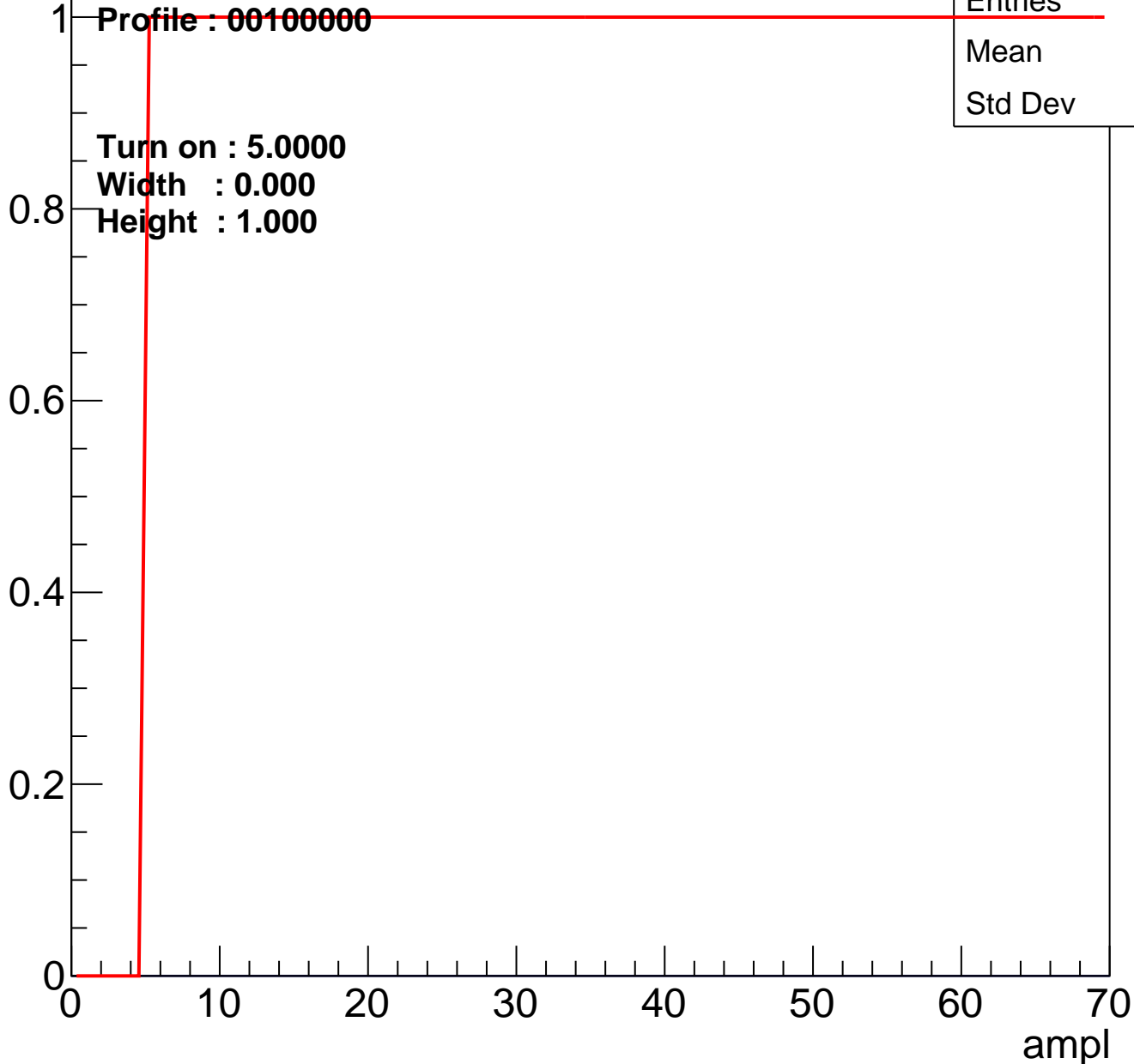
Entry



B1L003S, U18-ch19

calib_packv5_042523_0143.root, FC#13, Port D2

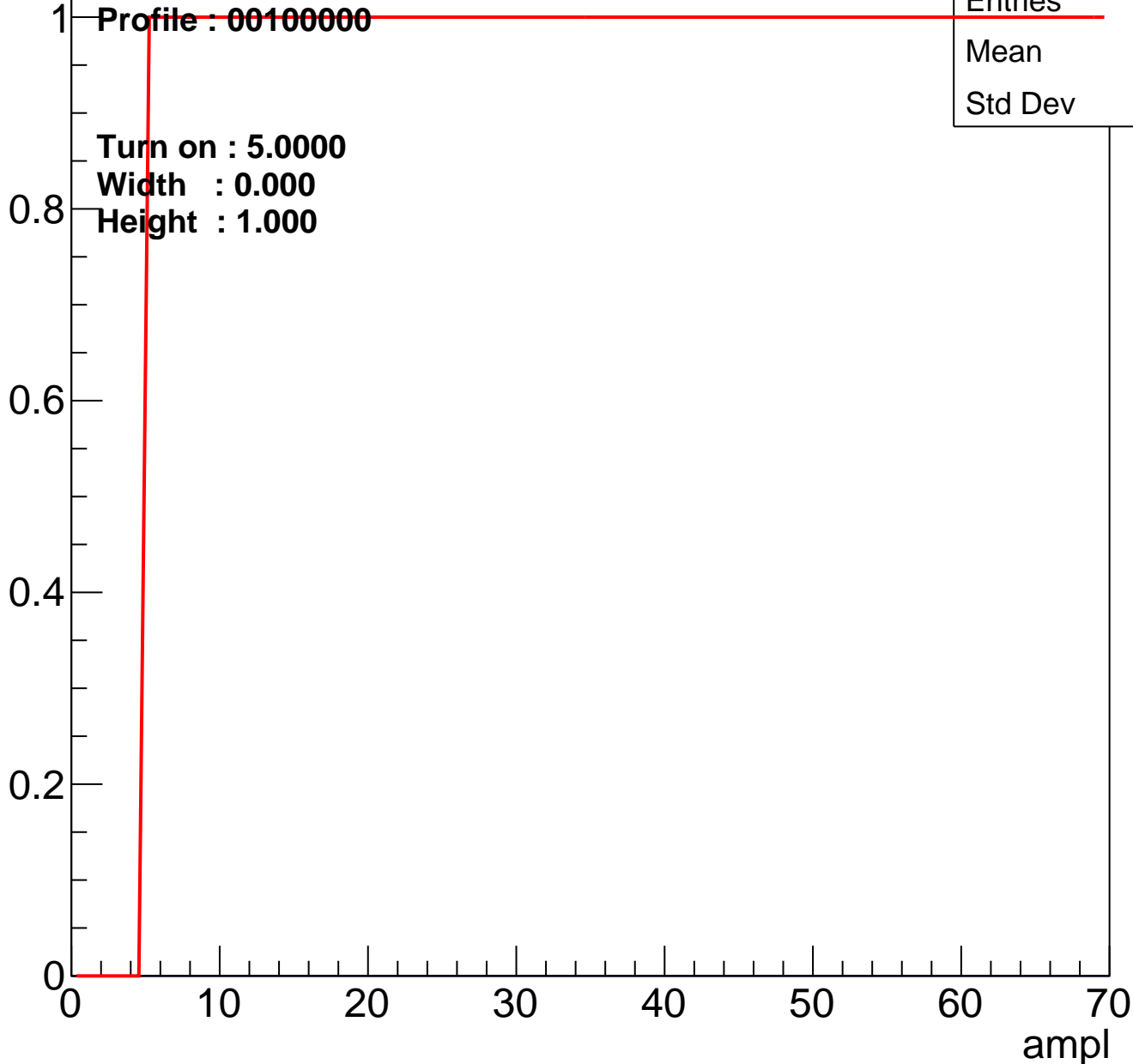
Entry



B1L003S, U18-ch20

calib_packv5_042523_0143.root, FC#13, Port D2

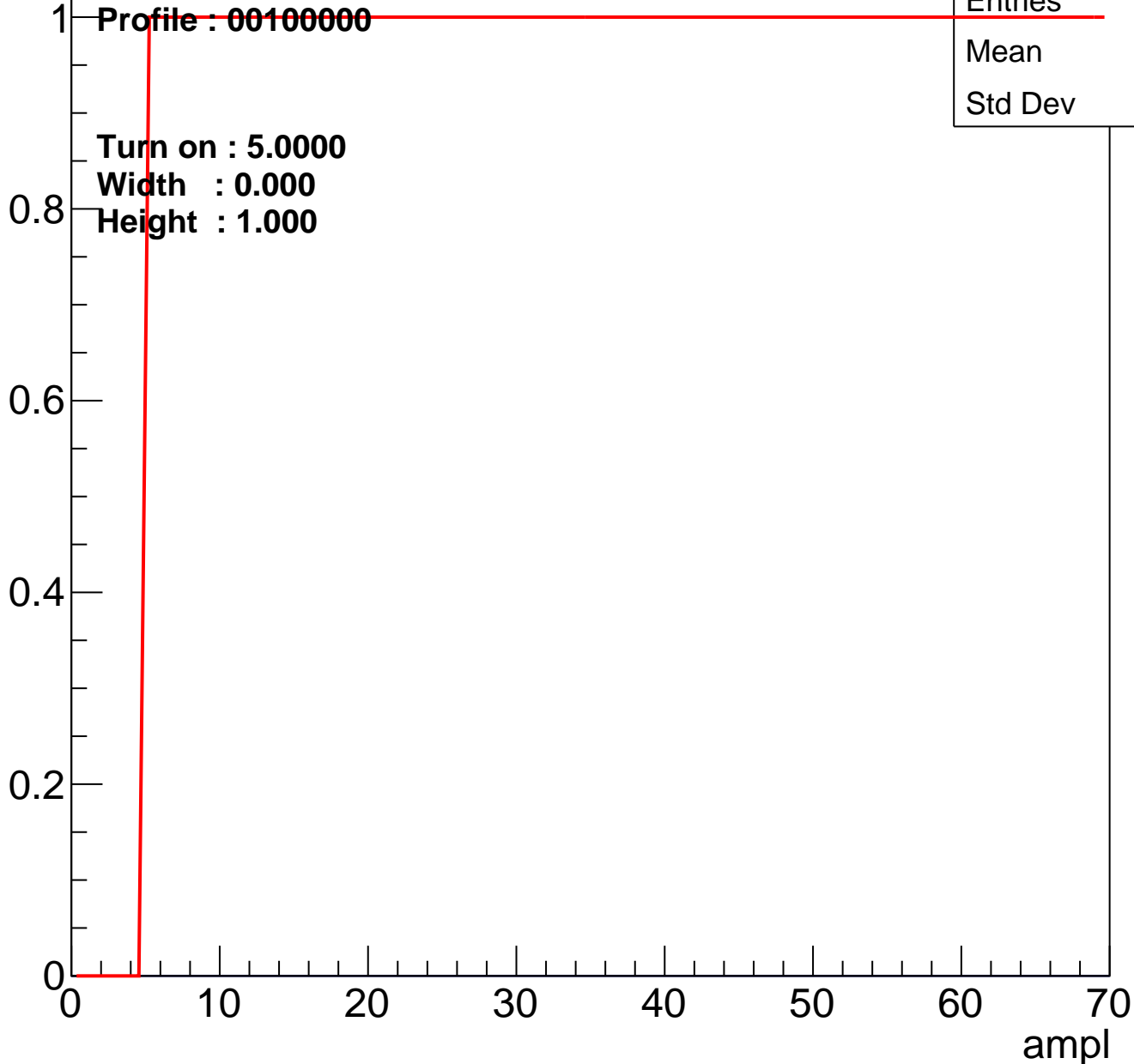
Entry



B1L003S, U18-ch21

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U18-ch22

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

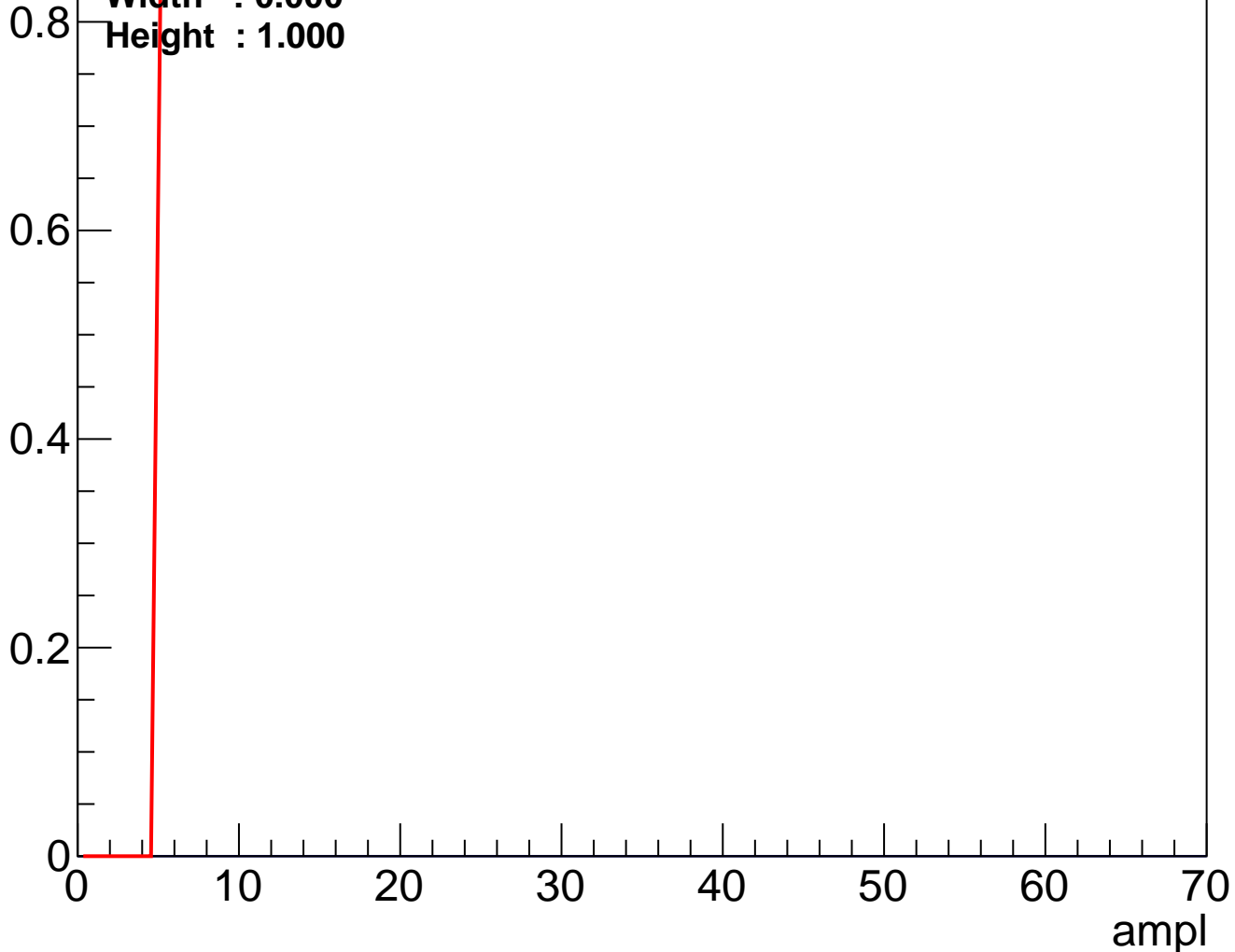
Profile : 001000000

Turn on : 5.0000

Width : 0.000

Height : 1.000

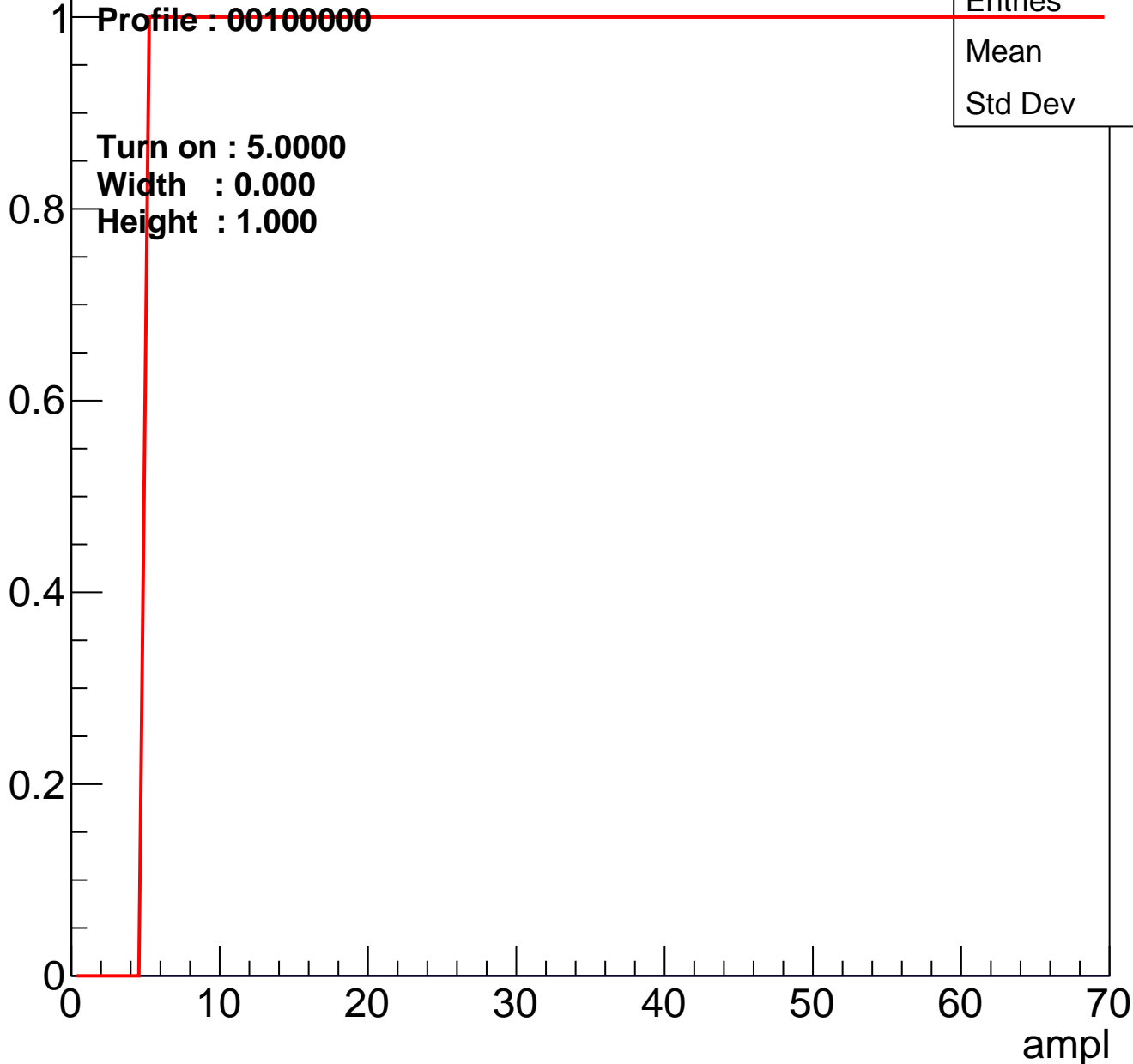
Entries	0
Mean	0
Std Dev	0



B1L003S, U18-ch23

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

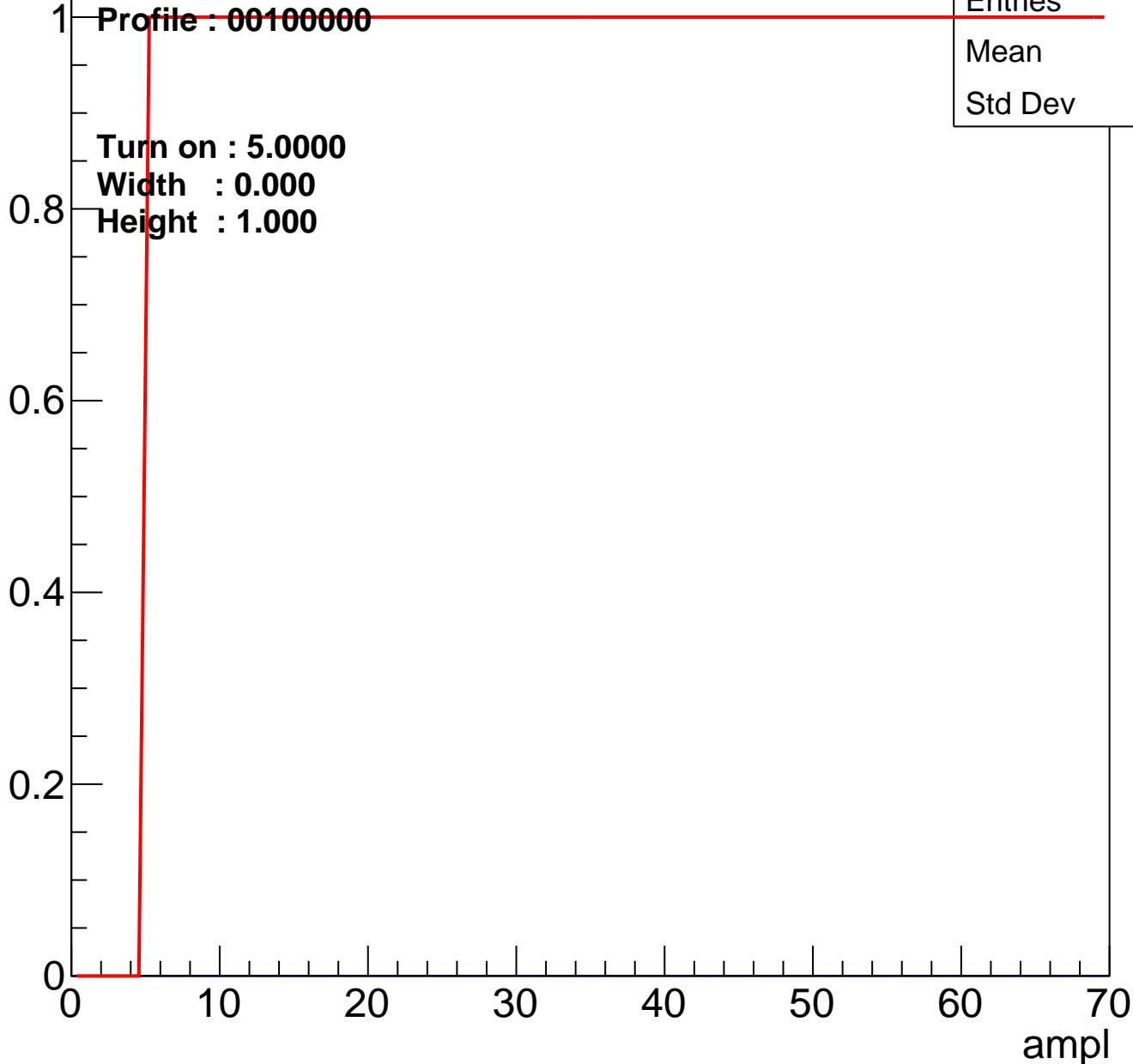


Entries	0
Mean	0
Std Dev	0

B1L003S, U18-ch24

calib_packv5_042523_0143.root, FC#13, Port D2

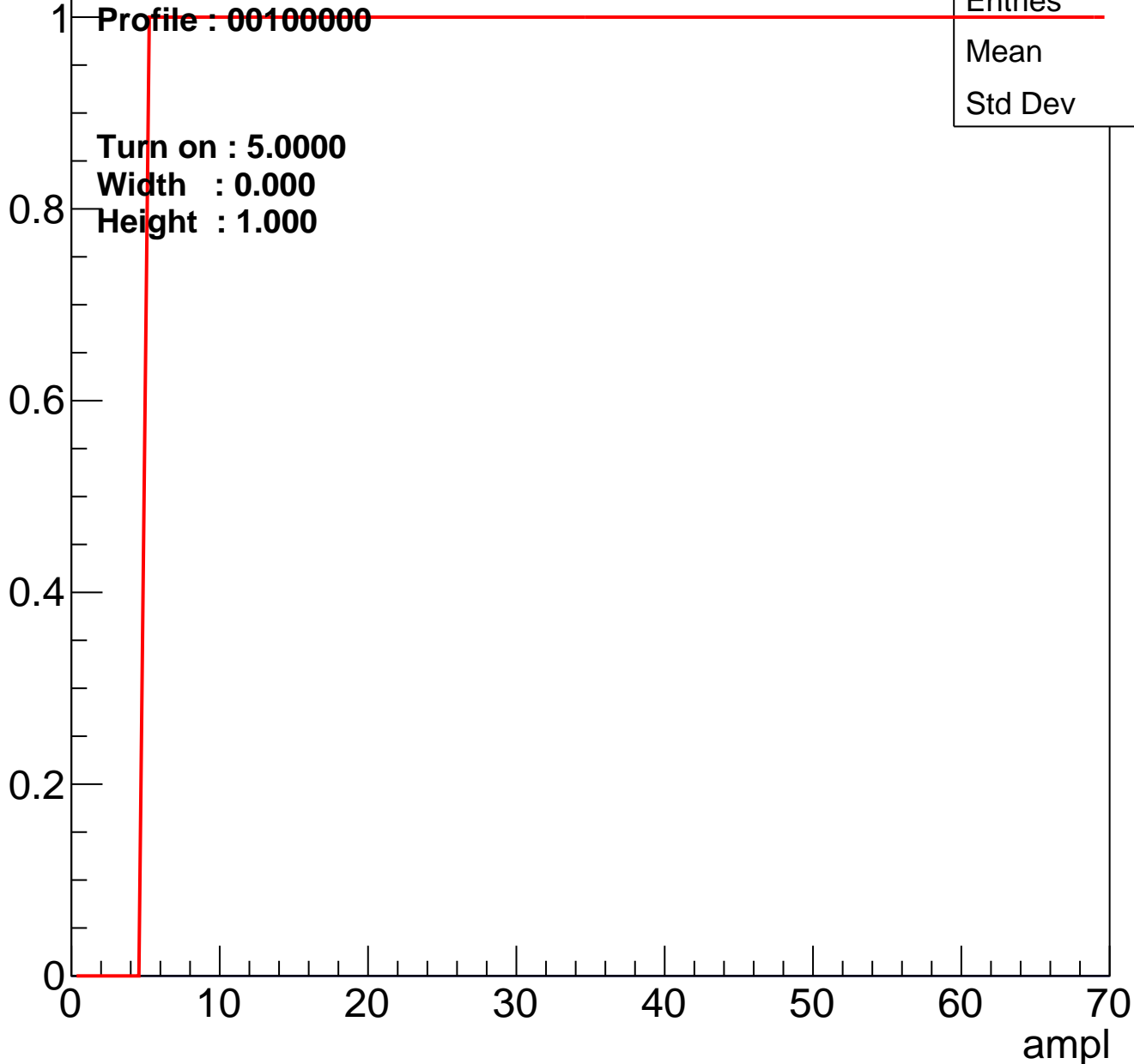
Entry



B1L003S, U18-ch25

calib_packv5_042523_0143.root, FC#13, Port D2

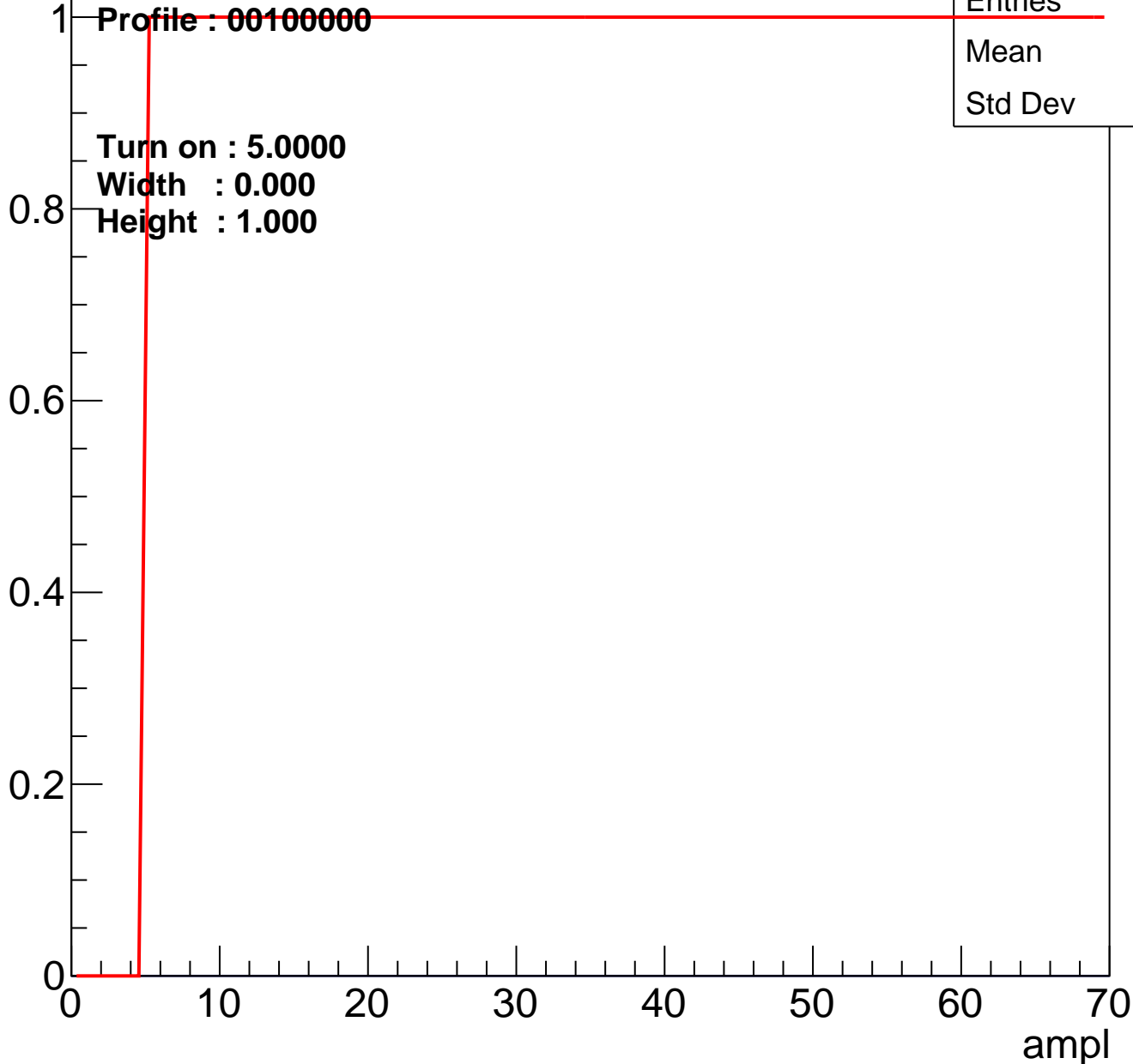
Entry



B1L003S, U18-ch26

calib_packv5_042523_0143.root, FC#13, Port D2

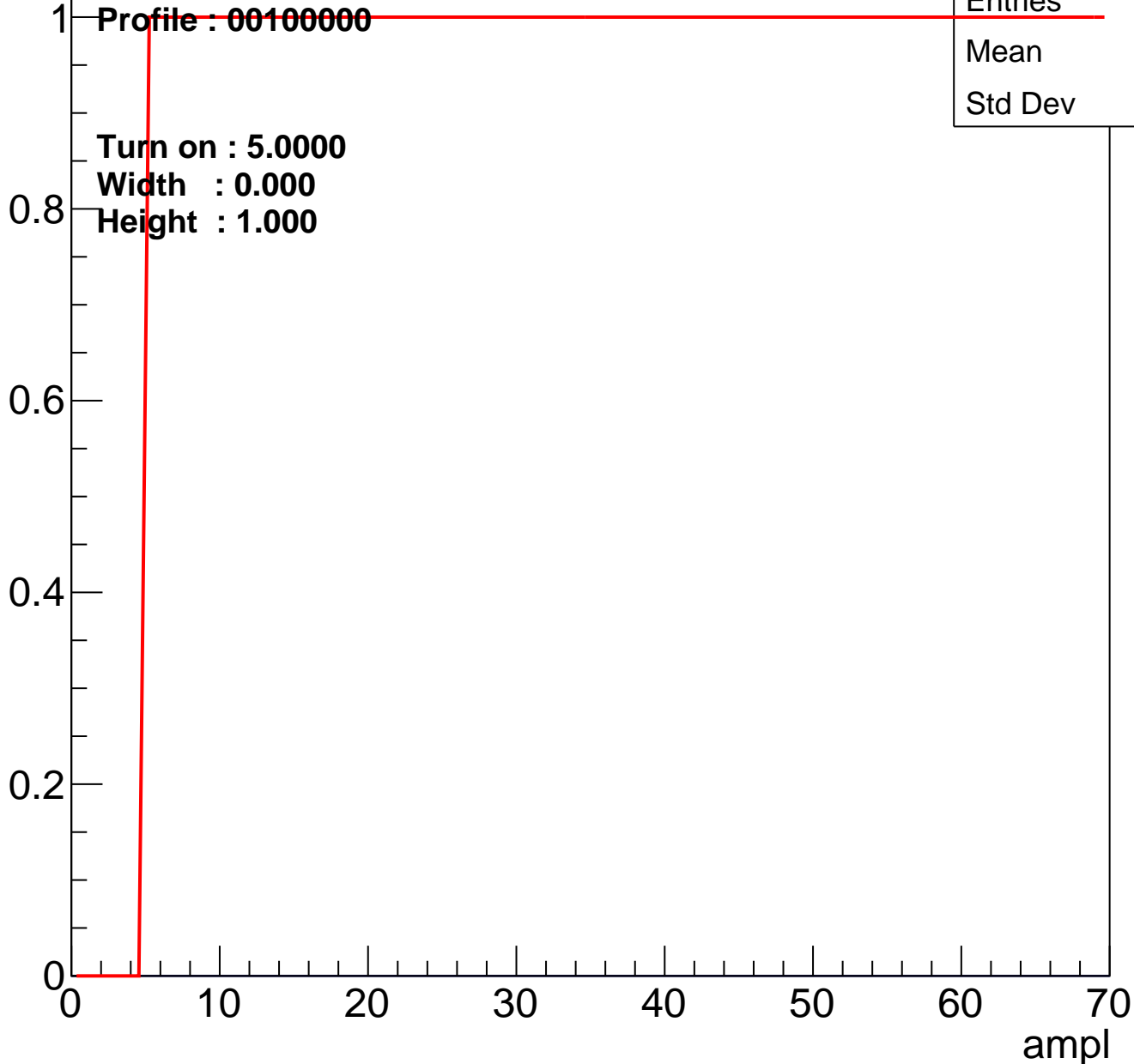
Entry



B1L003S, U18-ch27

calib_packv5_042523_0143.root, FC#13, Port D2

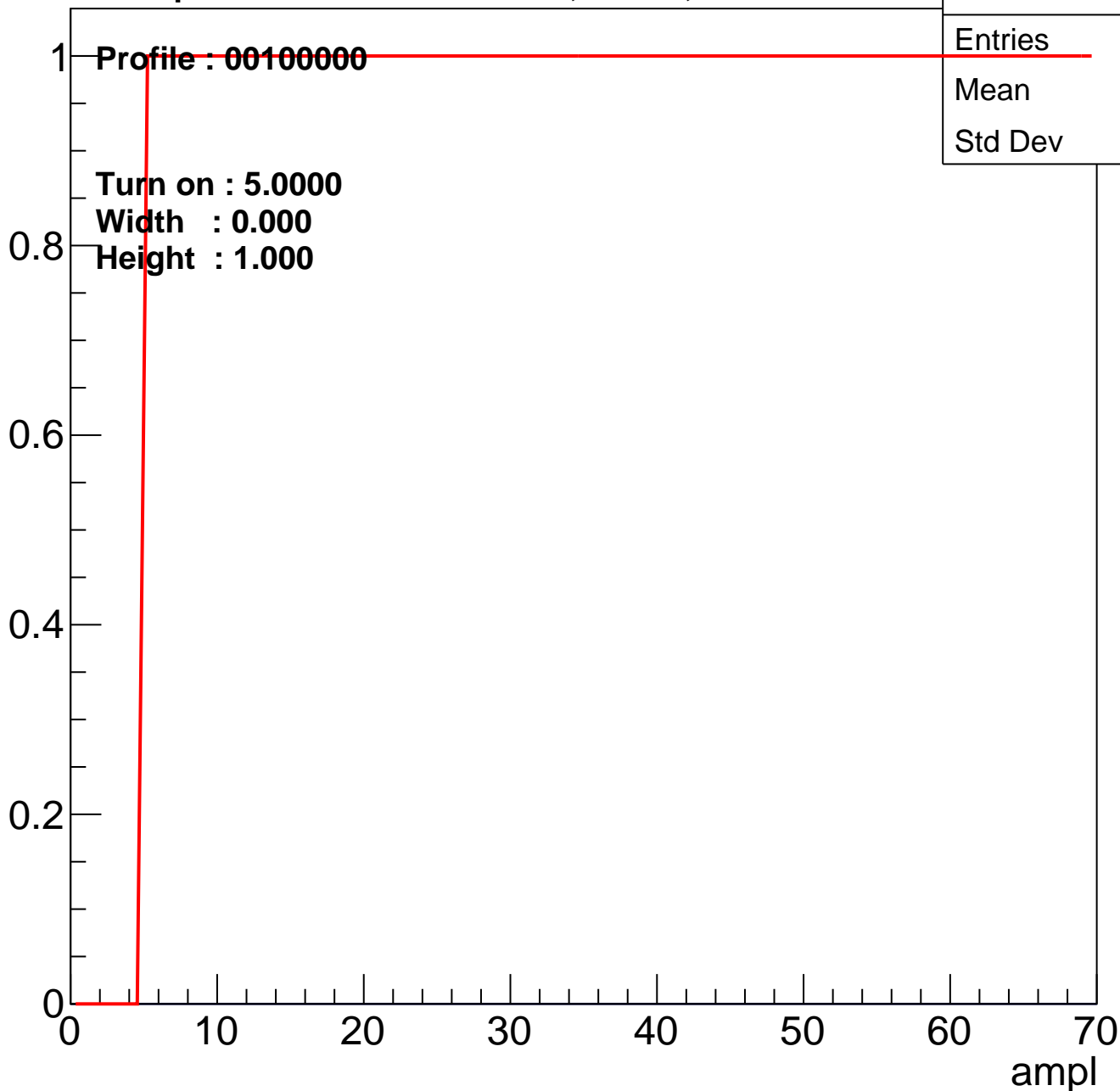
Entry



B1L003S, U18-ch28

calib_packv5_042523_0143.root, FC#13, Port D2

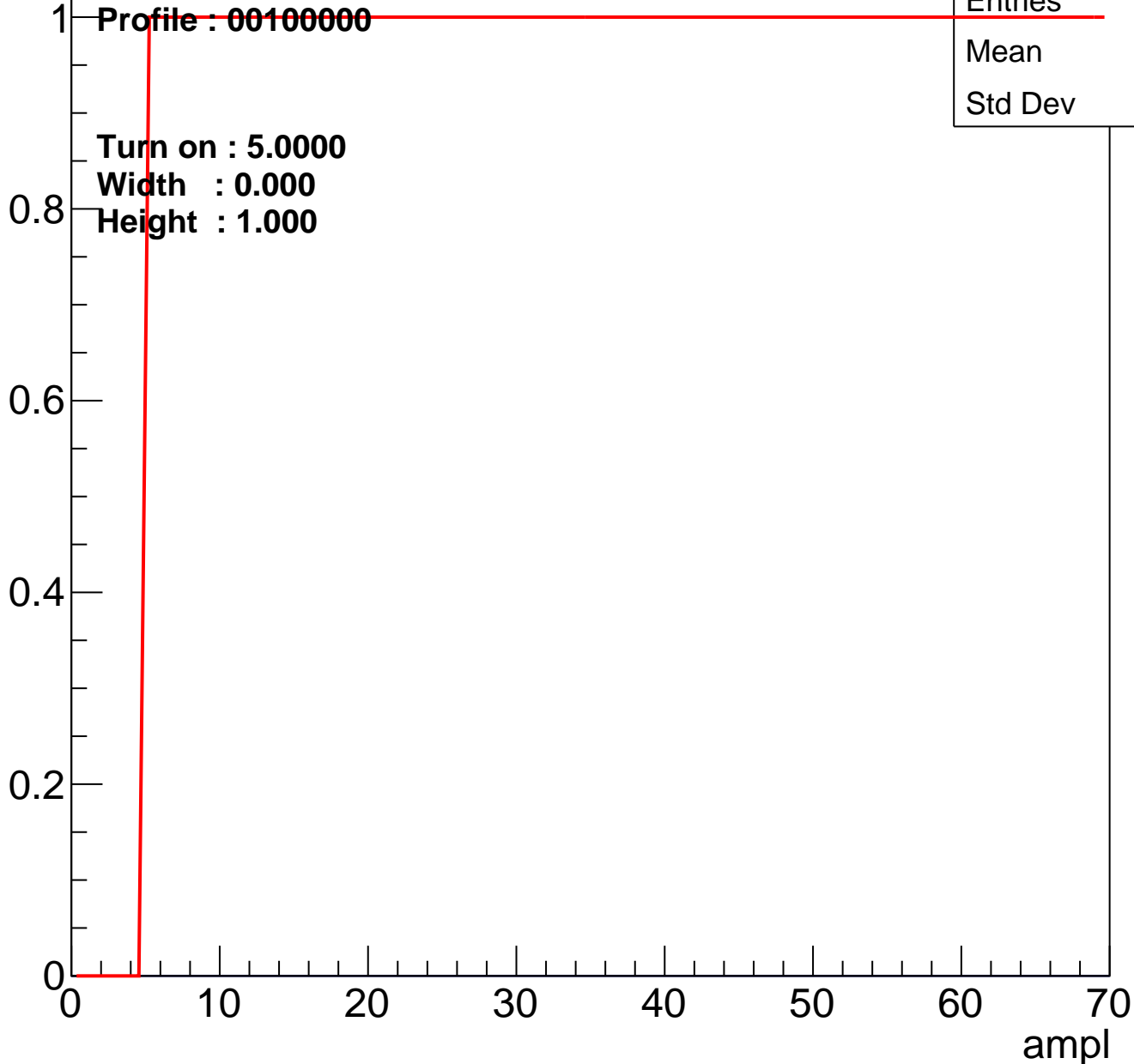
Entry



B1L003S, U18-ch29

calib_packv5_042523_0143.root, FC#13, Port D2

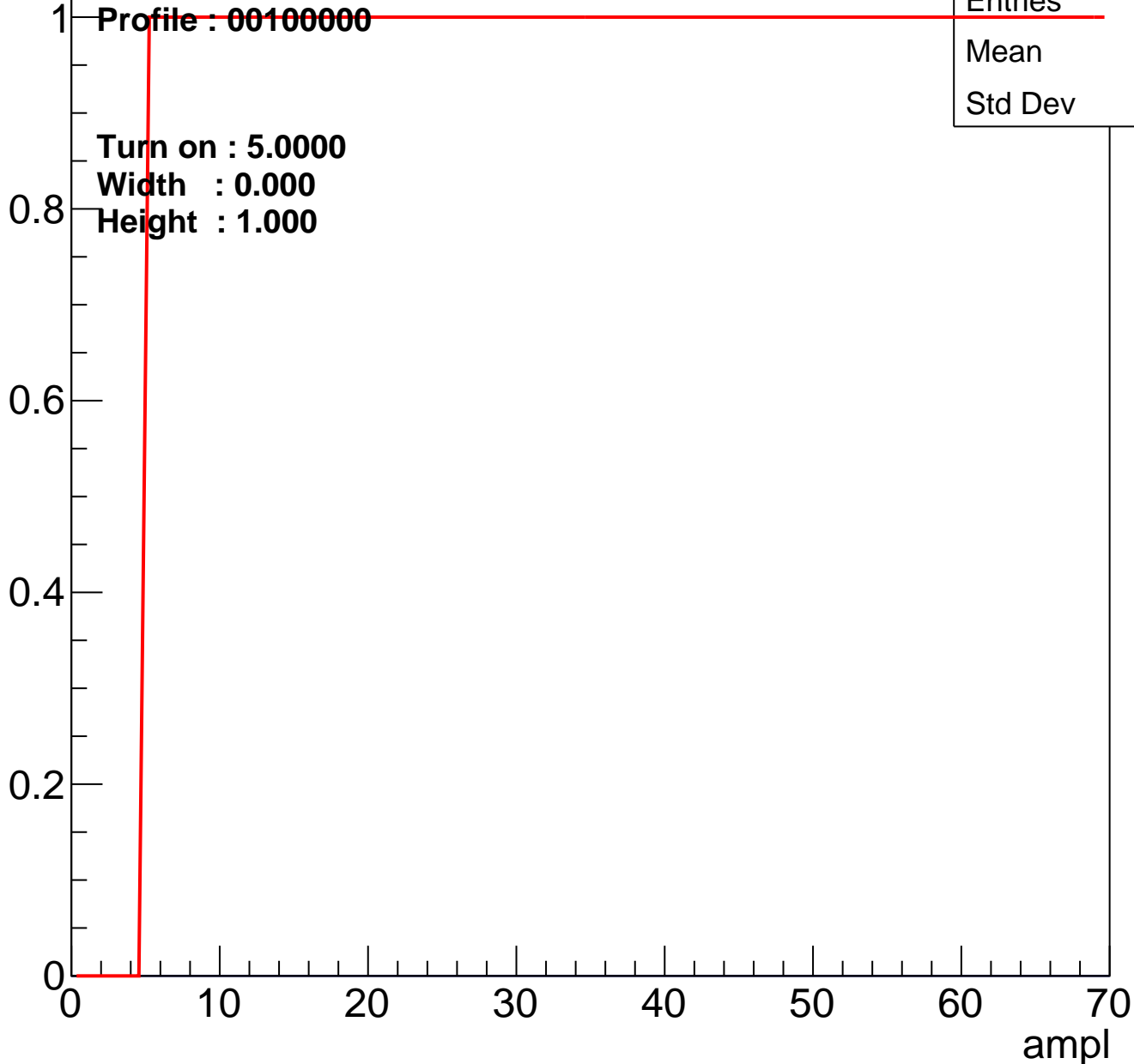
Entry



B1L003S, U18-ch30

calib_packv5_042523_0143.root, FC#13, Port D2

Entry



B1L003S, U18-ch31

calib_packv5_042523_0143.root, FC#13, Port D2

Entry

