

B1L103S, U9-ch0

calib_packv5_042523_0143.root, FC#7, port C2

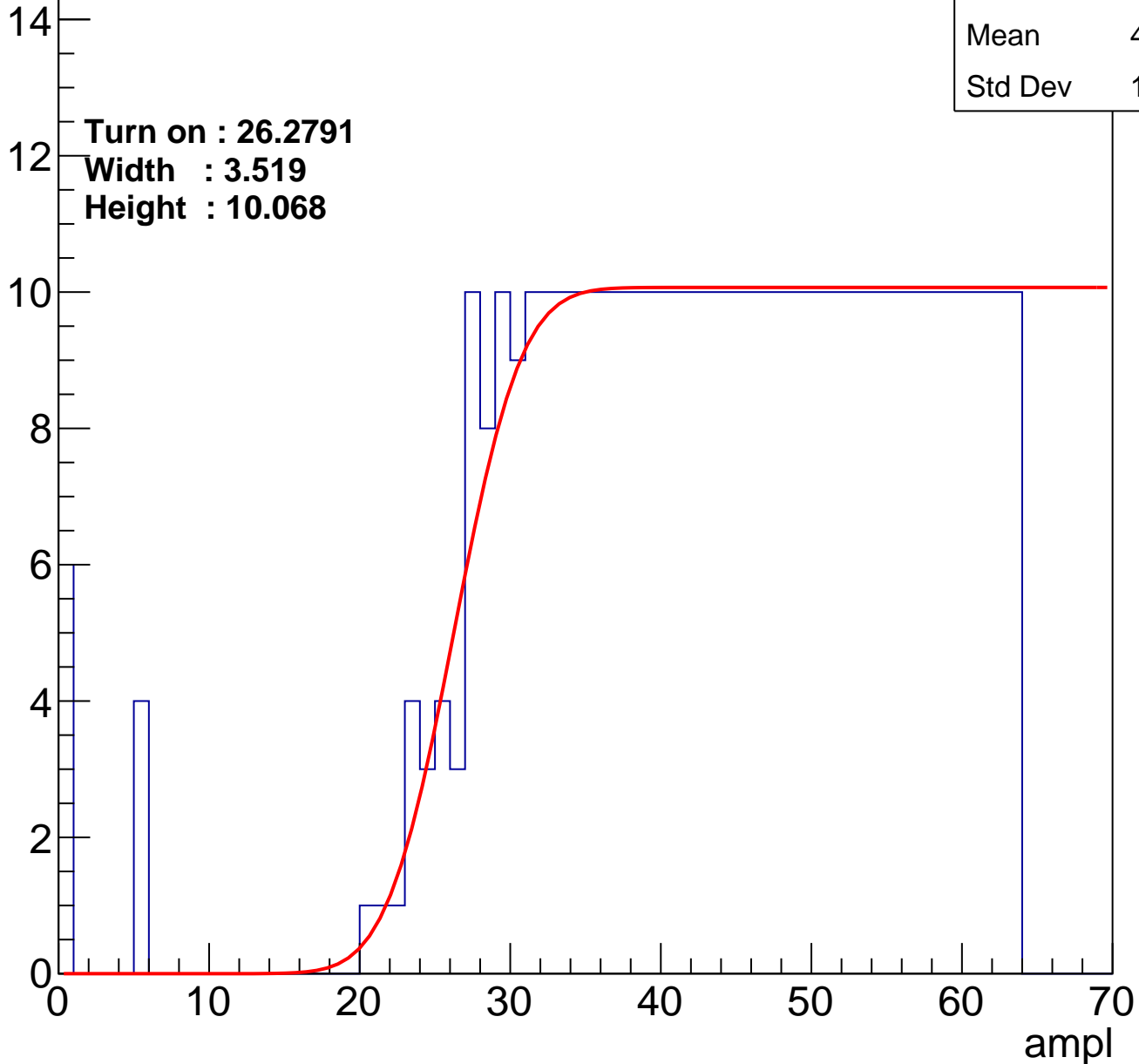
Entries	394
Mean	43.12
Std Dev	12.96

Turn on : 26.2791

Width : 3.519

Height : 10.068

Entry



B1L103S, U9-ch1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.23
Std Dev	11.03

Turn on : 28.9413

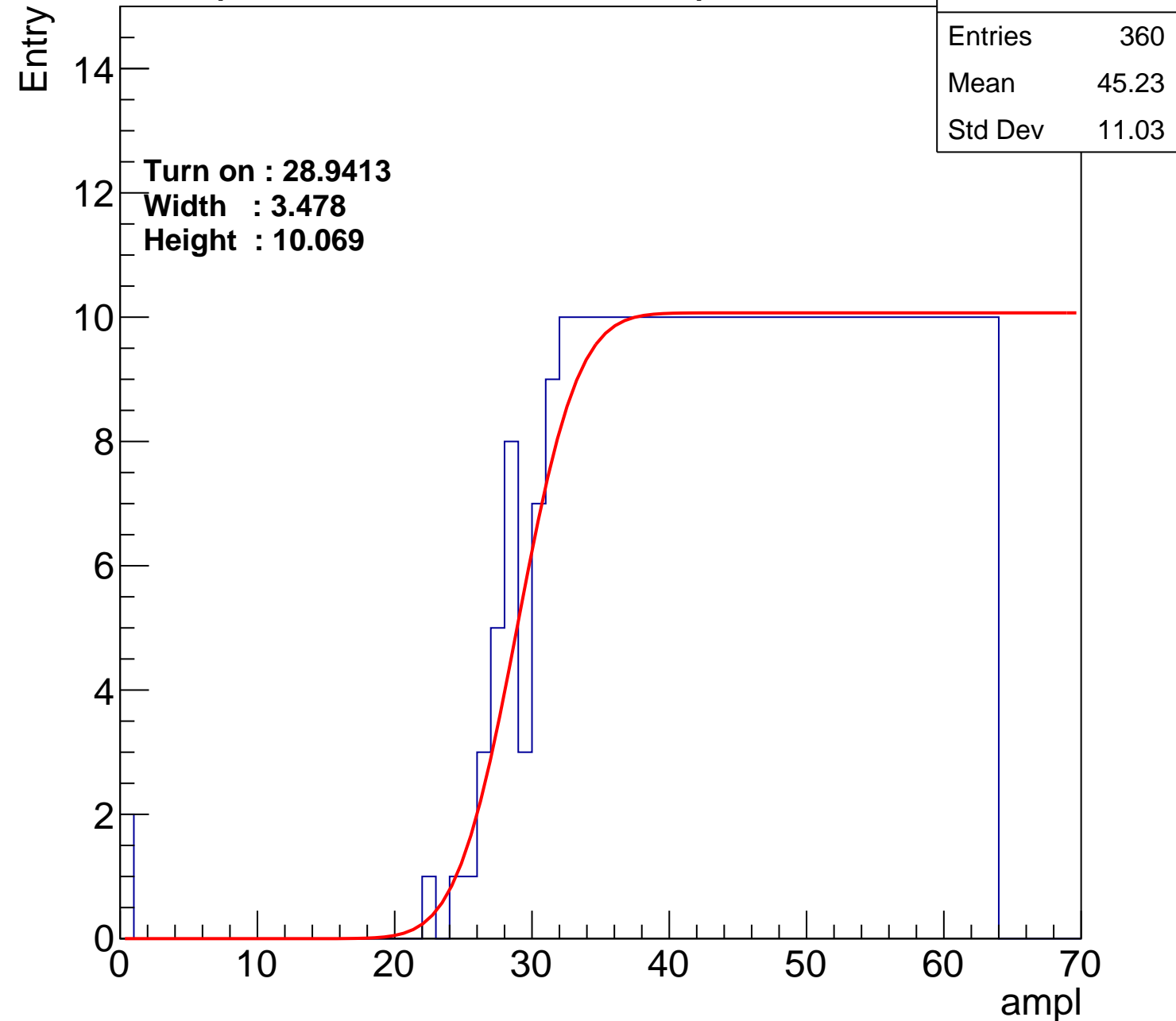
Width : 3.478

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch2

calib_packv5_042523_0143.root, FC#7, port C2

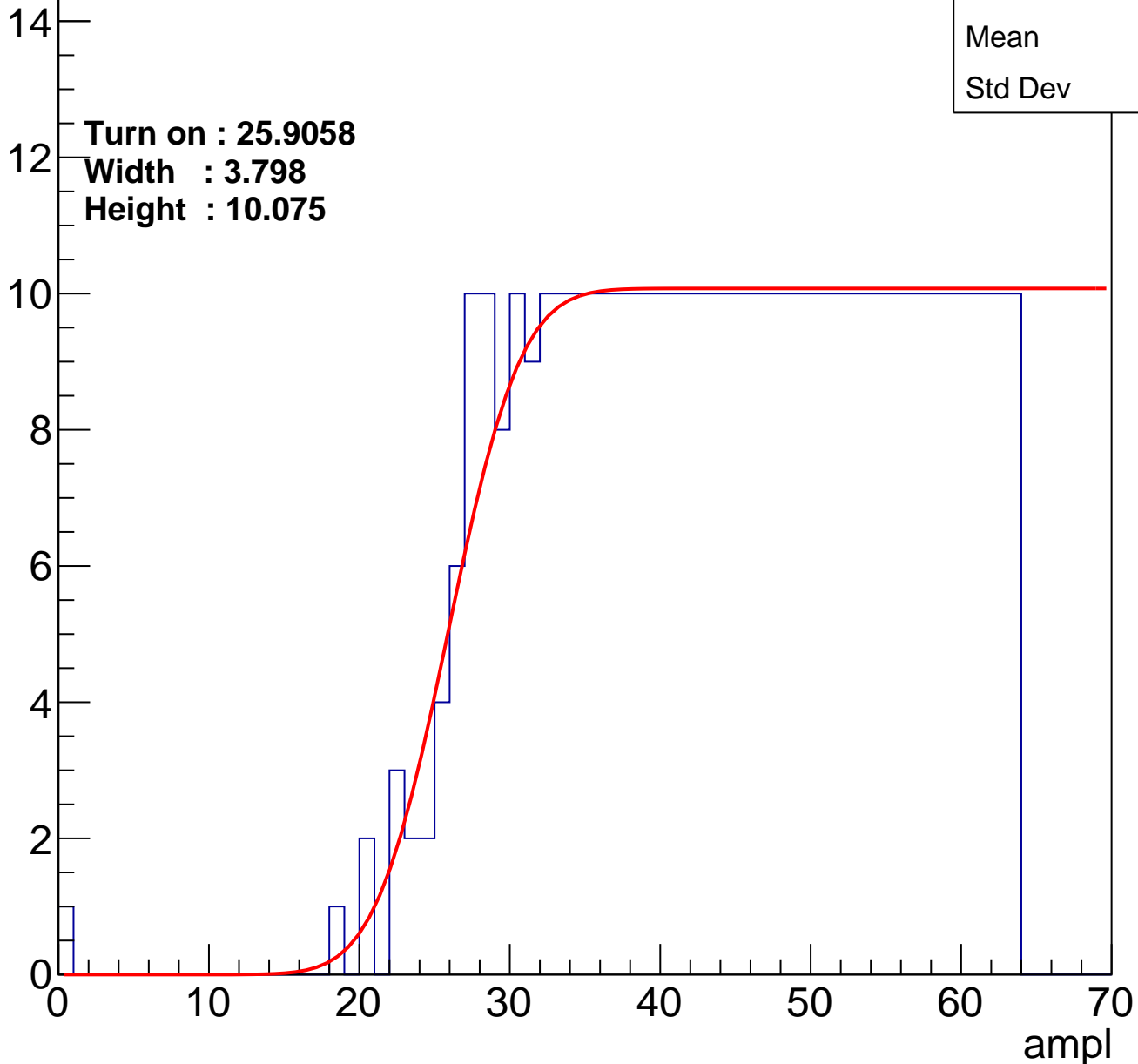
Entries	388
Mean	43.9
Std Dev	11.6

Turn on : 25.9058

Width : 3.798

Height : 10.075

Entry



B1L103S, U9-ch3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.8073

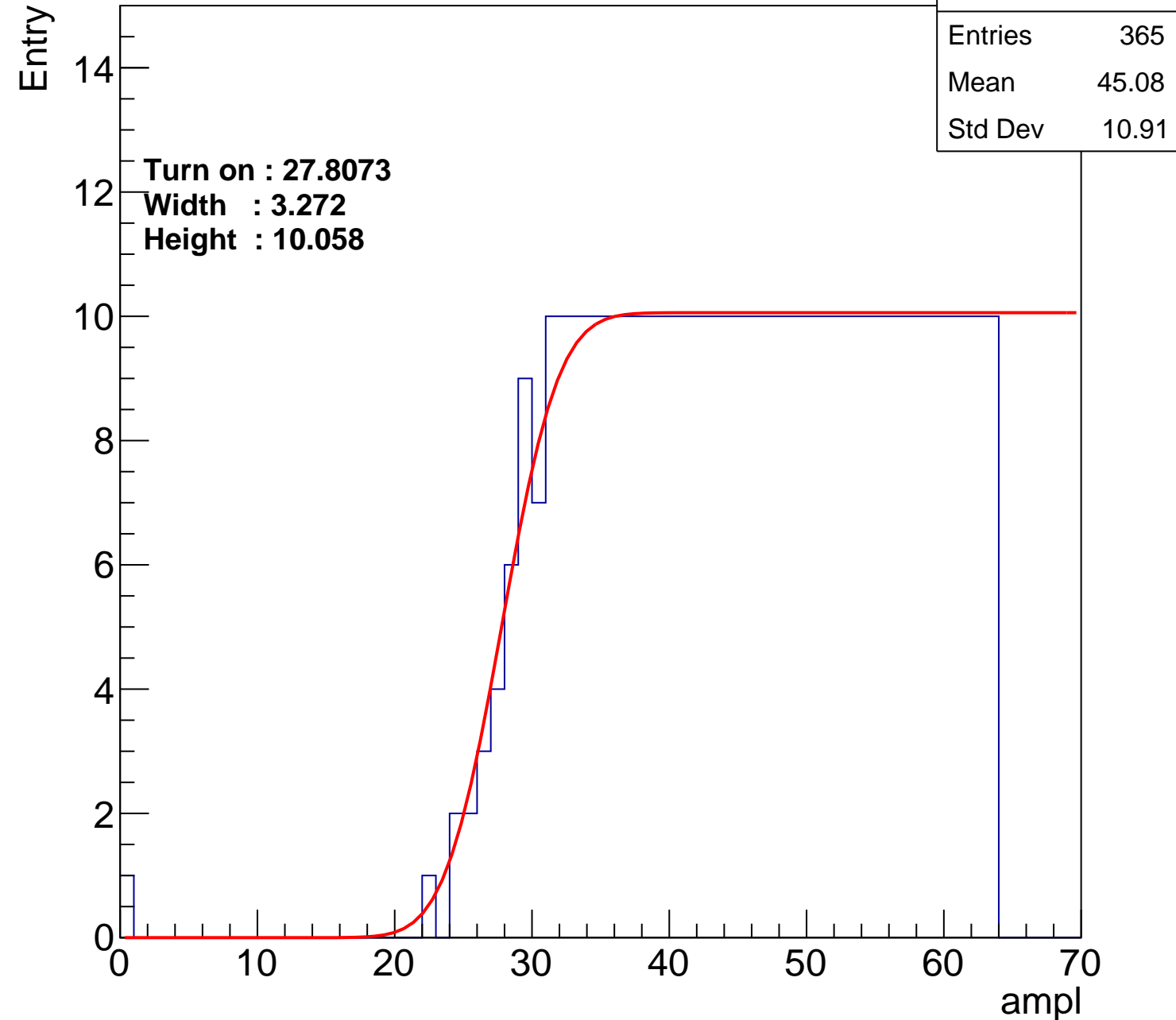
Width : 3.272

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch4

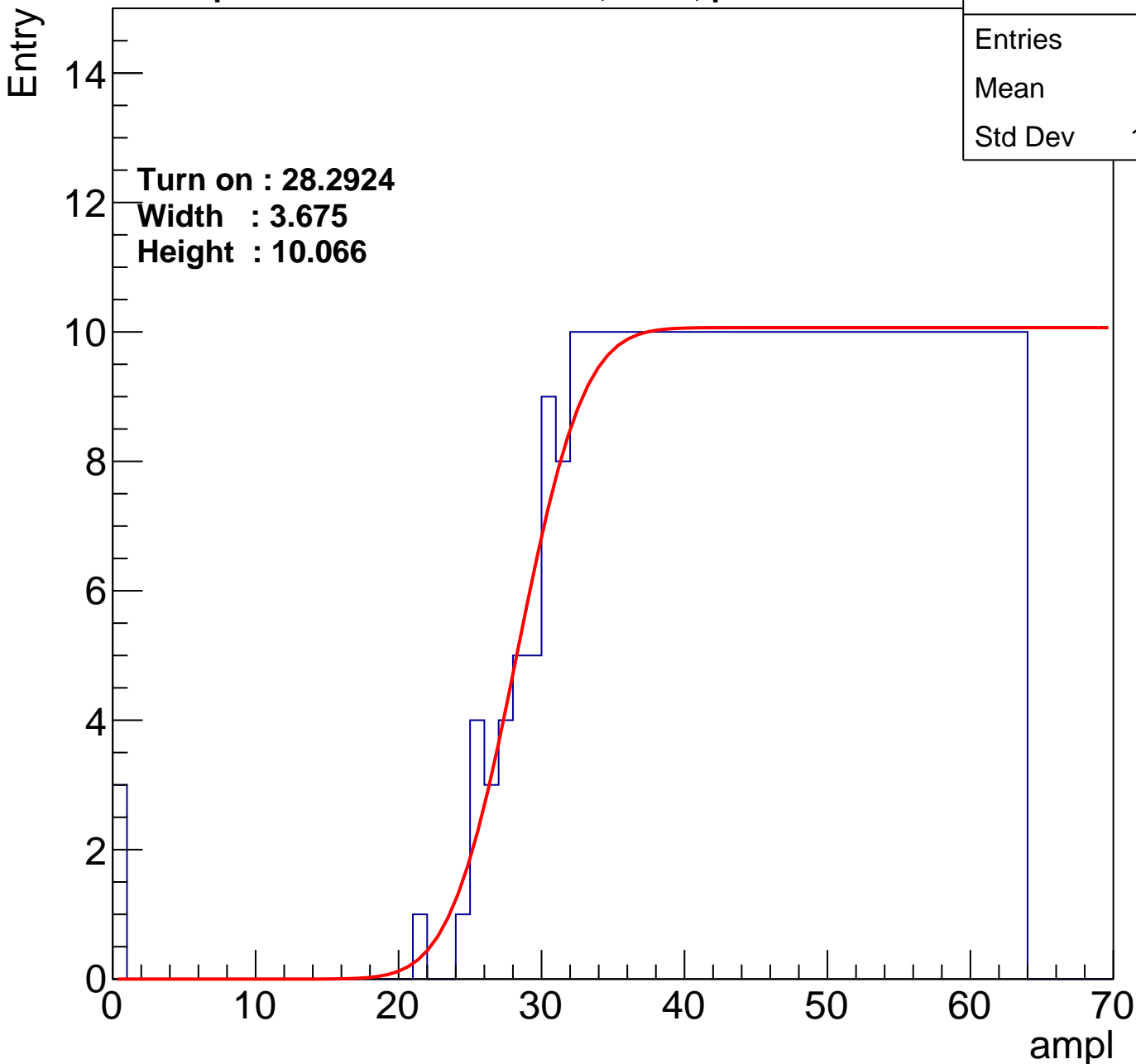
calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	45
Std Dev	11.34

Turn on : 28.2924

Width : 3.675

Height : 10.066



B1L103S, U9-ch5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.3
Std Dev	10.78

Turn on : 28.3689

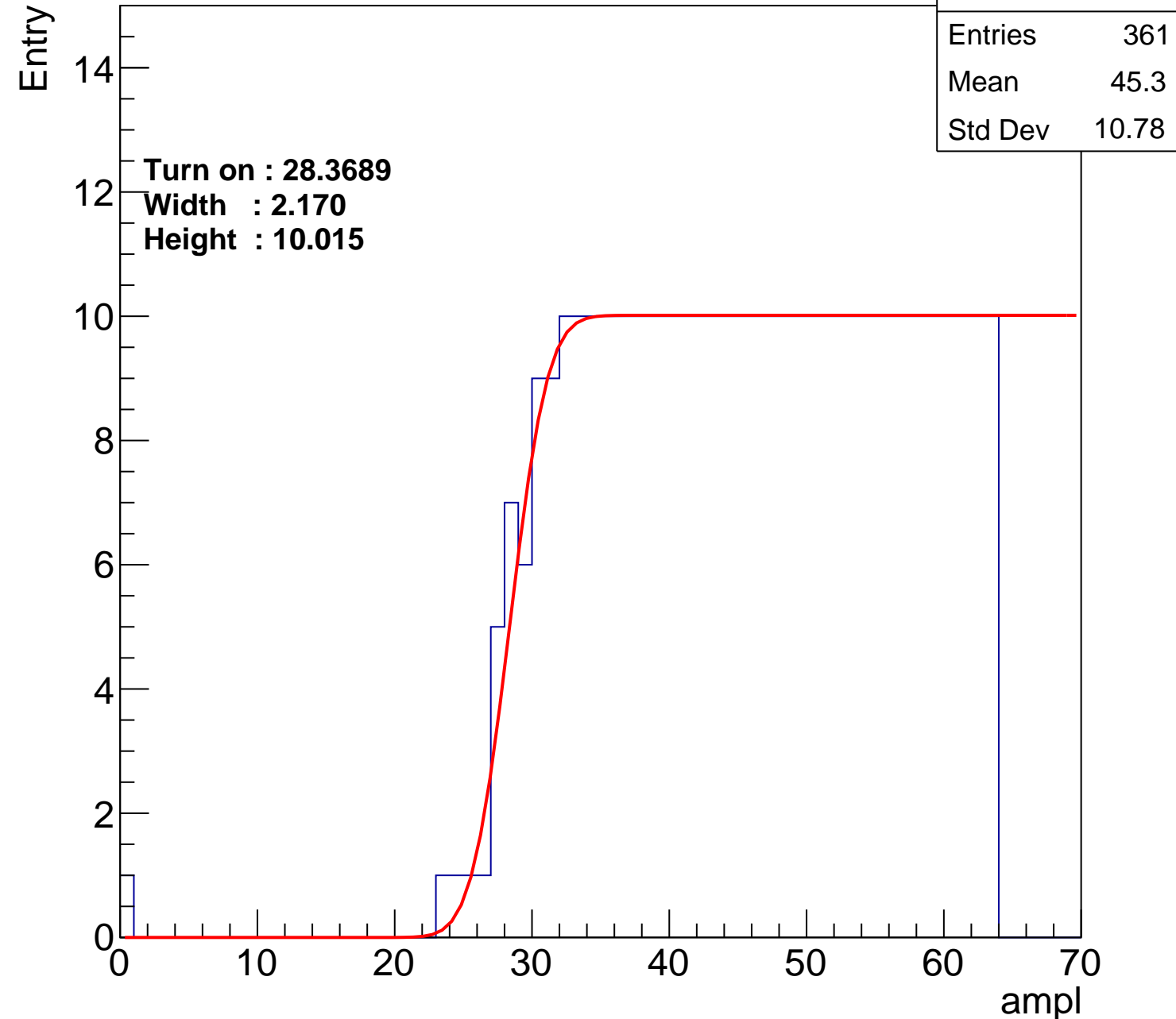
Width : 2.170

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch6

calib_packv5_042523_0143.root, FC#7, port C2

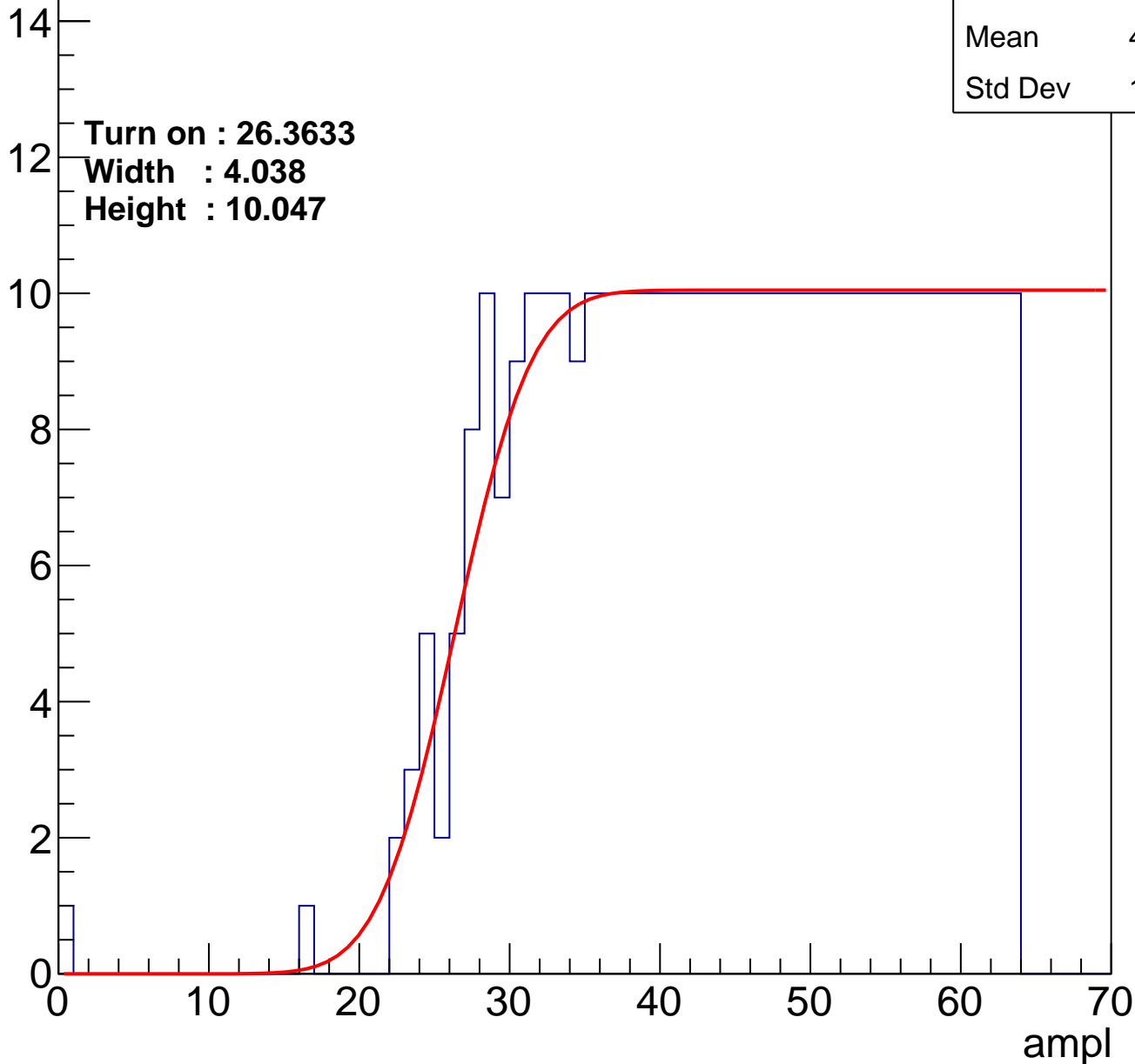
Entry

Entries	382
Mean	44.17
Std Dev	11.48

Turn on : 26.3633

Width : 4.038

Height : 10.047



B1L103S, U9-ch7

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.49
Std Dev	10.89

Turn on : 29.1576

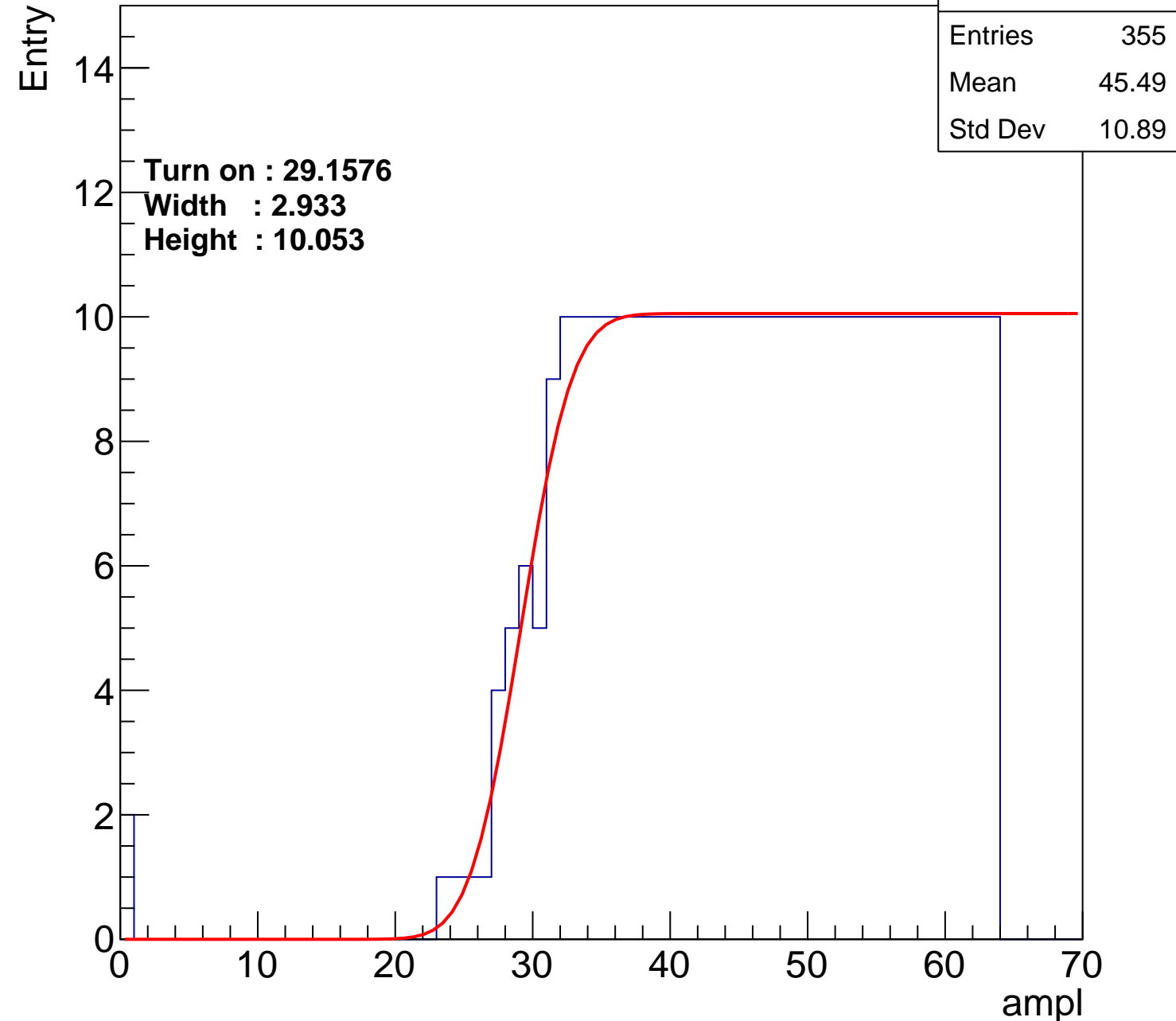
Width : 2.933

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch8

calib_packv5_042523_0143.root, FC#7, port C2

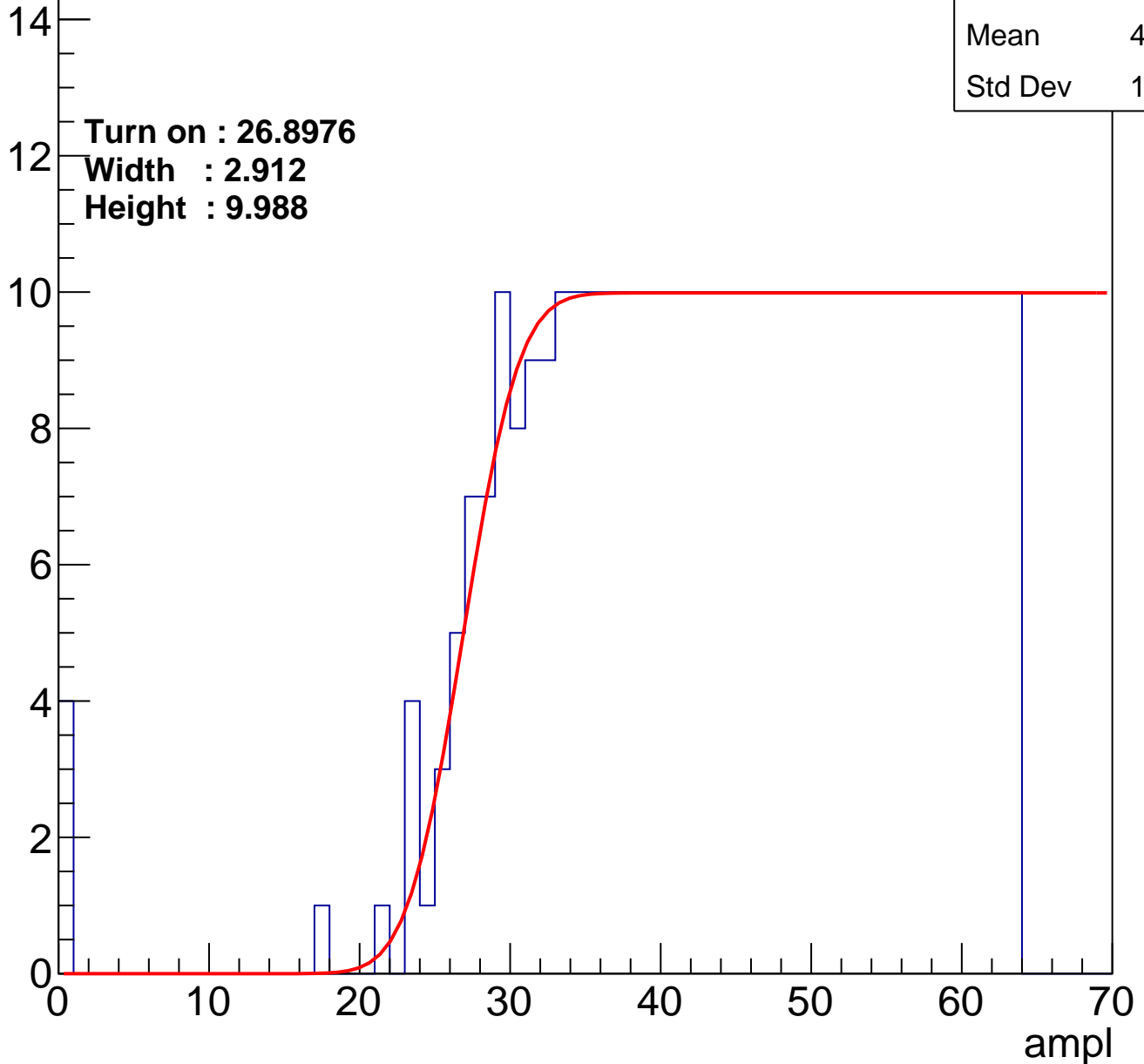
Entries	379
Mean	44.12
Std Dev	11.94

Turn on : 26.8976

Width : 2.912

Height : 9.988

Entry



B1L103S, U9-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.93
Std Dev	11.67

Turn on : 25.7428

Width : 3.106

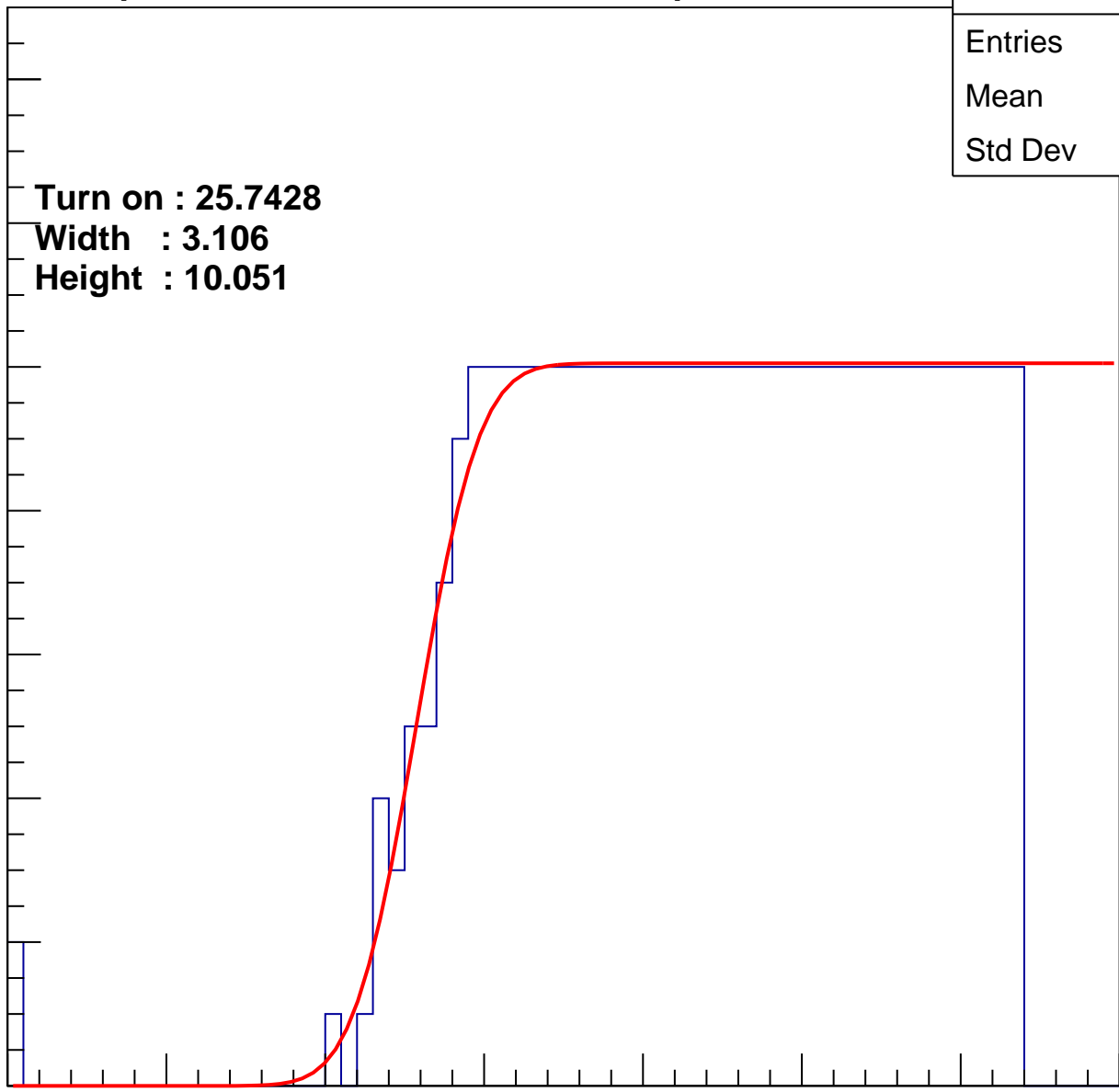
Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U9-ch10

calib_packv5_042523_0143.root, FC#7, port C2

Entries	398
Mean	43.23
Std Dev	12.33

Turn on : 25.0261

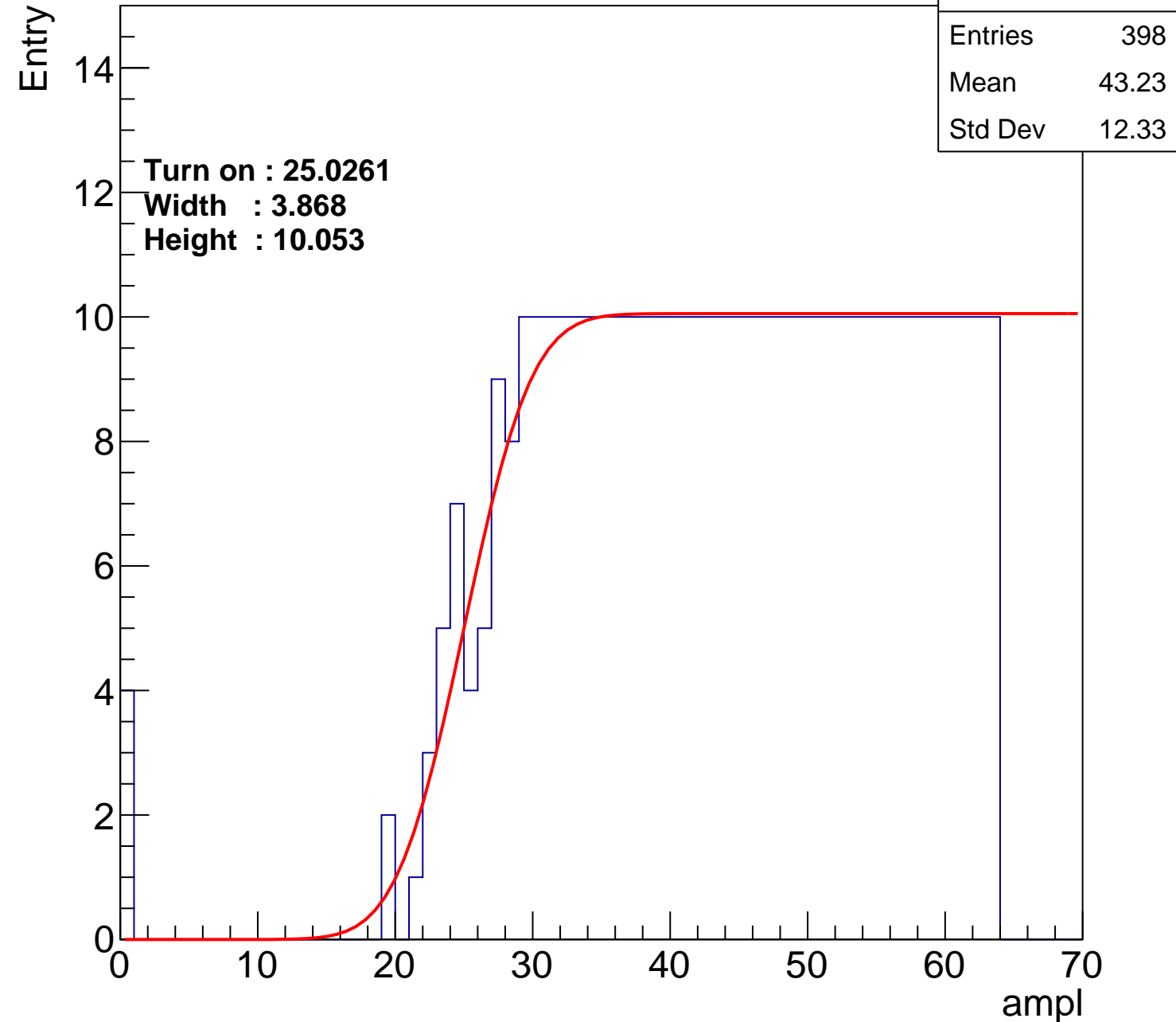
Width : 3.868

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch11

calib_packv5_042523_0143.root, FC#7, port C2

Entries	352
Mean	45.62
Std Dev	10.83

Turn on : 28.5606

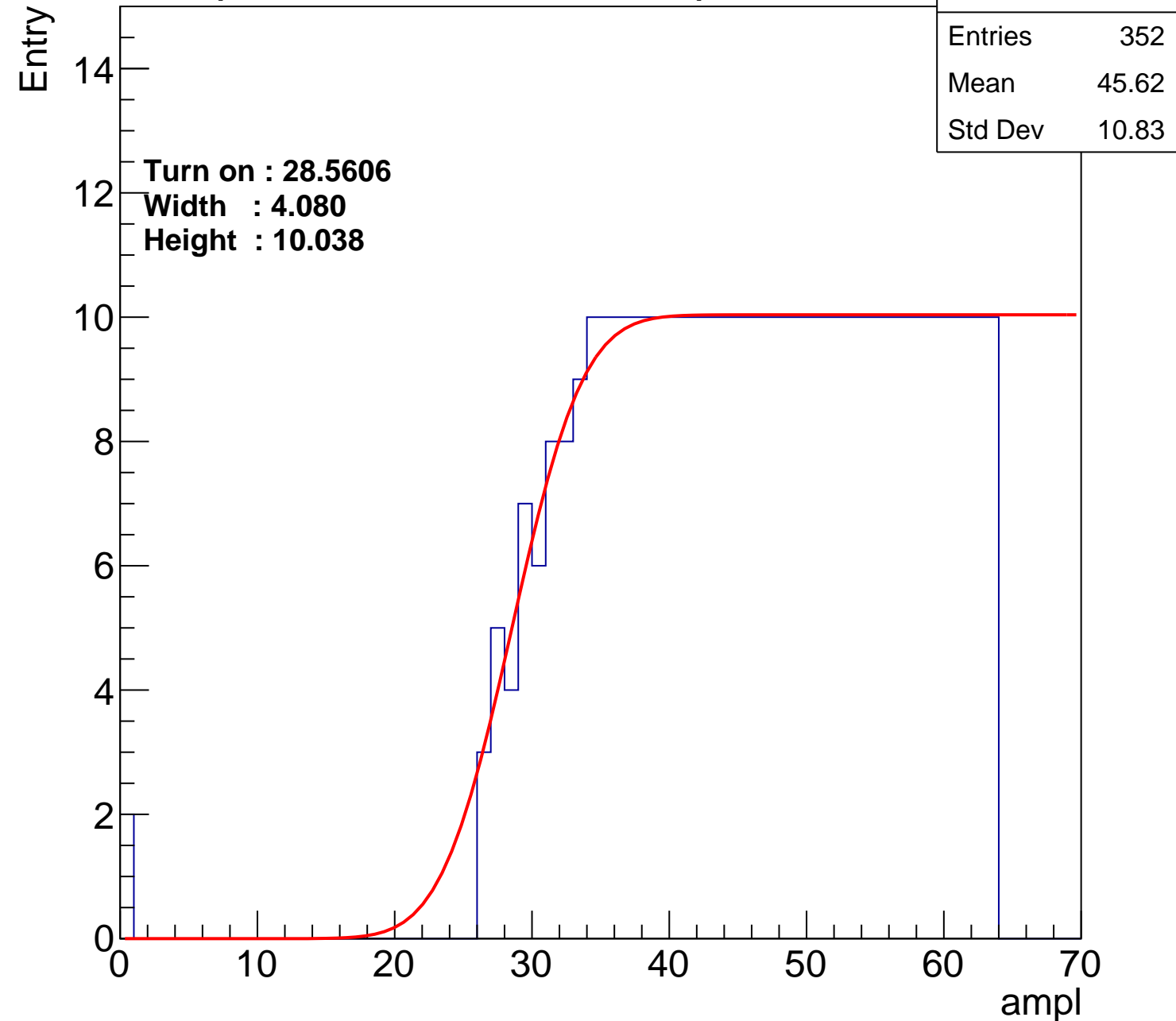
Width : 4.080

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch12

calib_packv5_042523_0143.root, FC#7, port C2

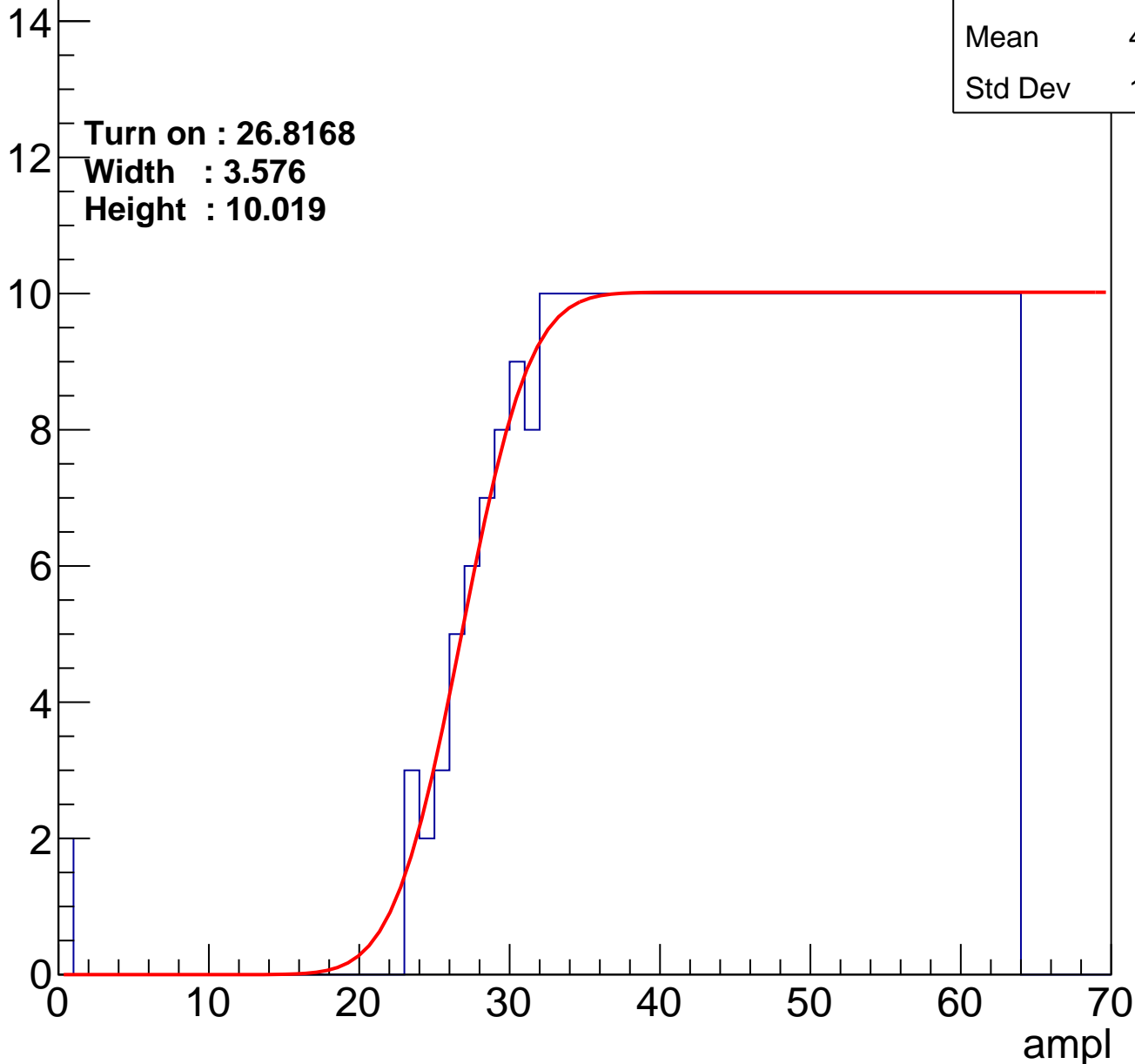
Entries	373
Mean	44.58
Std Dev	11.37

Turn on : 26.8168

Width : 3.576

Height : 10.019

Entry



B1L103S, U9-ch13

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.27
Std Dev	12

Turn on : 27.2226

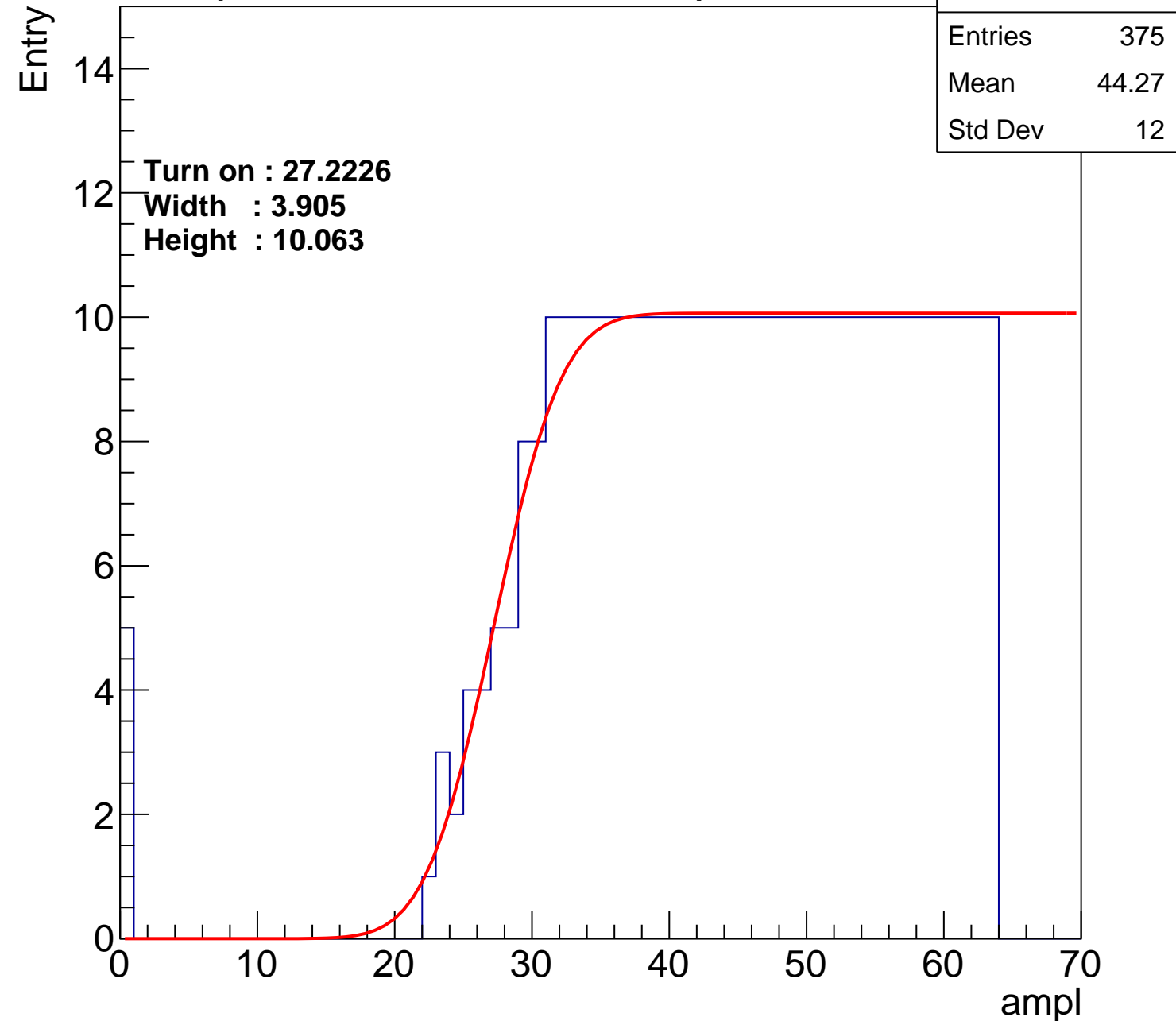
Width : 3.905

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch14

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.38
Std Dev	11.92

Turn on : 27.5450

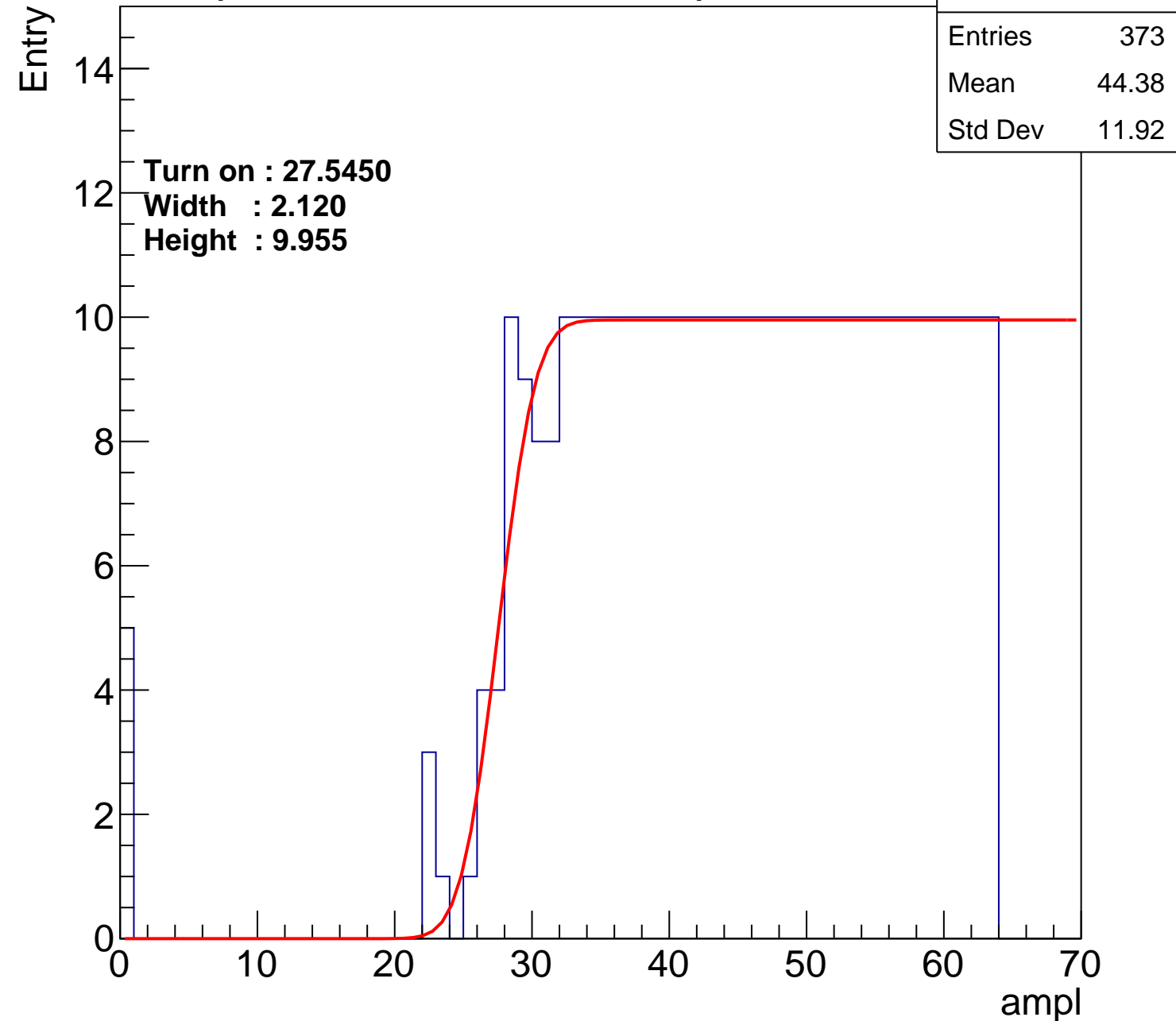
Width : 2.120

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch15

calib_packv5_042523_0143.root, FC#7, port C2

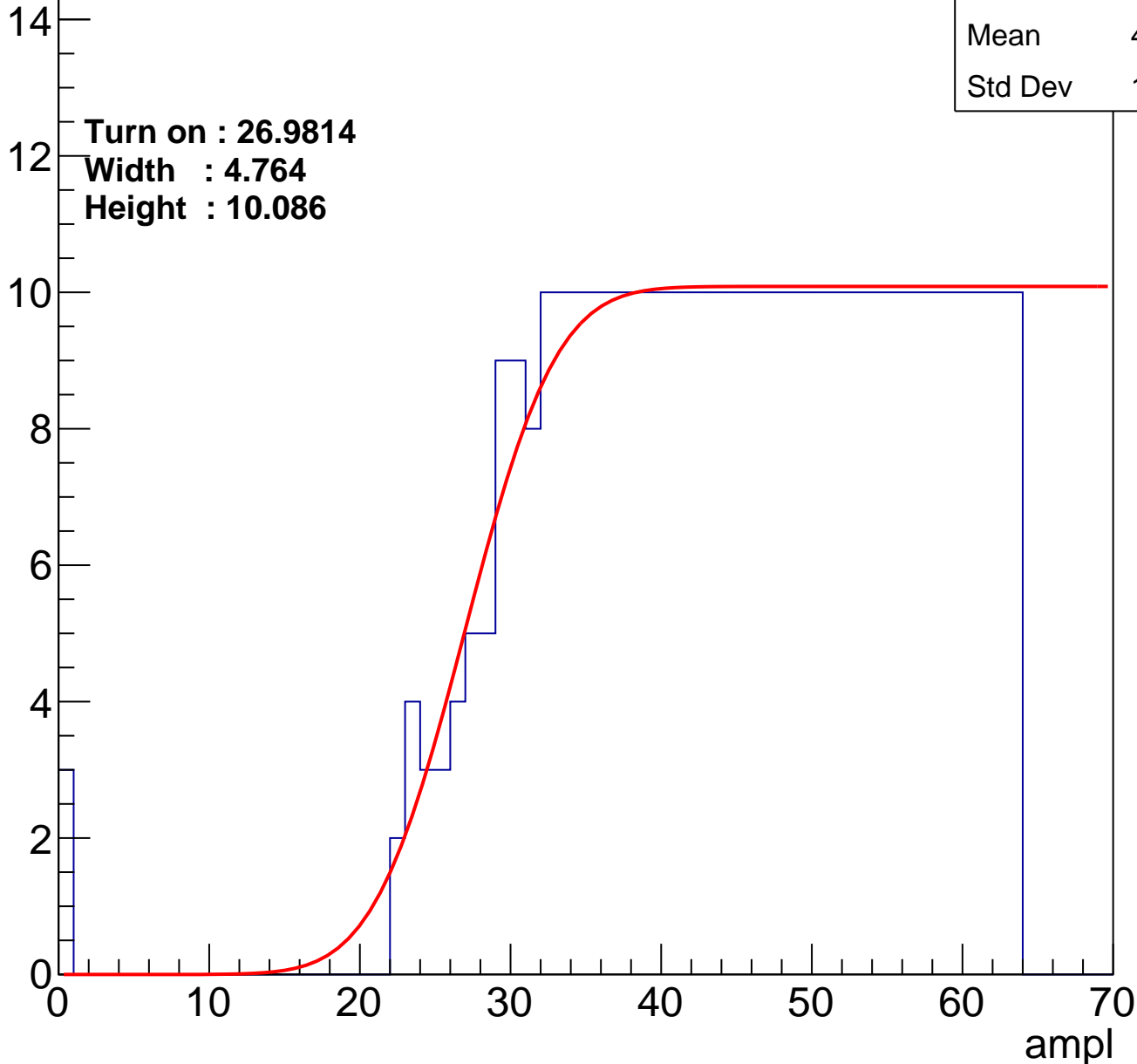
Entries	375
Mean	44.38
Std Dev	11.68

Turn on : 26.9814

Width : 4.764

Height : 10.086

Entry



B1L103S, U9-ch16

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.8171

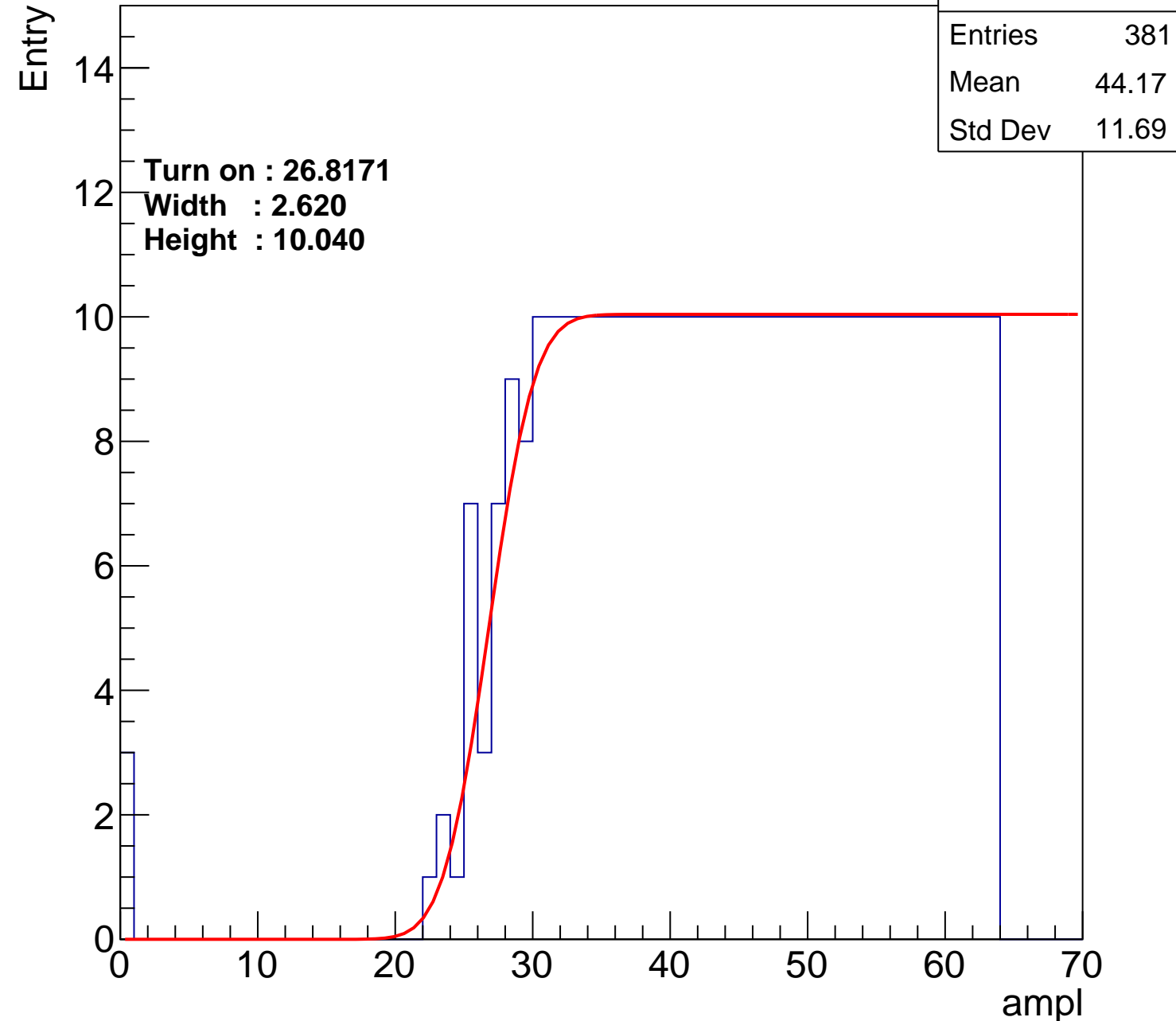
Width : 2.620

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch17

calib_packv5_042523_0143.root, FC#7, port C2

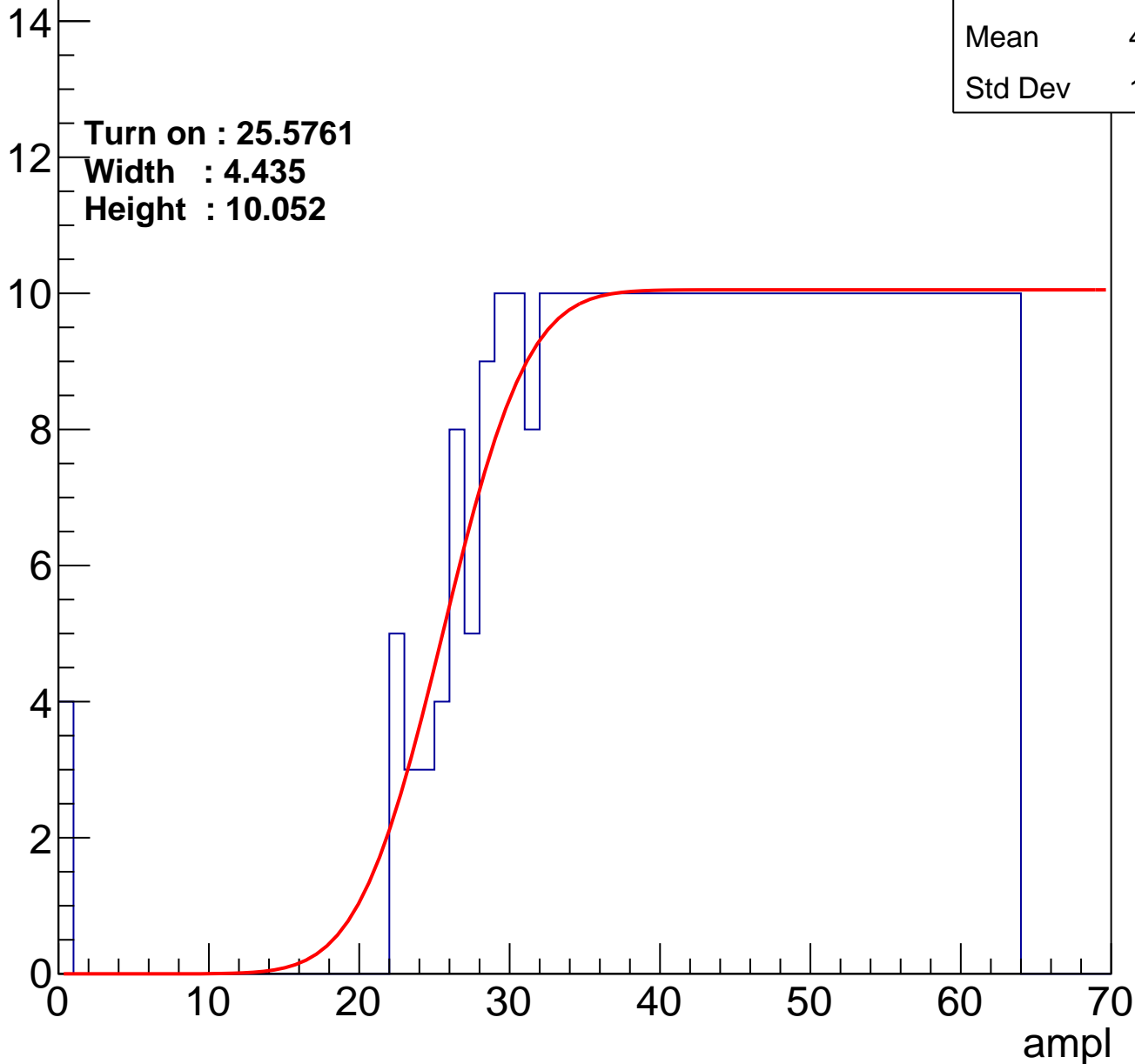
Entries	389
Mean	43.66
Std Dev	12.13

Turn on : 25.5761

Width : 4.435

Height : 10.052

Entry



B1L103S, U9-ch18

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 27.6287

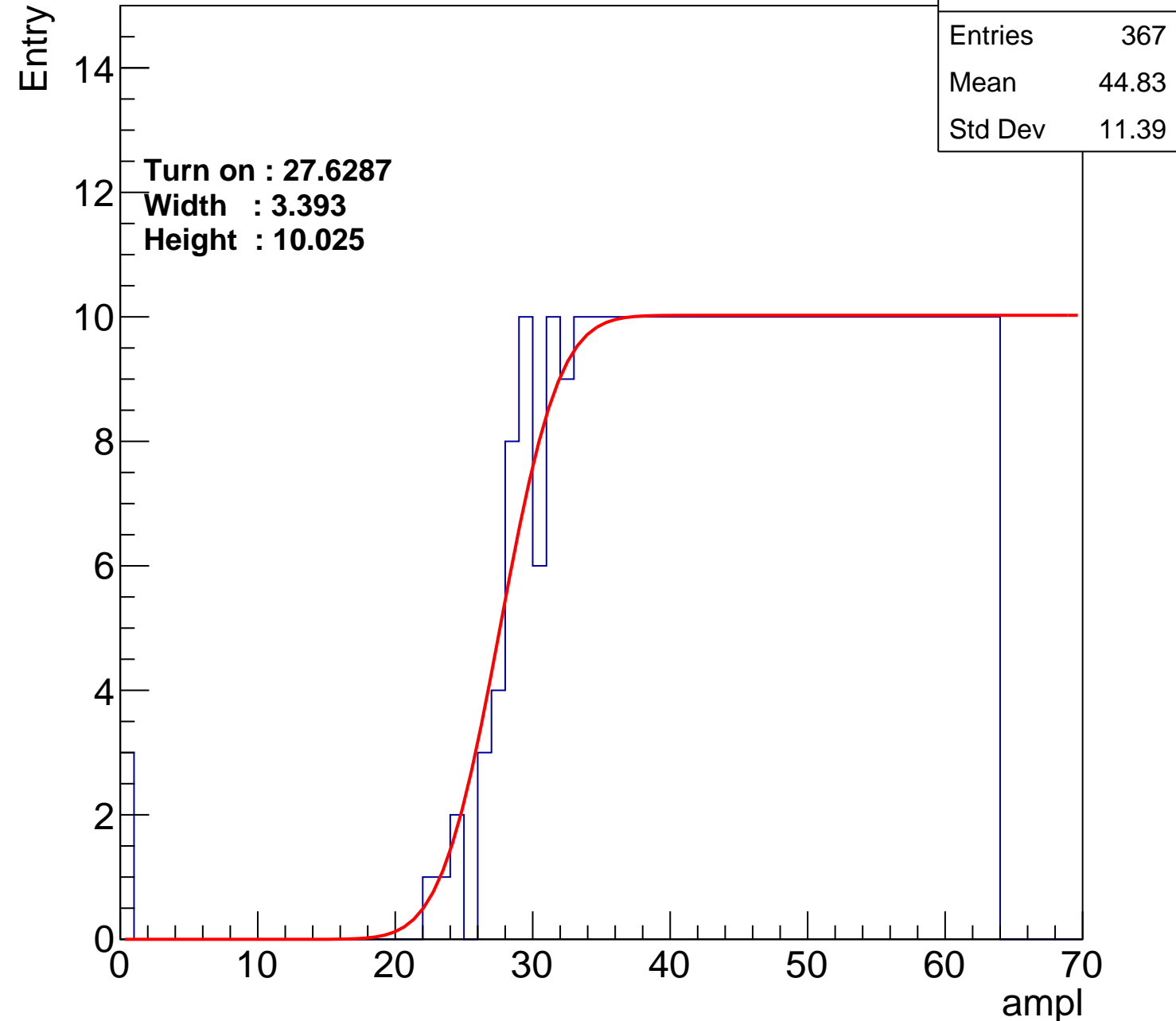
Width : 3.393

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch19

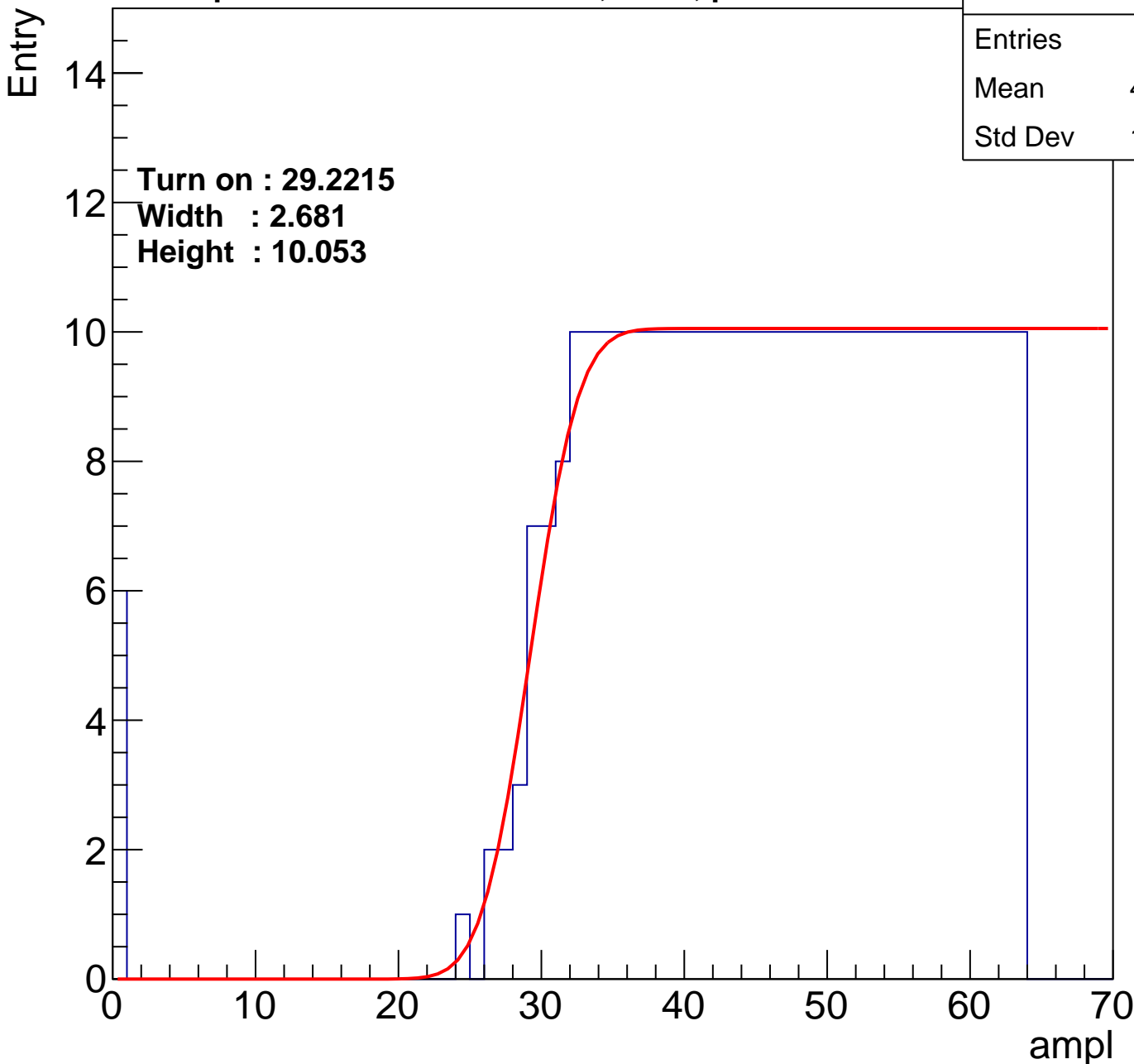
calib_packv5_042523_0143.root, FC#7, port C2

Entries	356
Mean	45.15
Std Dev	11.74

Turn on : 29.2215

Width : 2.681

Height : 10.053



B1L103S, U9-ch20

calib_packv5_042523_0143.root, FC#7, port C2

Entries	362
Mean	45.09
Std Dev	11.14

Turn on : 28.2910

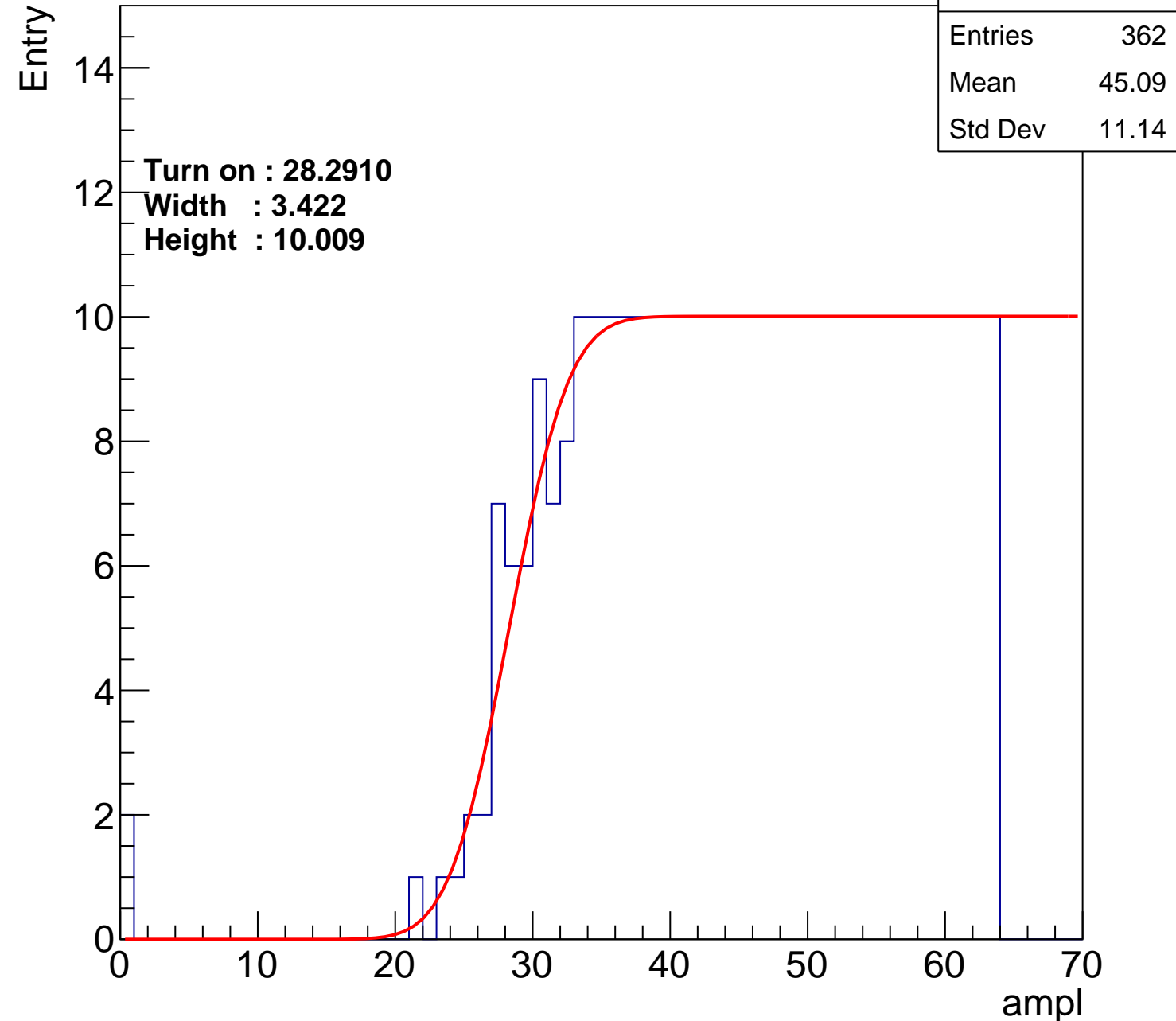
Width : 3.422

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch21

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	45.05
Std Dev	11.1

Turn on : 27.8568

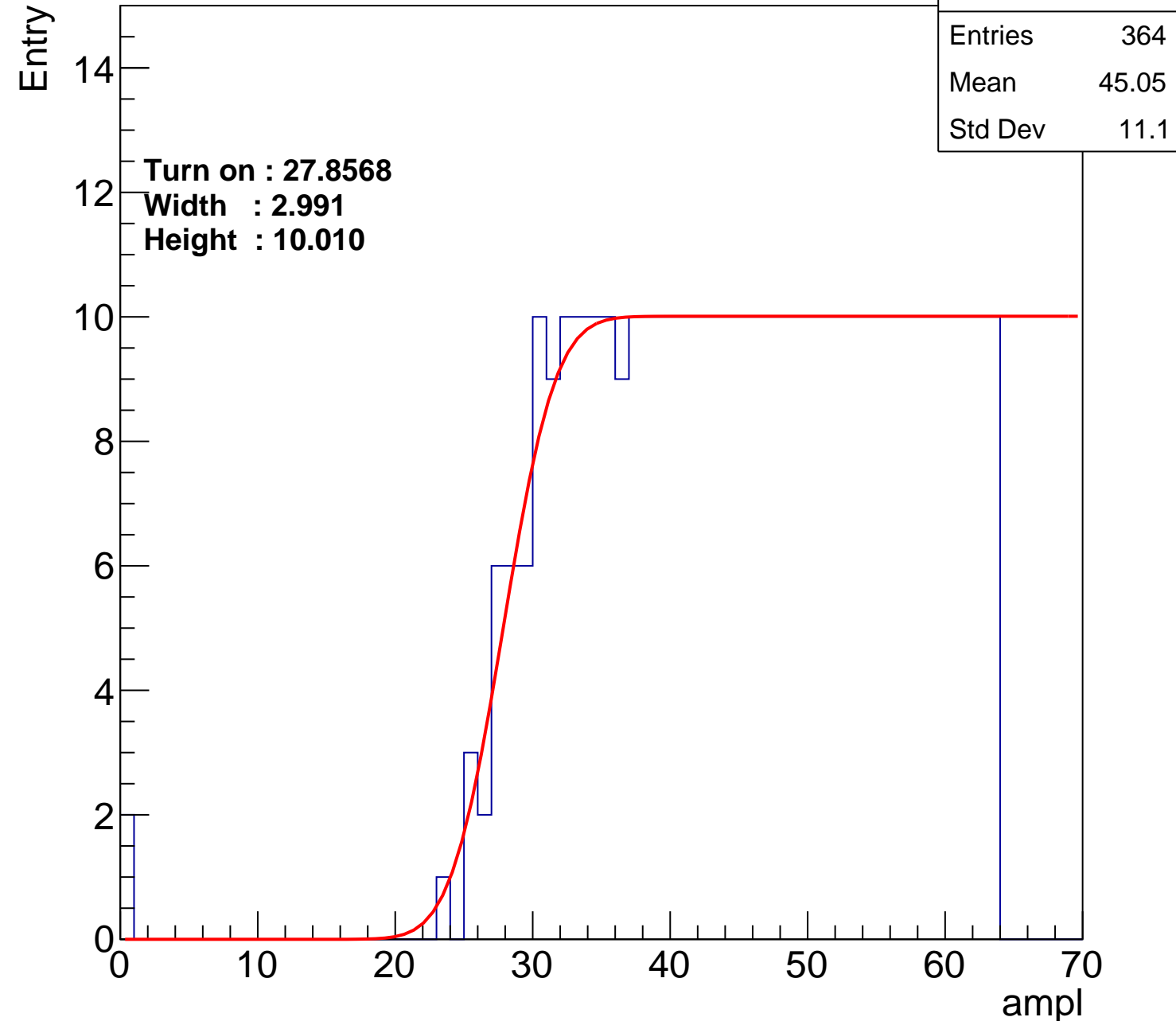
Width : 2.991

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch22

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.5
Std Dev	11.35

Turn on : 26.8439

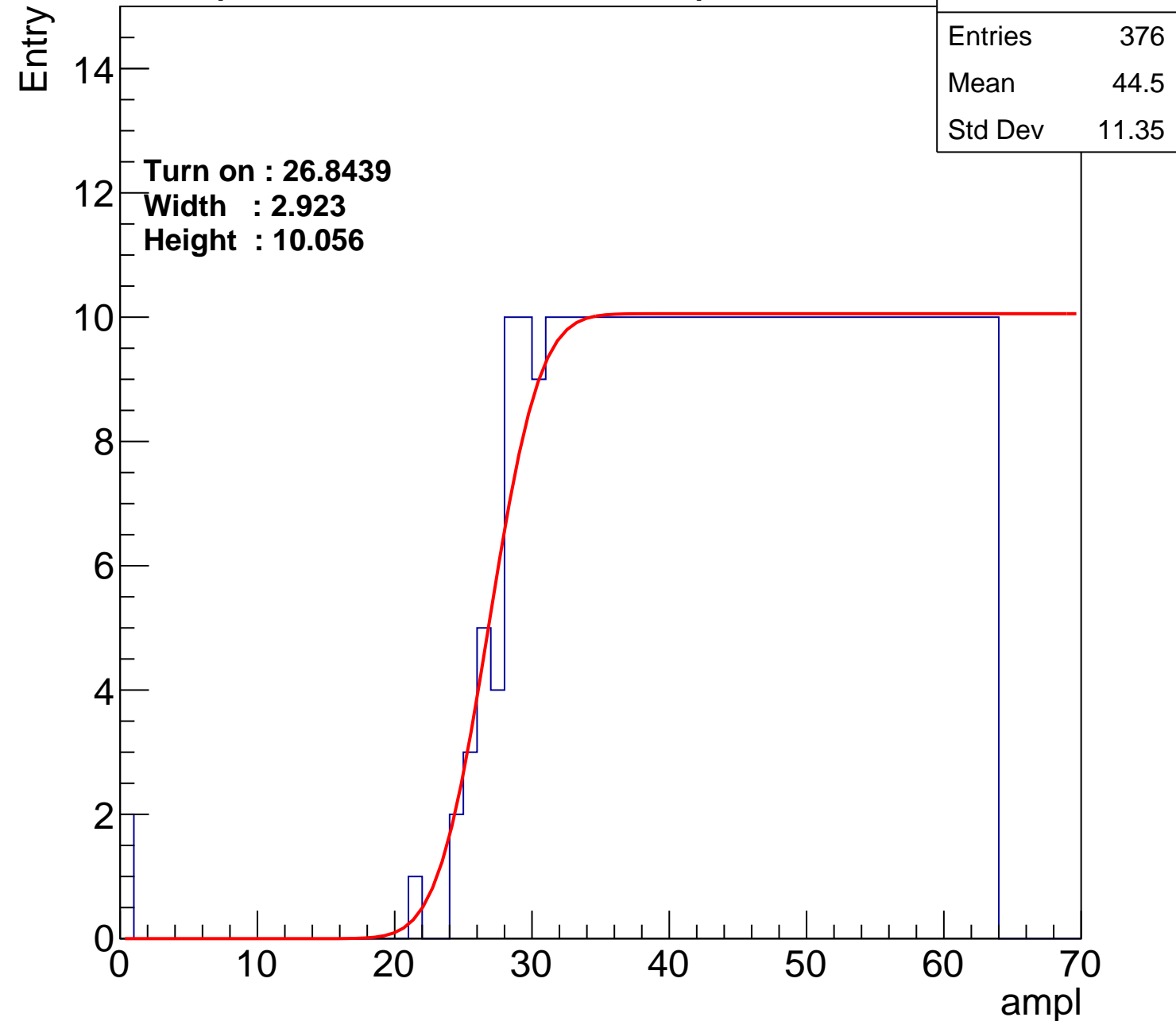
Width : 2.923

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch23

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.5574

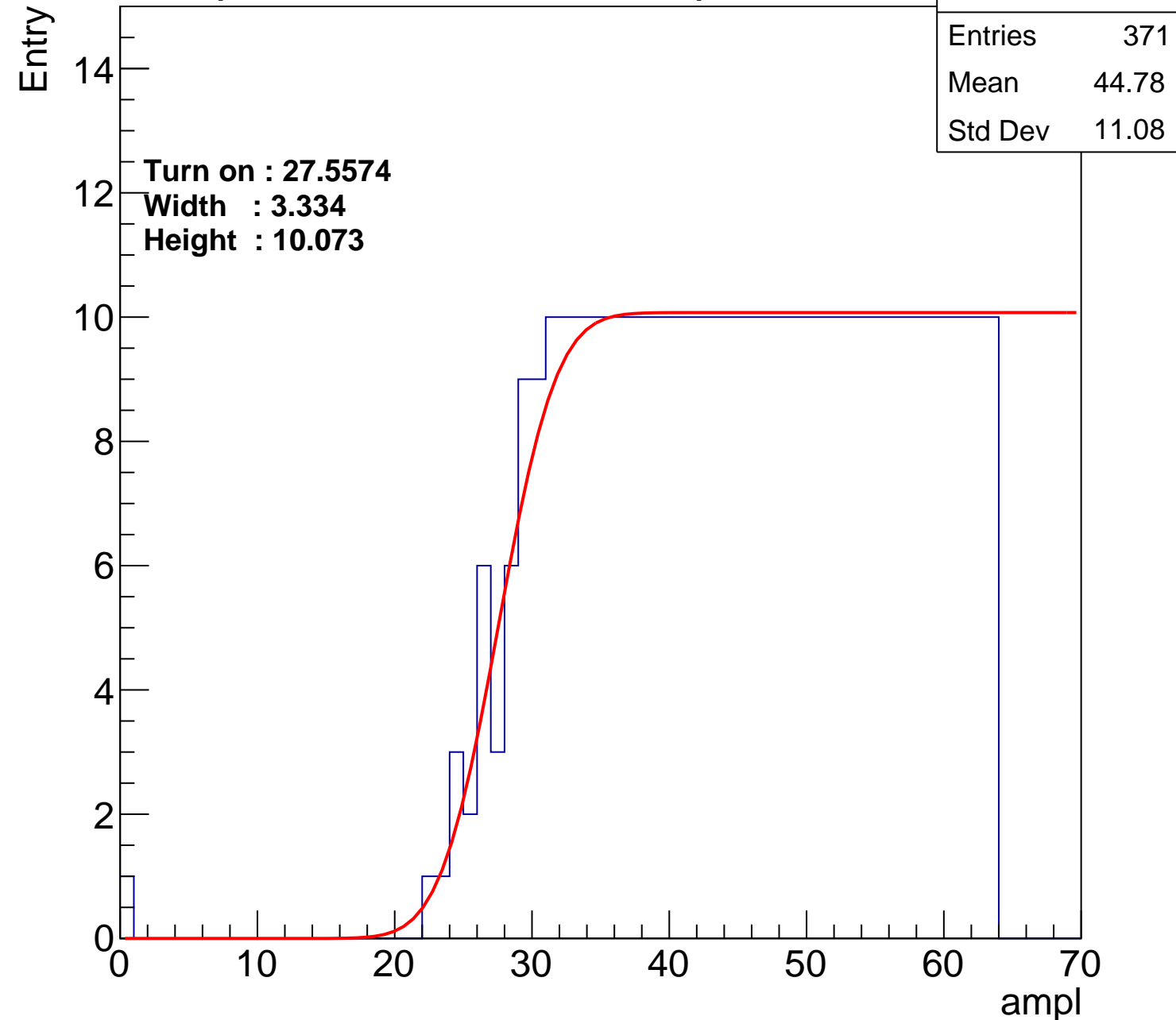
Width : 3.334

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch24

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.6977

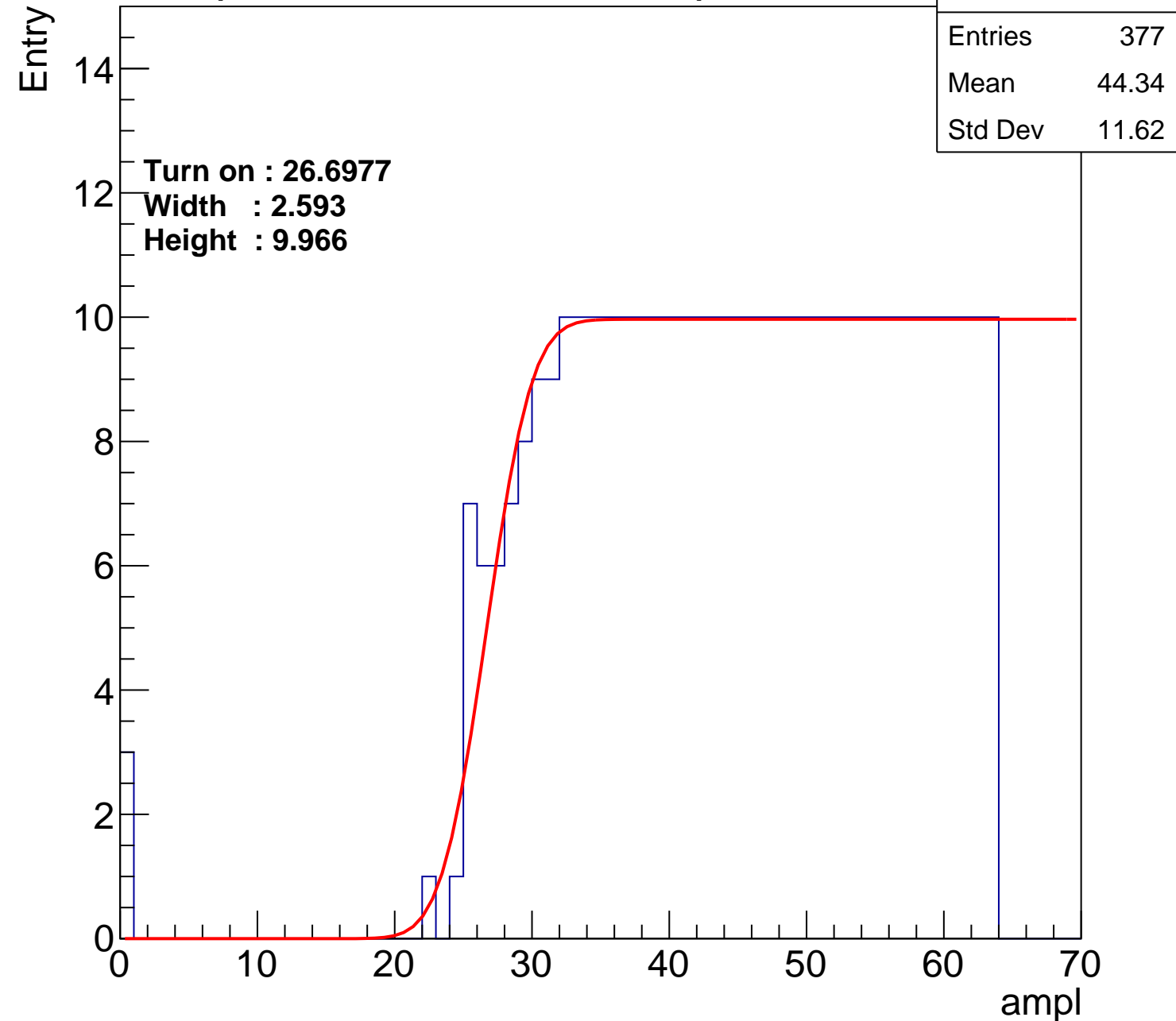
Width : 2.593

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch25

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.54
Std Dev	11.51

Turn on : 26.6927

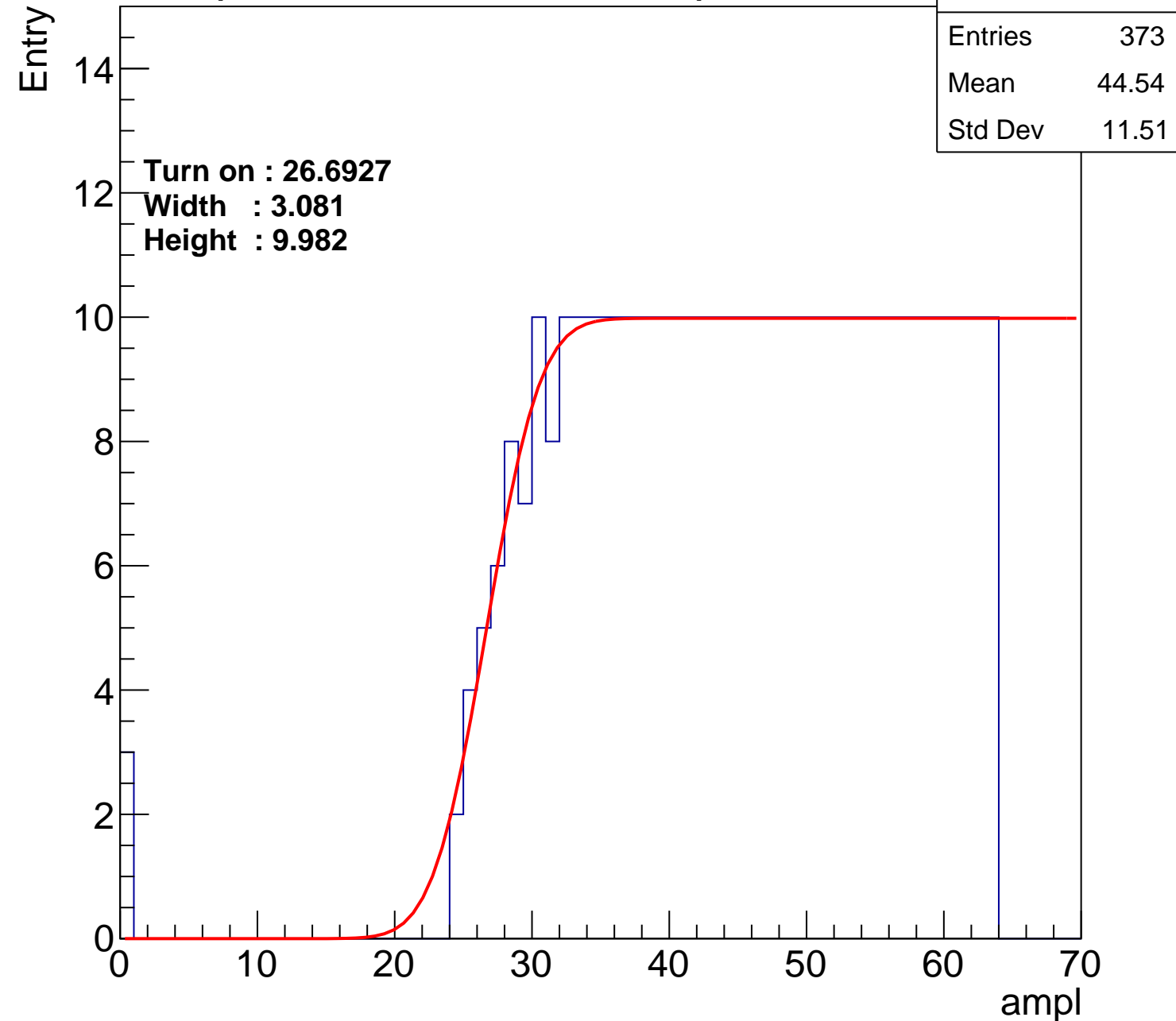
Width : 3.081

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch26

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.44
Std Dev	12.09

Turn on : 25.1634

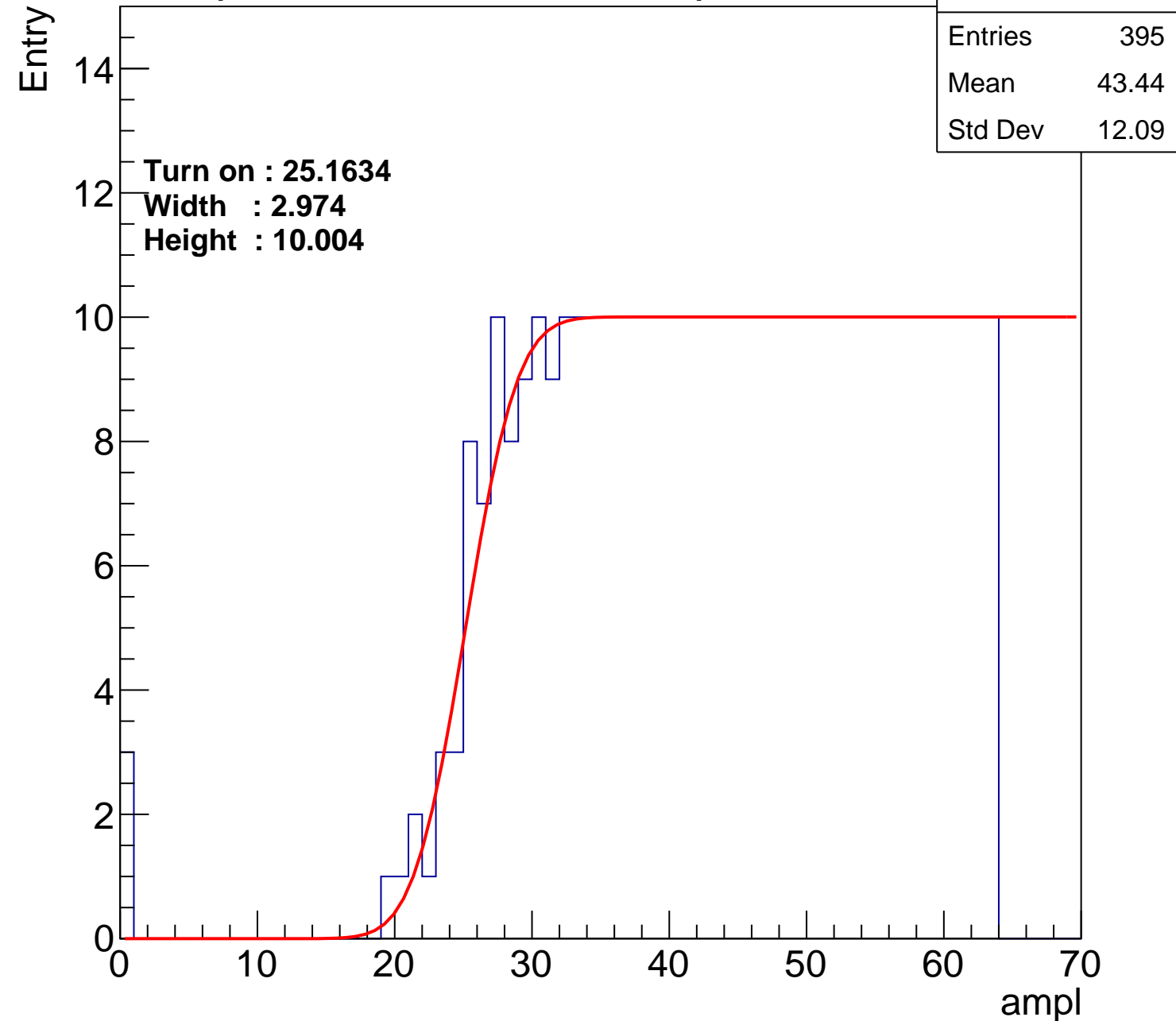
Width : 2.974

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch27

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	45.01
Std Dev	10.96

Turn on : 27.6539

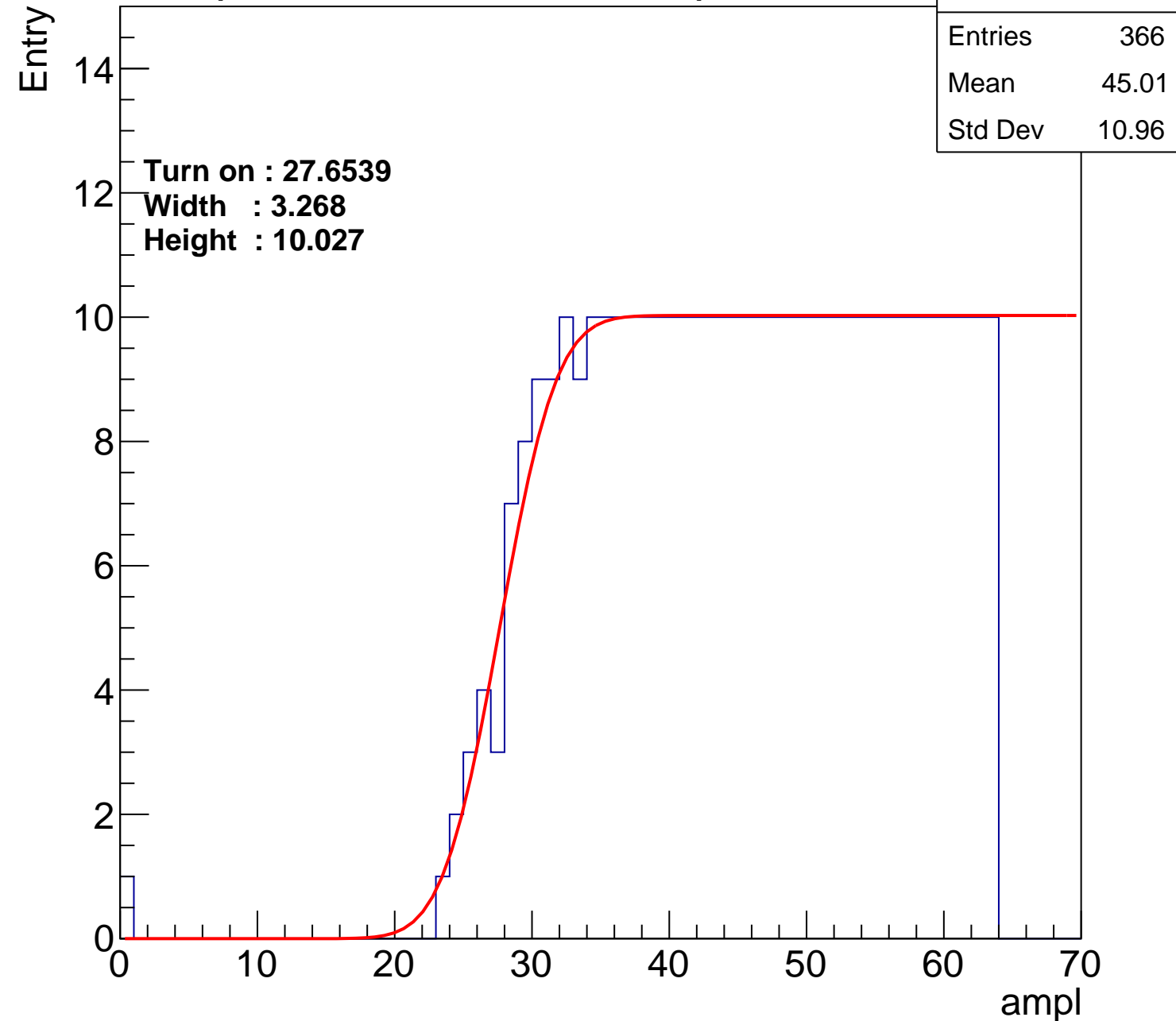
Width : 3.268

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch28

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.22
Std Dev	11.65

Turn on : 26.4894

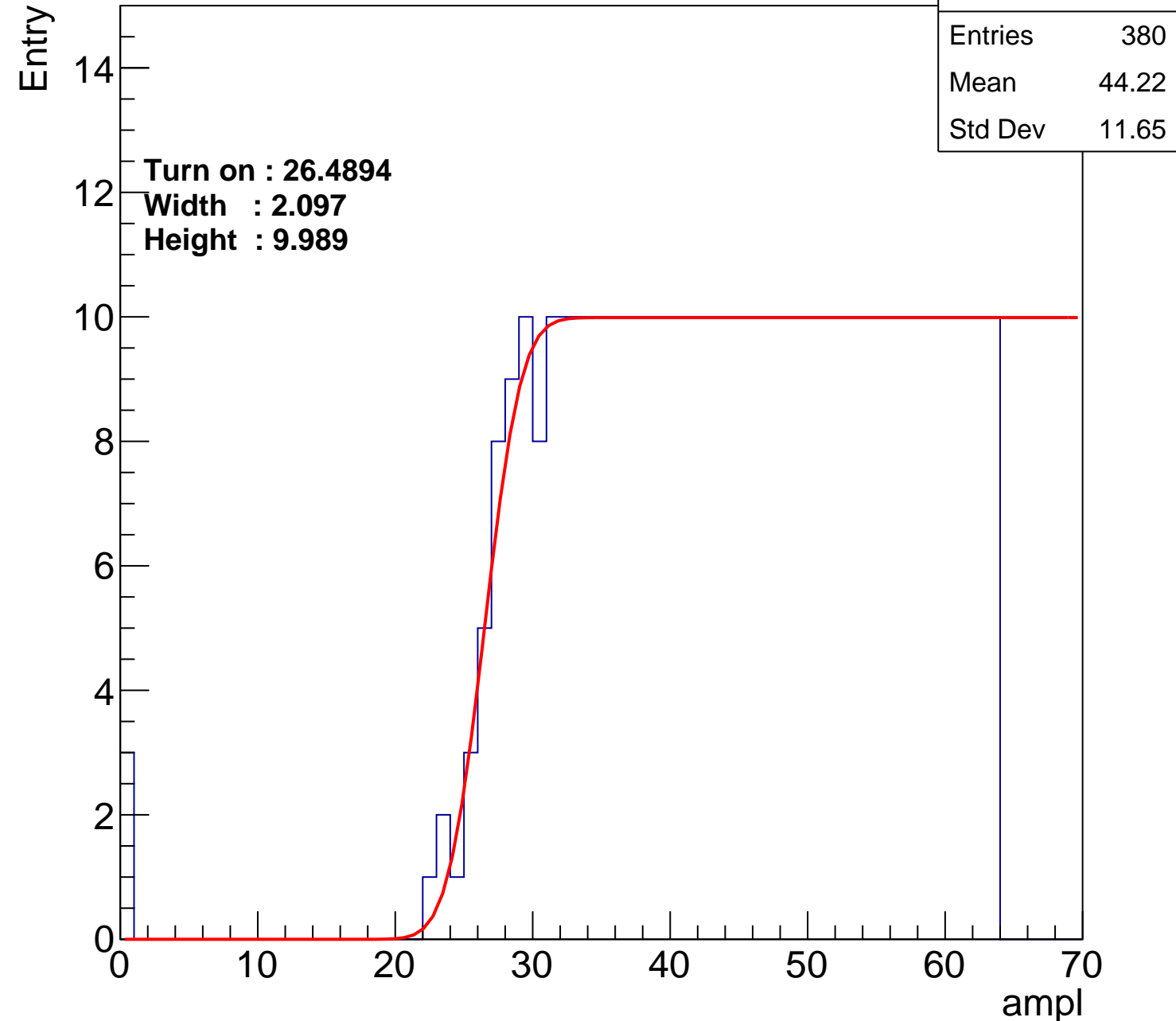
Width : 2.097

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch29

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.12
Std Dev	11.29

Turn on : 28.6850

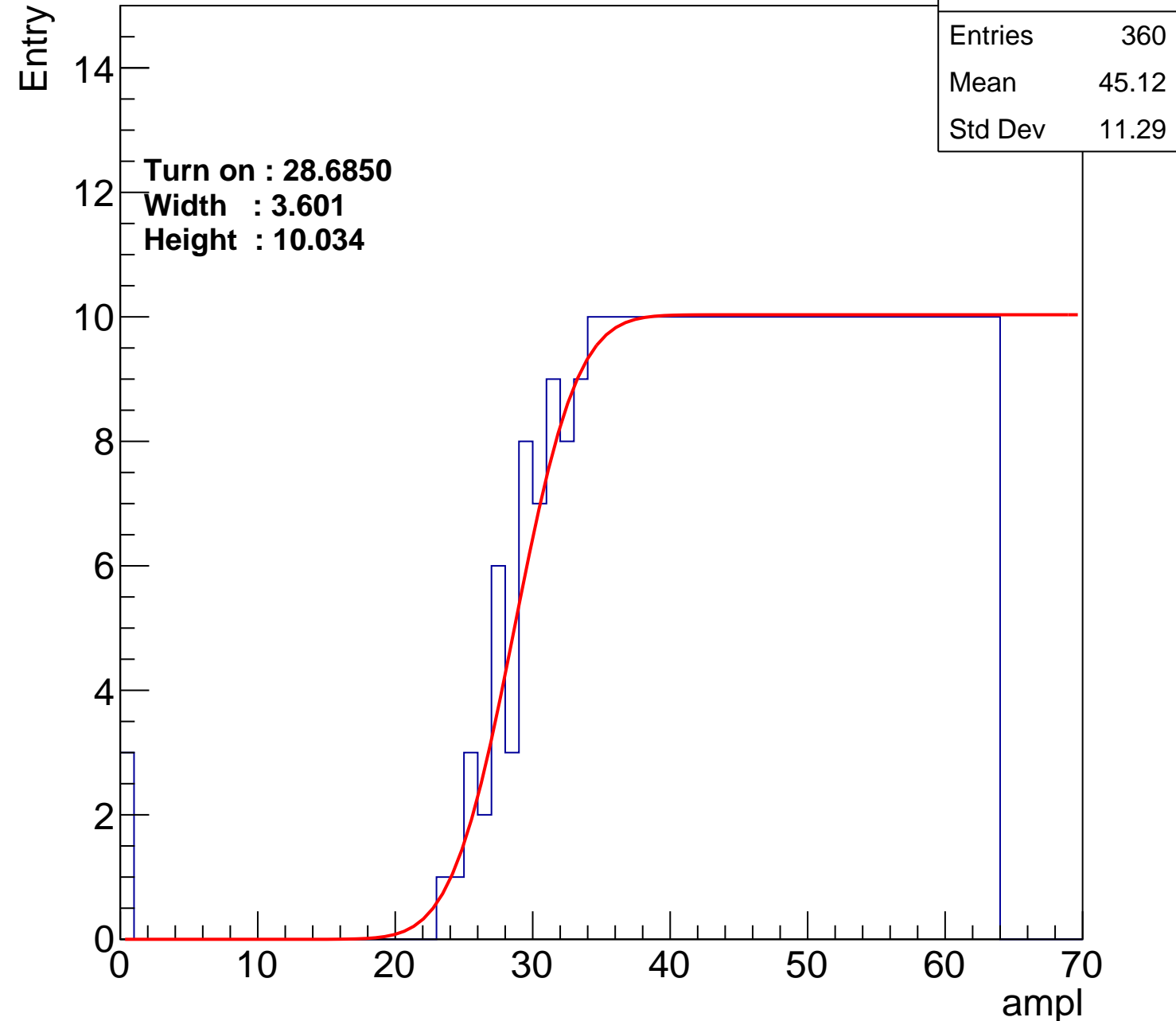
Width : 3.601

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch30

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.42
Std Dev	11.48

Turn on : 27.2618

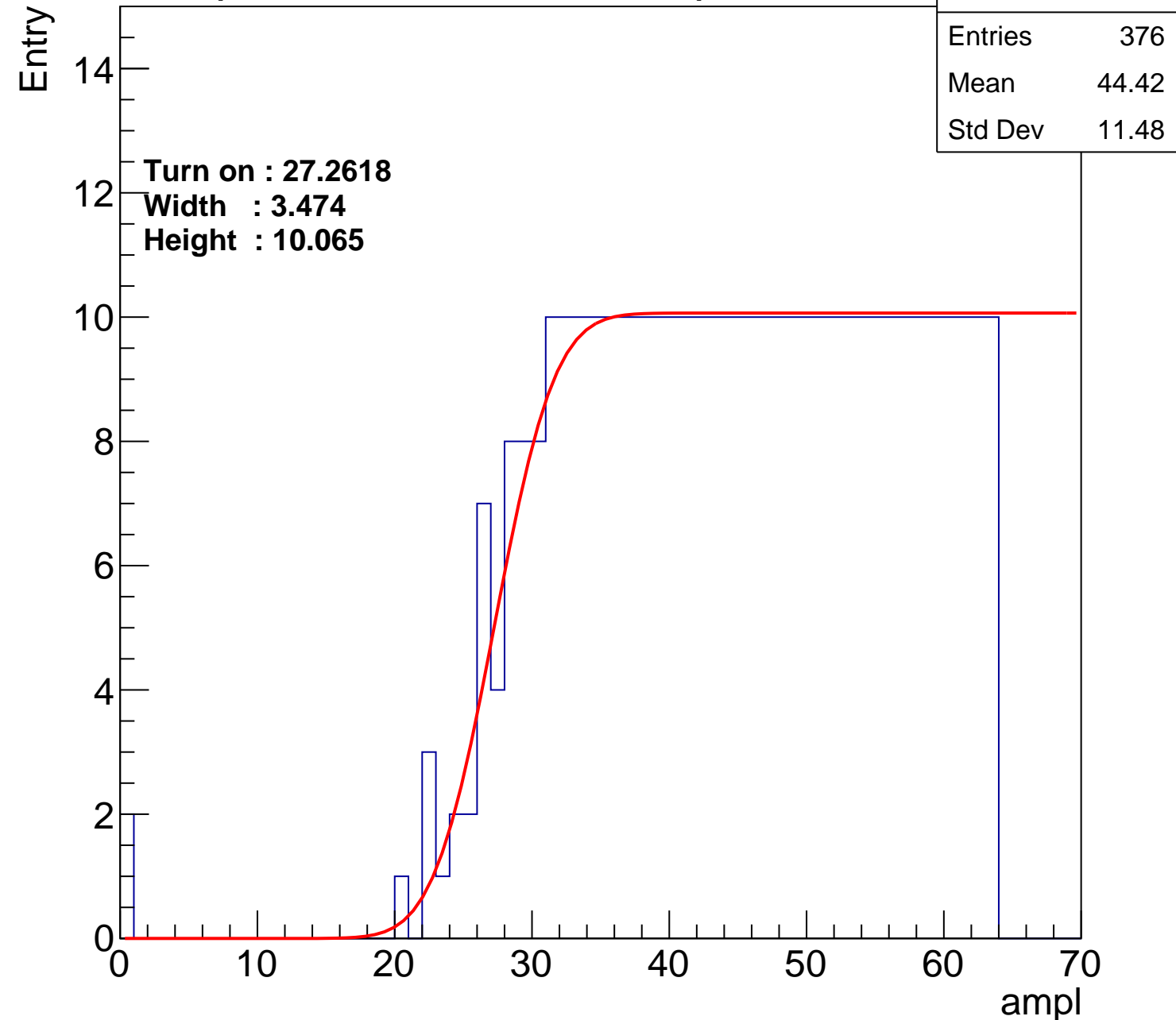
Width : 3.474

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch31

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.68
Std Dev	11.65

Turn on : 27.4872

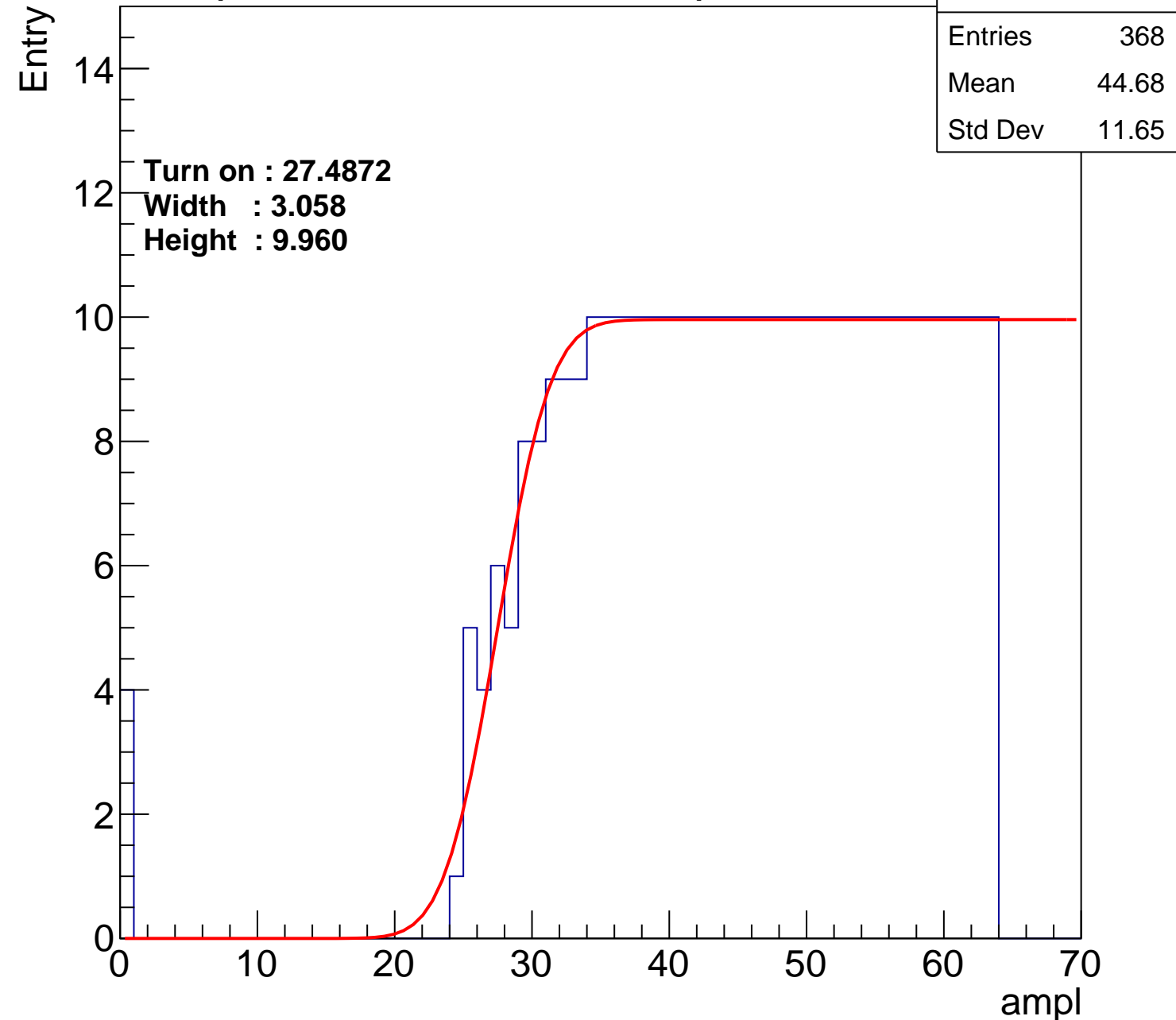
Width : 3.058

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.96
Std Dev	11.56

Turn on : 25.8766

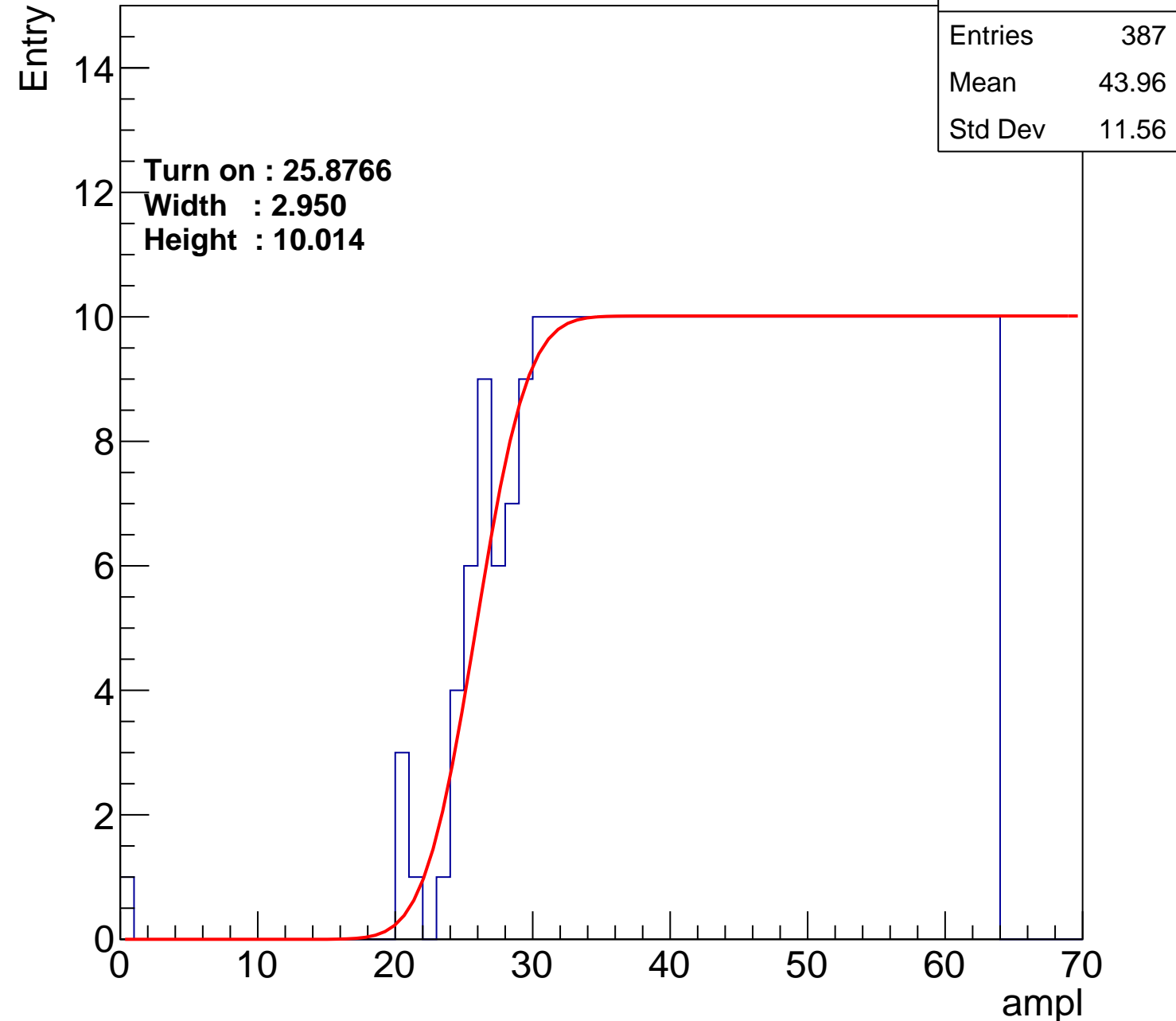
Width : 2.950

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch33

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.22
Std Dev	11.71

Turn on : 26.9267

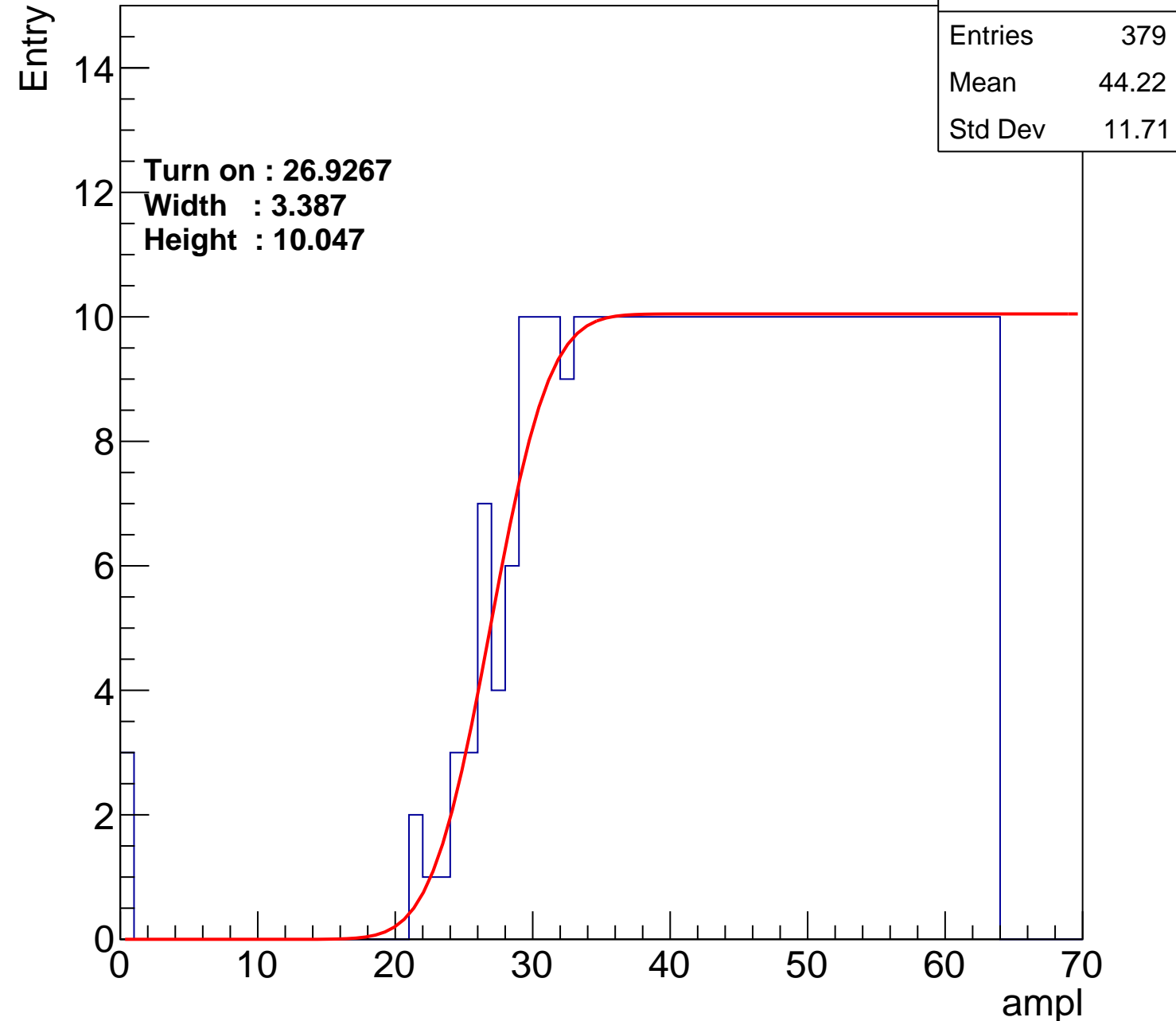
Width : 3.387

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch34

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.77
Std Dev	12.07

Turn on : 26.0675

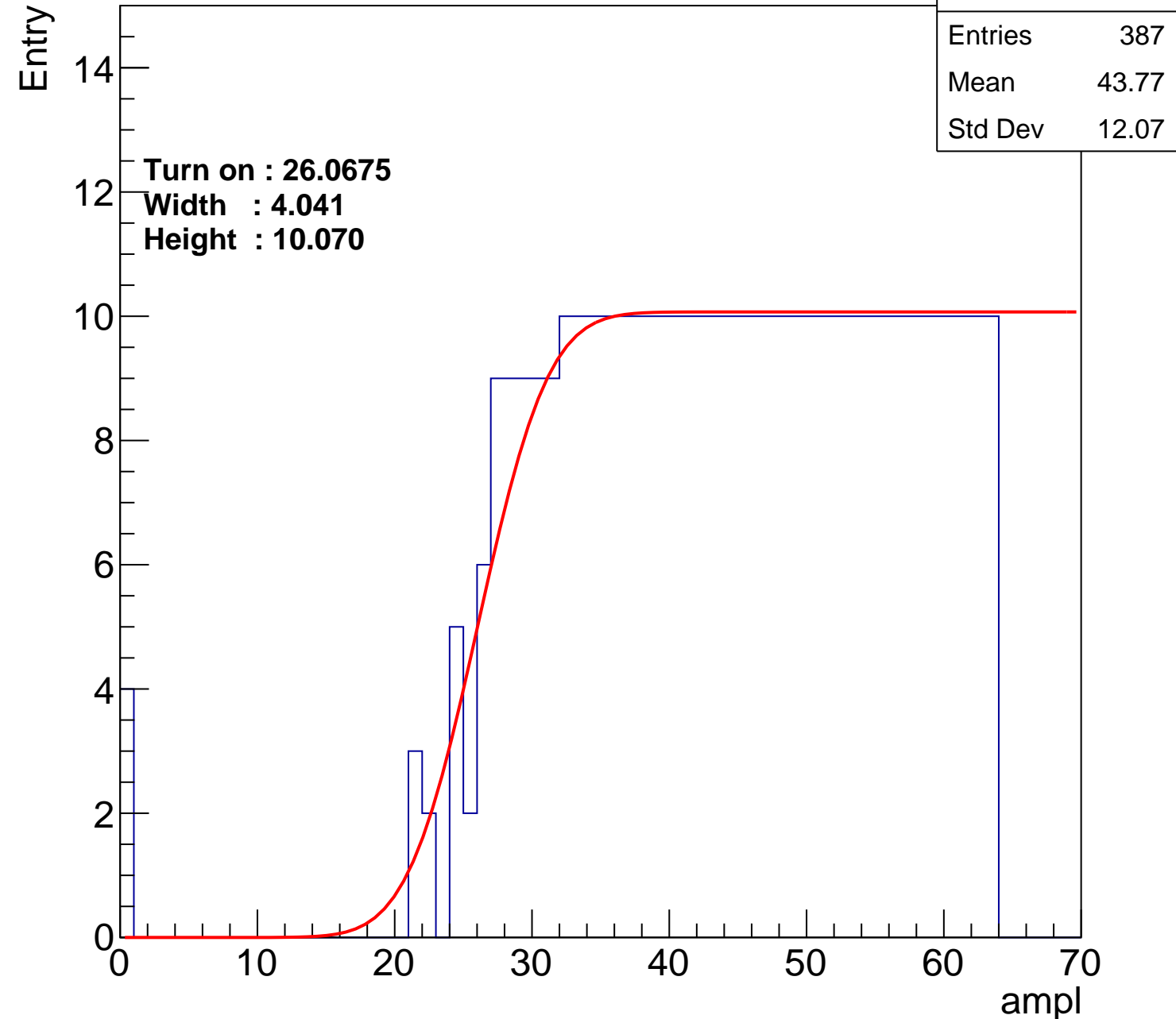
Width : 4.041

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch35

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.04
Std Dev	11.74

Turn on : 26.0482

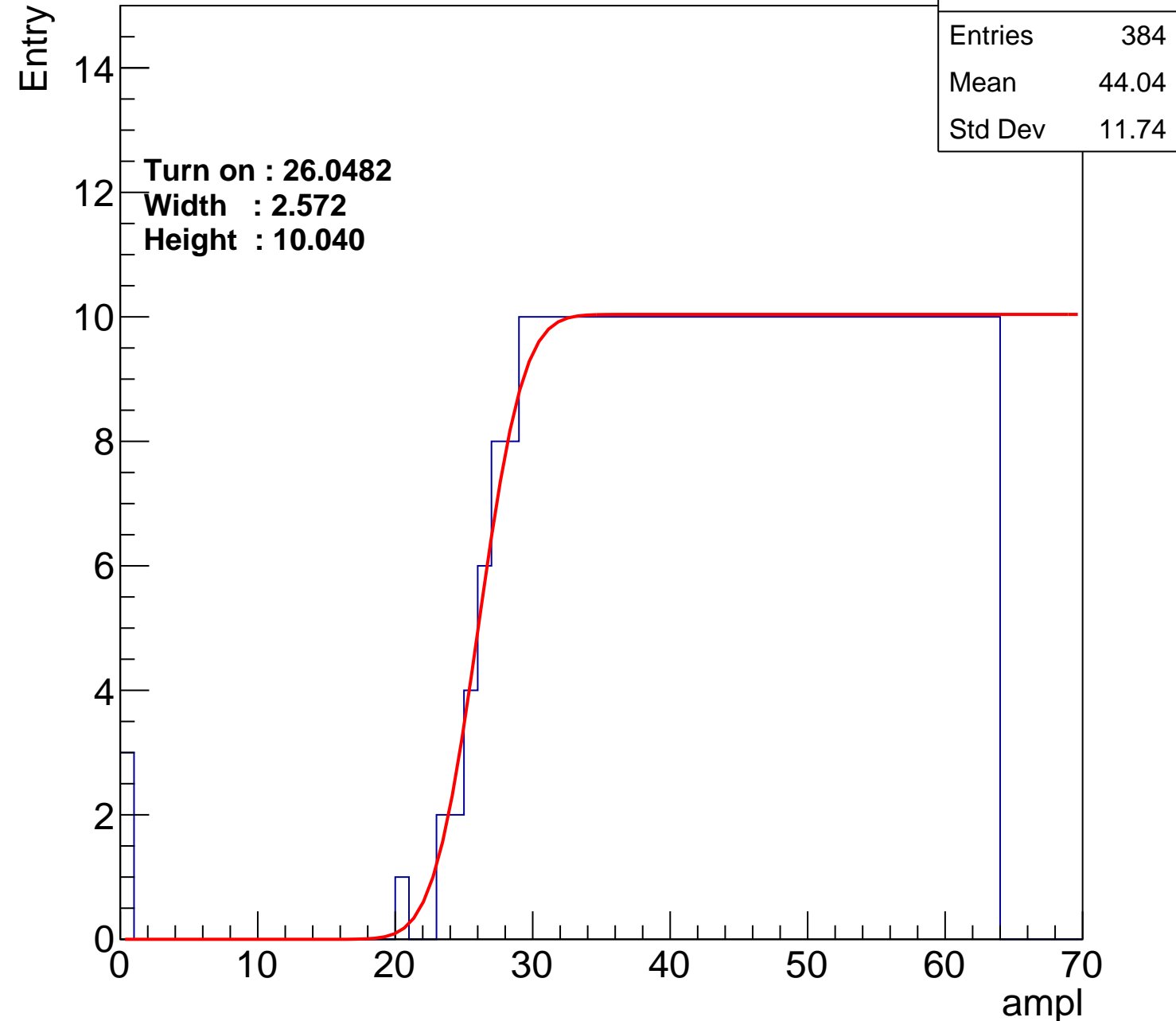
Width : 2.572

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch36

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.99
Std Dev	11.01

Turn on : 27.8490

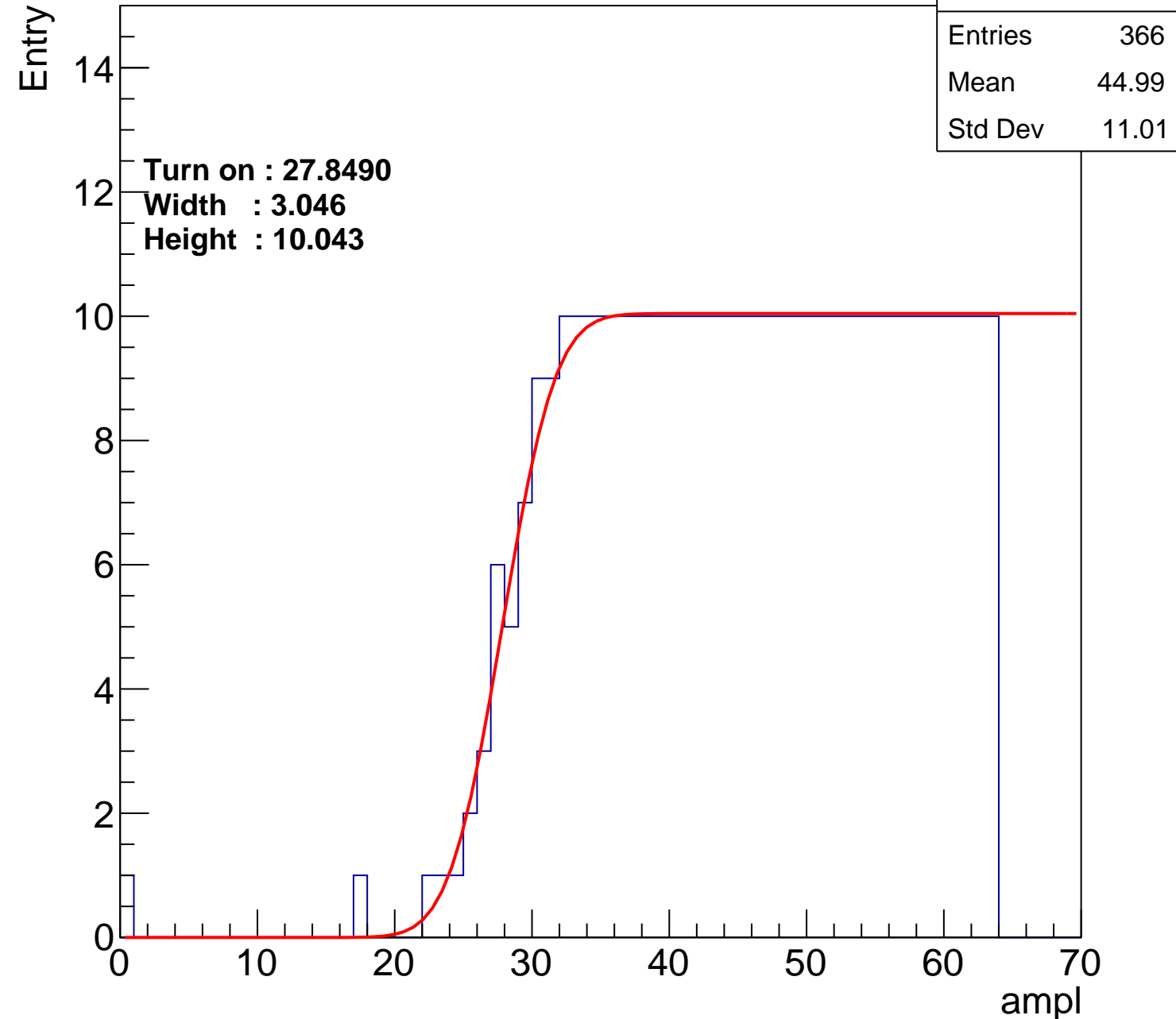
Width : 3.046

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch37

calib_packv5_042523_0143.root, FC#7, port C2

Entries	362
Mean	45.21
Std Dev	10.86

Turn on : 27.9137

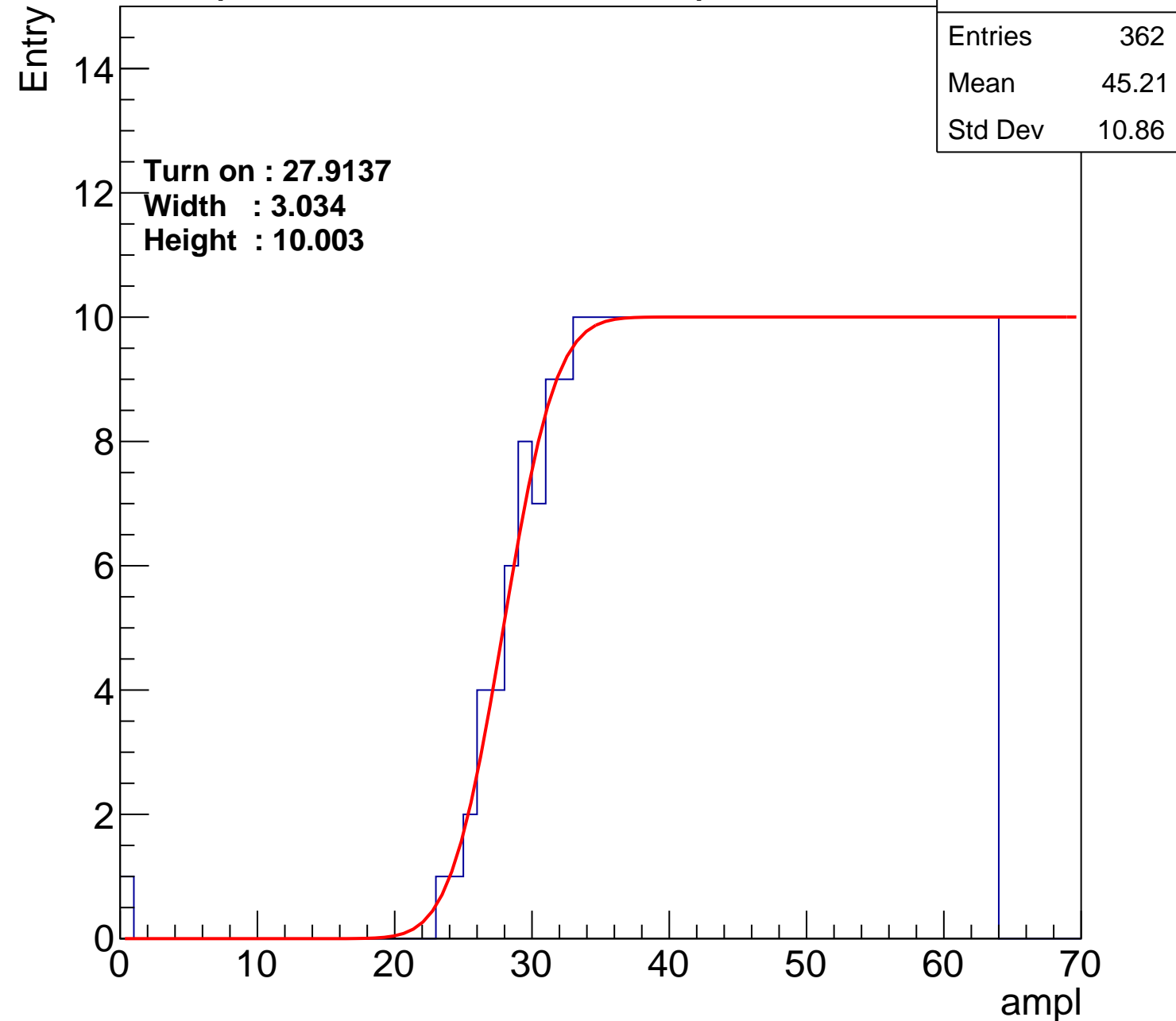
Width : 3.034

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch38

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.68
Std Dev	11.14

Turn on : 27.0196

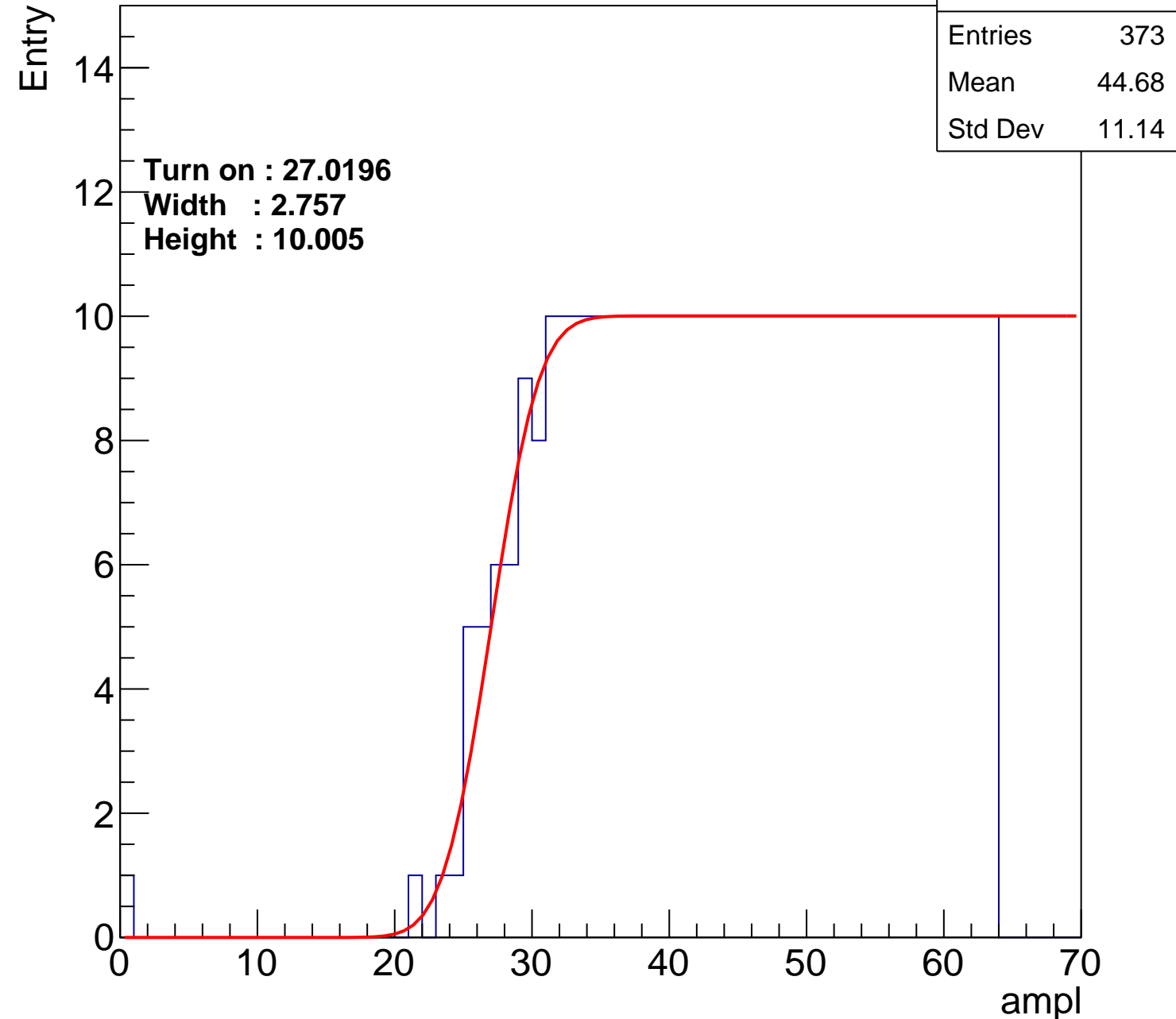
Width : 2.757

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch39

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.6042

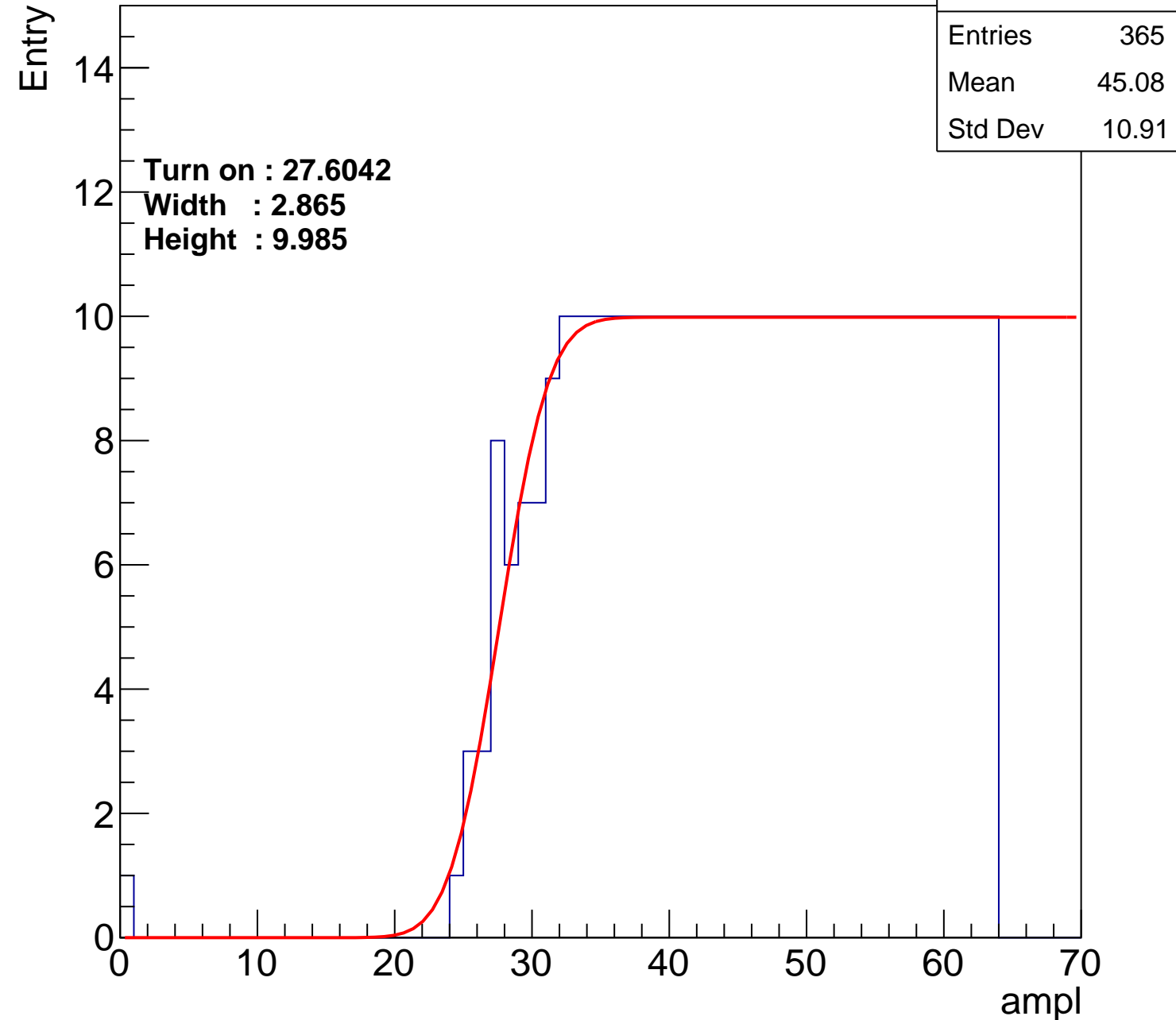
Width : 2.865

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch40

calib_packv5_042523_0143.root, FC#7, port C2

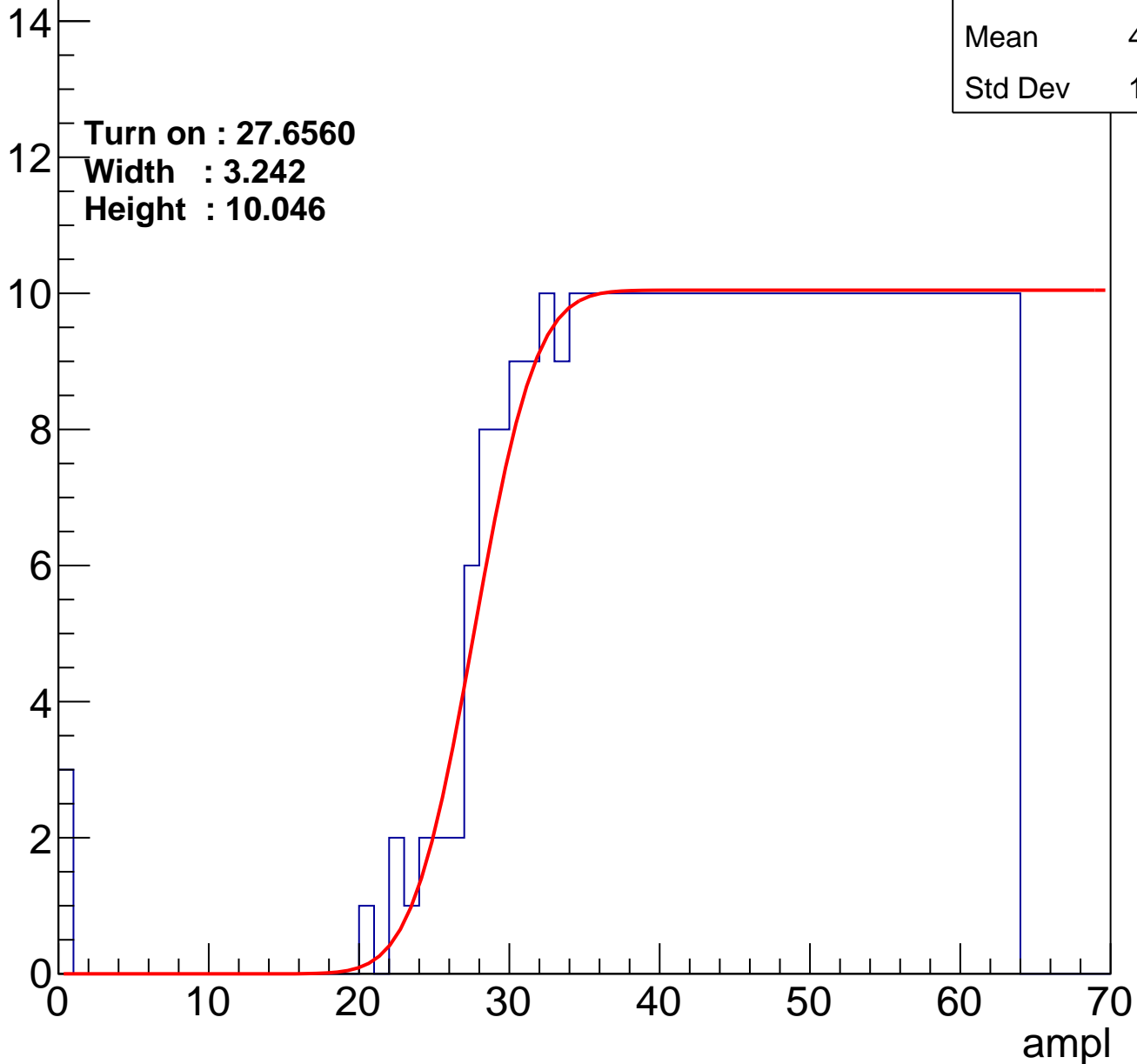
Entries	372
Mean	44.55
Std Dev	11.57

Turn on : 27.6560

Width : 3.242

Height : 10.046

Entry



B1L103S, U9-ch41

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.79
Std Dev	11.11

Turn on : 27.1963

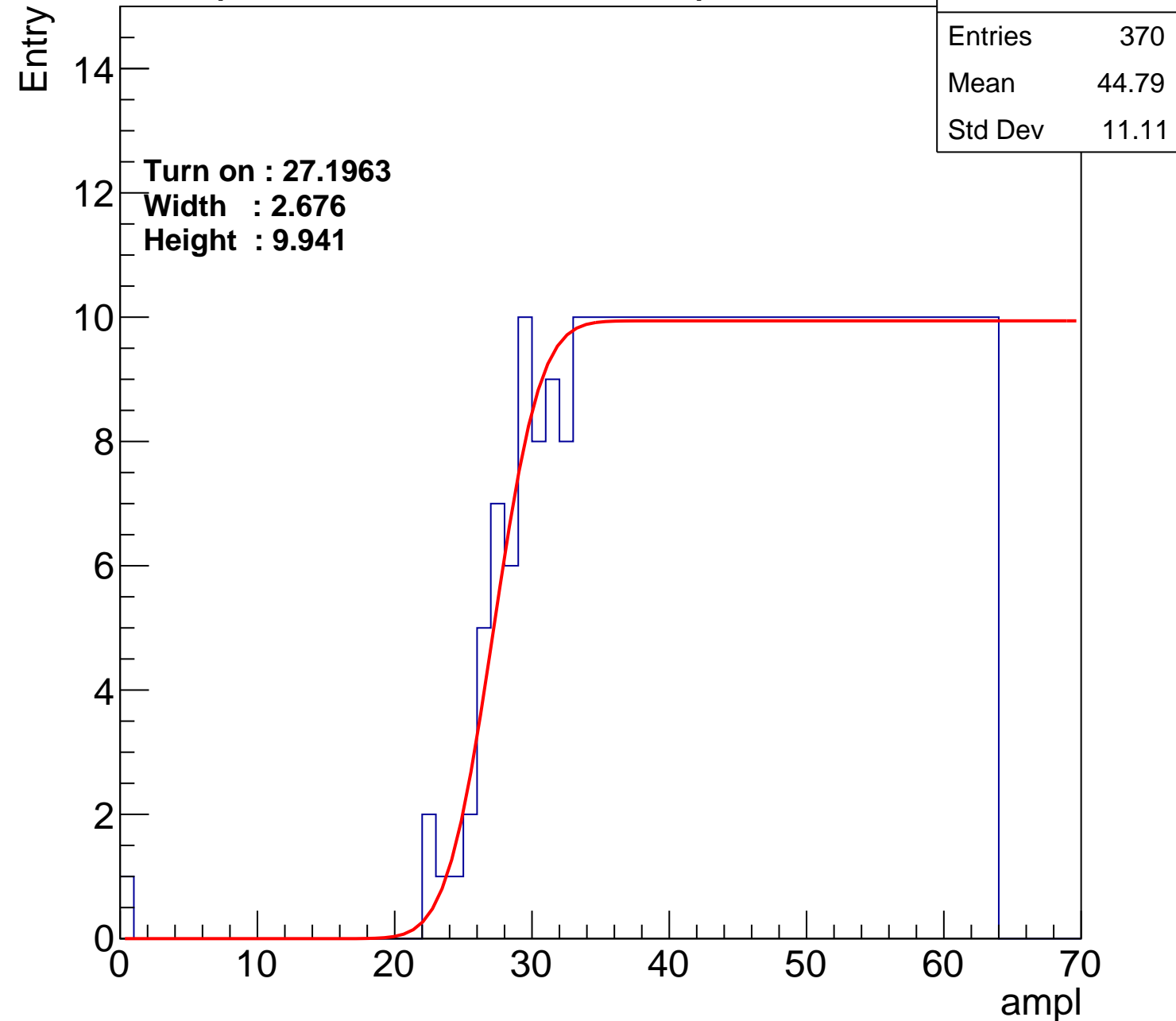
Width : 2.676

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch42

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.04
Std Dev	12.04

Turn on : 27.0568

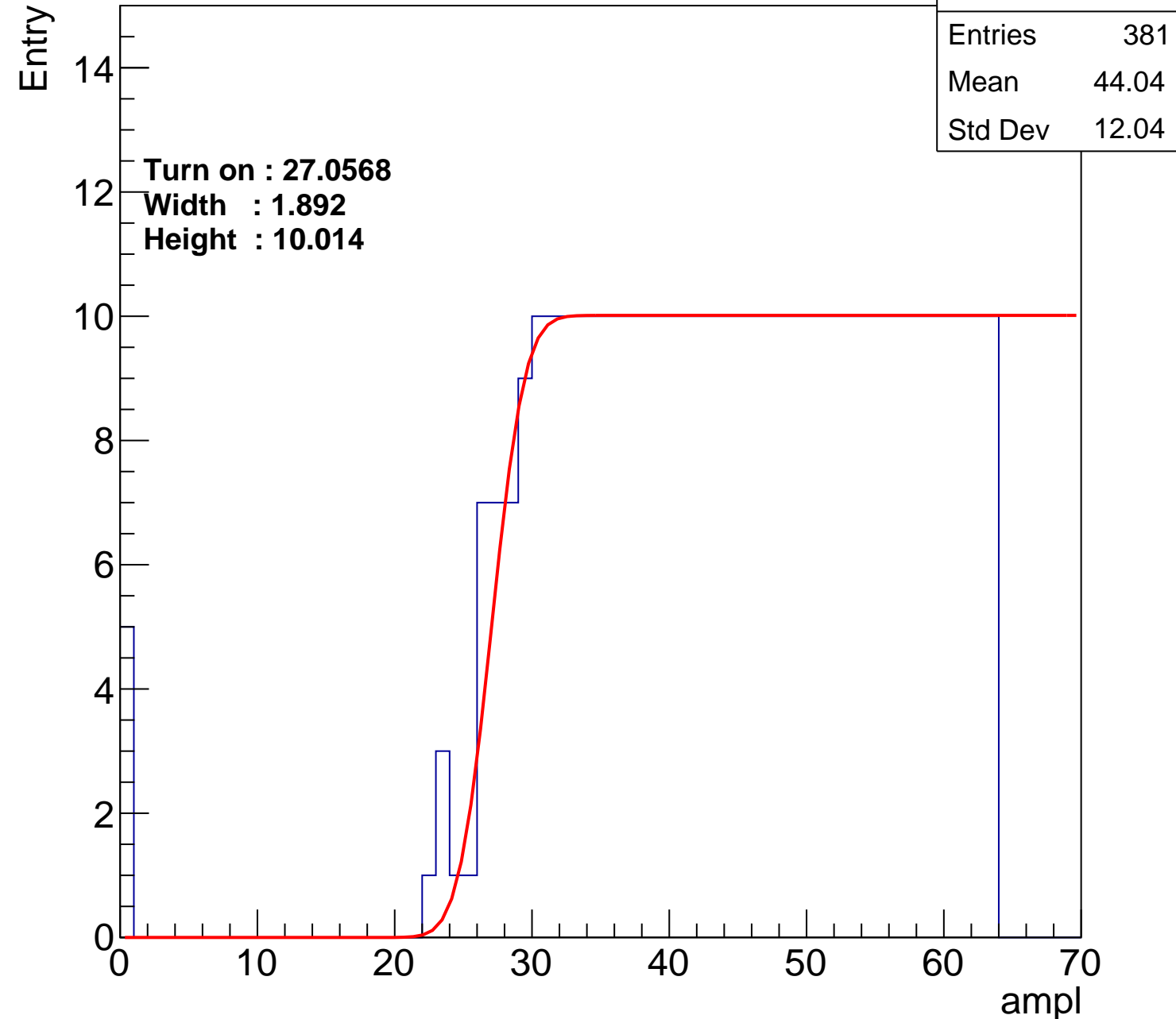
Width : 1.892

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch43

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.32
Std Dev	11.36

Turn on : 26.0912

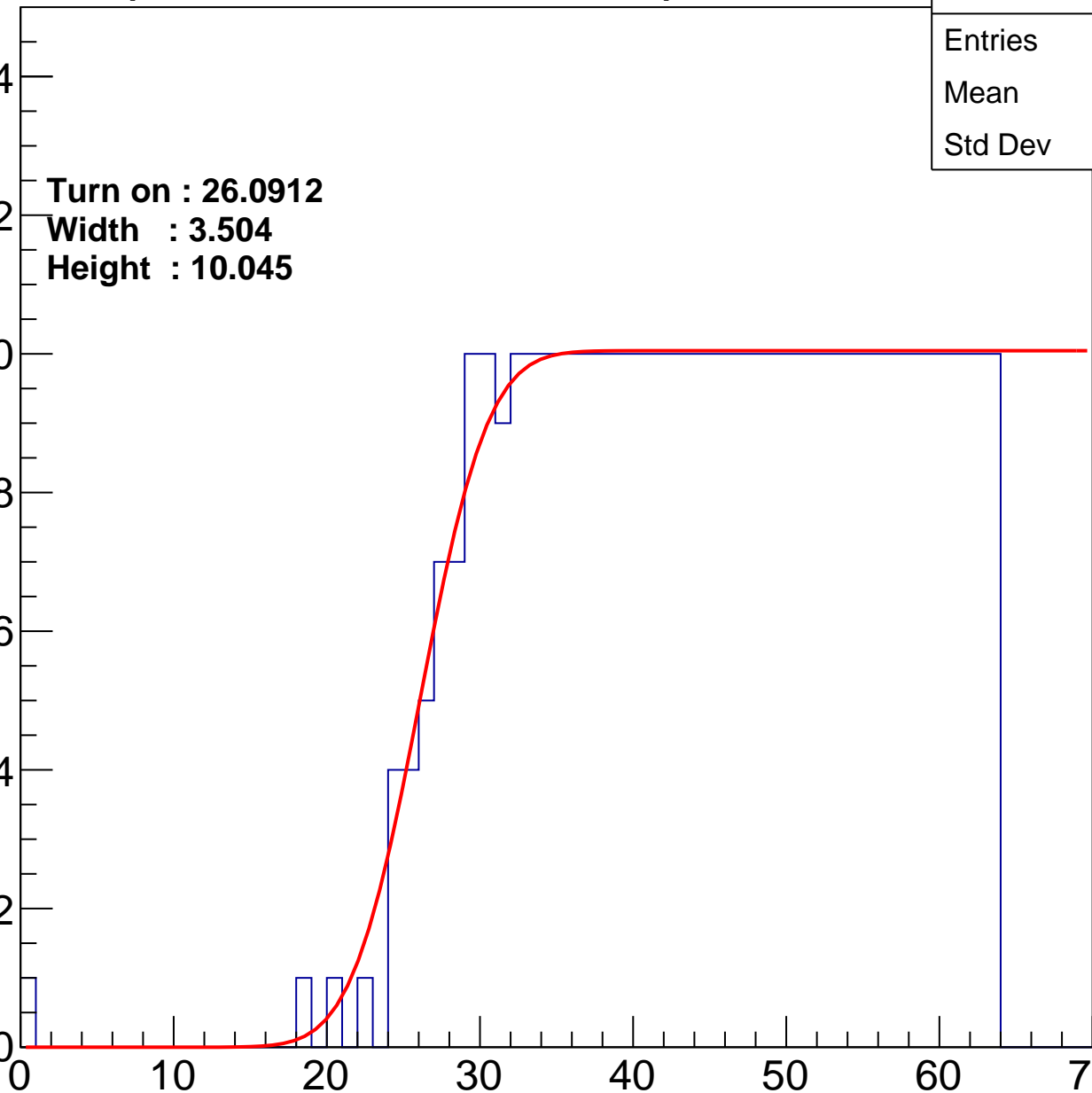
Width : 3.504

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch44

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.43
Std Dev	10.98

Turn on : 28.9594

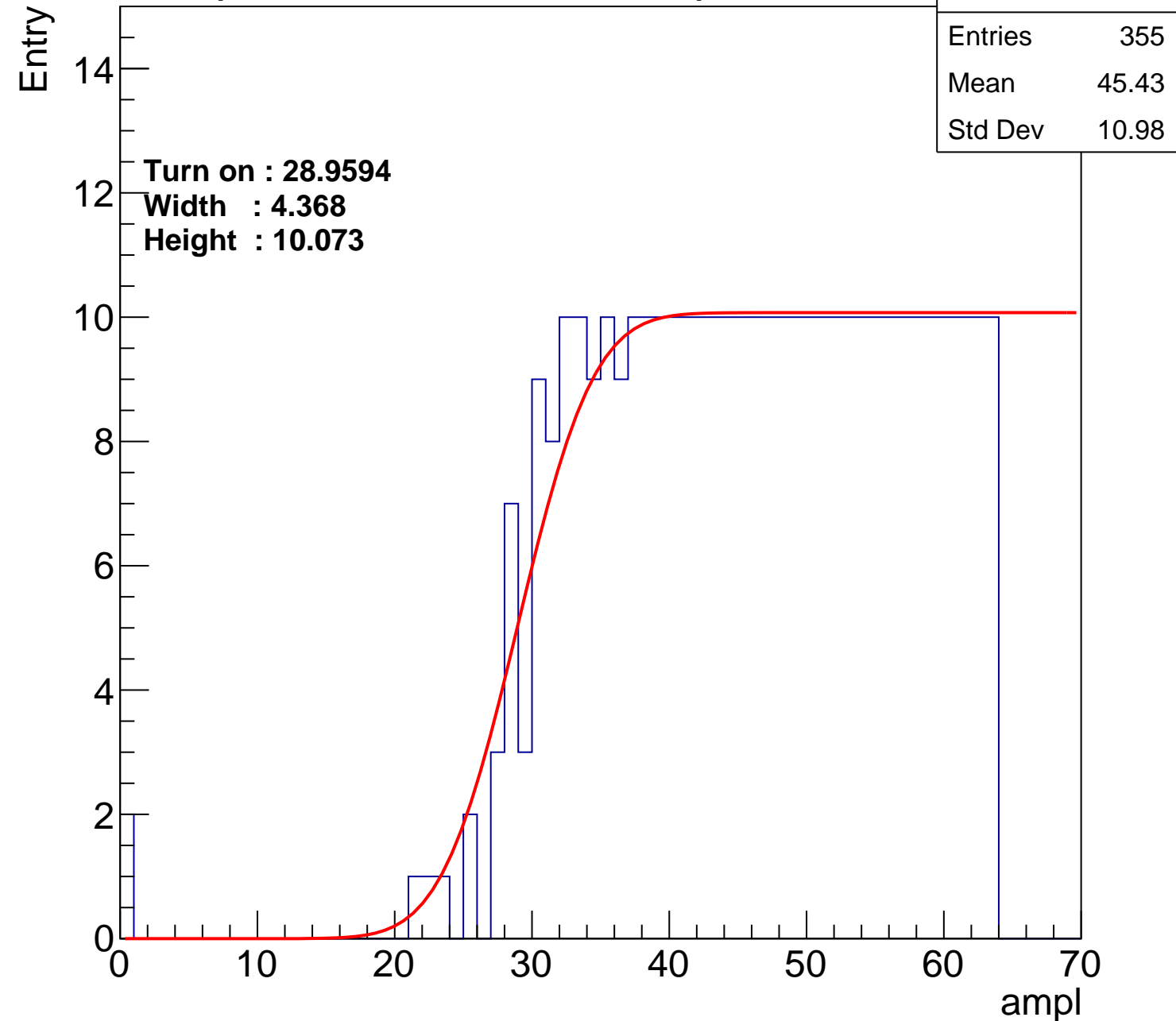
Width : 4.368

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch45

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.75
Std Dev	11.59

Turn on : 27.7940

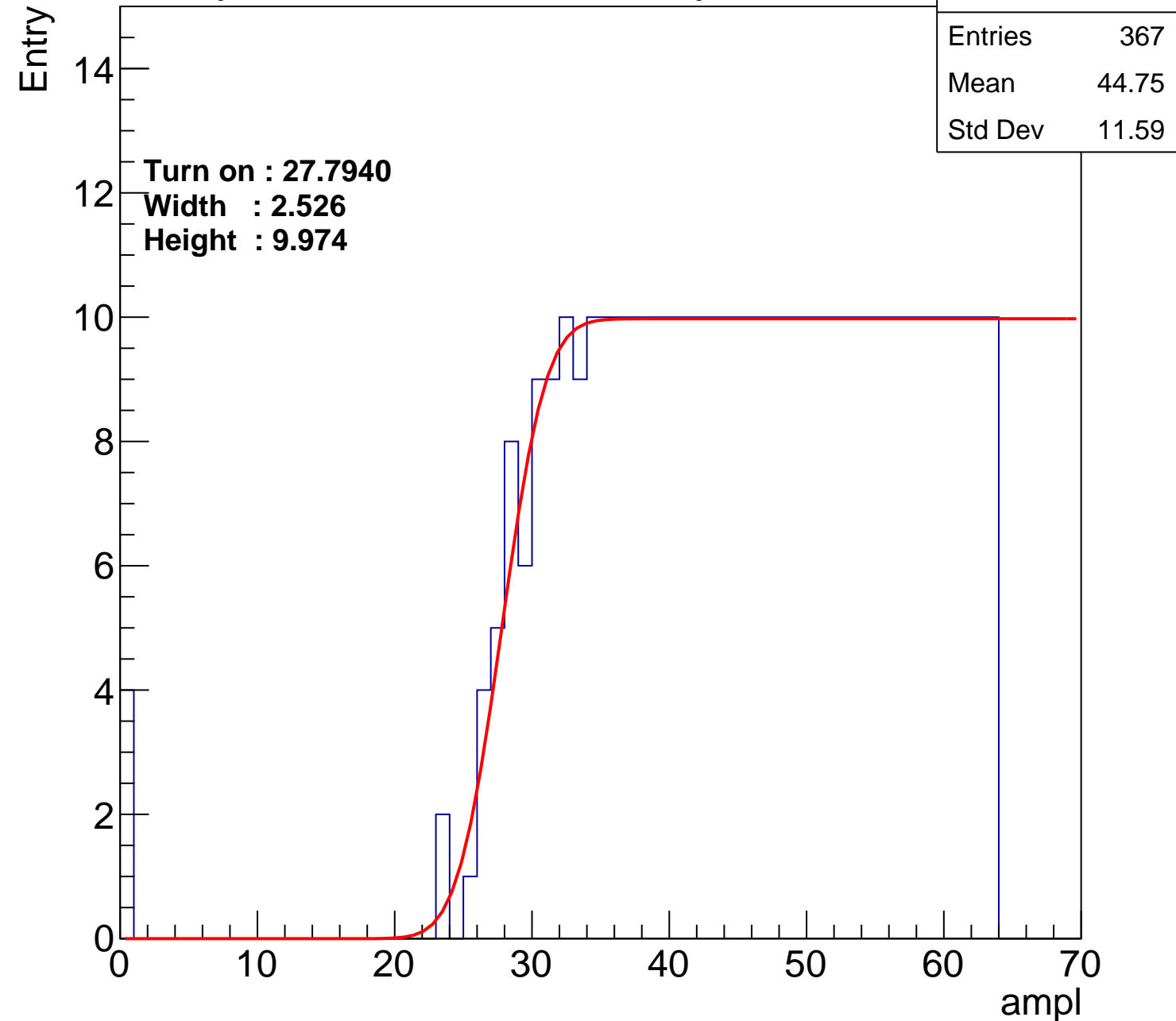
Width : 2.526

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch46

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 26.3549

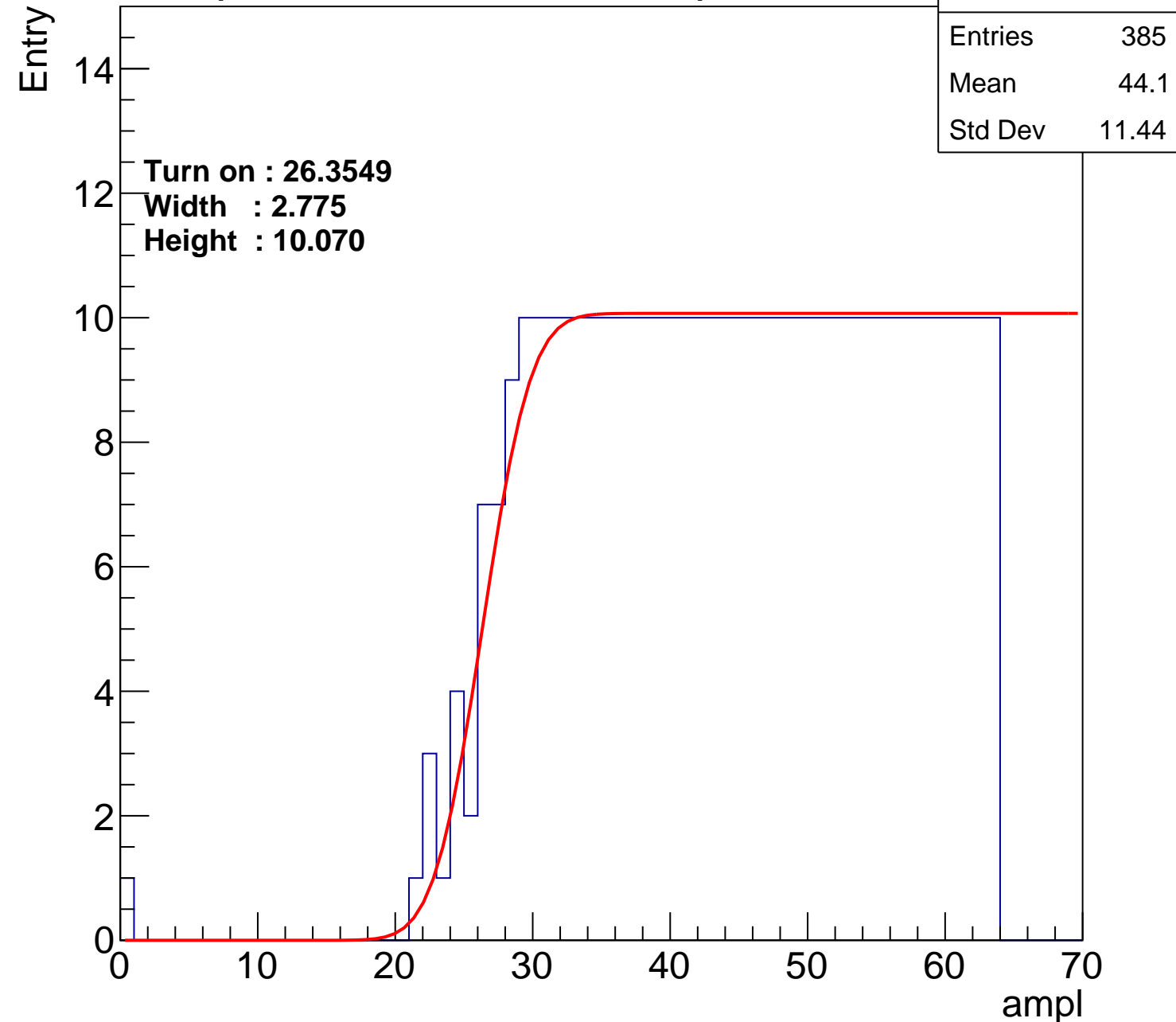
Width : 2.775

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch47

calib_packv5_042523_0143.root, FC#7, port C2

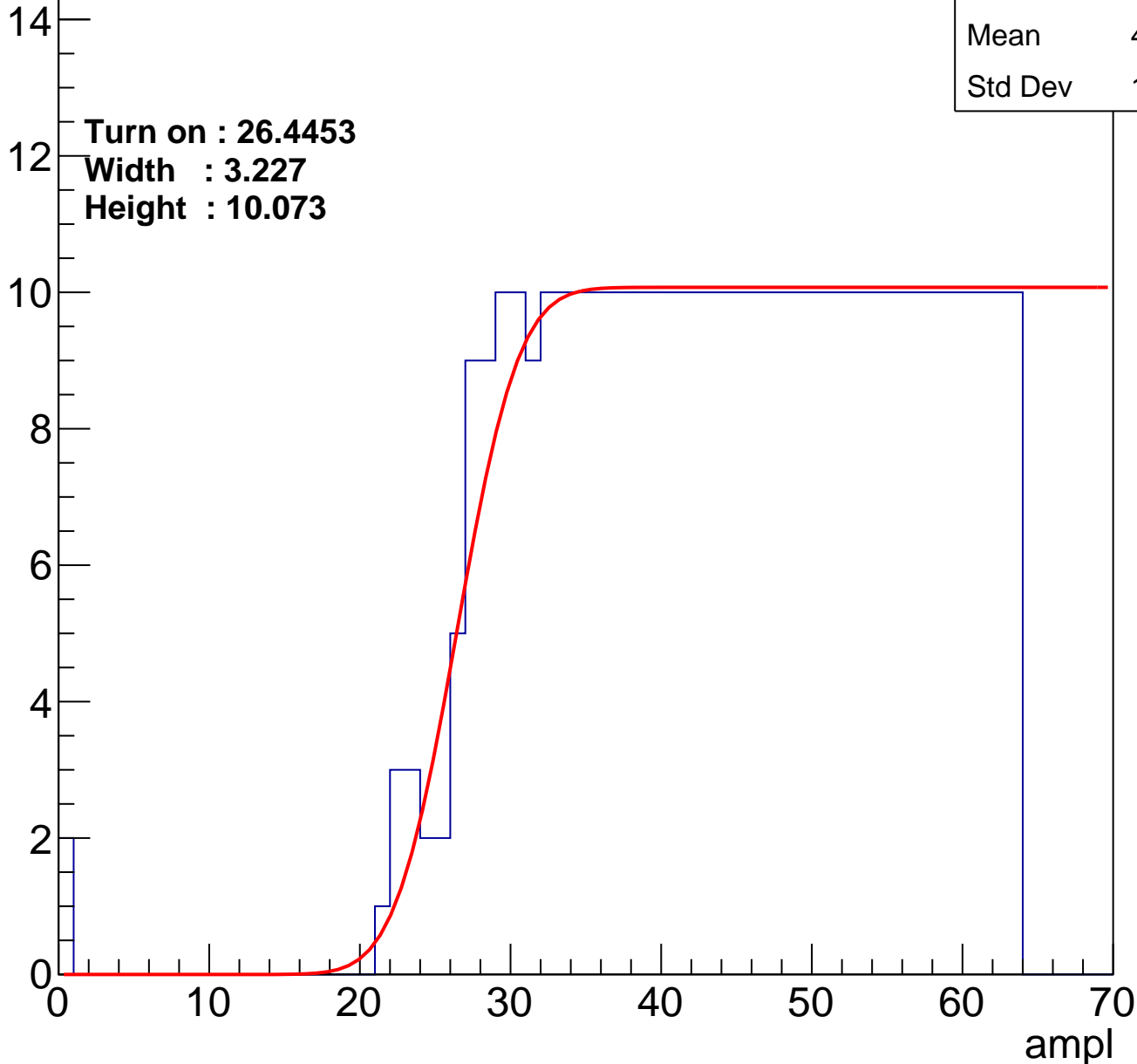
Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 26.4453

Width : 3.227

Height : 10.073

Entry



B1L103S, U9-ch48

calib_packv5_042523_0143.root, FC#7, port C2

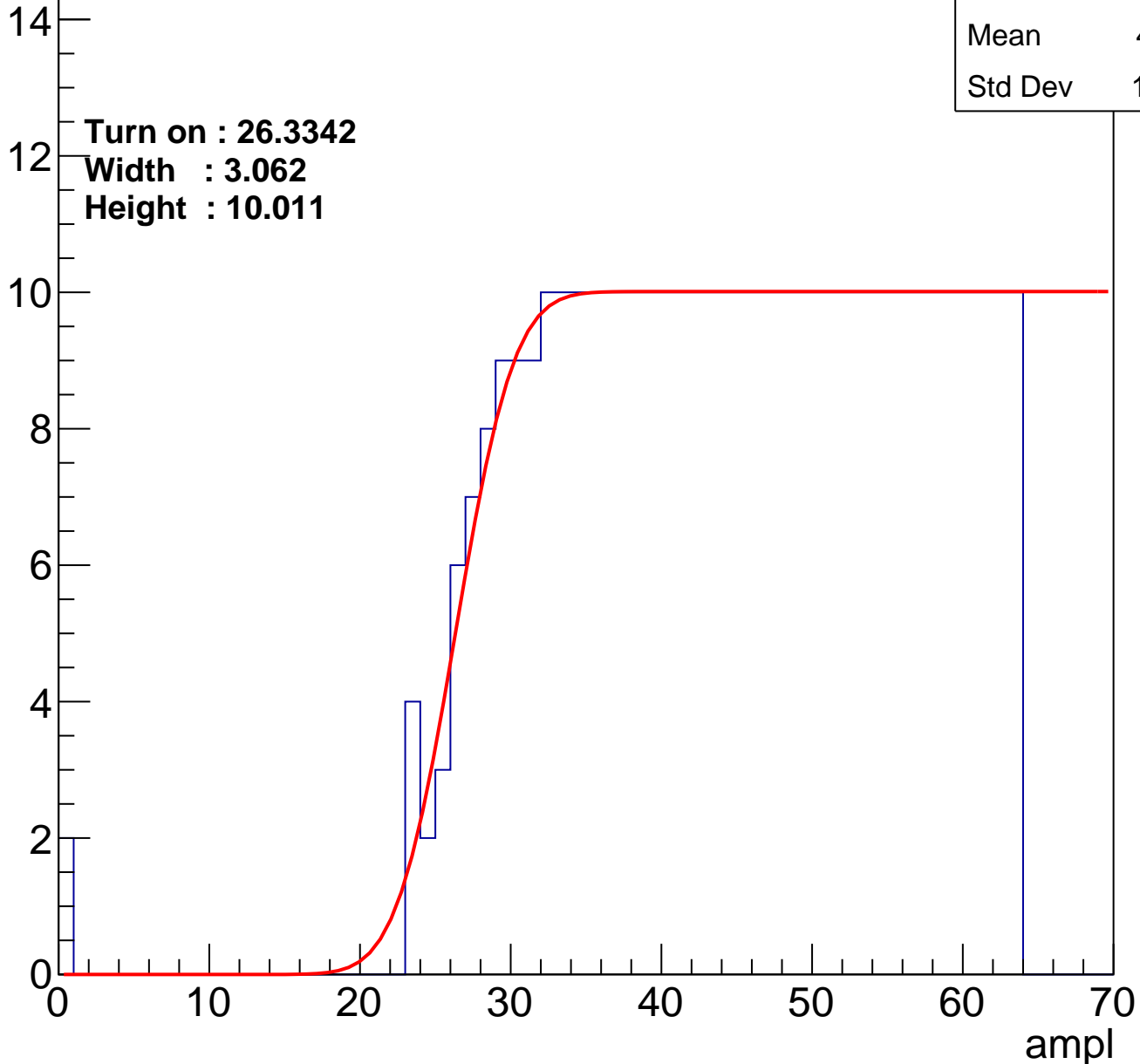
Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.3342

Width : 3.062

Height : 10.011

Entry



B1L103S, U9-ch49

calib_packv5_042523_0143.root, FC#7, port C2

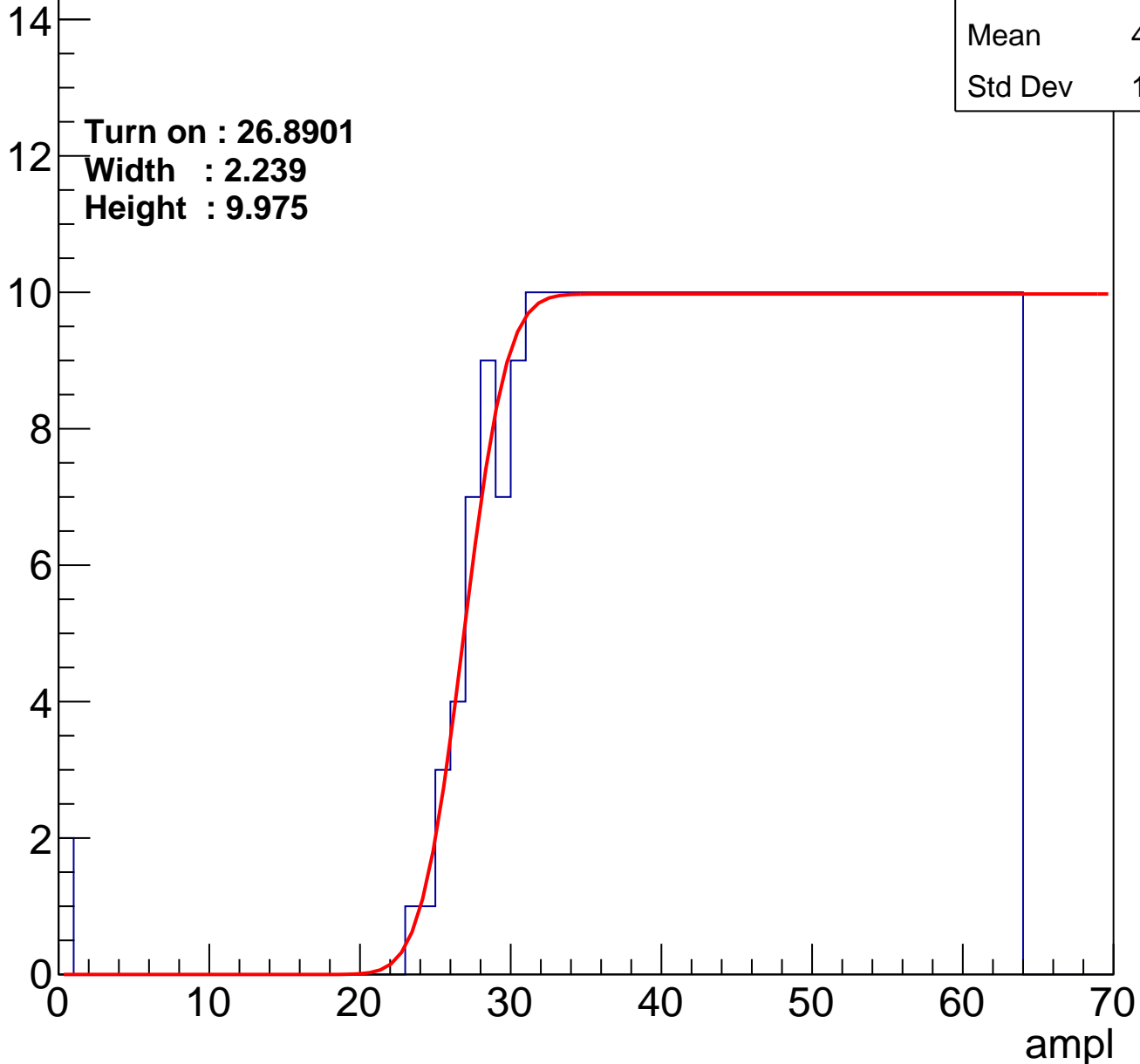
Entries	373
Mean	44.64
Std Dev	11.28

Turn on : 26.8901

Width : 2.239

Height : 9.975

Entry



B1L103S, U9-ch50

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	43.91
Std Dev	12.29

Turn on : 27.0344

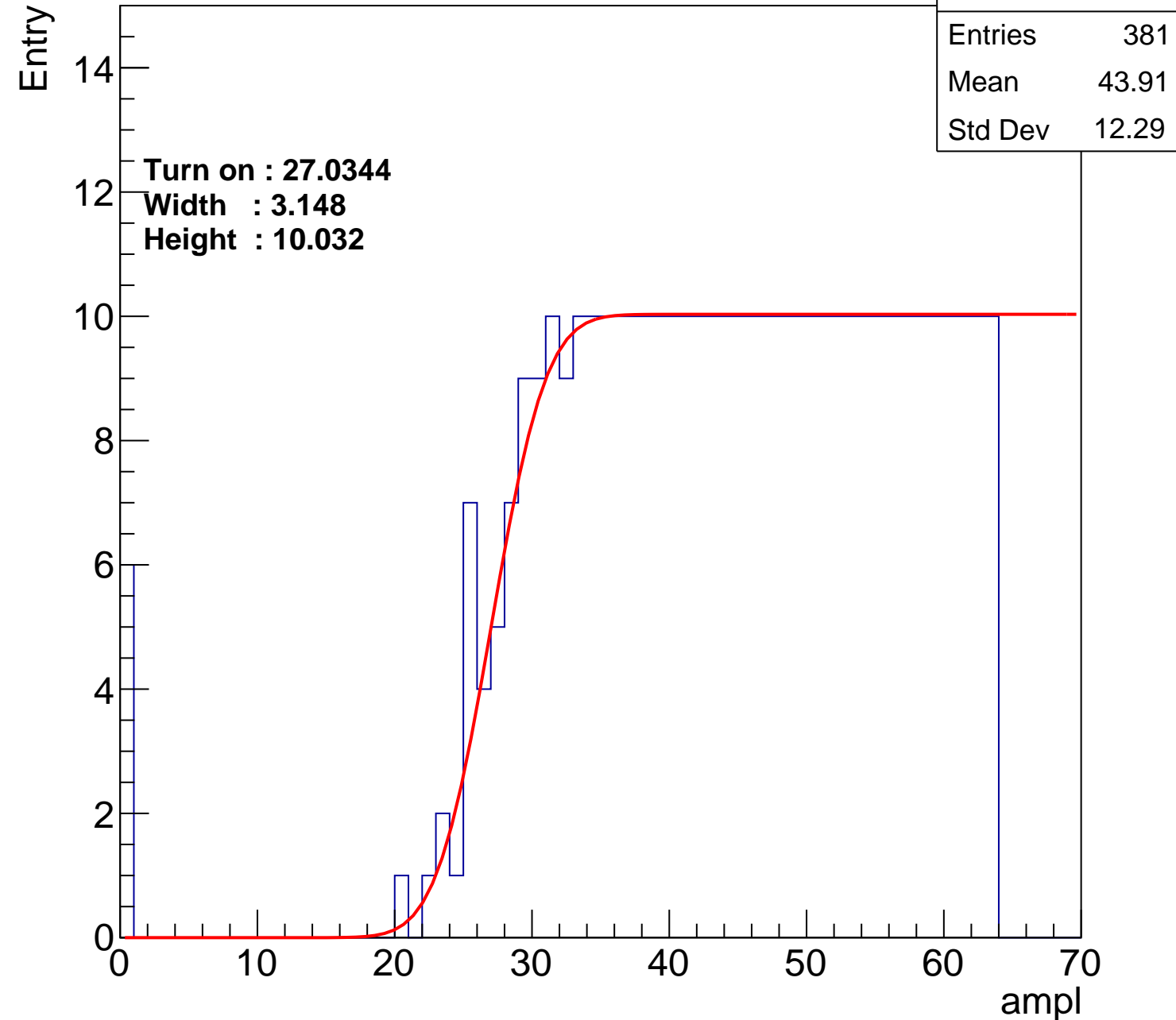
Width : 3.148

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch51

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.2818

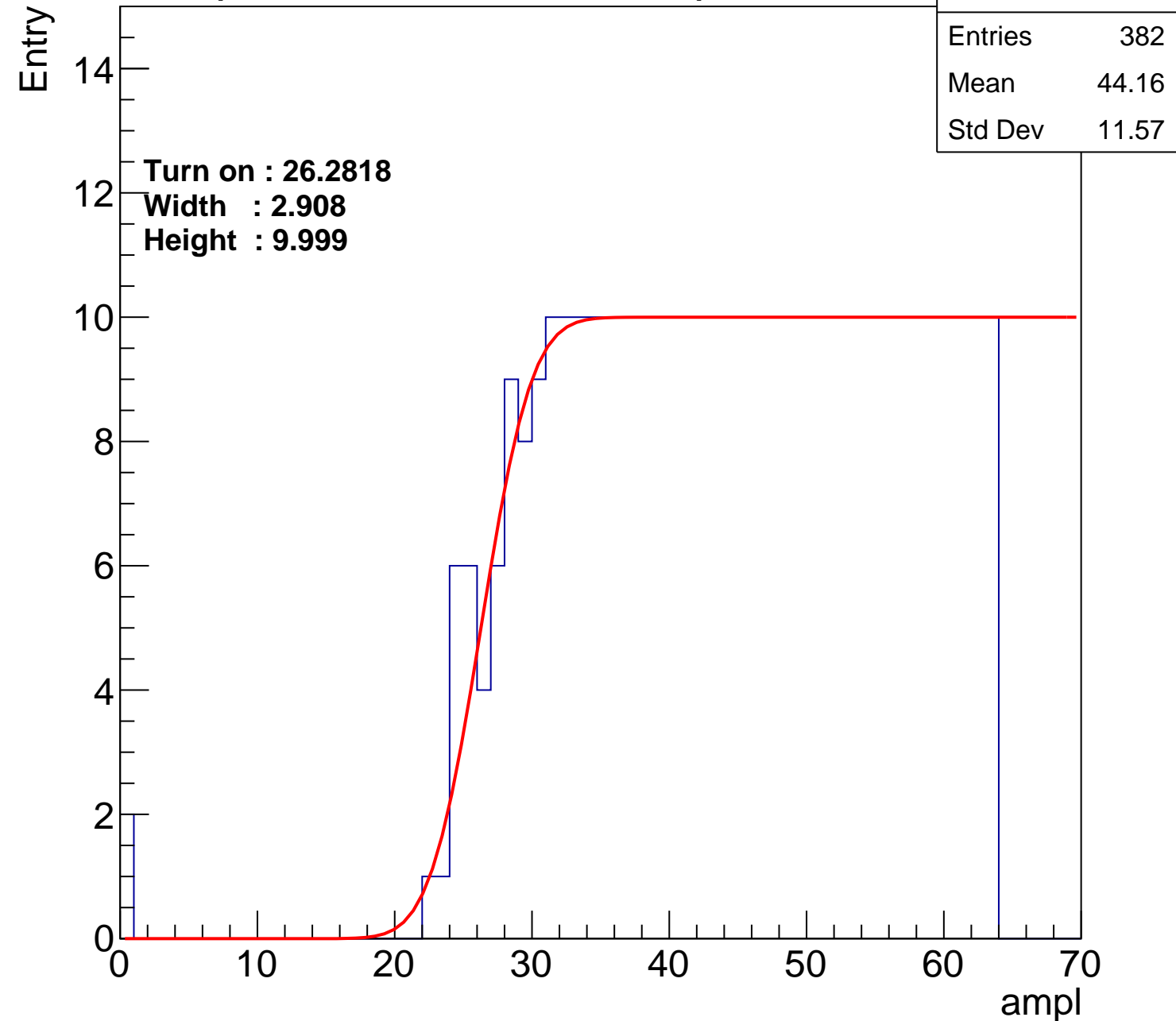
Width : 2.908

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch52

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.95
Std Dev	11.21

Turn on : 28.2113

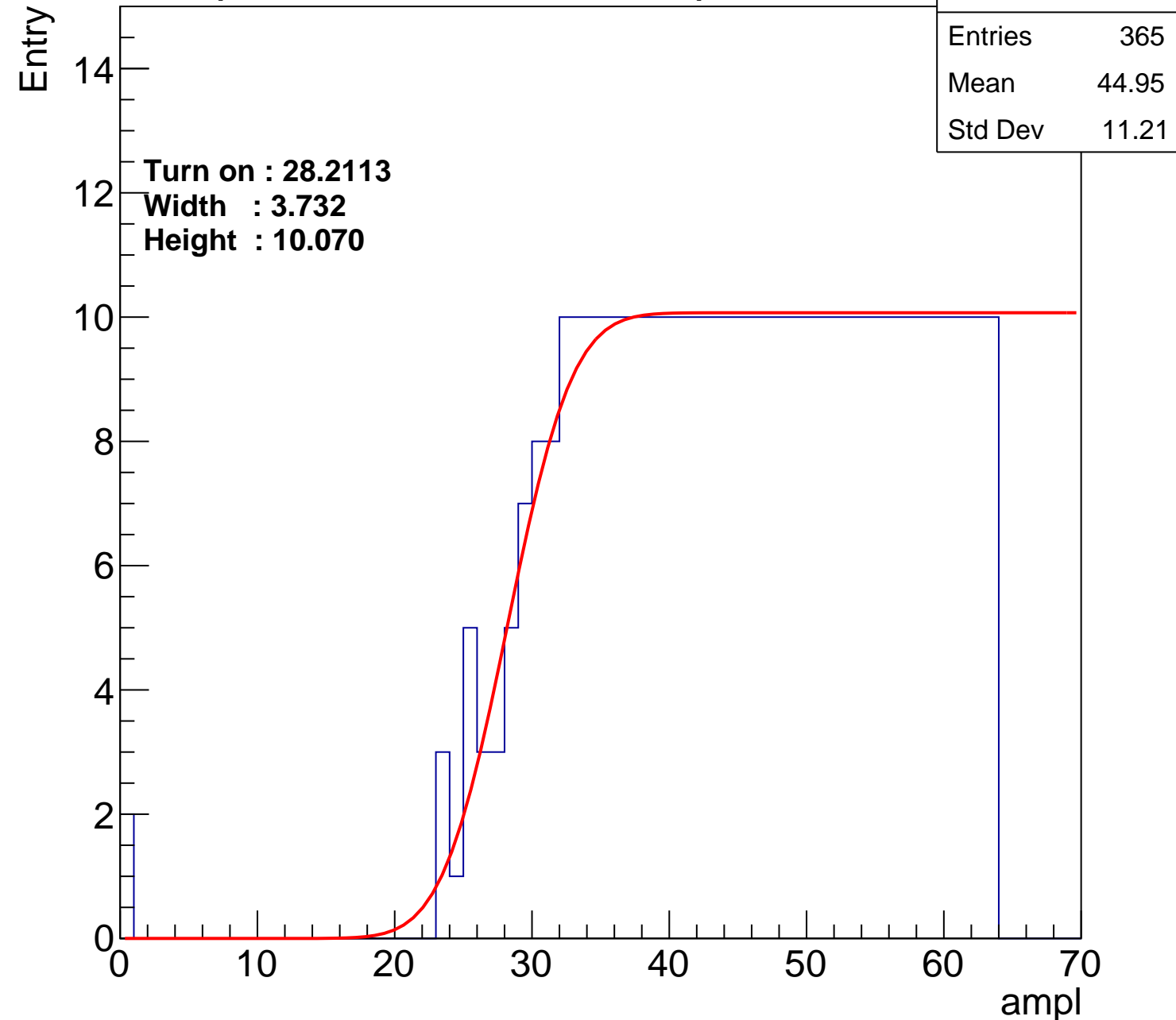
Width : 3.732

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch53

calib_packv5_042523_0143.root, FC#7, port C2

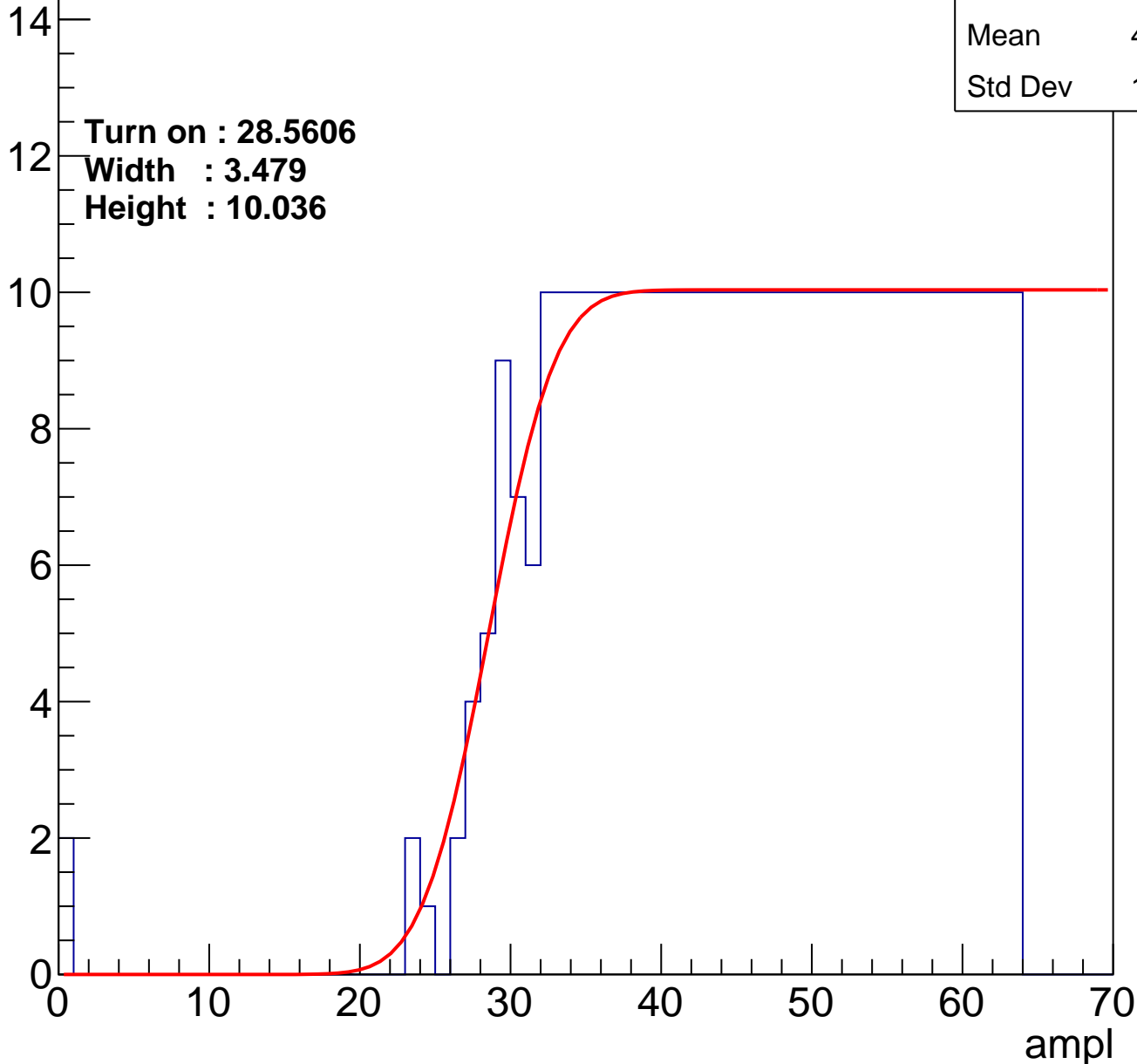
Entries	358
Mean	45.33
Std Dev	10.99

Turn on : 28.5606

Width : 3.479

Height : 10.036

Entry



B1L103S, U9-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.85
Std Dev	11.75

Turn on : 25.4708

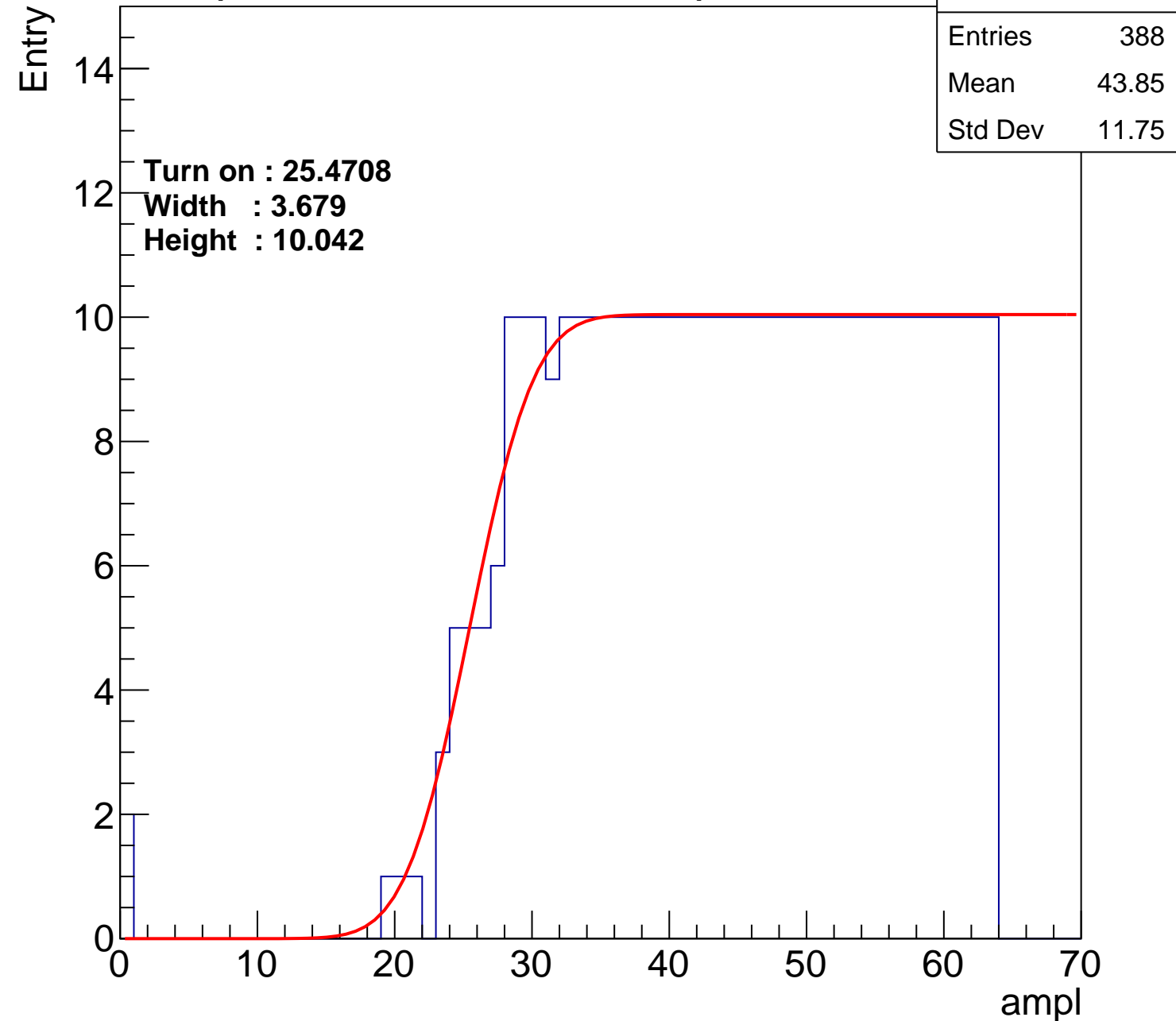
Width : 3.679

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch55

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.2
Std Dev	11.74

Turn on : 26.6186

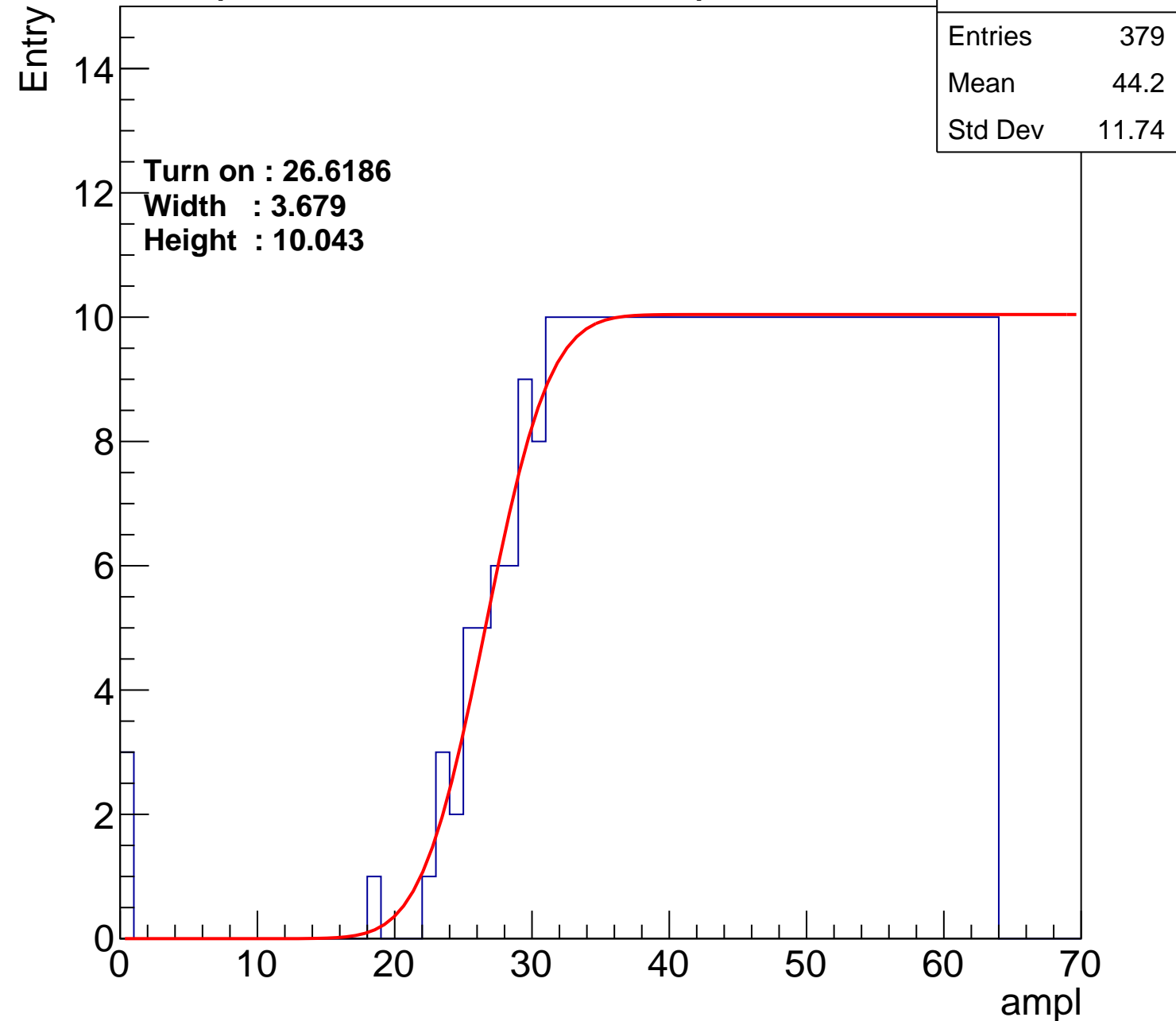
Width : 3.679

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch56

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.11
Std Dev	11.61

Turn on : 26.9450

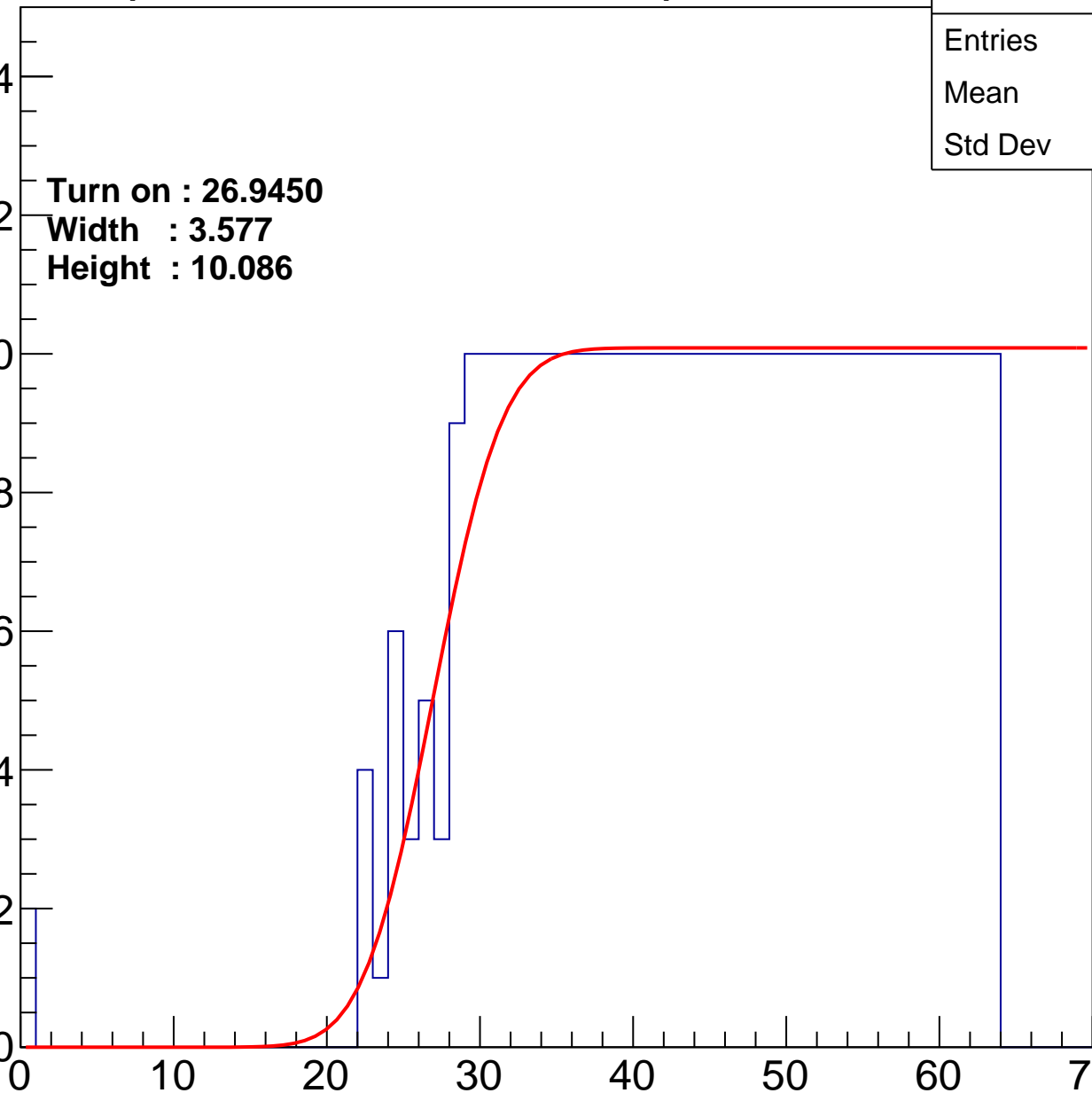
Width : 3.577

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch57

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	45.02
Std Dev	10.95

Turn on : 28.5446

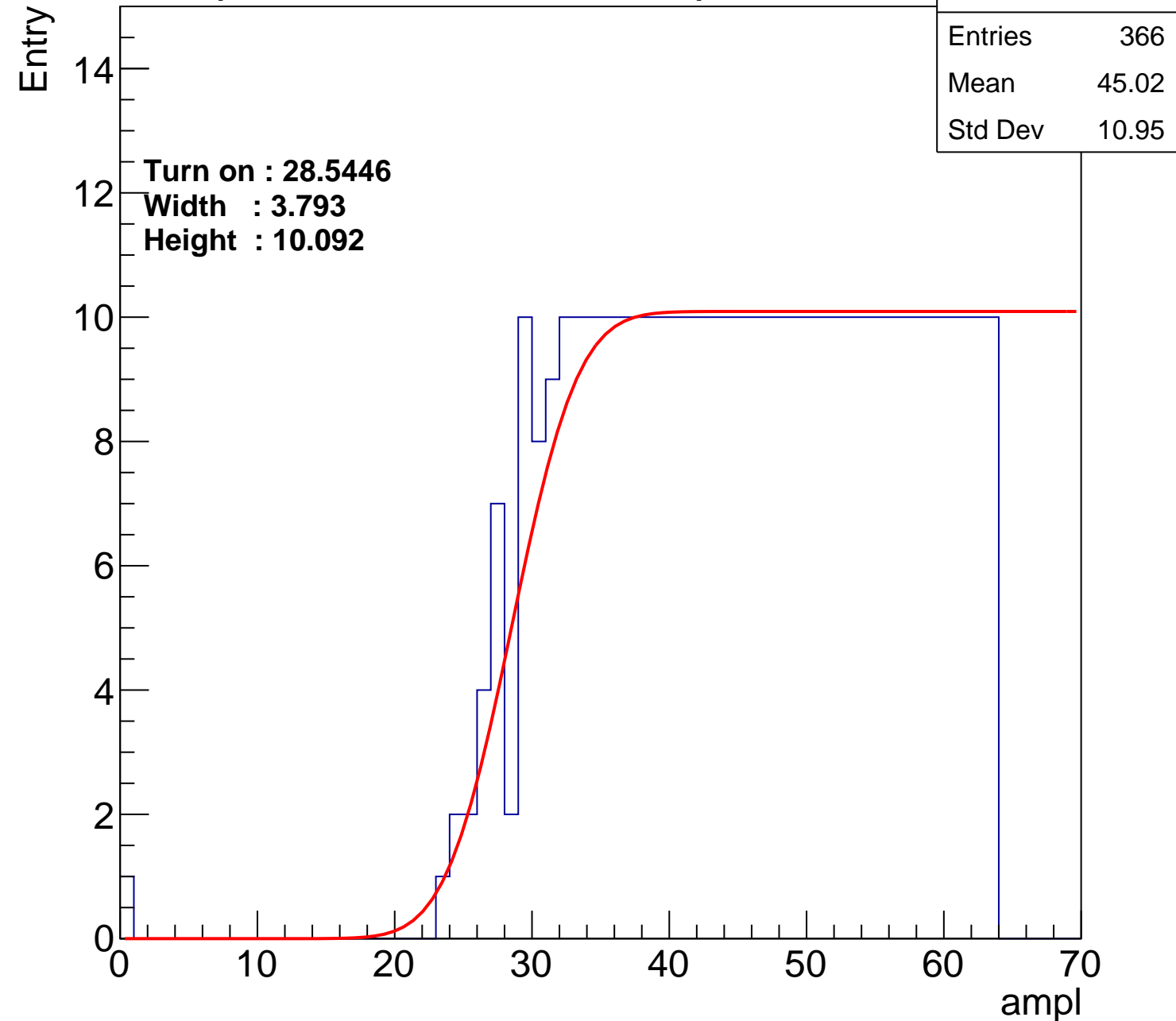
Width : 3.793

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch58

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.85
Std Dev	11.93

Turn on : 25.9580

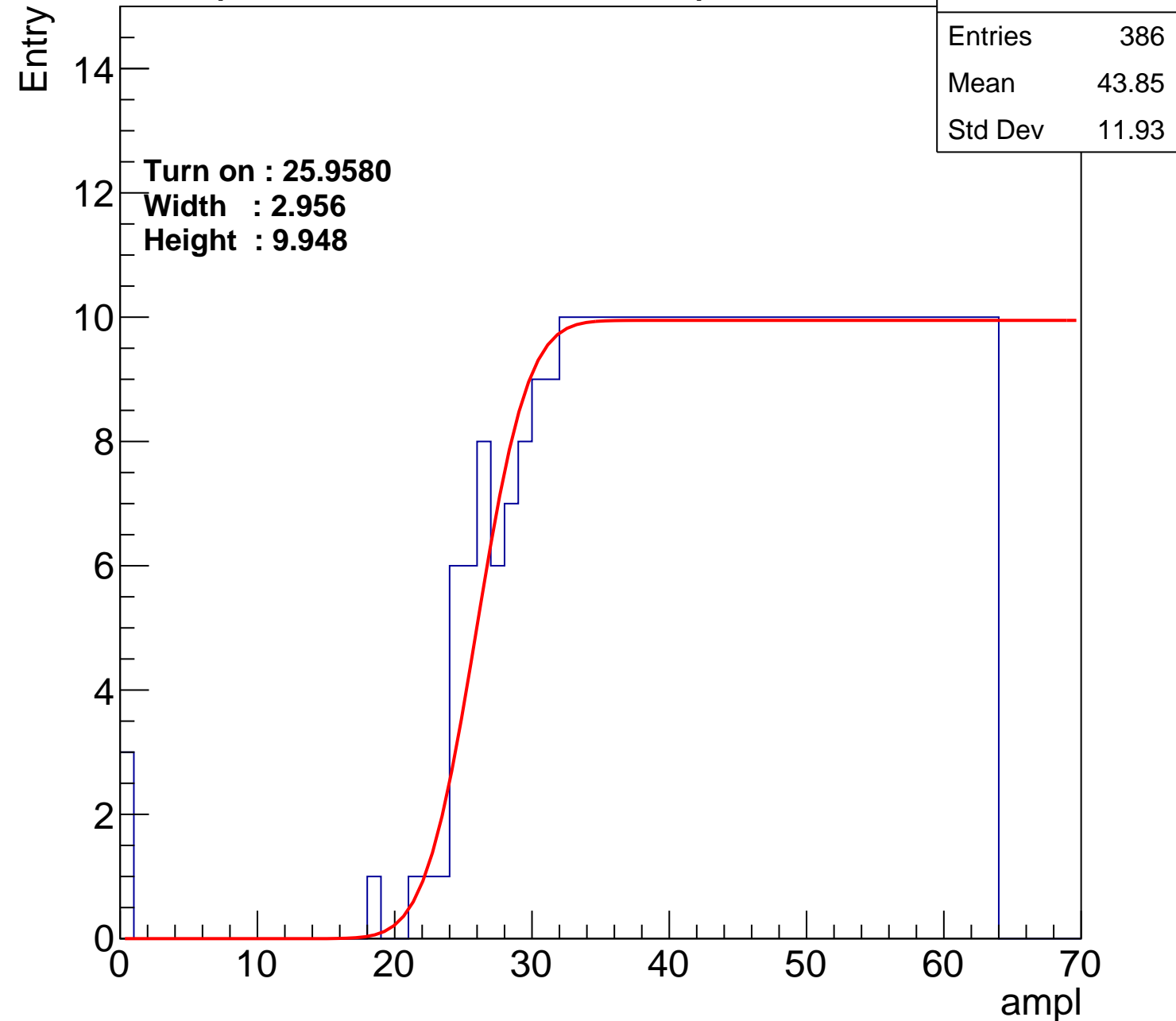
Width : 2.956

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.27
Std Dev	11.37

Turn on : 26.2767

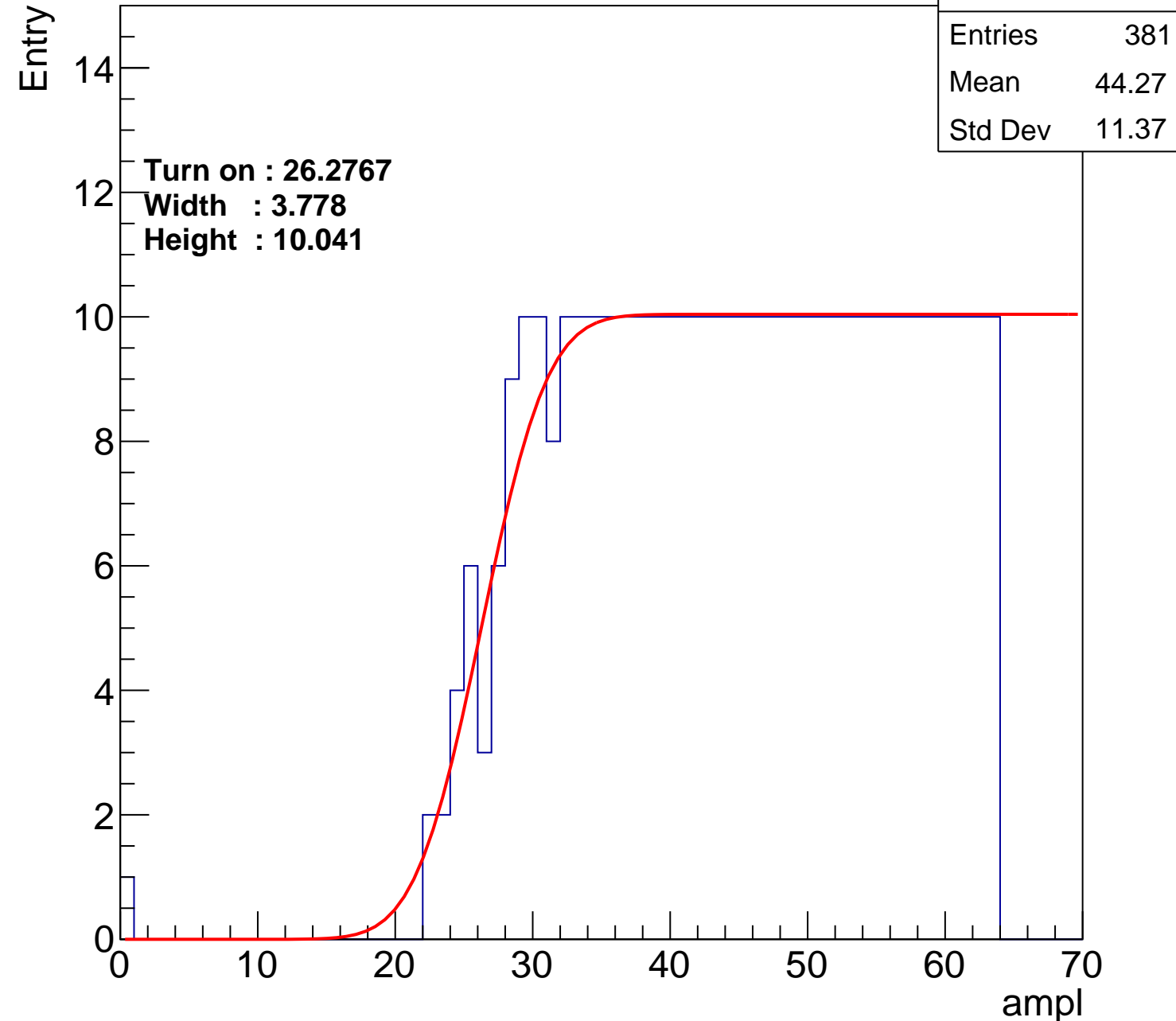
Width : 3.778

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch60

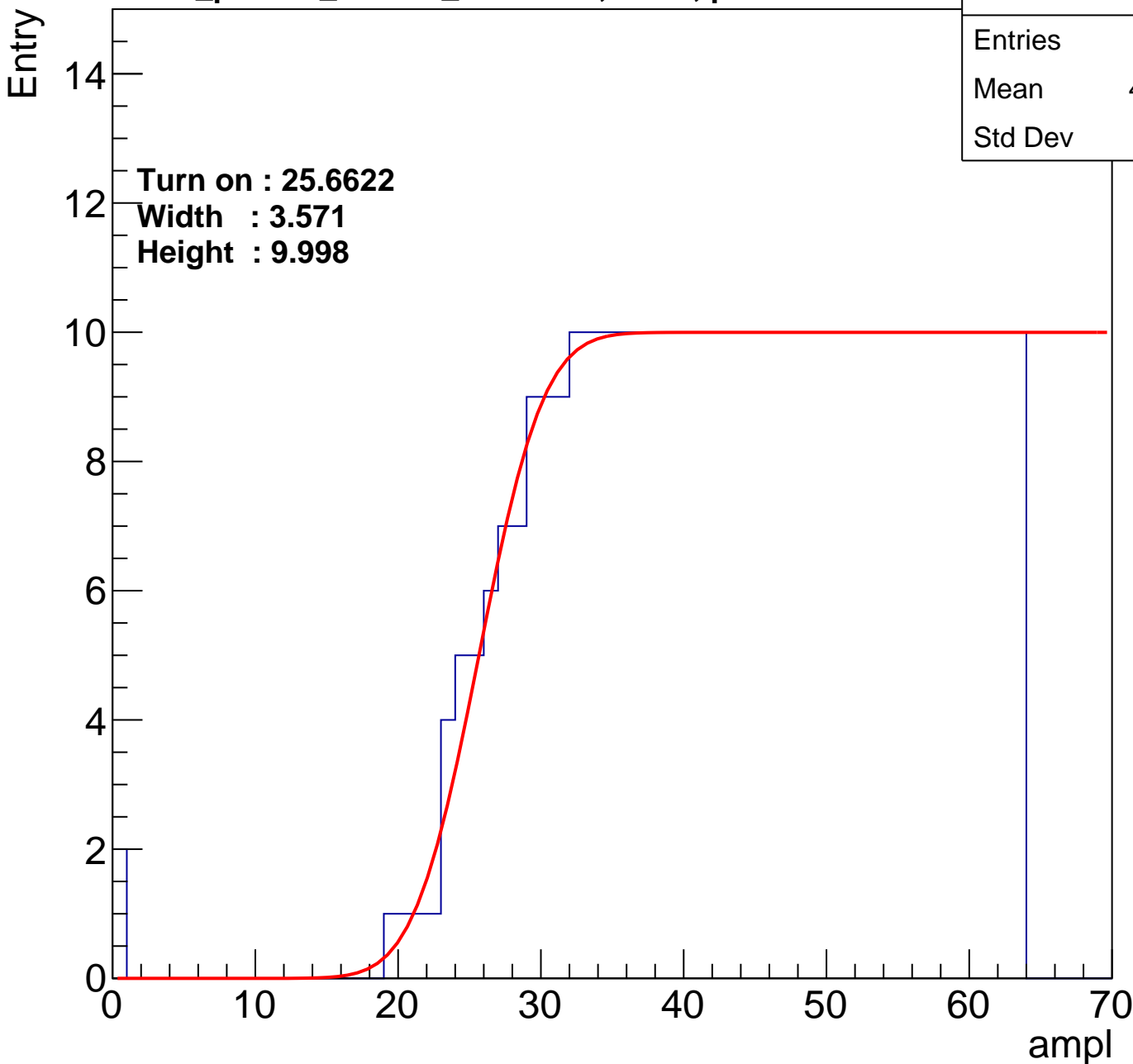
calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.85
Std Dev	11.8

Turn on : 25.6622

Width : 3.571

Height : 9.998



B1L103S, U9-ch61

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.37
Std Dev	11.93

Turn on : 26.8778

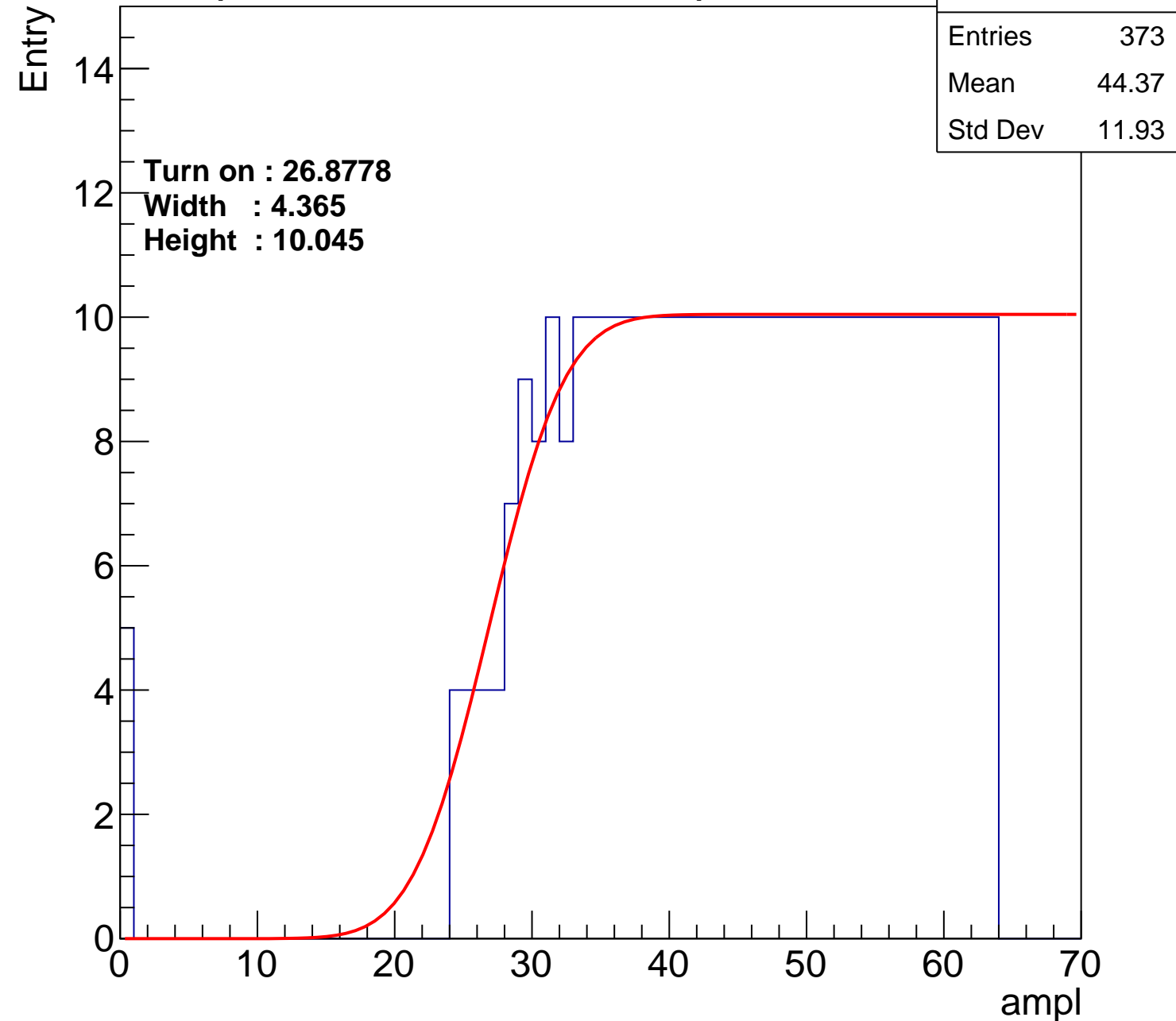
Width : 4.365

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch62

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.43
Std Dev	11.47

Turn on : 27.0970

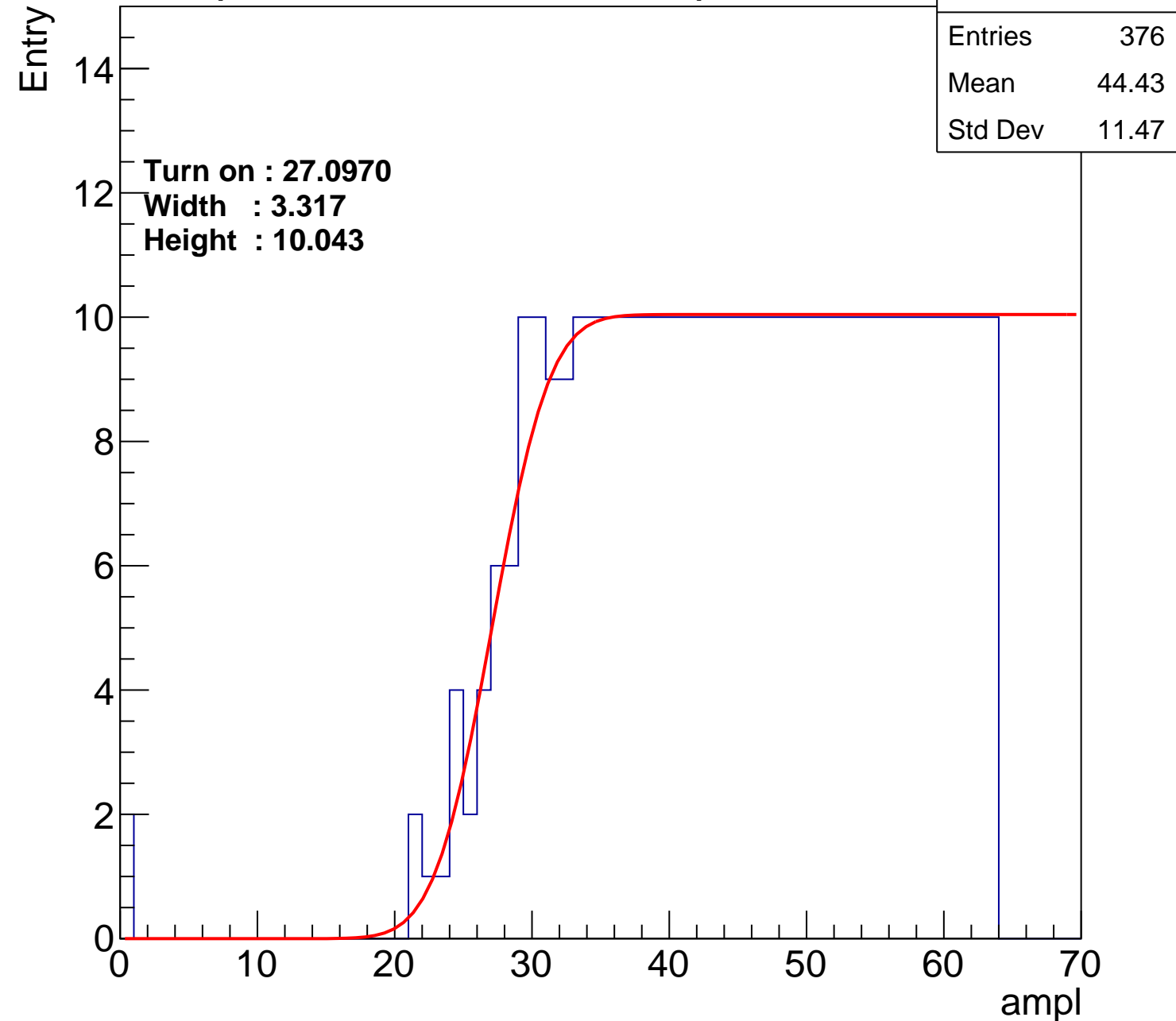
Width : 3.317

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch63

calib_packv5_042523_0143.root, FC#7, port C2

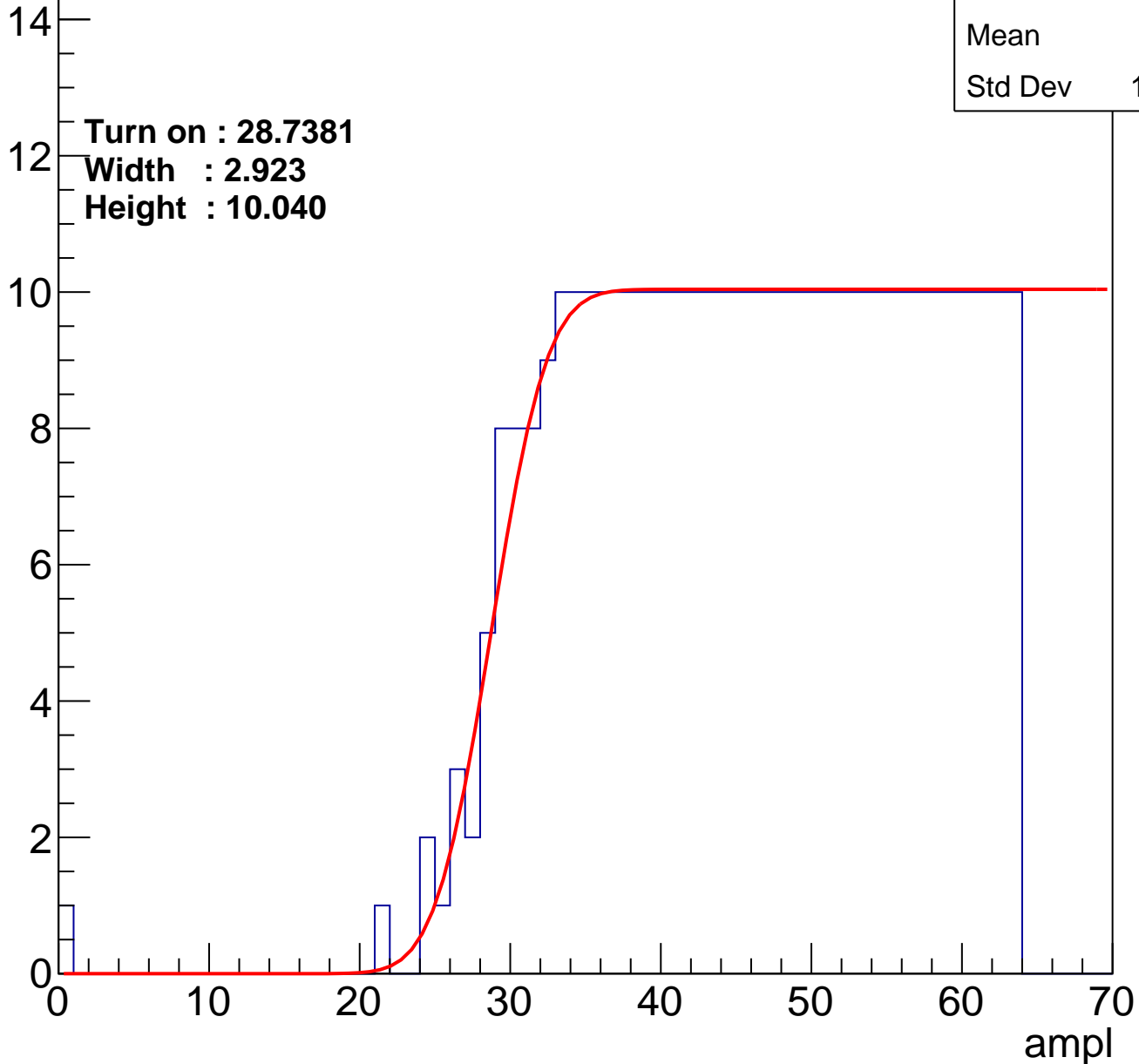
Entries	358
Mean	45.4
Std Dev	10.77

Turn on : 28.7381

Width : 2.923

Height : 10.040

Entry



B1L103S, U9-ch64

calib_packv5_042523_0143.root, FC#7, port C2

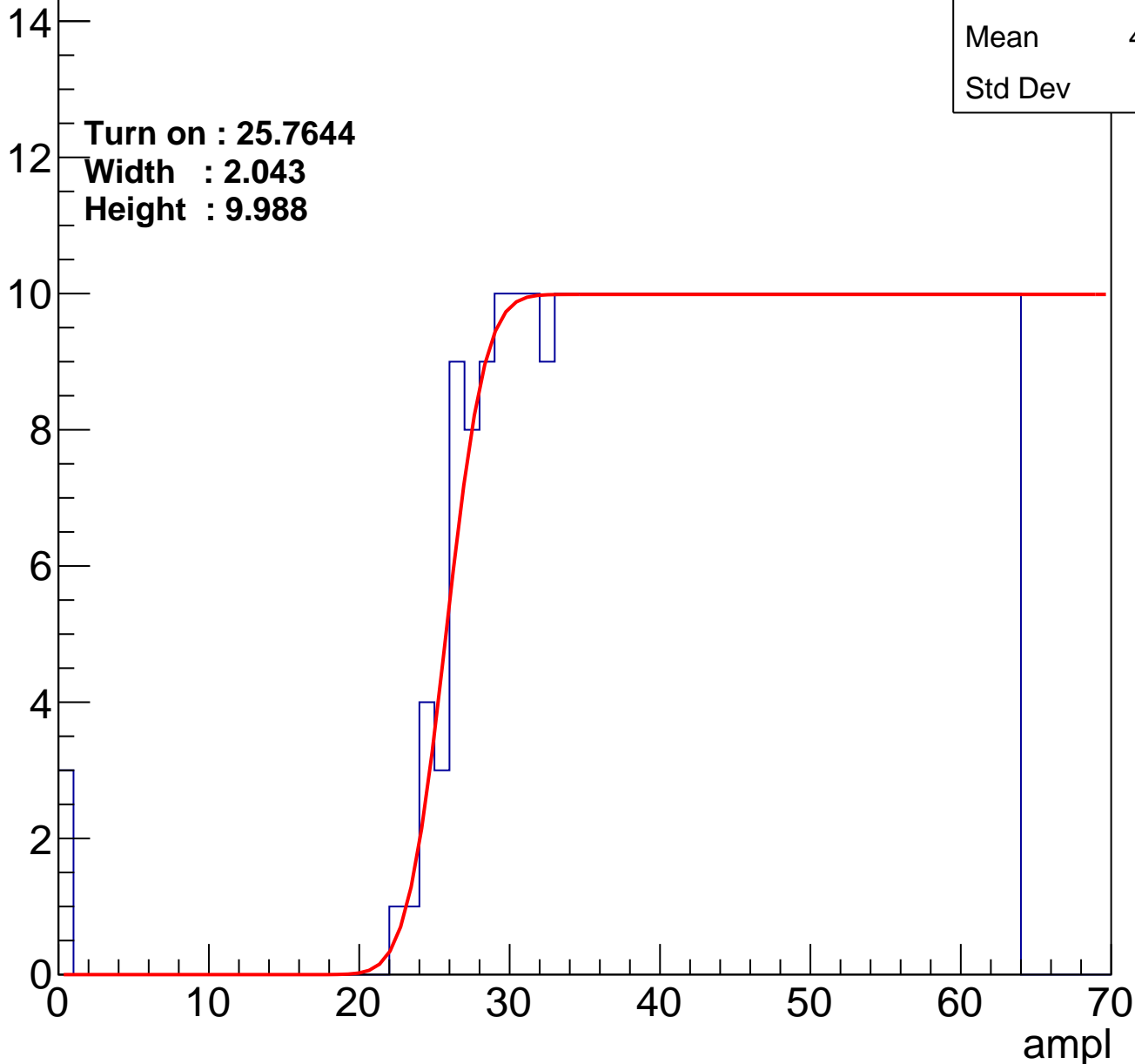
Entry

Entries	387
Mean	43.89
Std Dev	11.8

Turn on : 25.7644

Width : 2.043

Height : 9.988



B1L103S, U9-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.09
Std Dev	11.88

Turn on : 26.8327

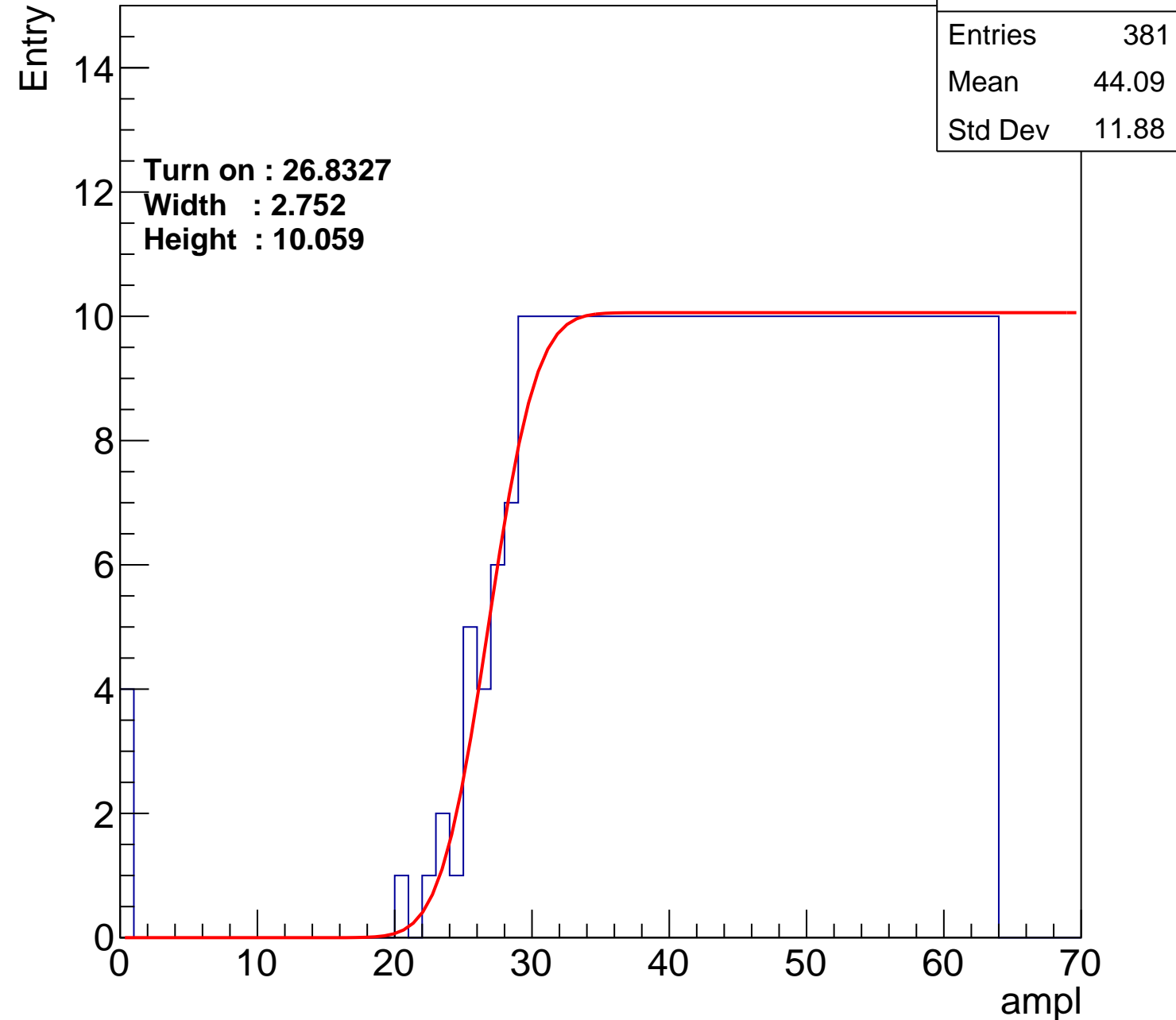
Width : 2.752

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch66

calib_packv5_042523_0143.root, FC#7, port C2

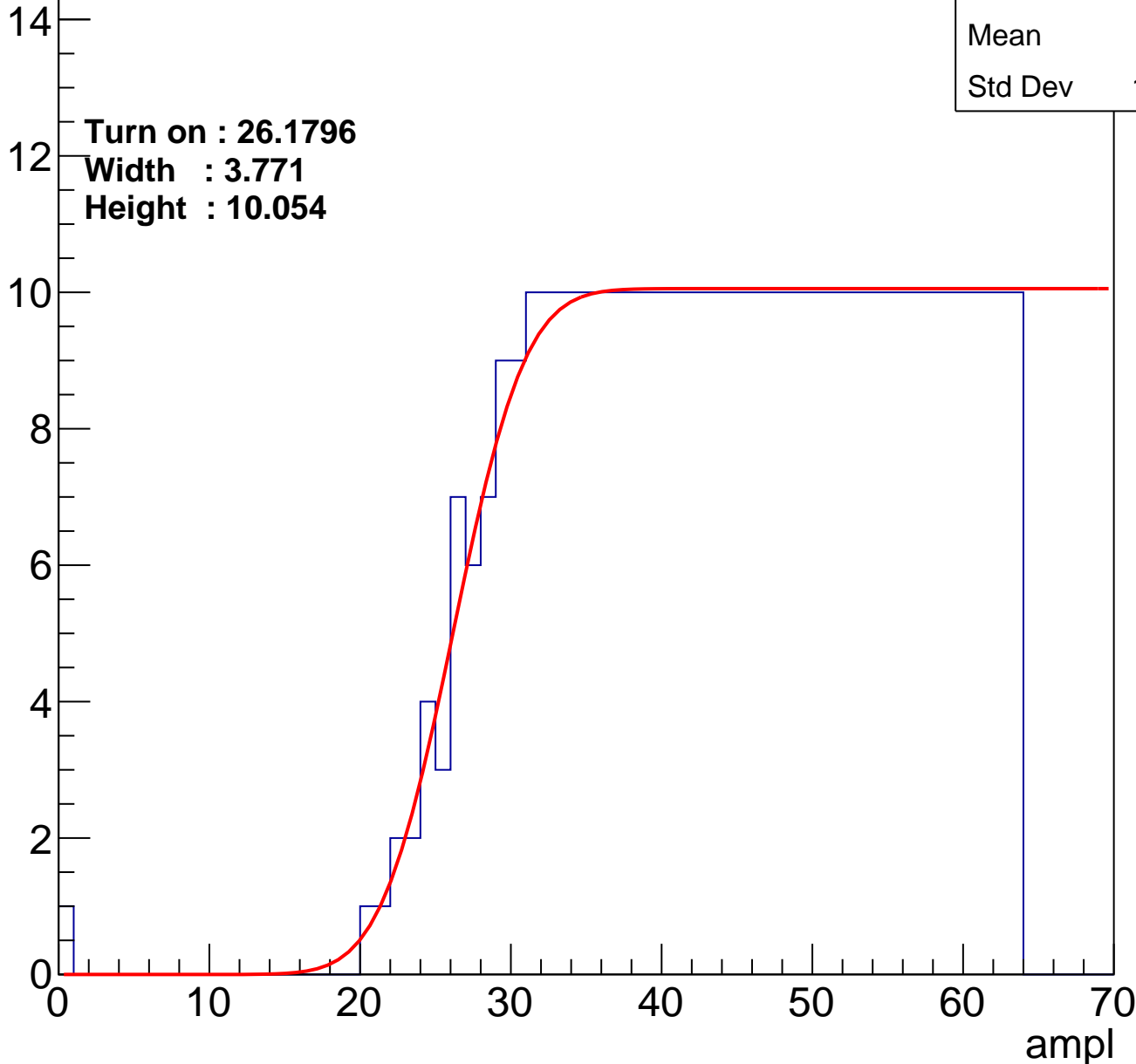
Entries	382
Mean	44.2
Std Dev	11.44

Turn on : 26.1796

Width : 3.771

Height : 10.054

Entry



B1L103S, U9-ch67

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.3
Std Dev	12.07

Turn on : 27.5917

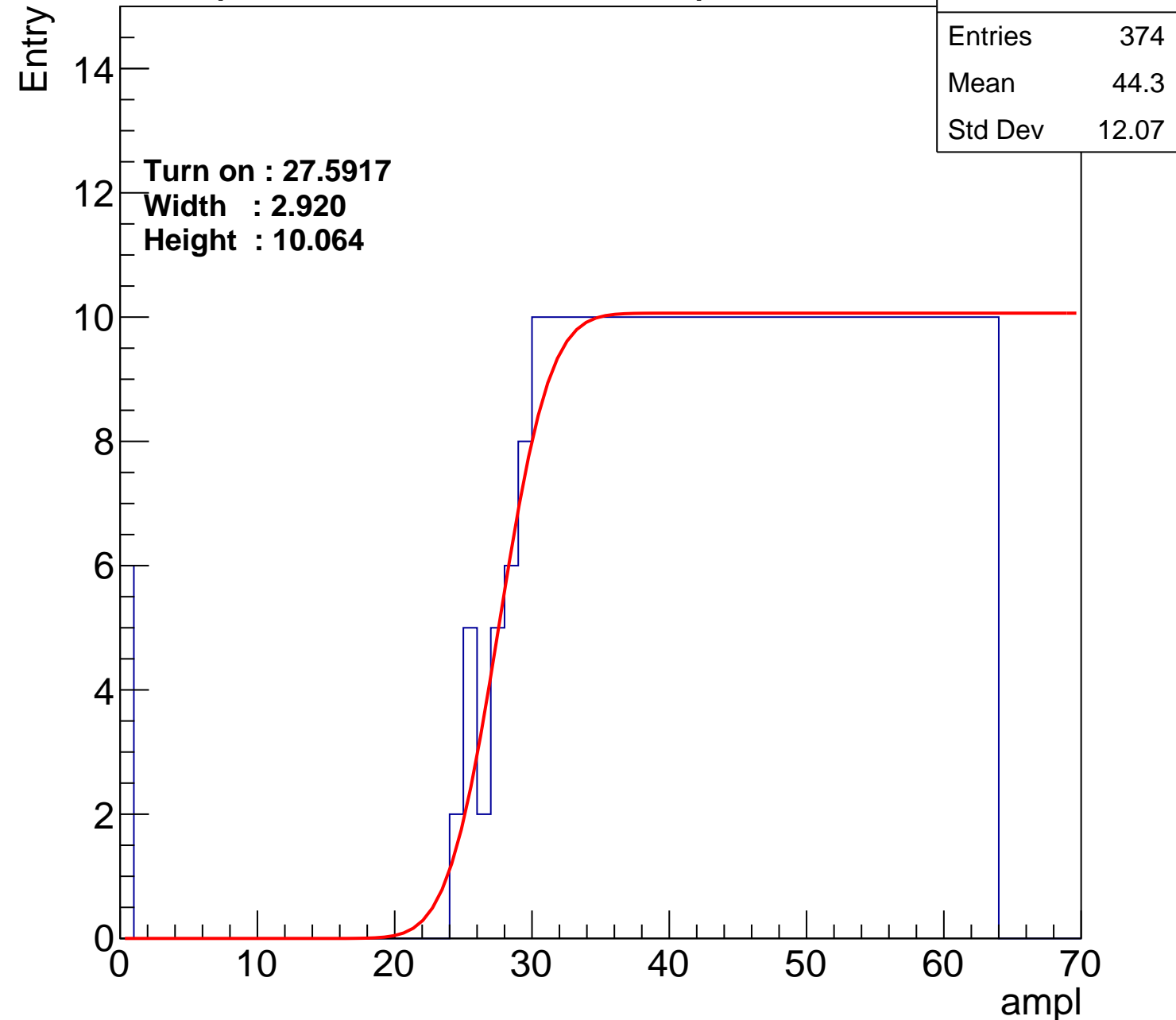
Width : 2.920

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch68

calib_packv5_042523_0143.root, FC#7, port C2

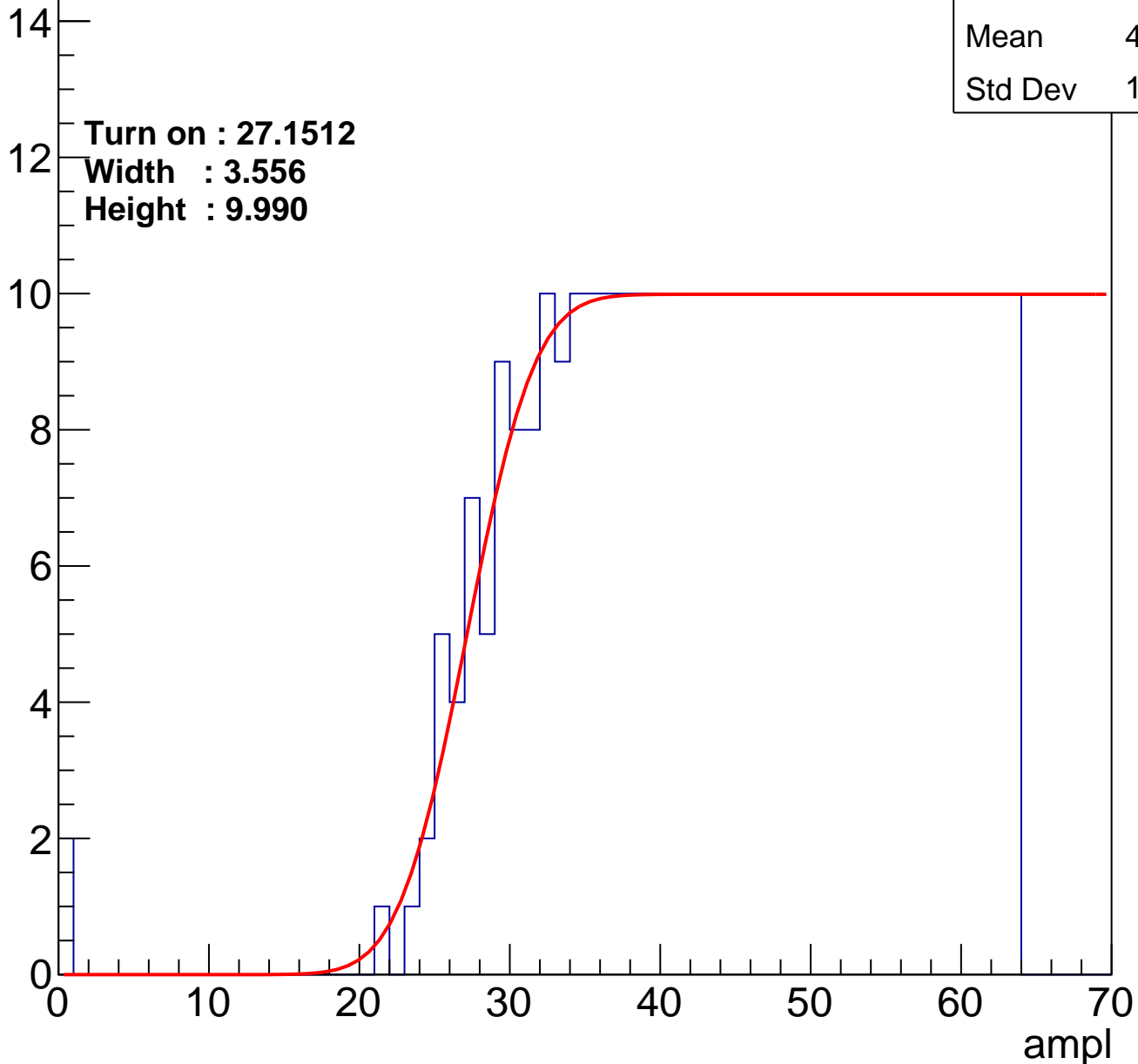
Entries	371
Mean	44.65
Std Dev	11.36

Turn on : 27.1512

Width : 3.556

Height : 9.990

Entry



B1L103S, U9-ch69

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.79
Std Dev	11.26

Turn on : 27.7556

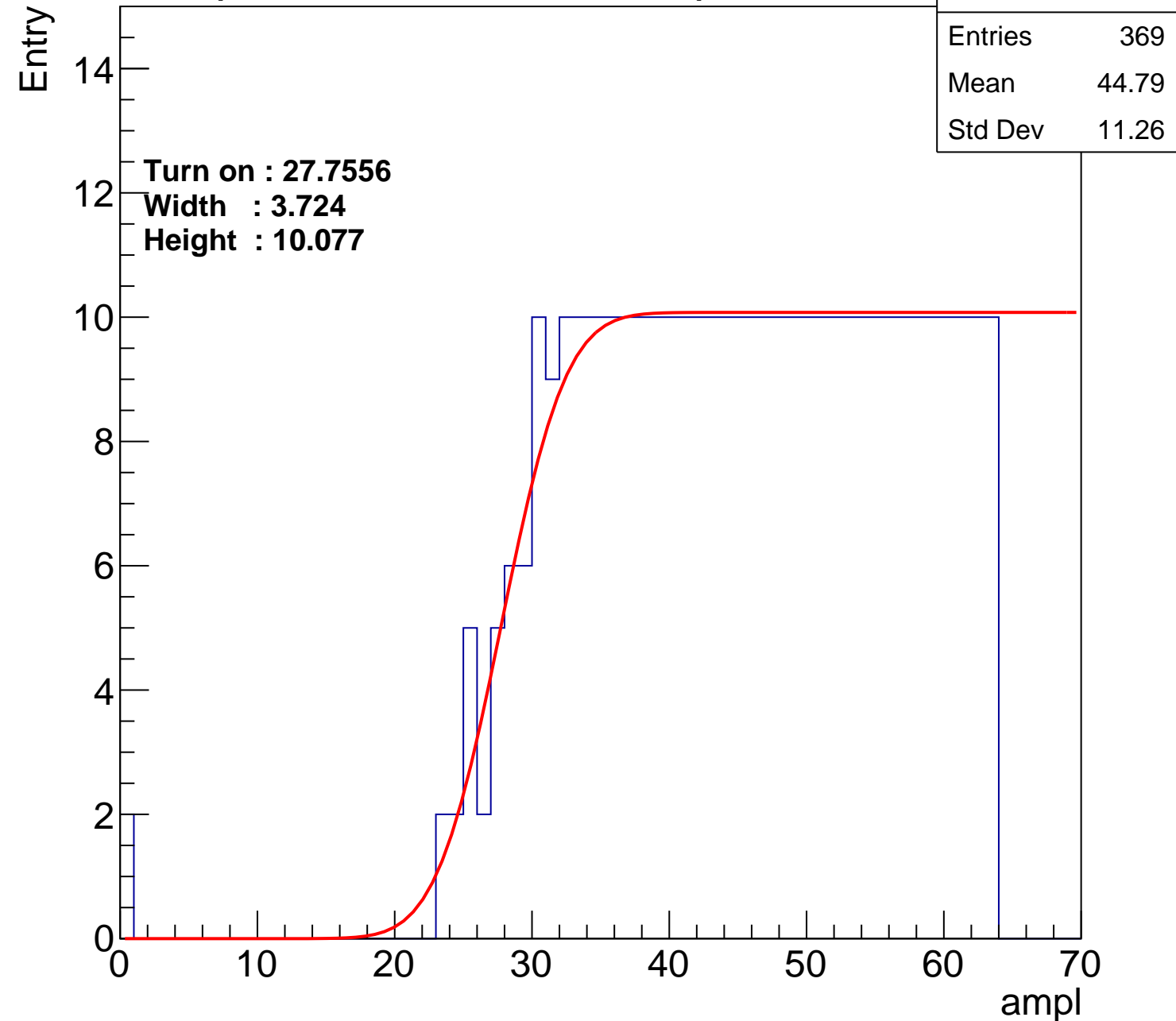
Width : 3.724

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch70

calib_packv5_042523_0143.root, FC#7, port C2

Entries	357
Mean	45.46
Std Dev	10.72

Turn on : 28.3988

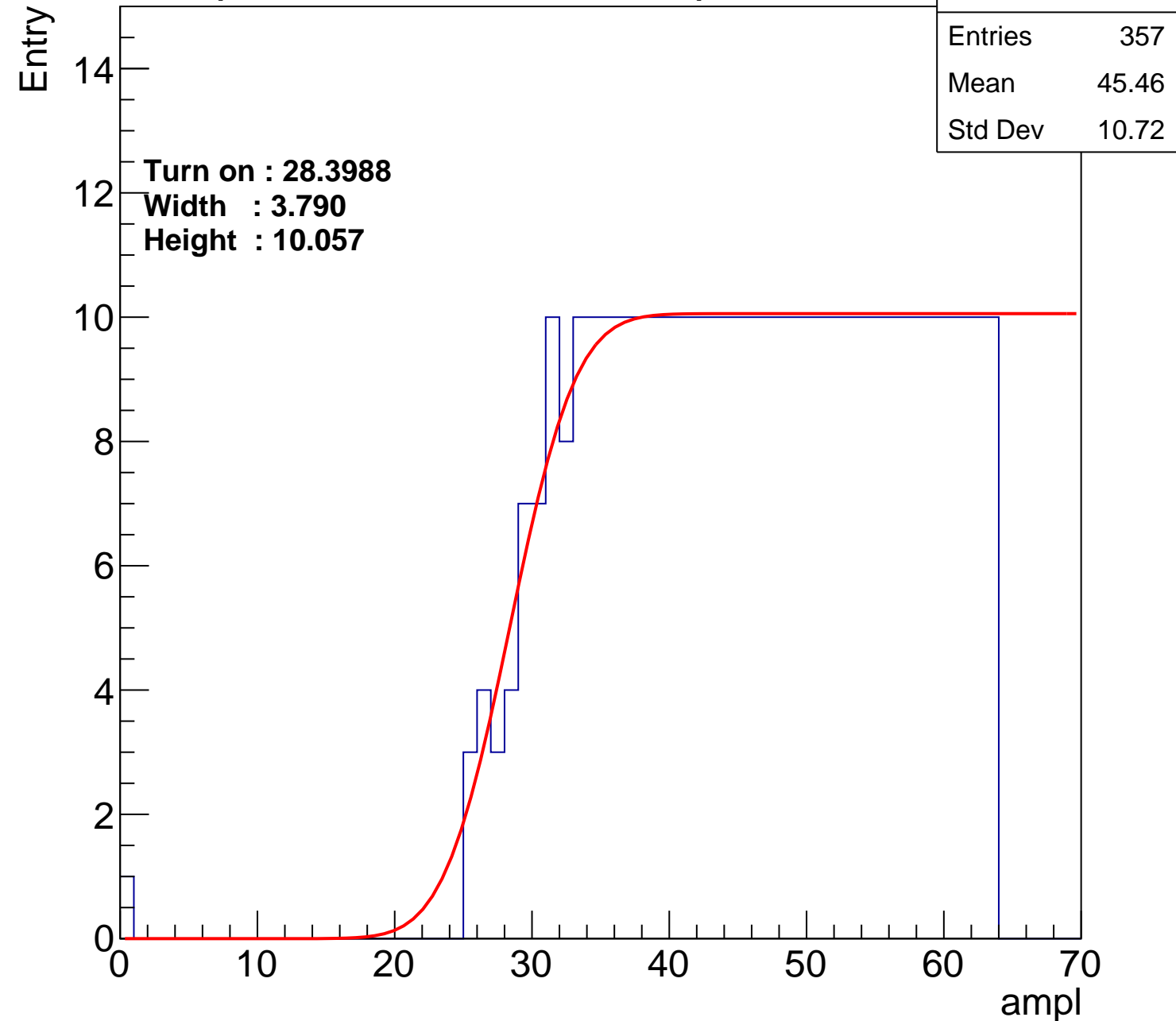
Width : 3.790

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch71

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.32
Std Dev	11.35

Turn on : 26.2971

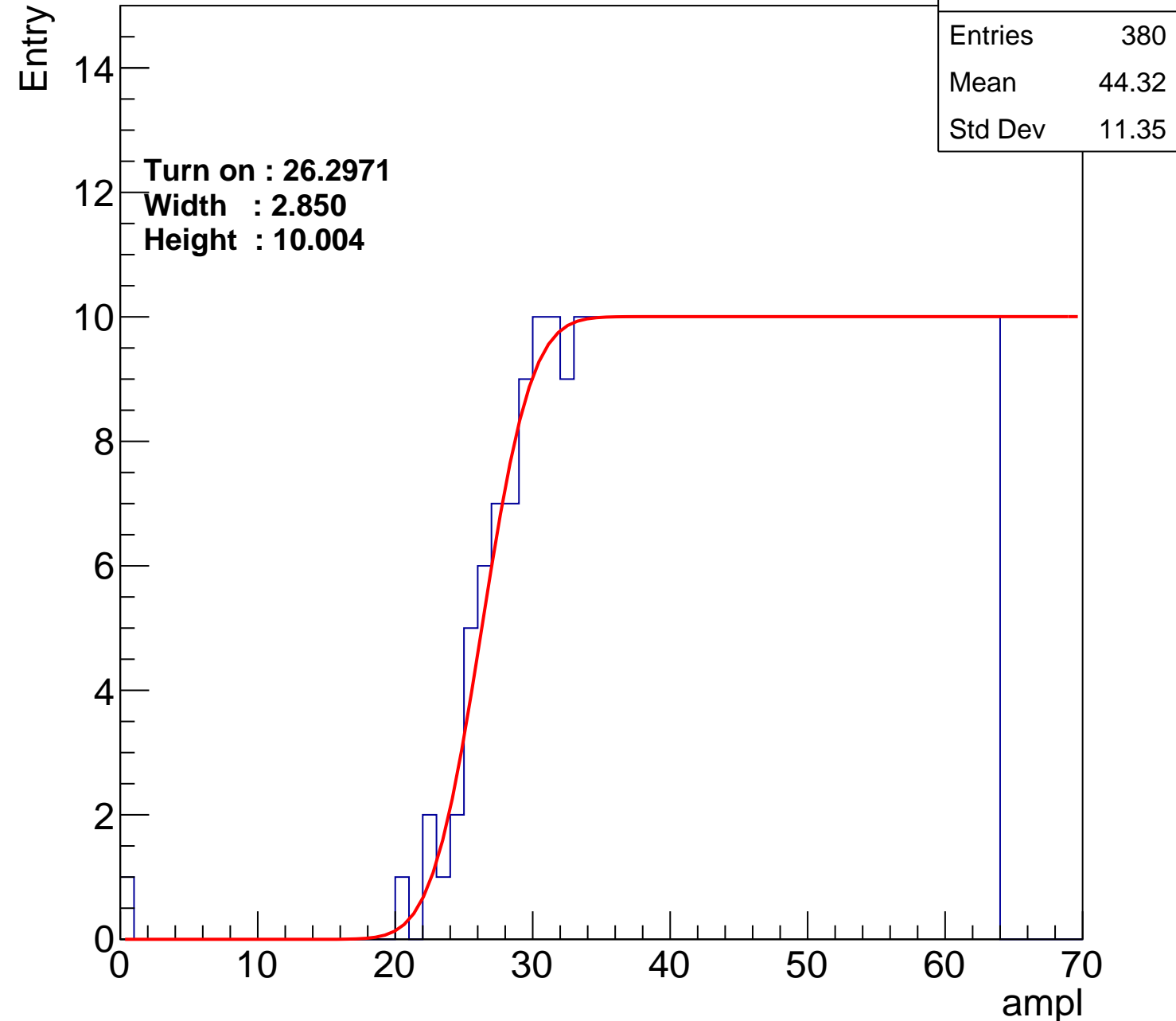
Width : 2.850

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch72

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 27.2490

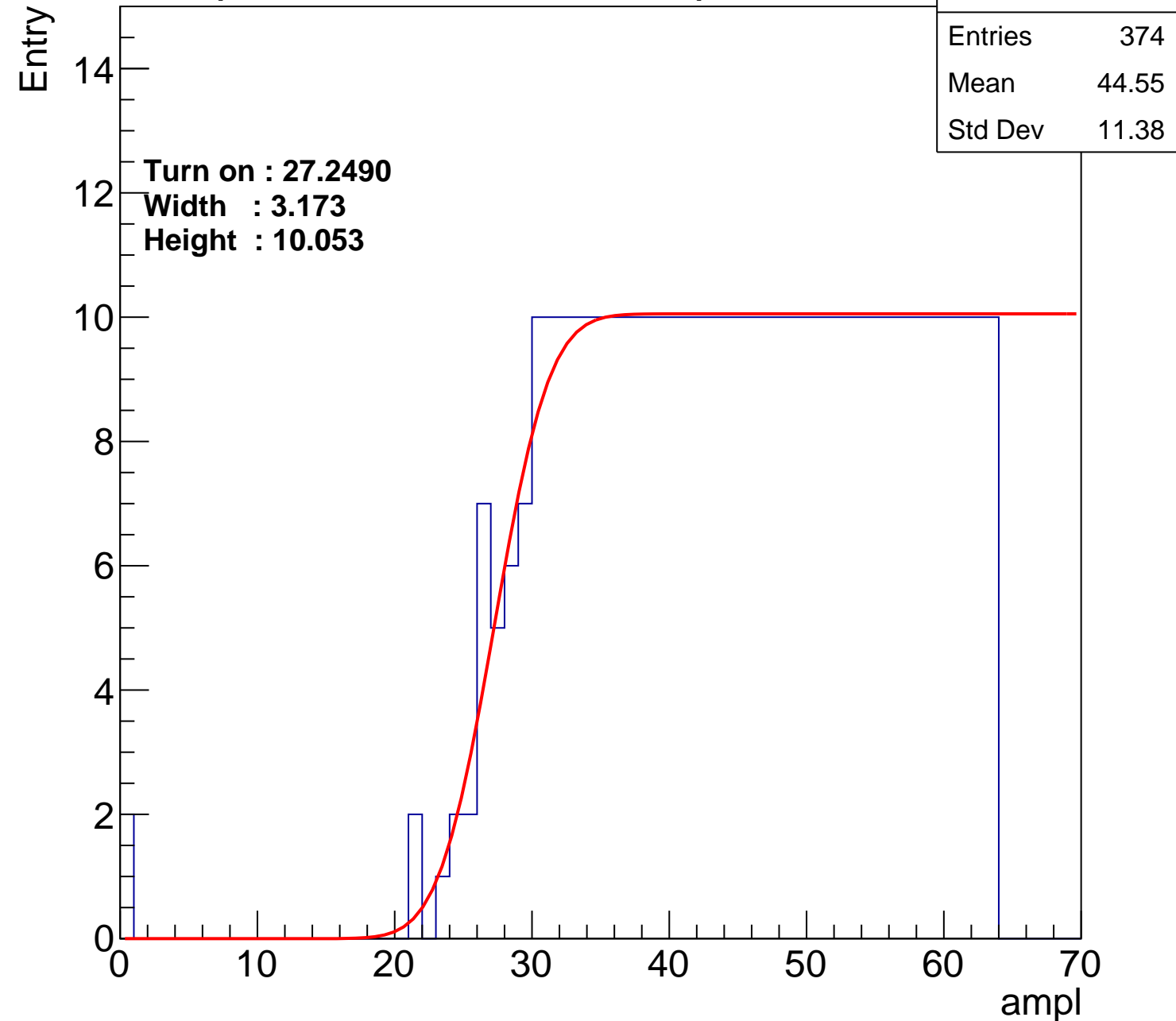
Width : 3.173

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch73

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 26.7827

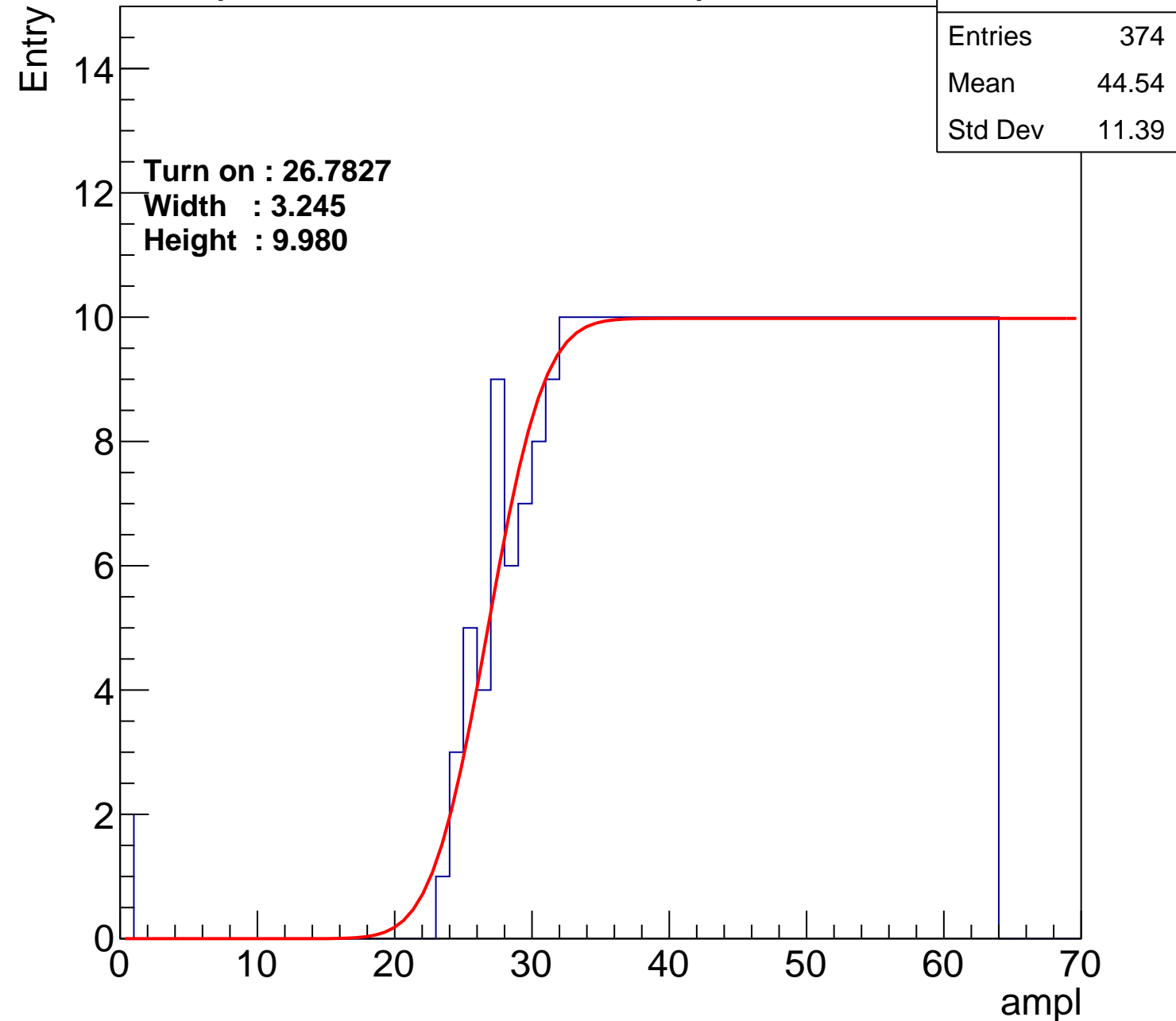
Width : 3.245

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch74

calib_packv5_042523_0143.root, FC#7, port C2

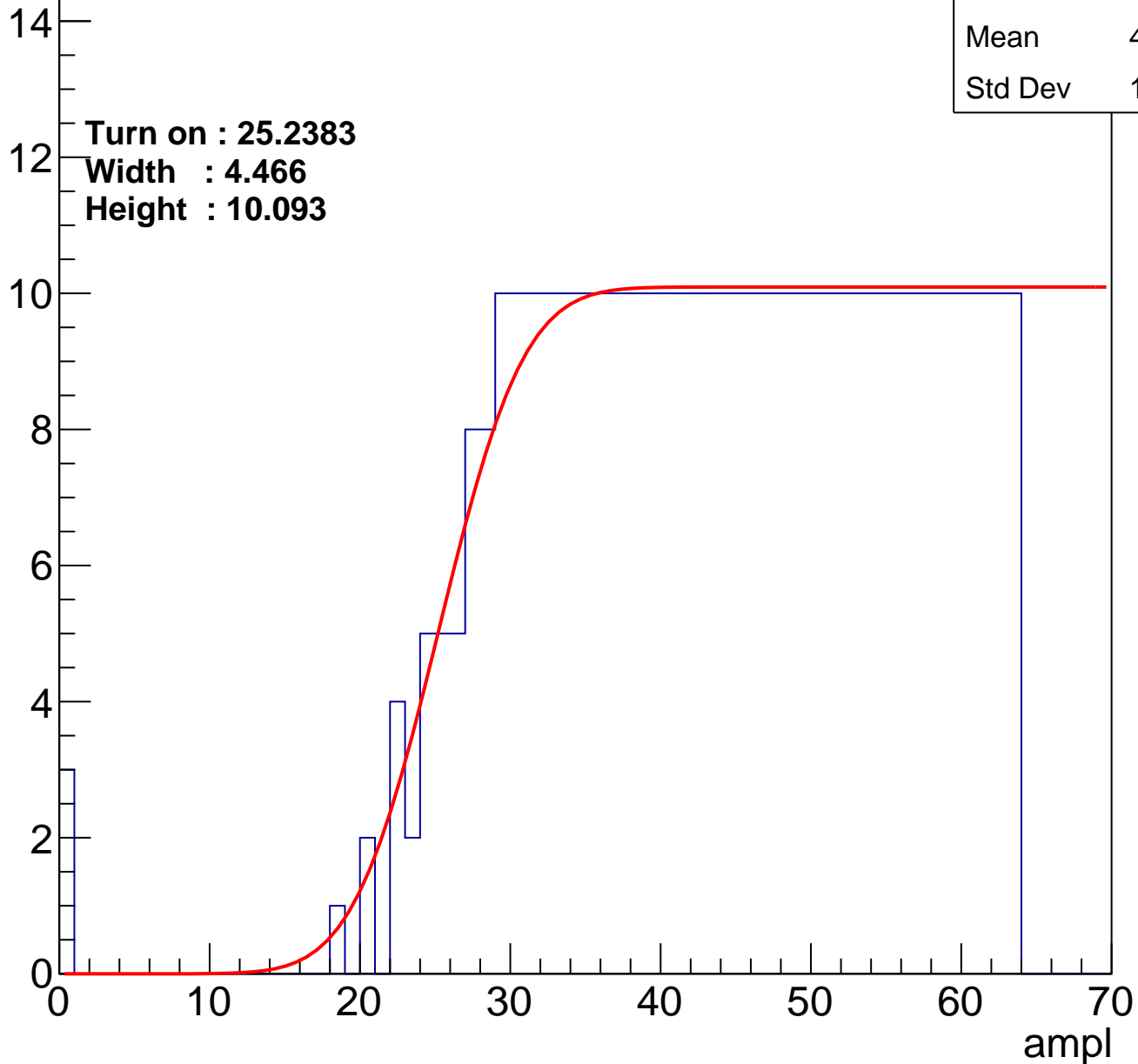
Entries	393
Mean	43.53
Std Dev	12.07

Turn on : 25.2383

Width : 4.466

Height : 10.093

Entry



B1L103S, U9-ch75

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.19
Std Dev	11.71

Turn on : 26.4858

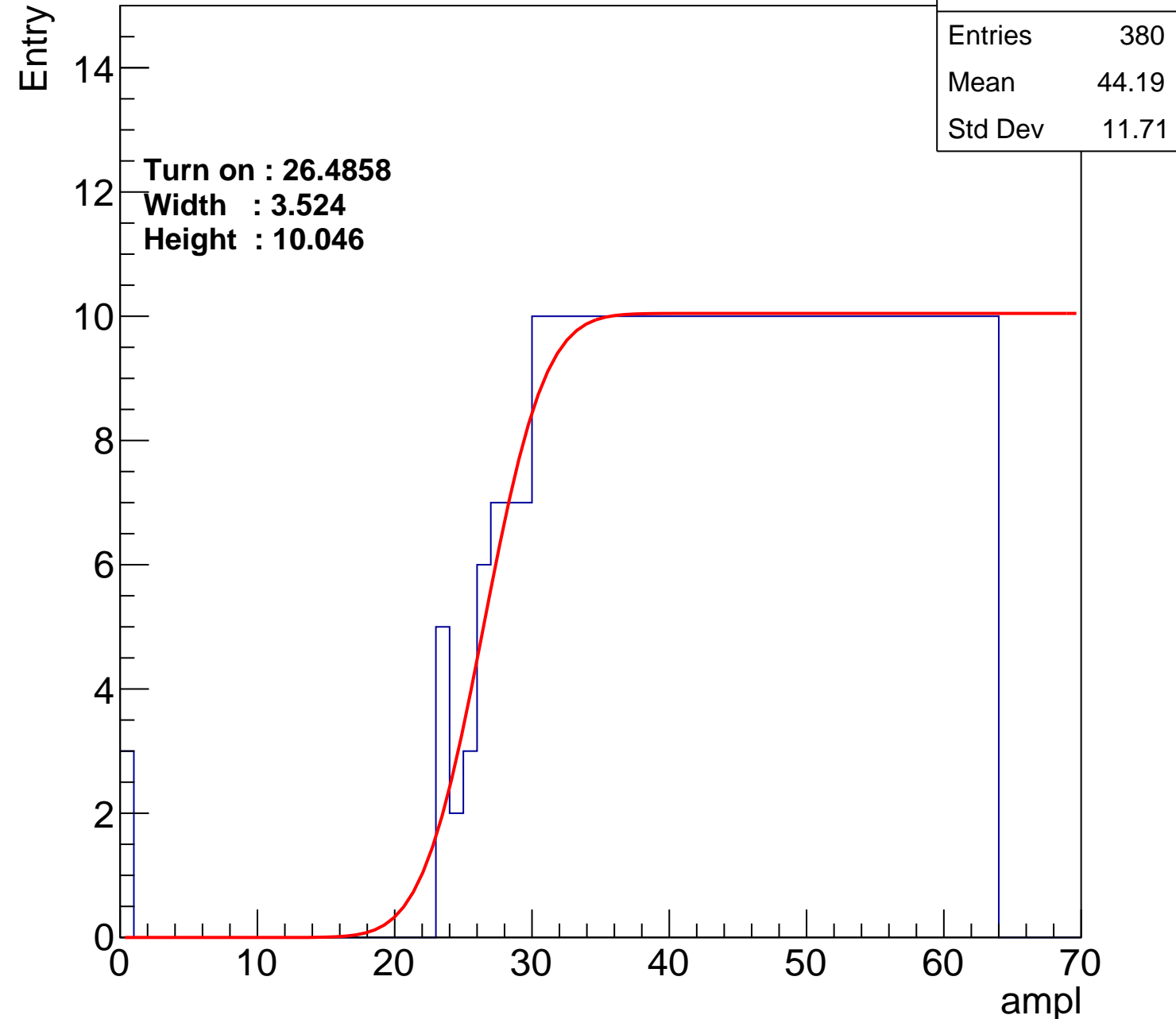
Width : 3.524

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch76

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.48
Std Dev	11.29

Turn on : 26.7276

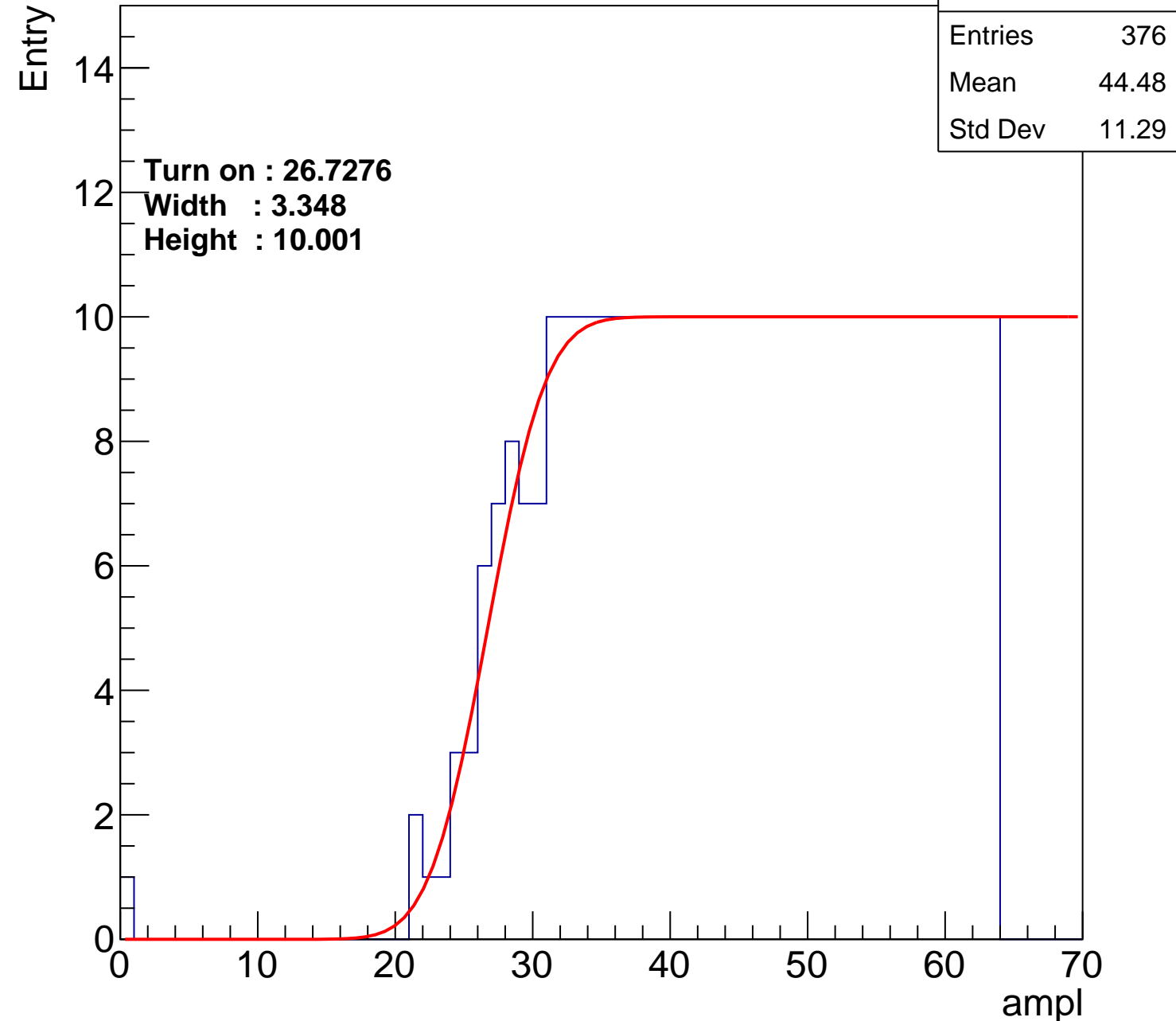
Width : 3.348

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch77

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.92
Std Dev	11.21

Turn on : 27.8039

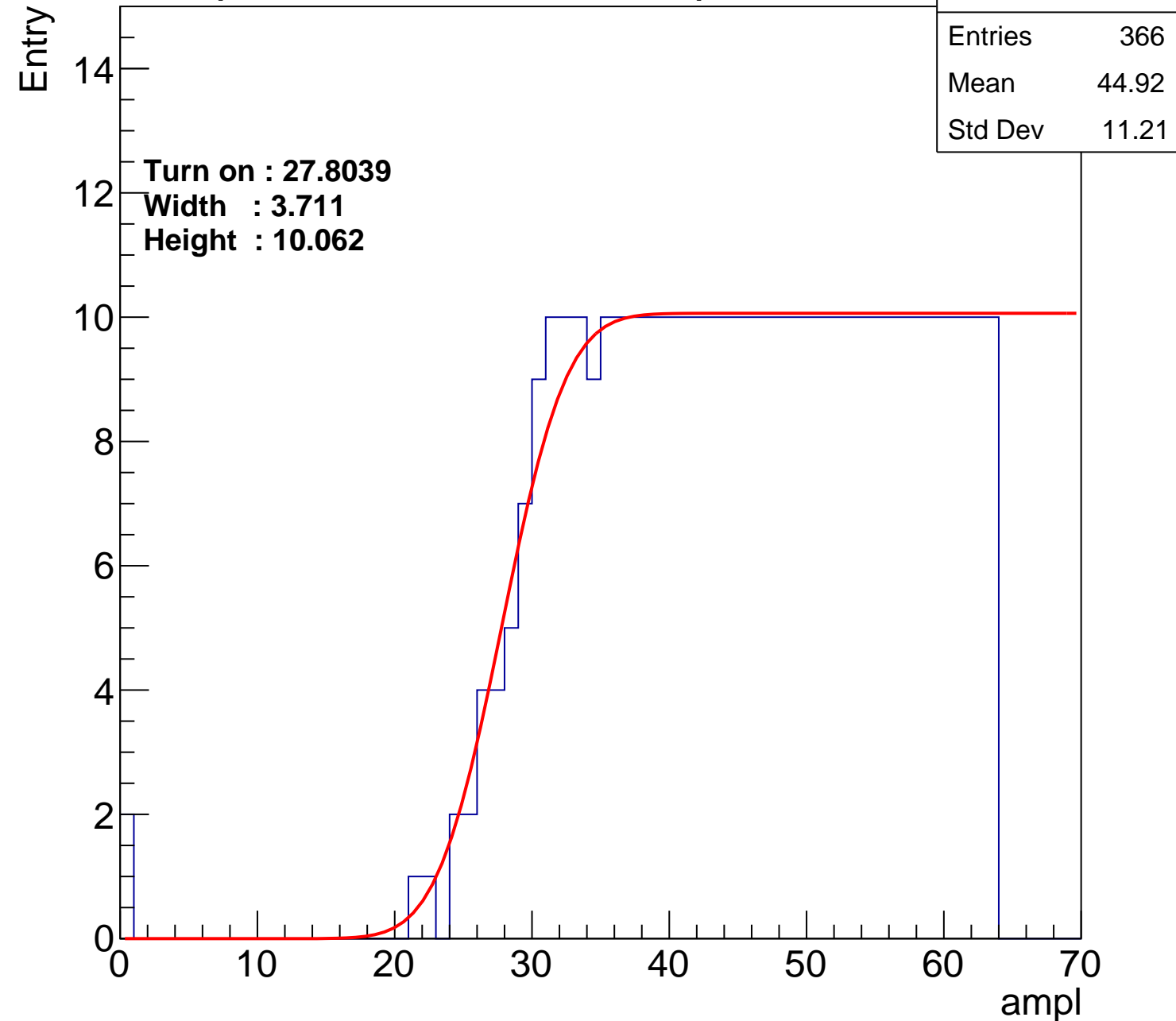
Width : 3.711

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch78

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.31
Std Dev	11.5

Turn on : 26.5328

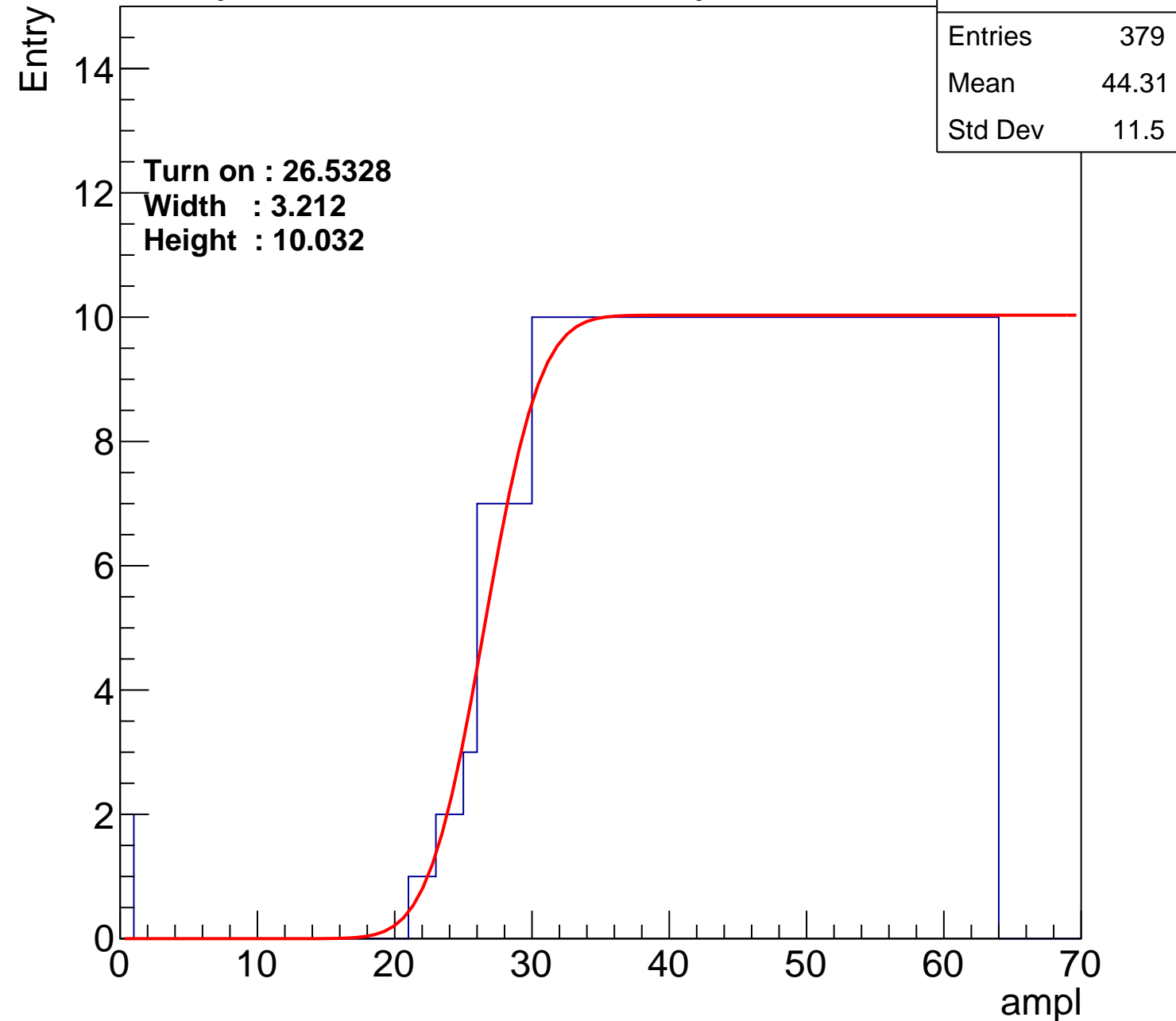
Width : 3.212

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch79

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.4441

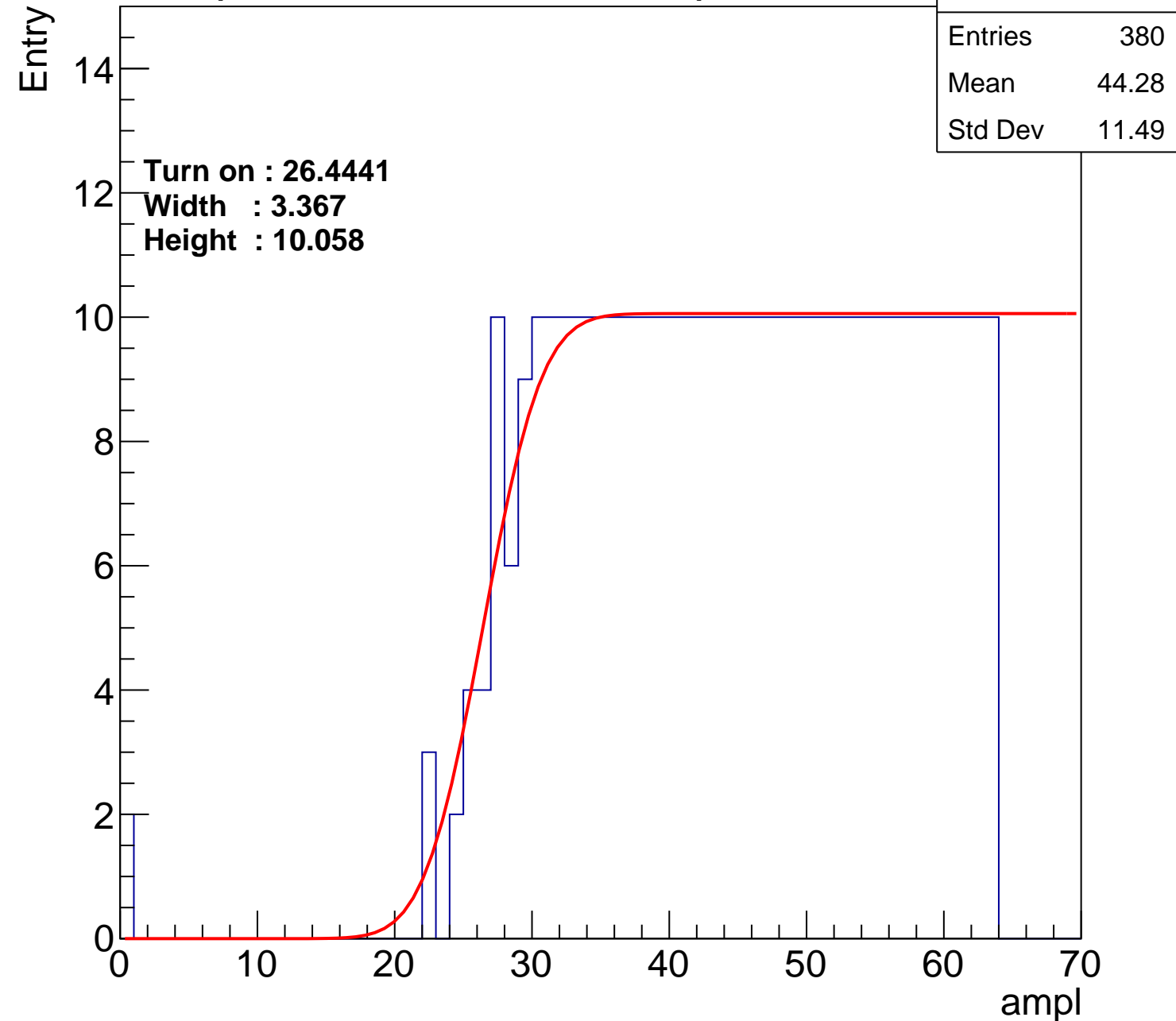
Width : 3.367

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch80

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.3
Std Dev	11.63

Turn on : 26.7309

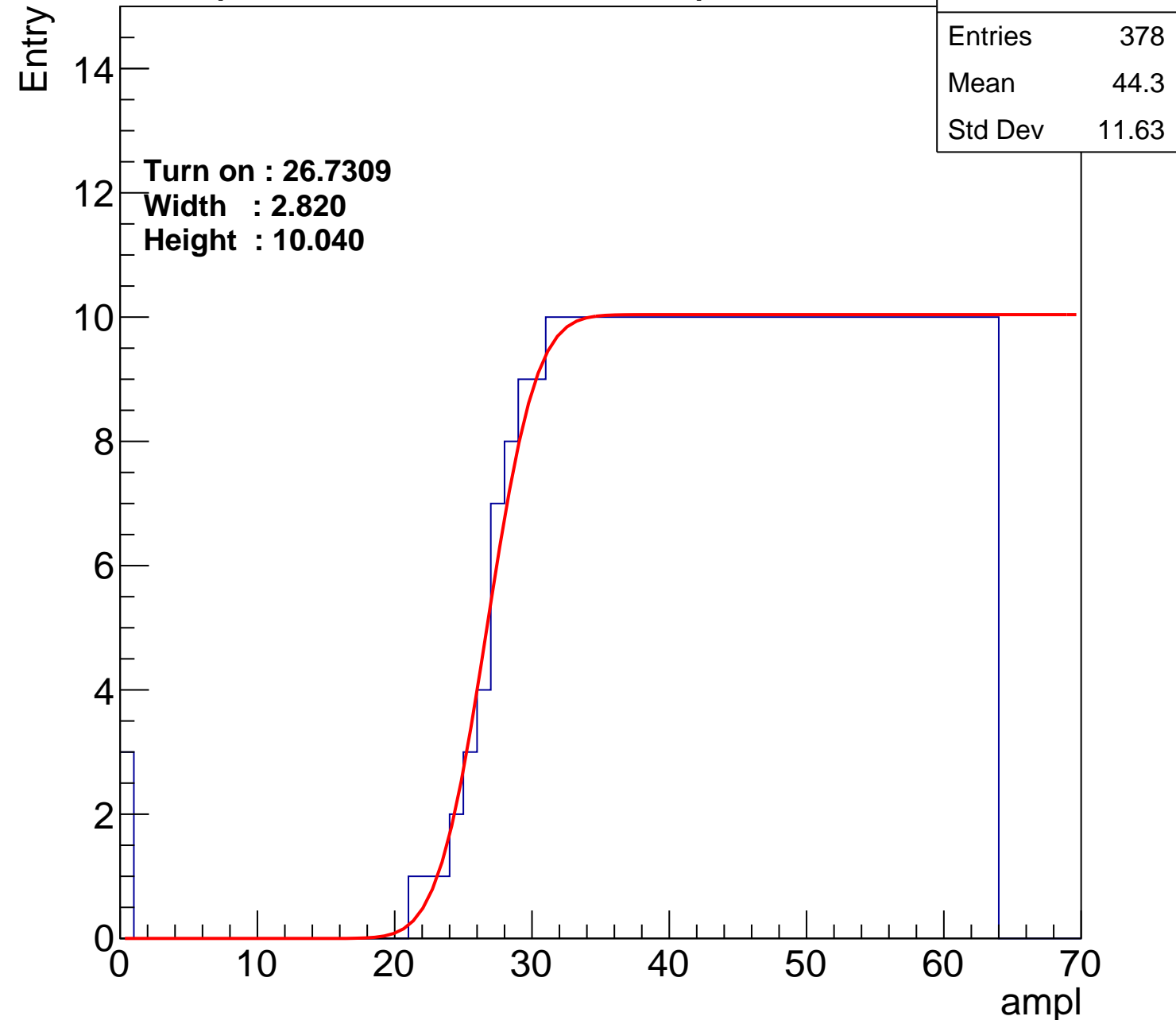
Width : 2.820

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch81

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 27.2230

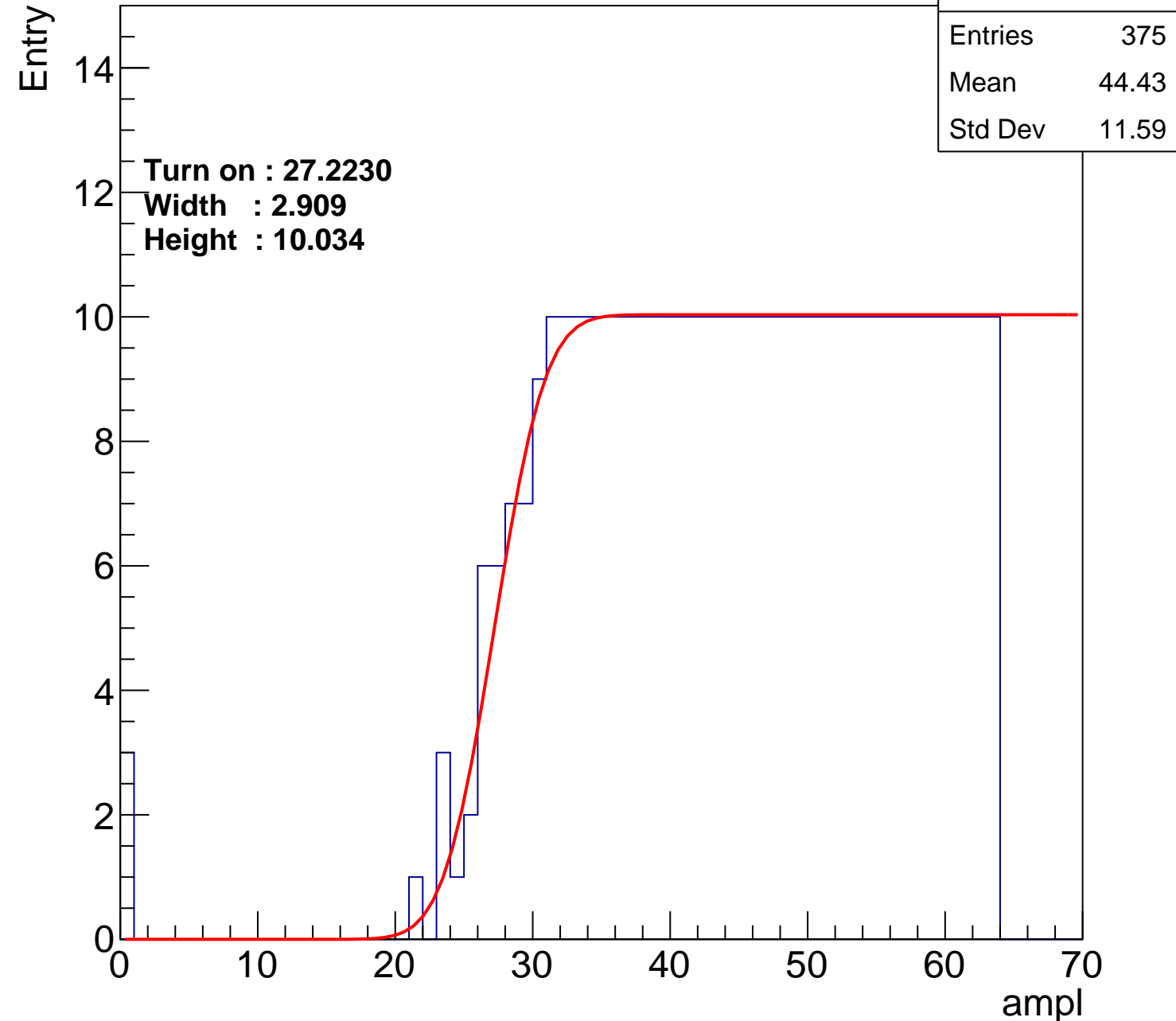
Width : 2.909

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch82

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.92
Std Dev	11.51

Turn on : 25.9124

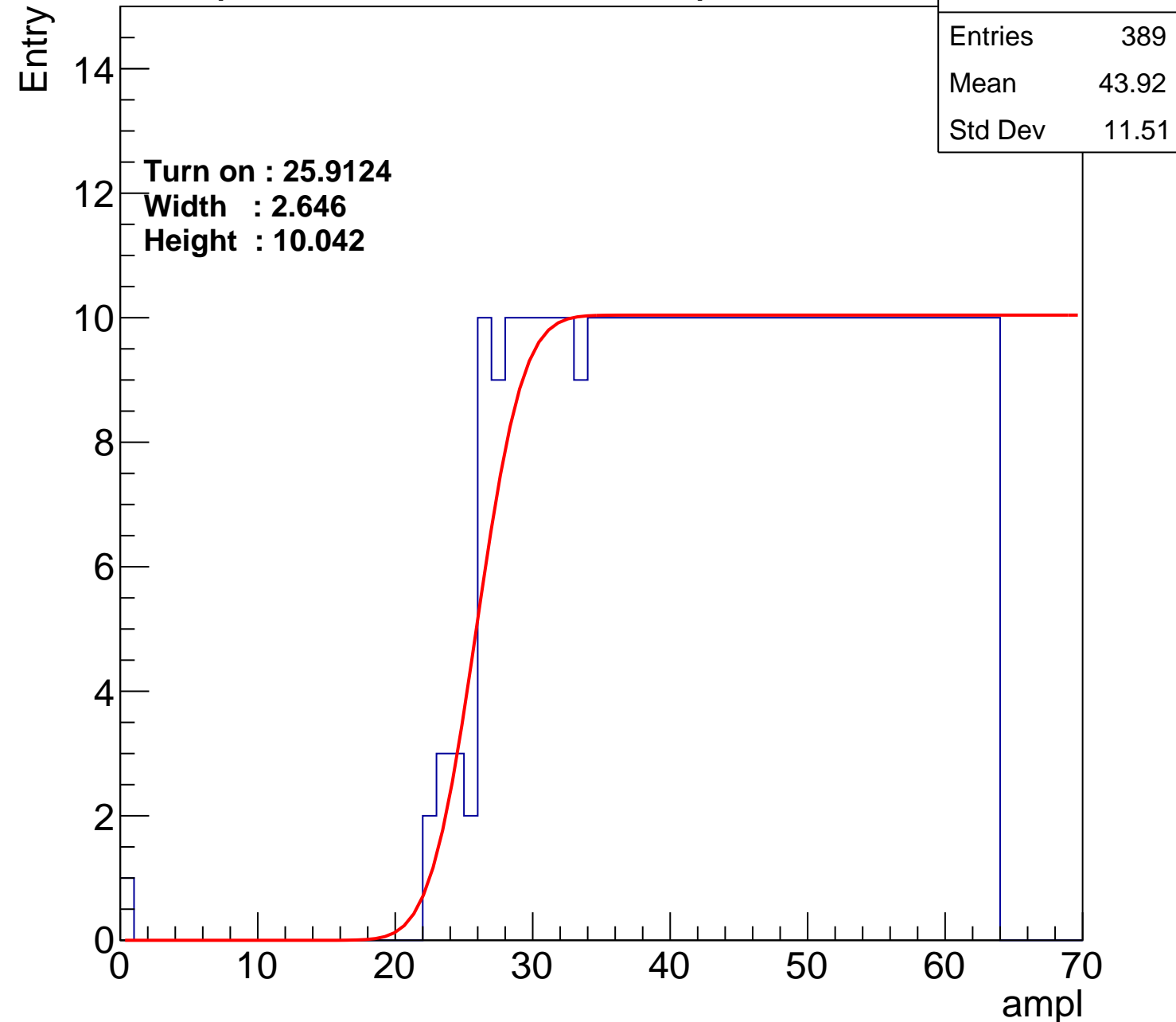
Width : 2.646

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch83

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.92
Std Dev	11.01

Turn on : 27.4150

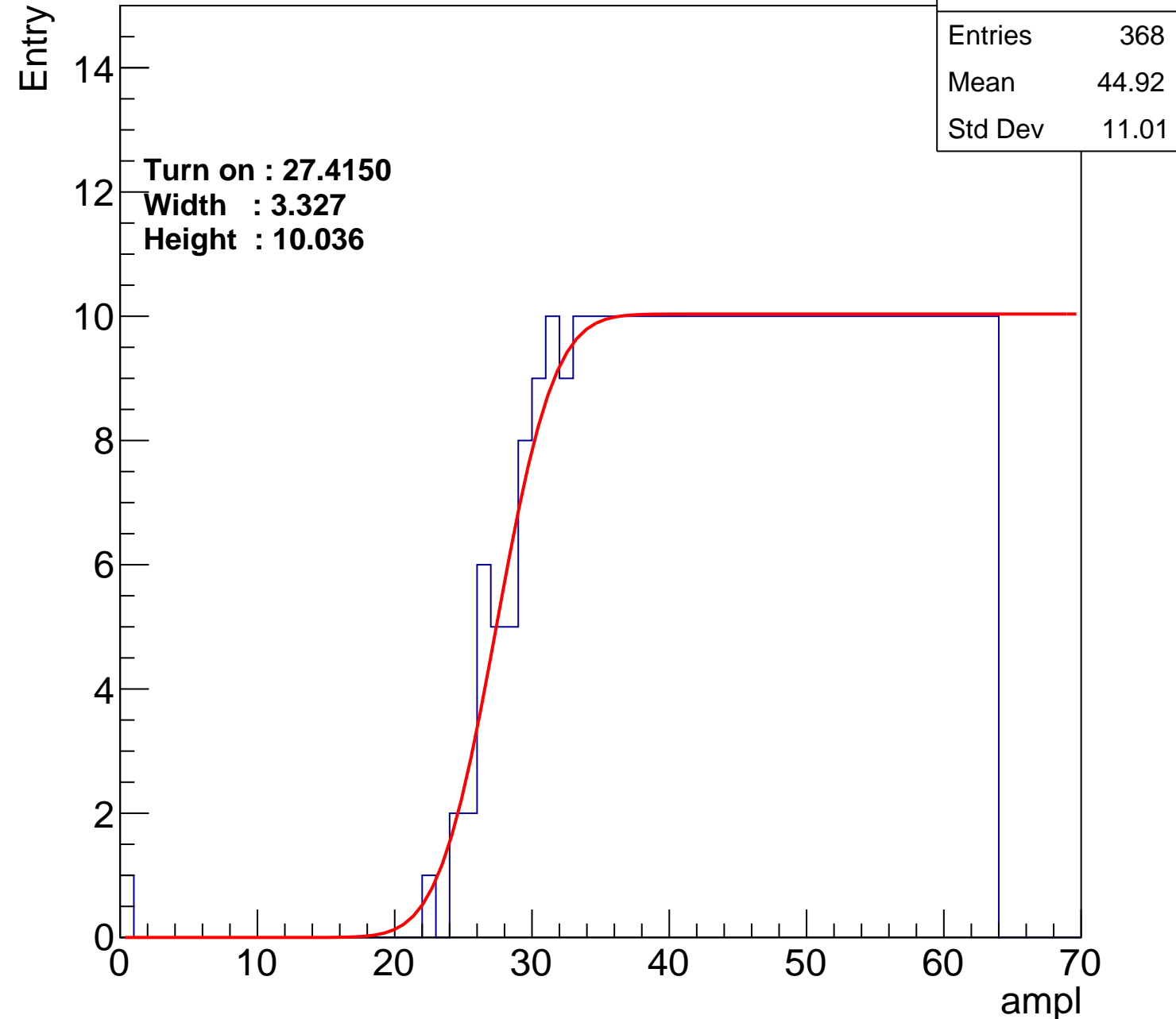
Width : 3.327

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch84

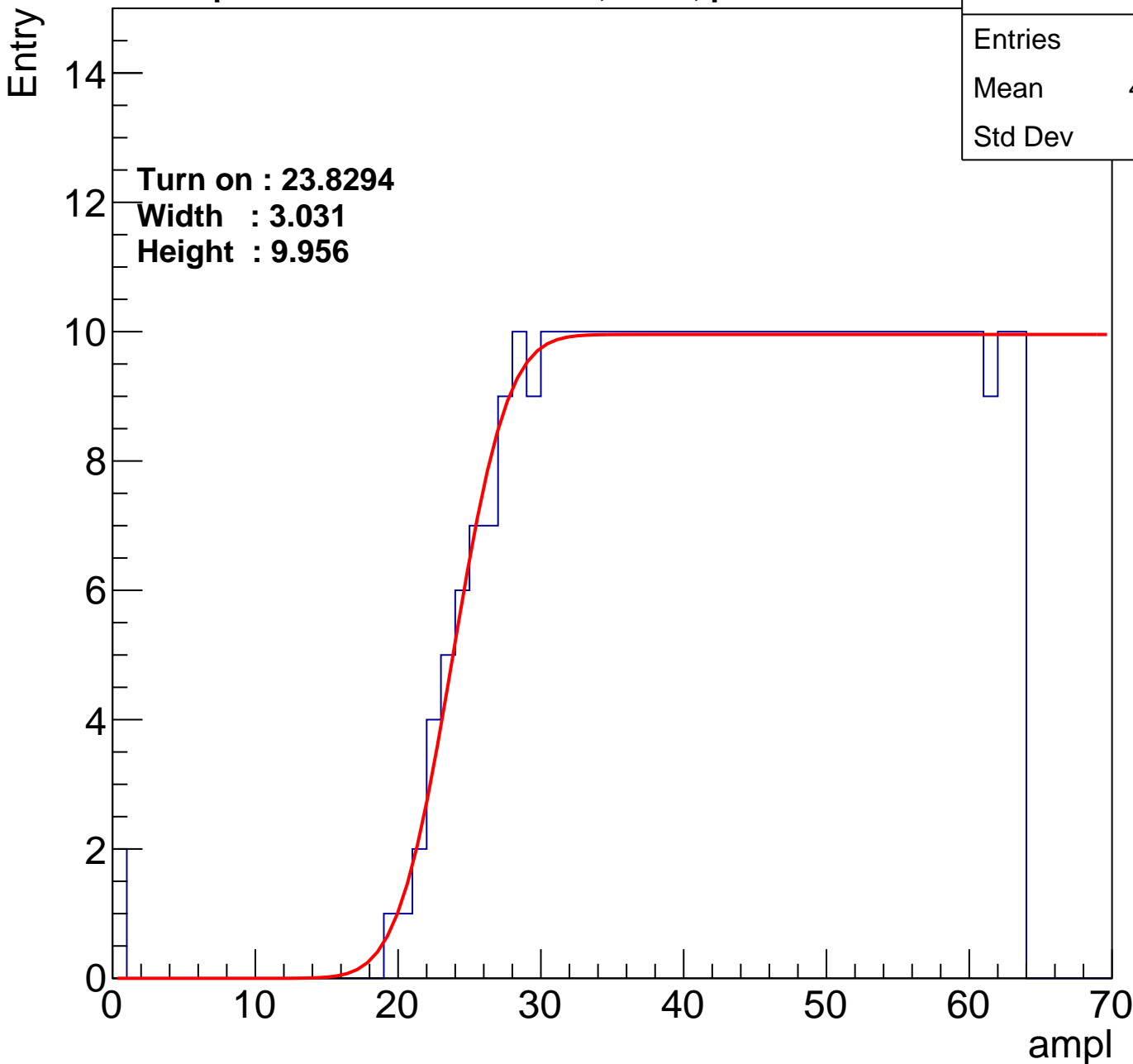
calib_packv5_042523_0143.root, FC#7, port C2

Entries	402
Mean	43.08
Std Dev	12.1

Turn on : 23.8294

Width : 3.031

Height : 9.956



B1L103S, U9-ch85

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.35
Std Dev	11.52

Turn on : 25.6595

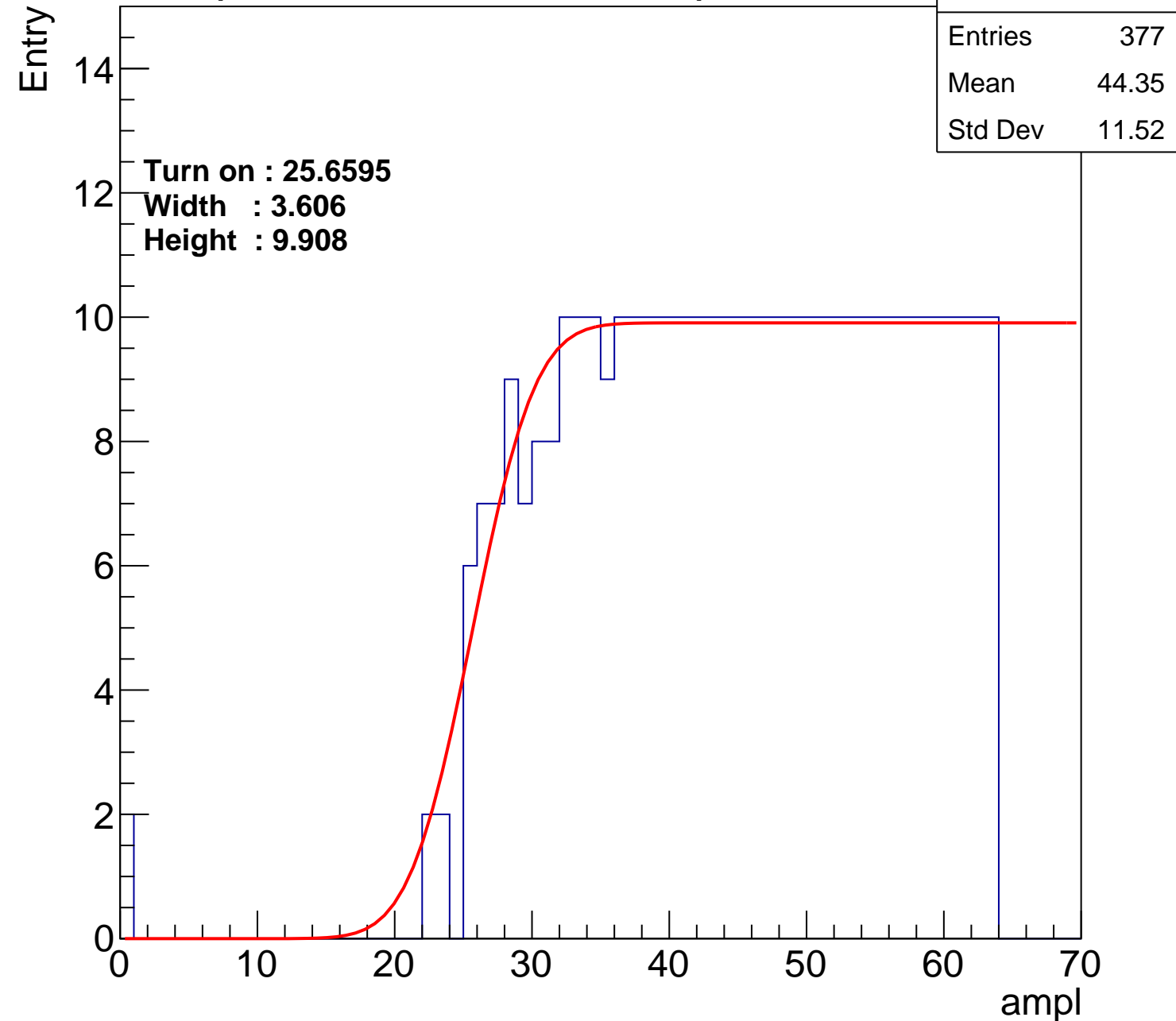
Width : 3.606

Height : 9.908

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch86

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.44
Std Dev	11.32

Turn on : 27.0046

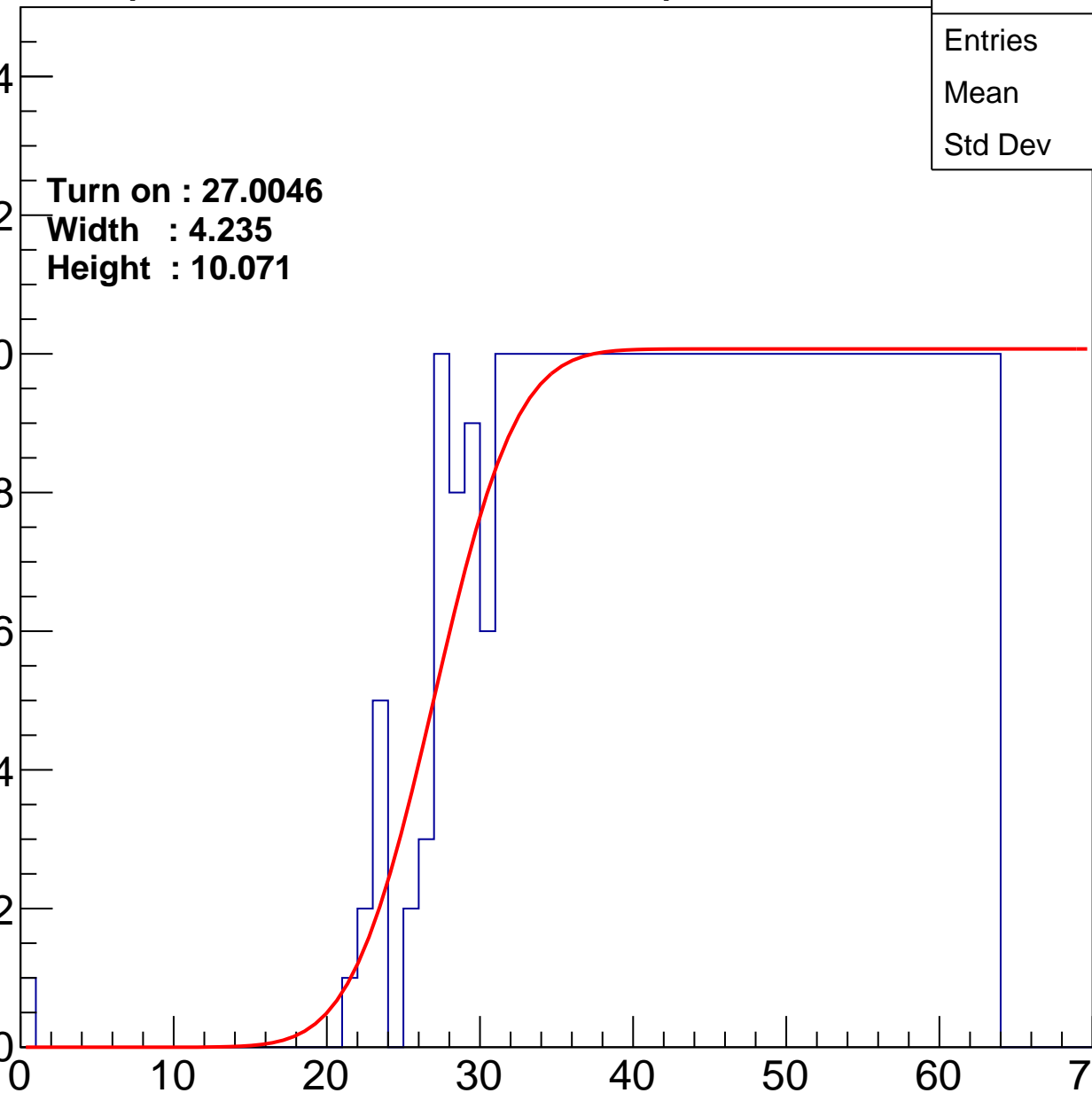
Width : 4.235

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch87

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.48
Std Dev	11.58

Turn on : 27.4936

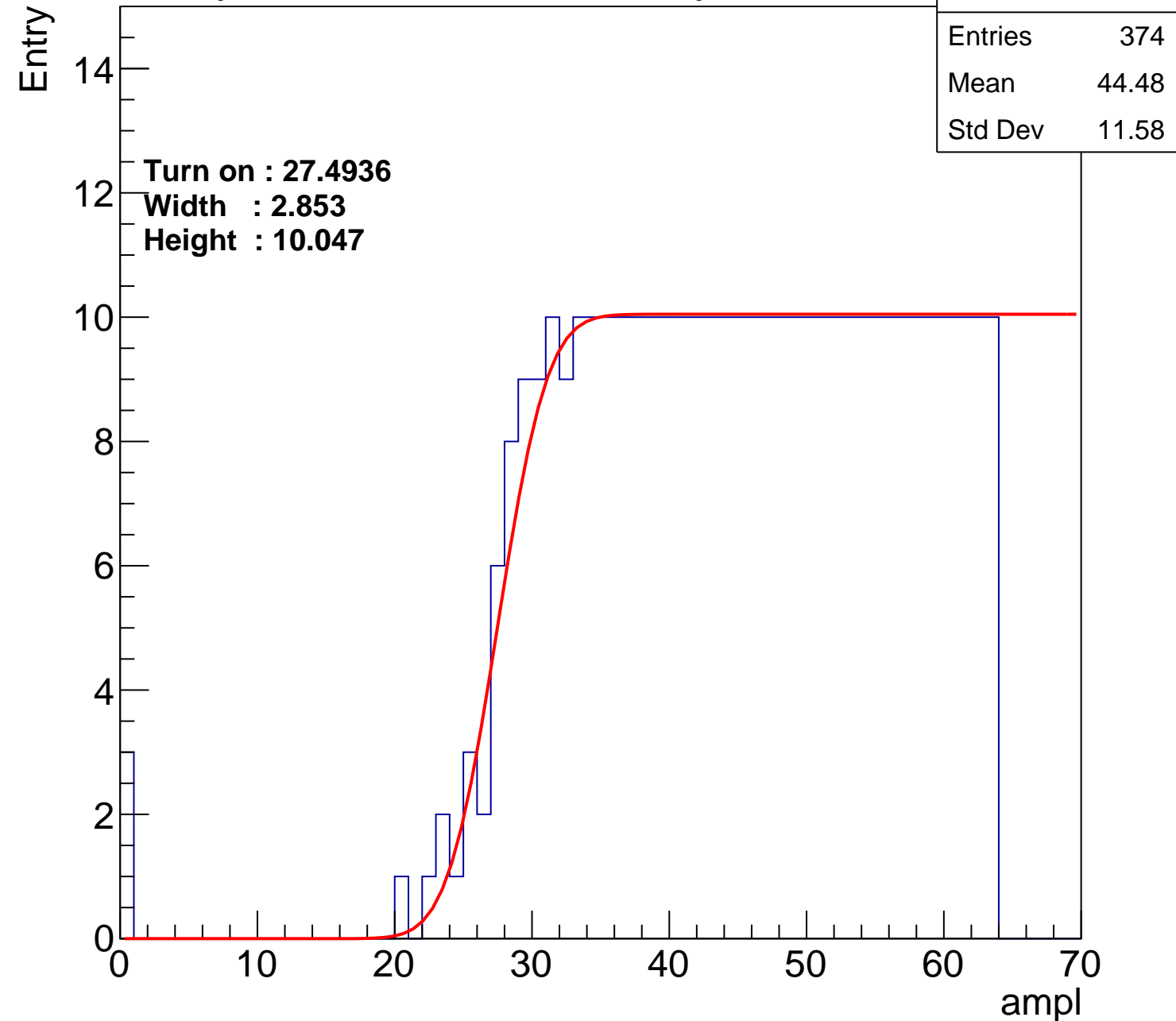
Width : 2.853

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch88

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.56
Std Dev	11.3

Turn on : 27.3799

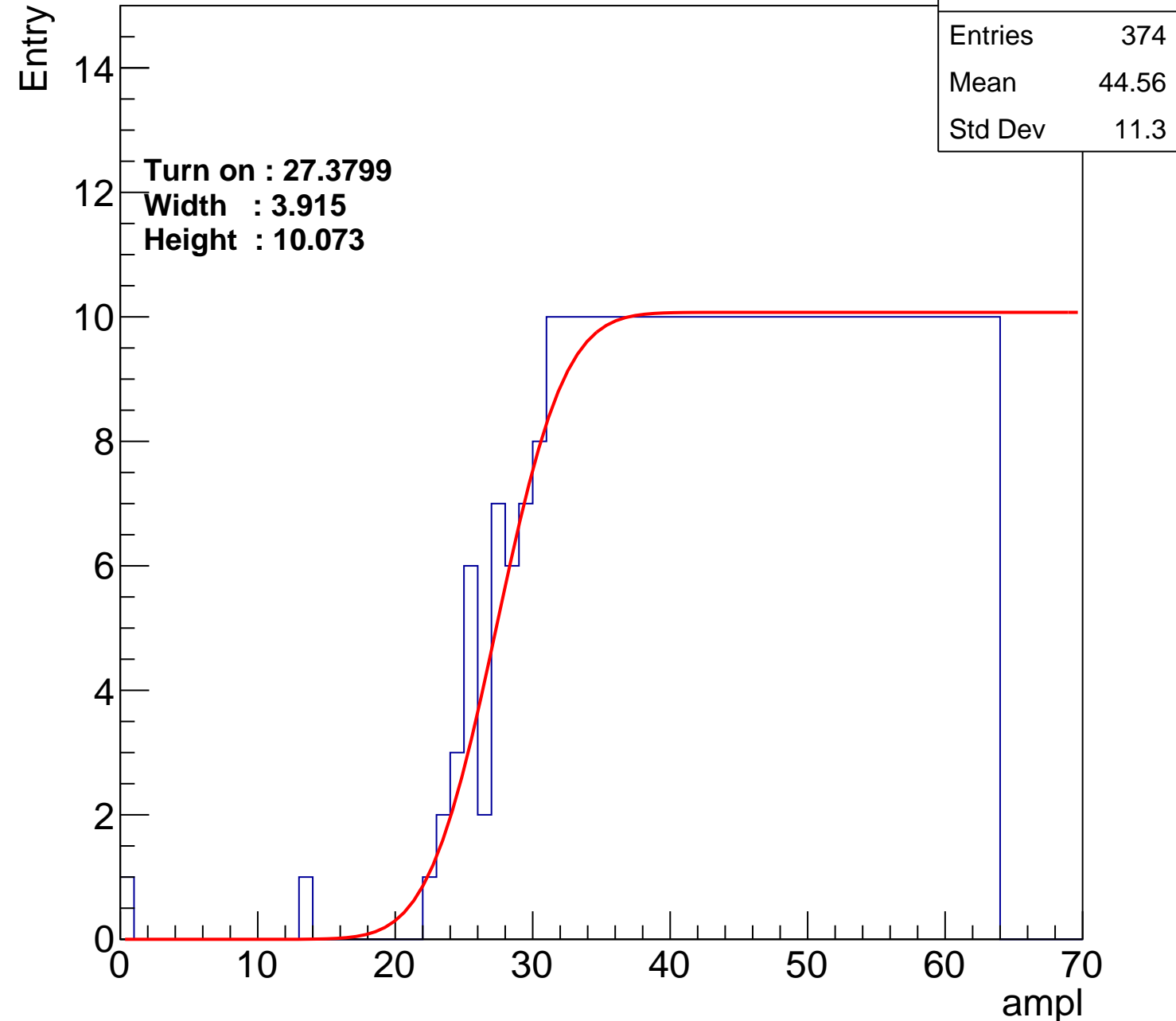
Width : 3.915

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch89

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.87
Std Dev	11.9

Turn on : 26.0157

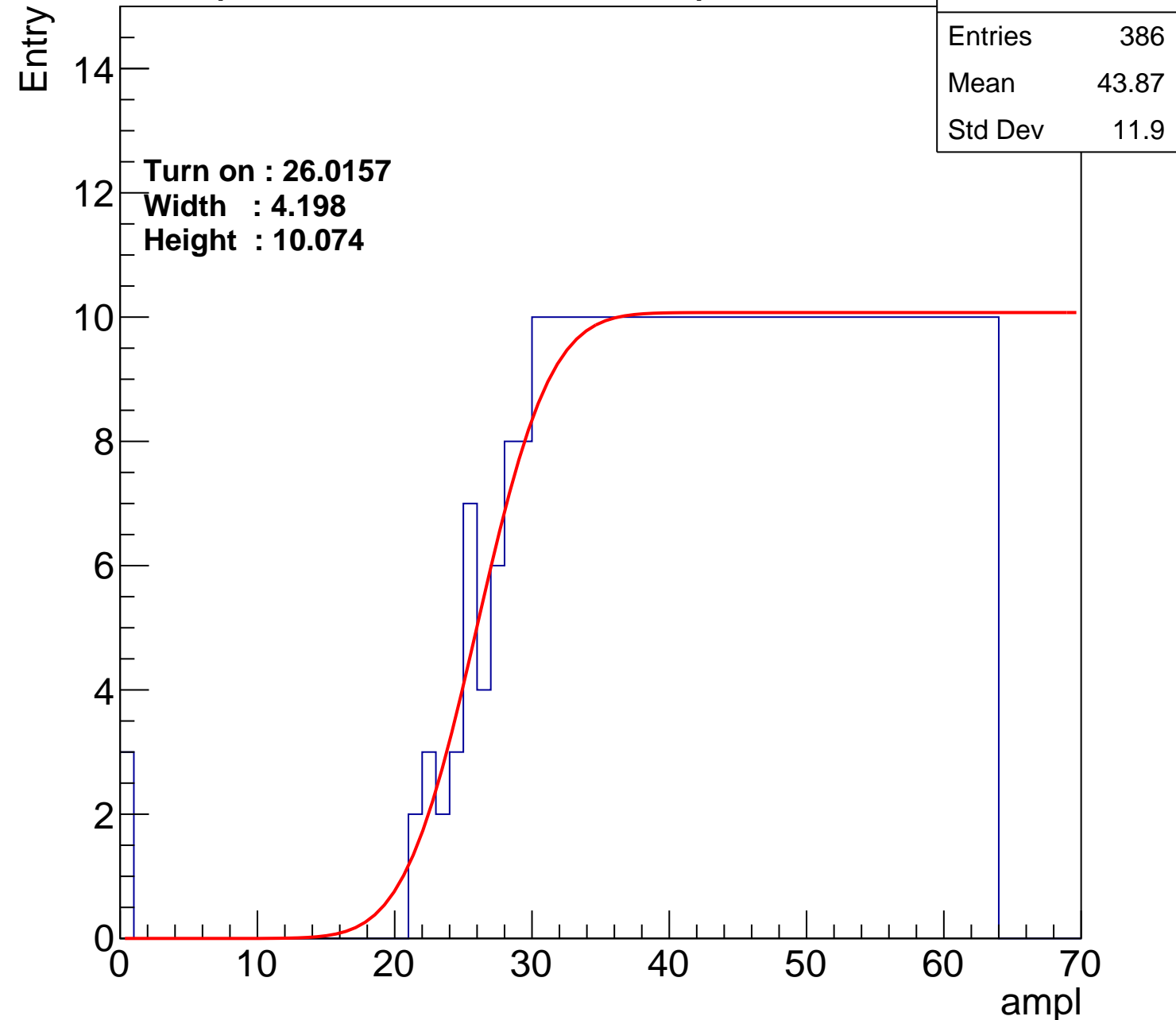
Width : 4.198

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch90

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.87
Std Dev	11.29

Turn on : 28.0390

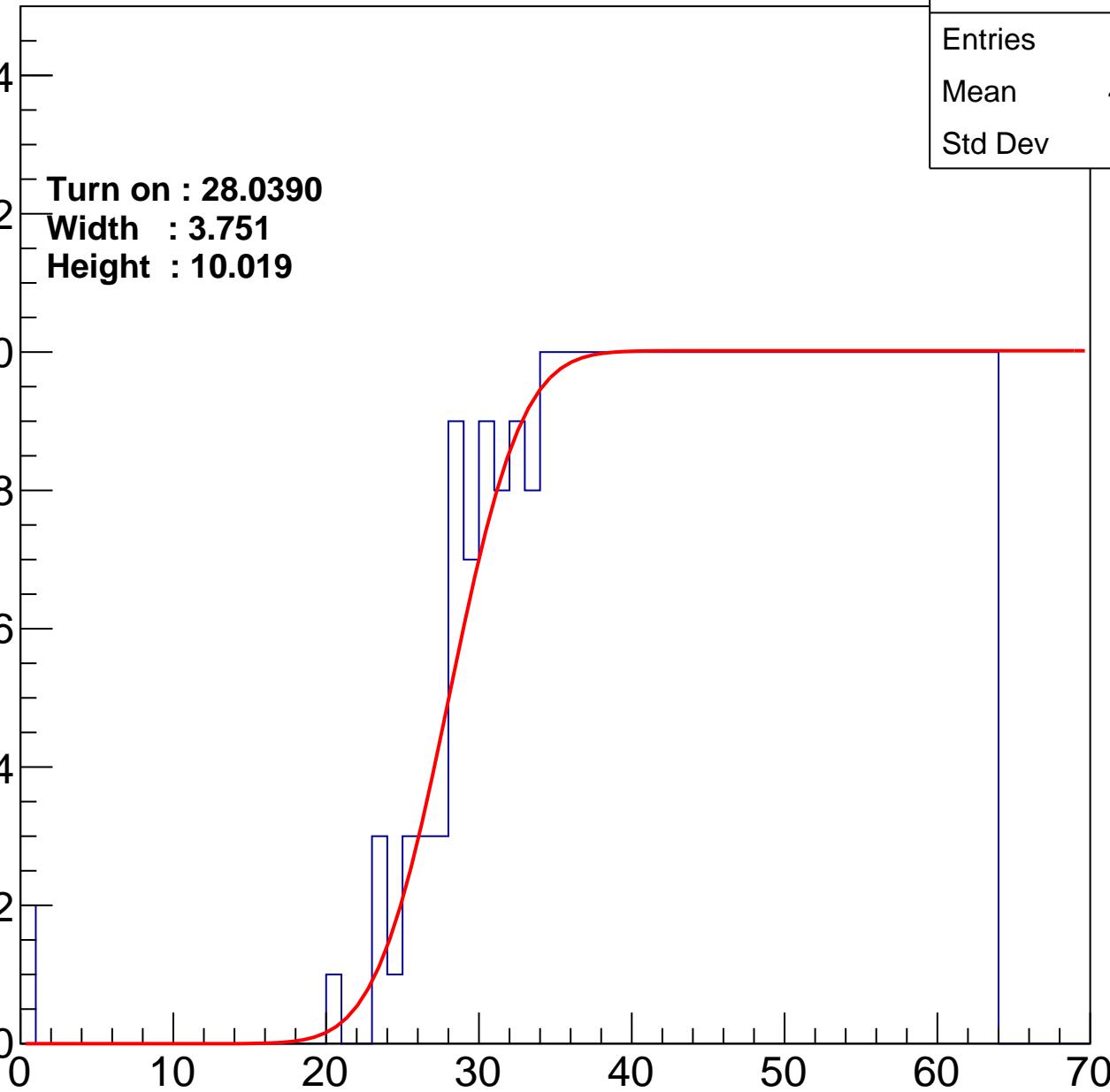
Width : 3.751

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch91

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.35
Std Dev	12.03

Turn on : 28.0775

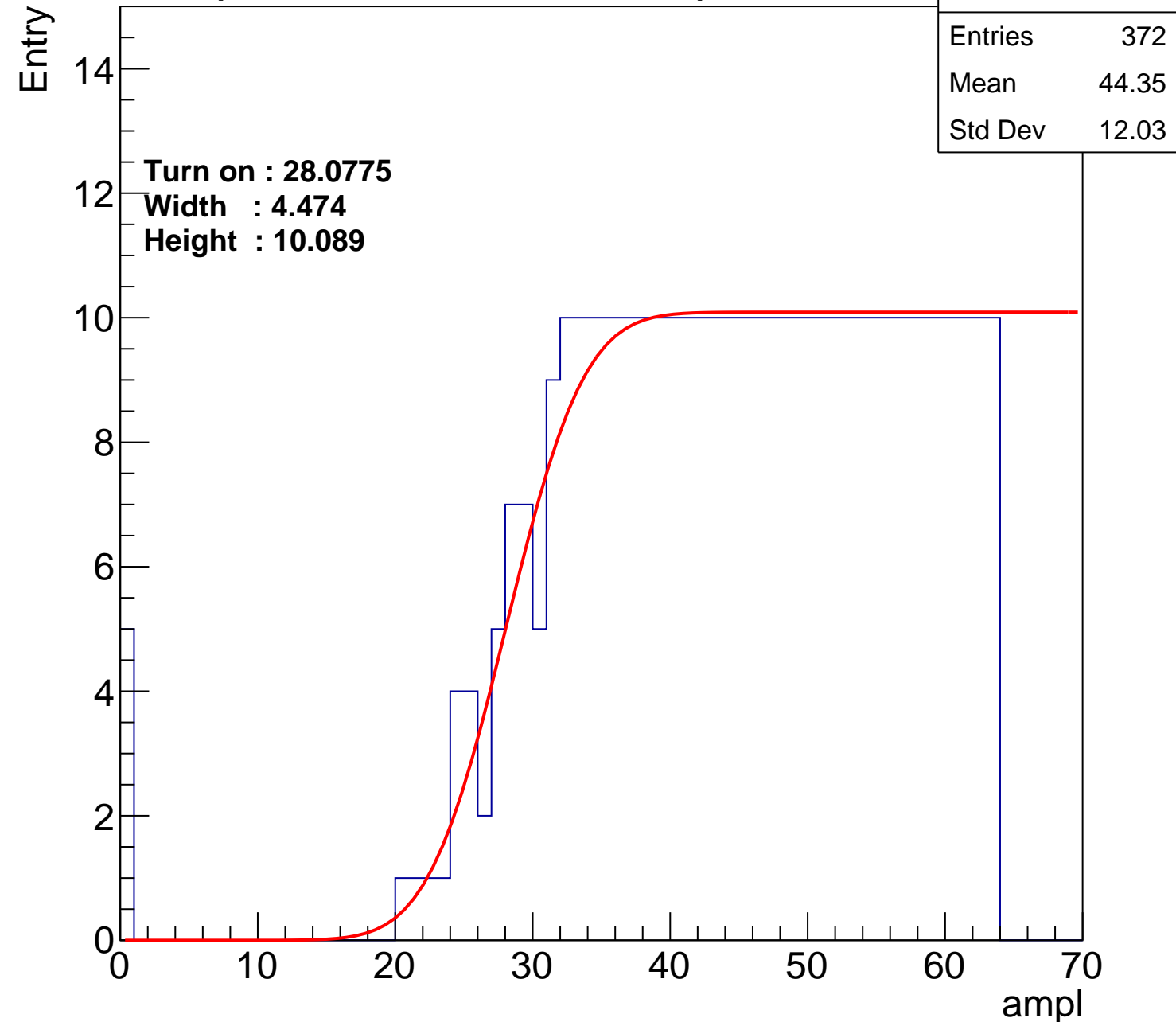
Width : 4.474

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch92

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.24
Std Dev	12.48

Turn on : 25.7418

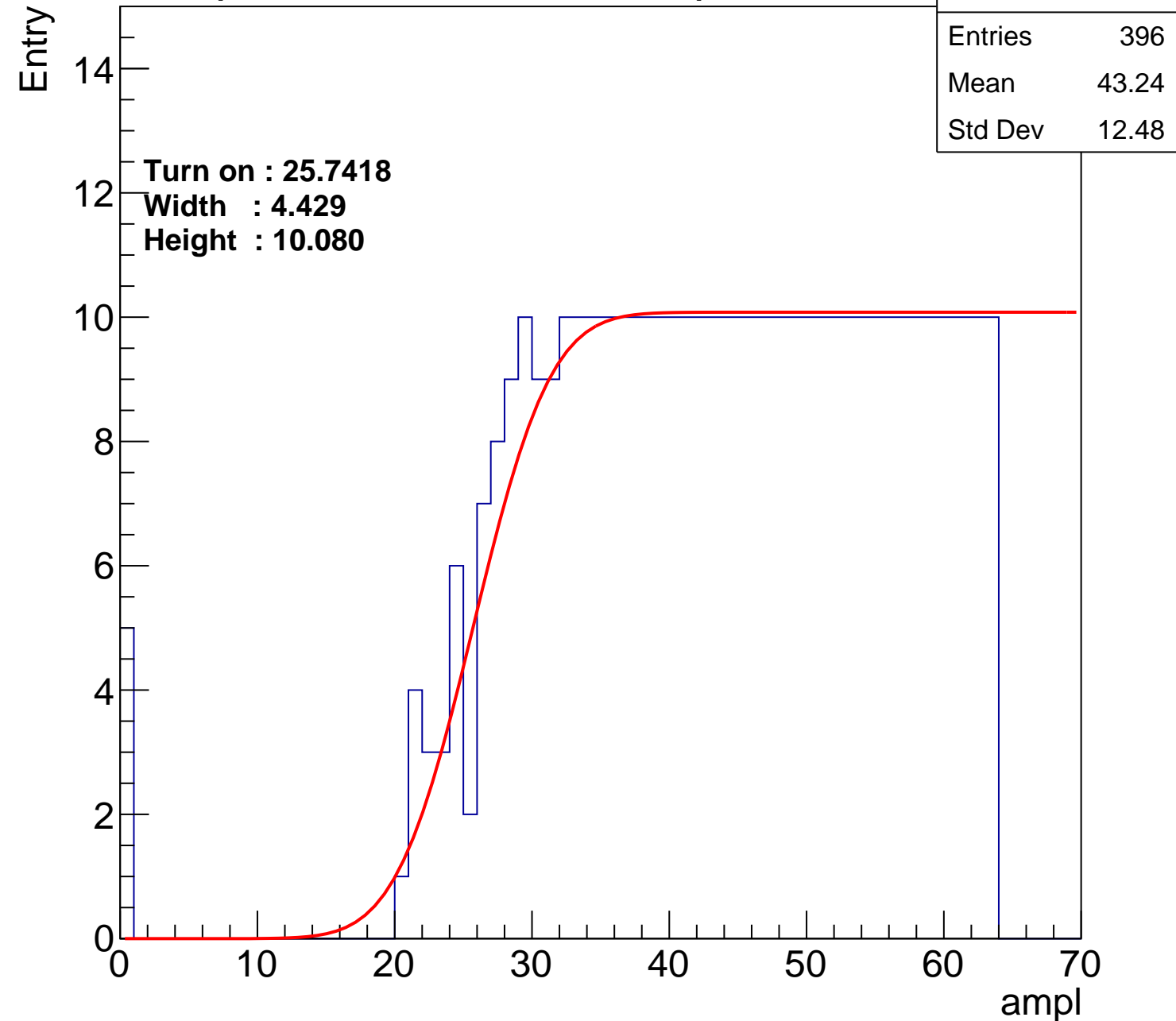
Width : 4.429

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch93

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.35
Std Dev	11.48

Turn on : 26.7572

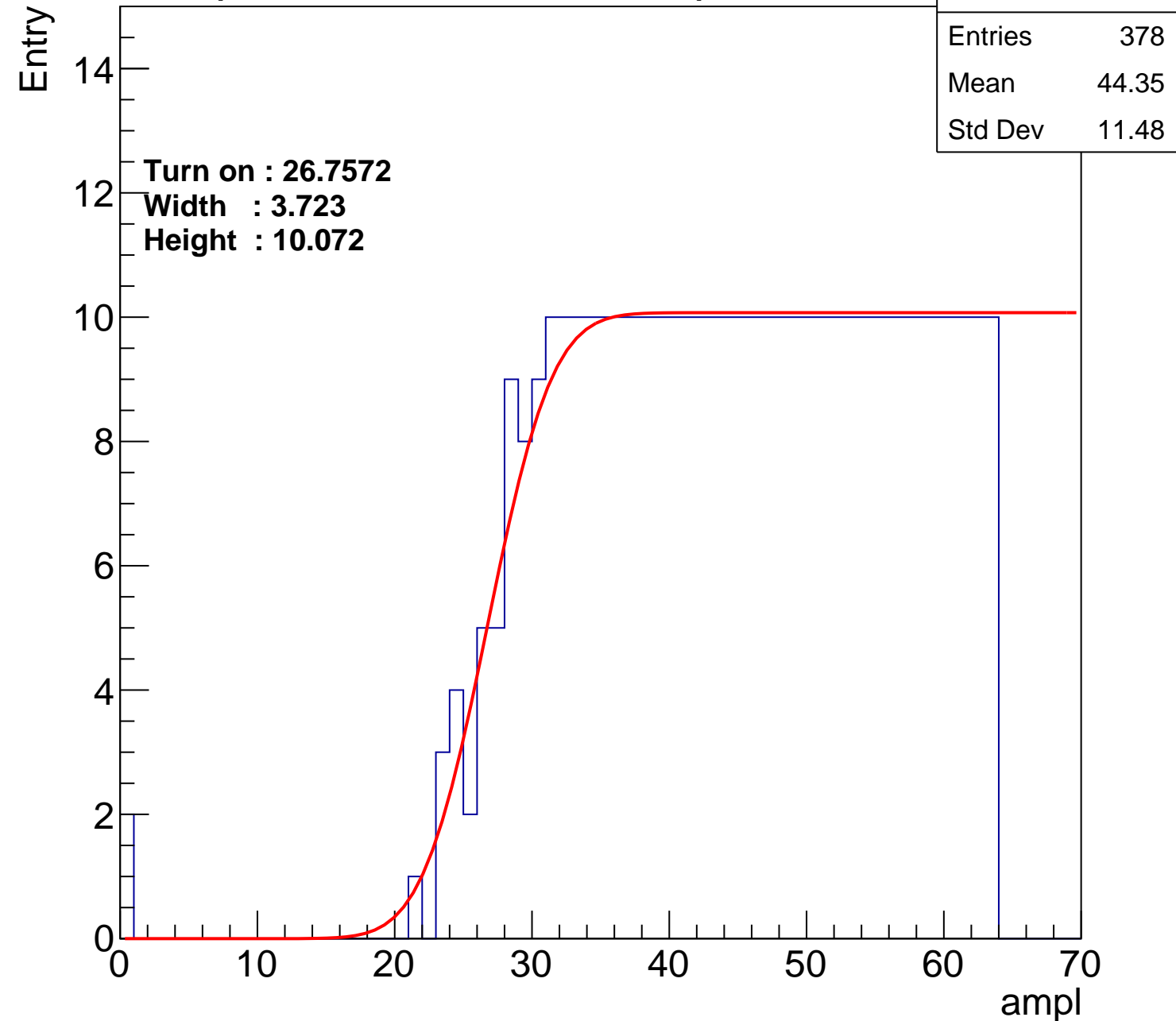
Width : 3.723

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch94

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.9
Std Dev	12.14

Turn on : 26.3648

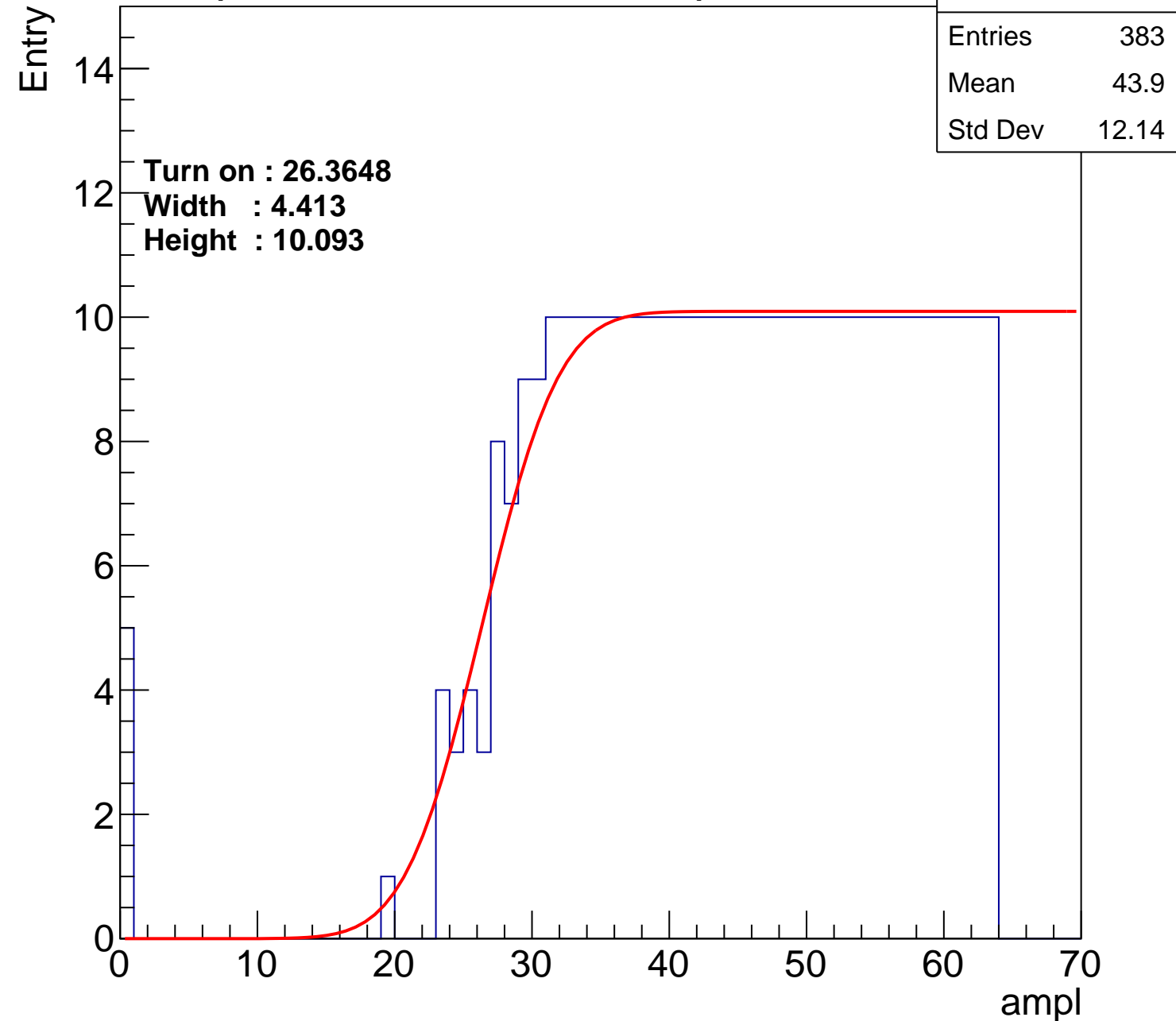
Width : 4.413

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch95

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.45
Std Dev	11.3

Turn on : 26.4370

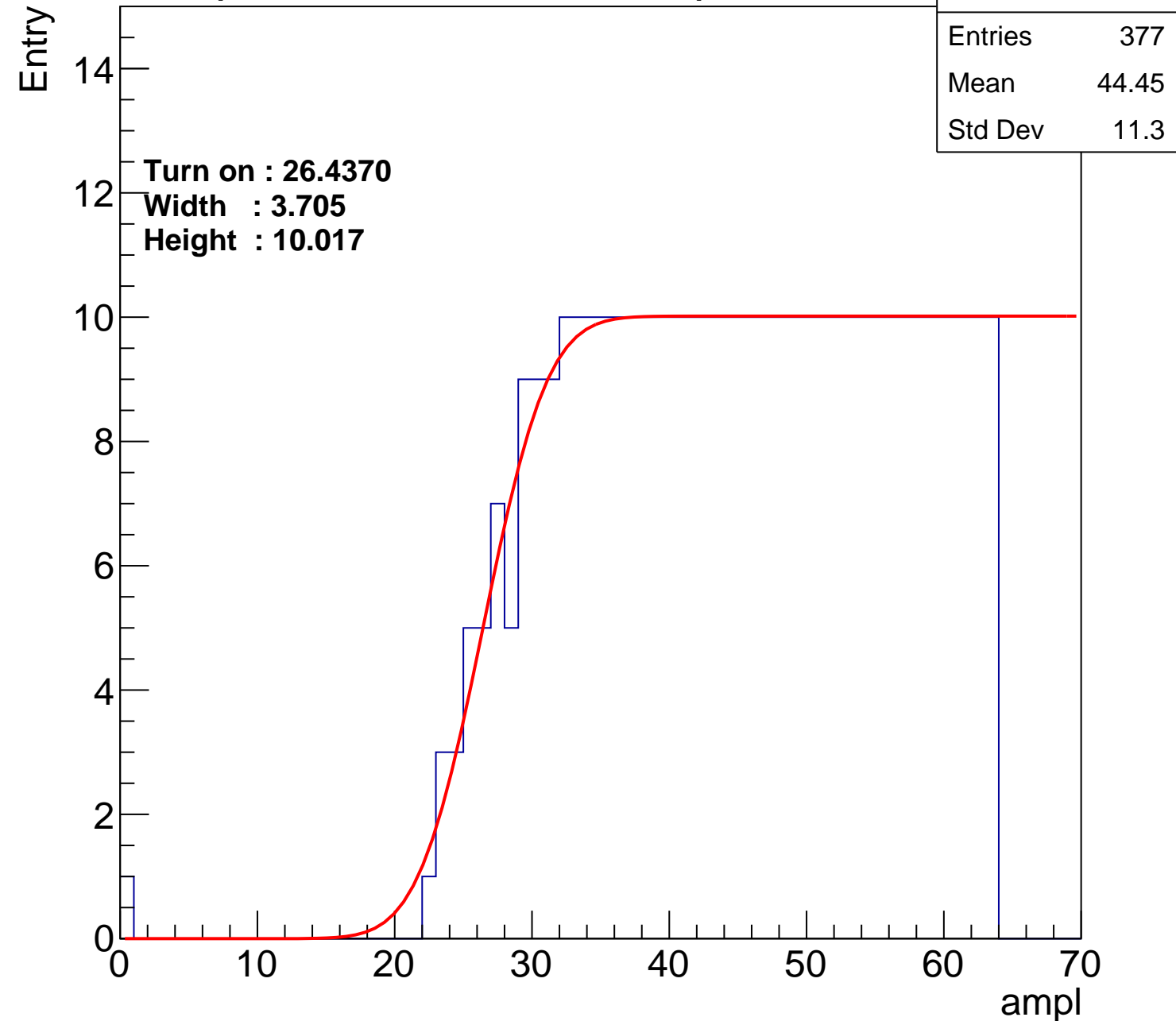
Width : 3.705

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch96

calib_packv5_042523_0143.root, FC#7, port C2

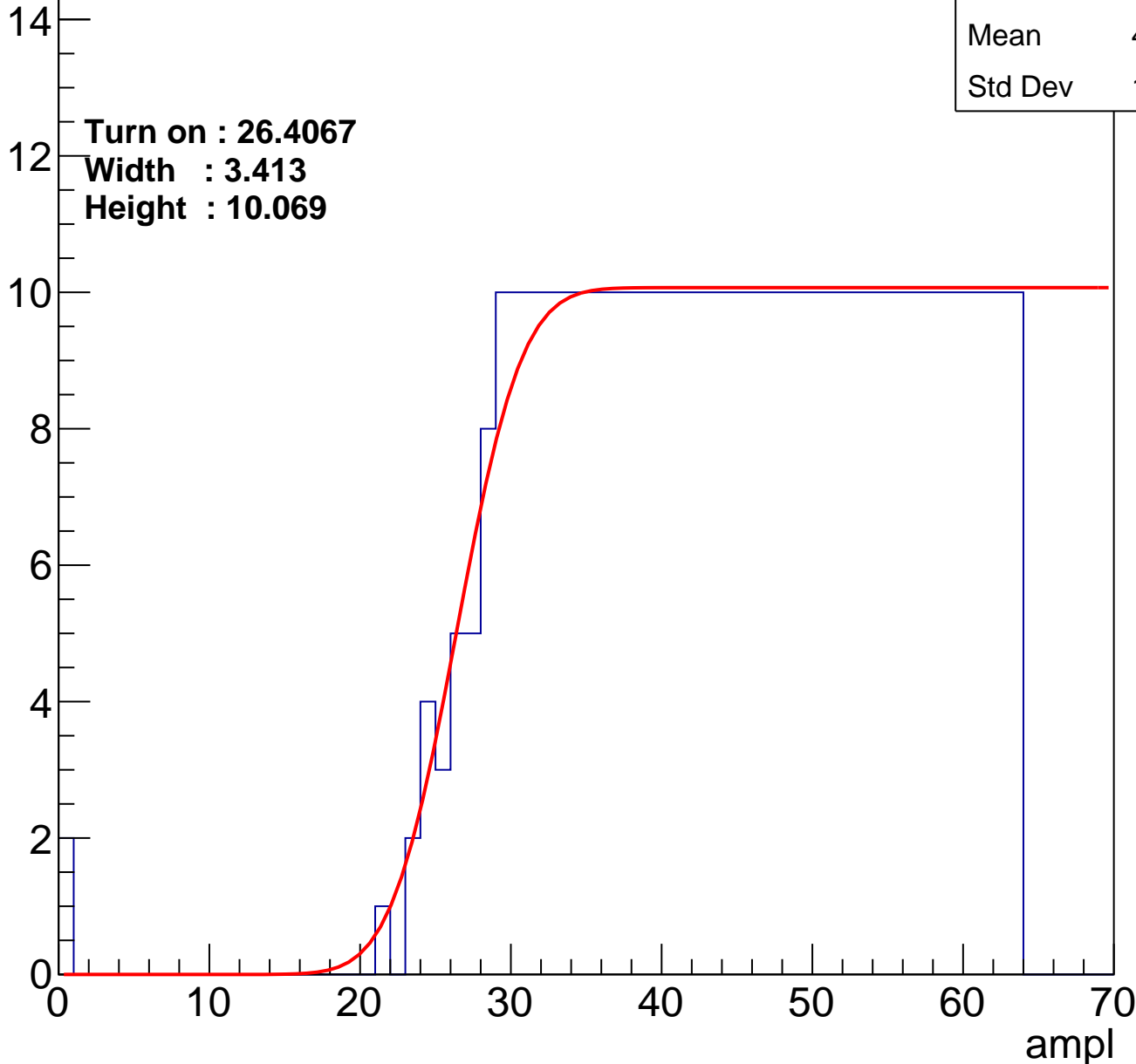
Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.4067

Width : 3.413

Height : 10.069

Entry



B1L103S, U9-ch97

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.0020

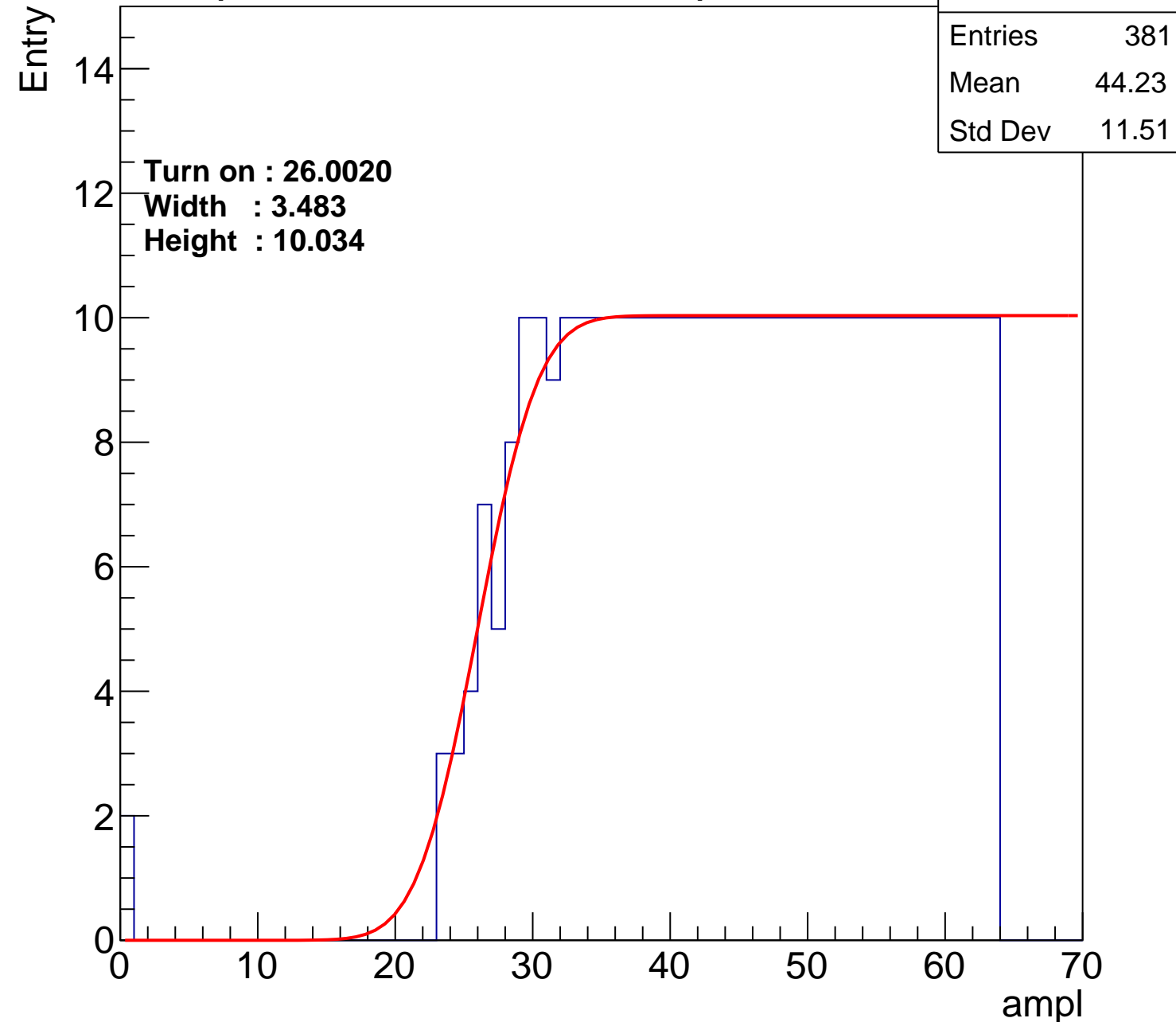
Width : 3.483

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.97
Std Dev	11.79

Turn on : 26.6997

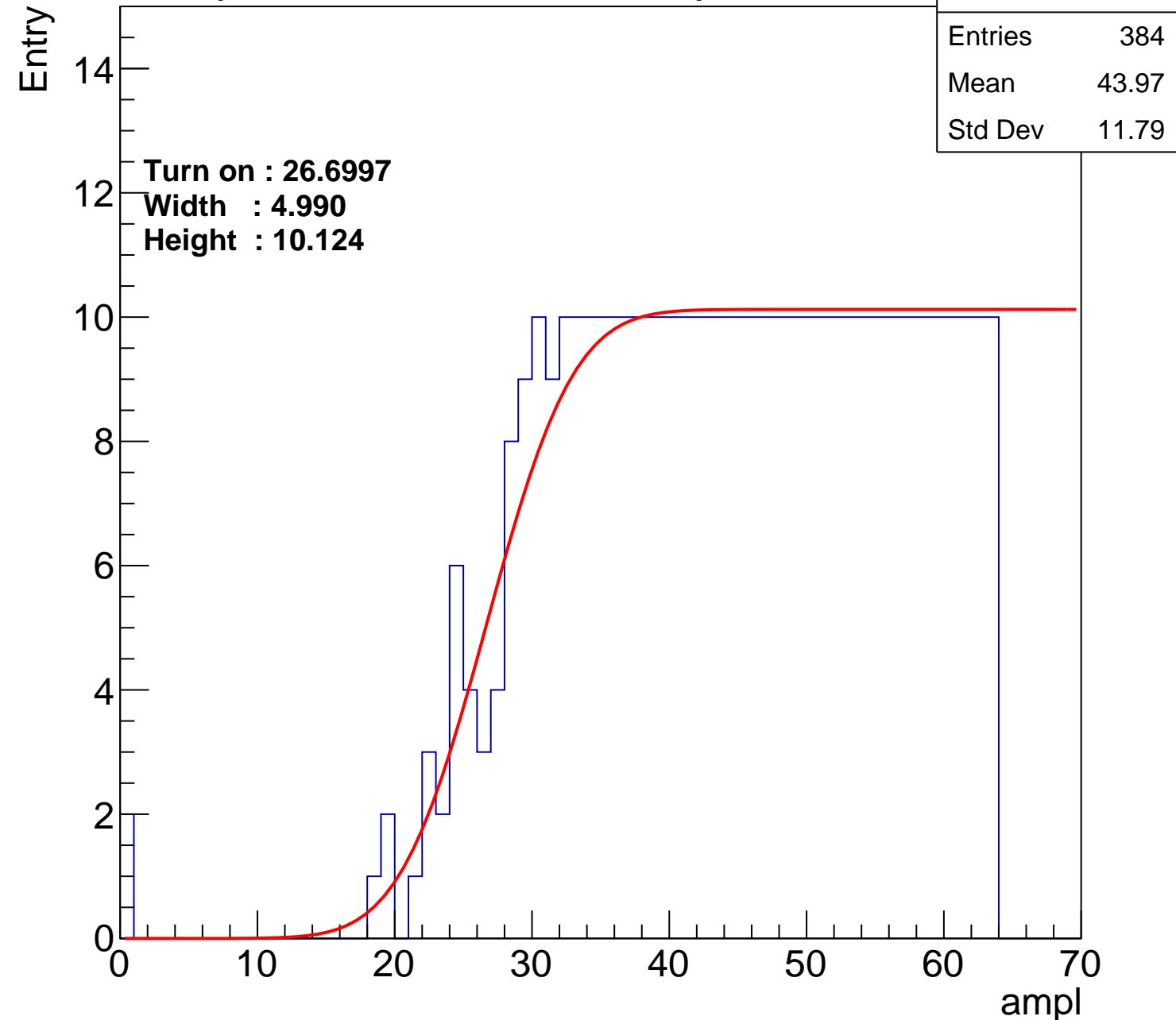
Width : 4.990

Height : 10.124

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch99

calib_packv5_042523_0143.root, FC#7, port C2

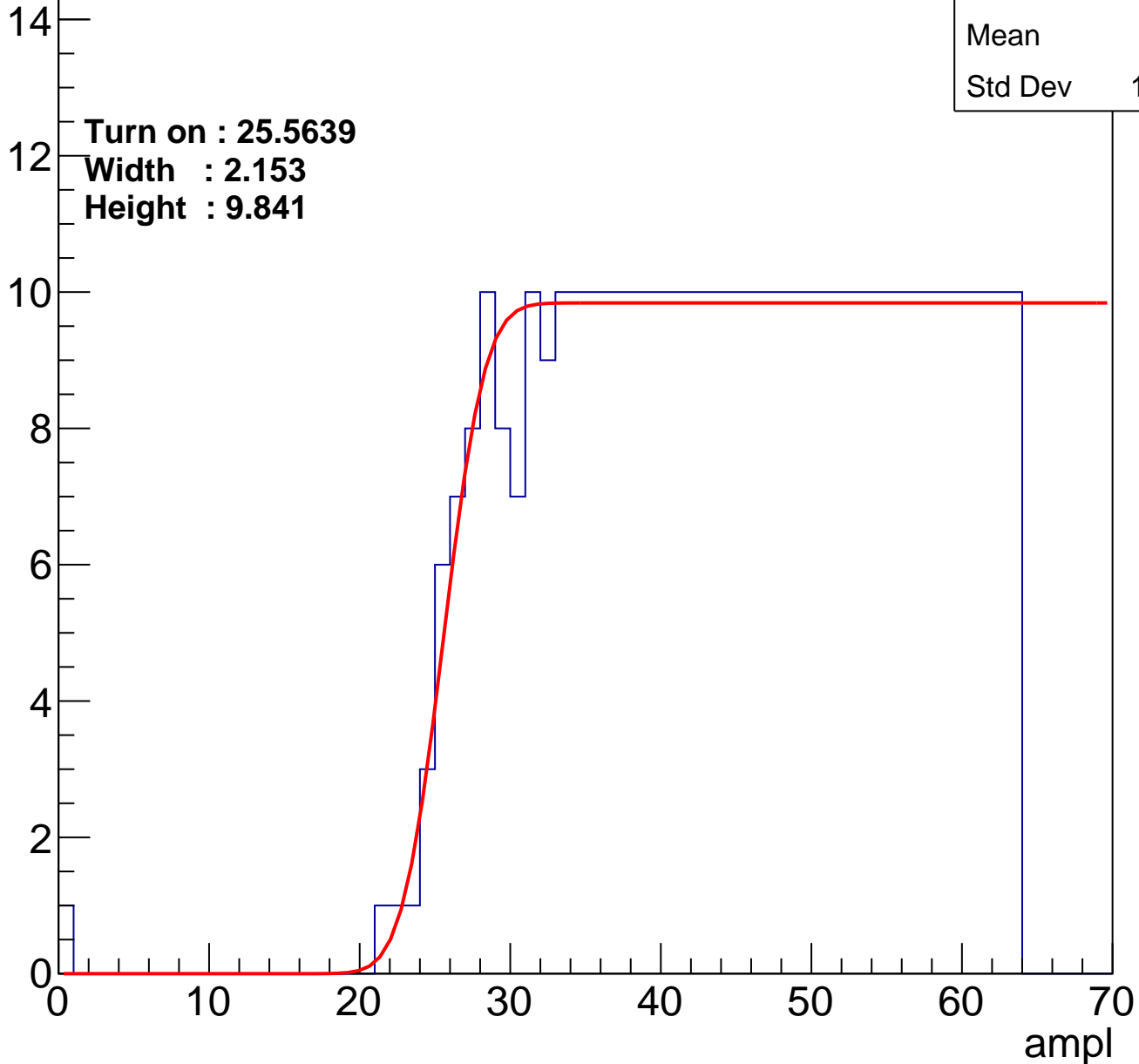
Entries	382
Mean	44.2
Std Dev	11.42

Turn on : 25.5639

Width : 2.153

Height : 9.841

Entry



B1L103S, U9-ch100

calib_packv5_042523_0143.root, FC#7, port C2

Entries	398
Mean	43.37
Std Dev	11.99

Turn on : 24.8714

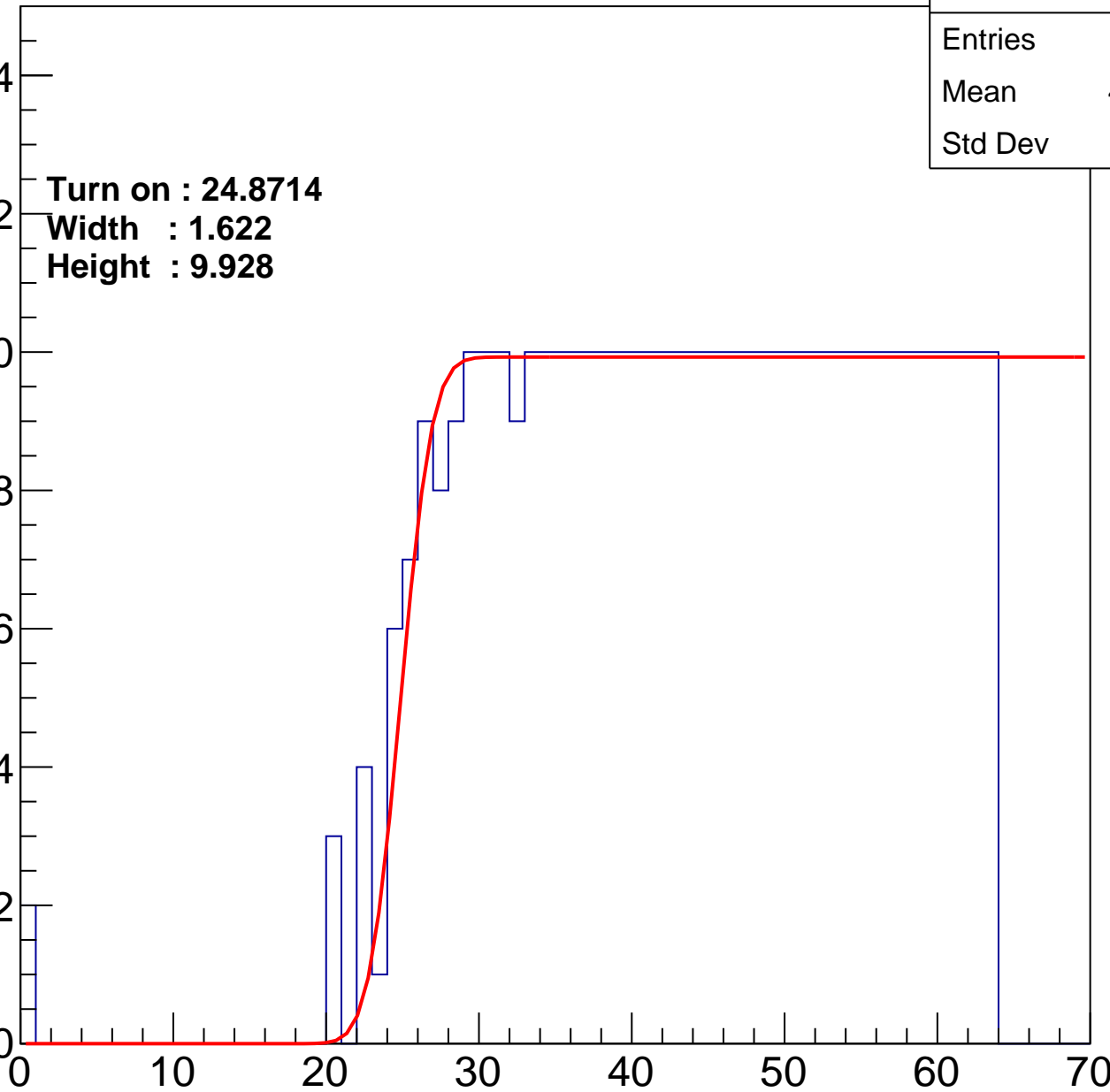
Width : 1.622

Height : 9.928

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch101

calib_packv5_042523_0143.root, FC#7, port C2

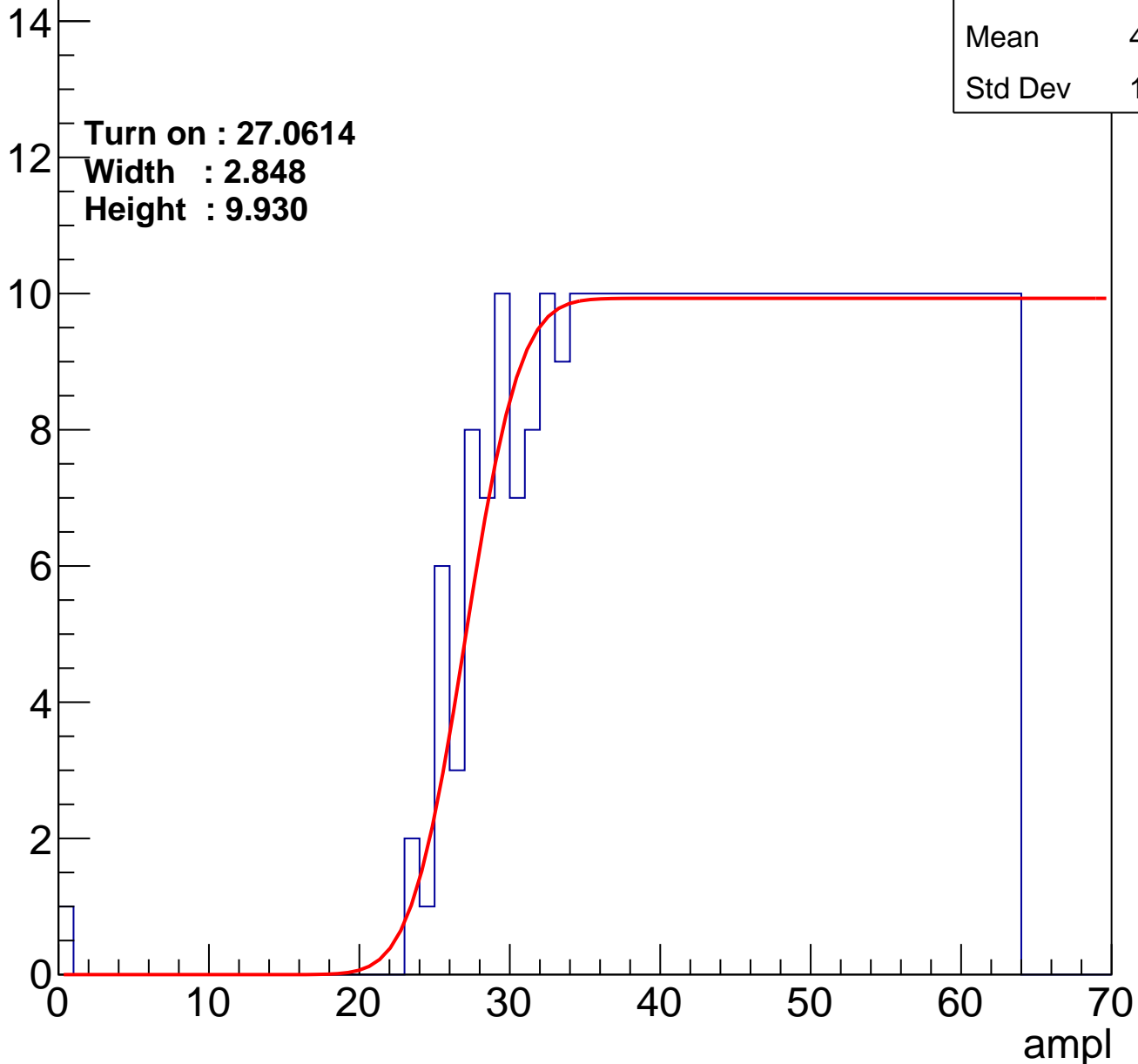
Entries	372
Mean	44.69
Std Dev	11.16

Turn on : 27.0614

Width : 2.848

Height : 9.930

Entry



B1L103S, U9-ch102

calib_packv5_042523_0143.root, FC#7, port C2

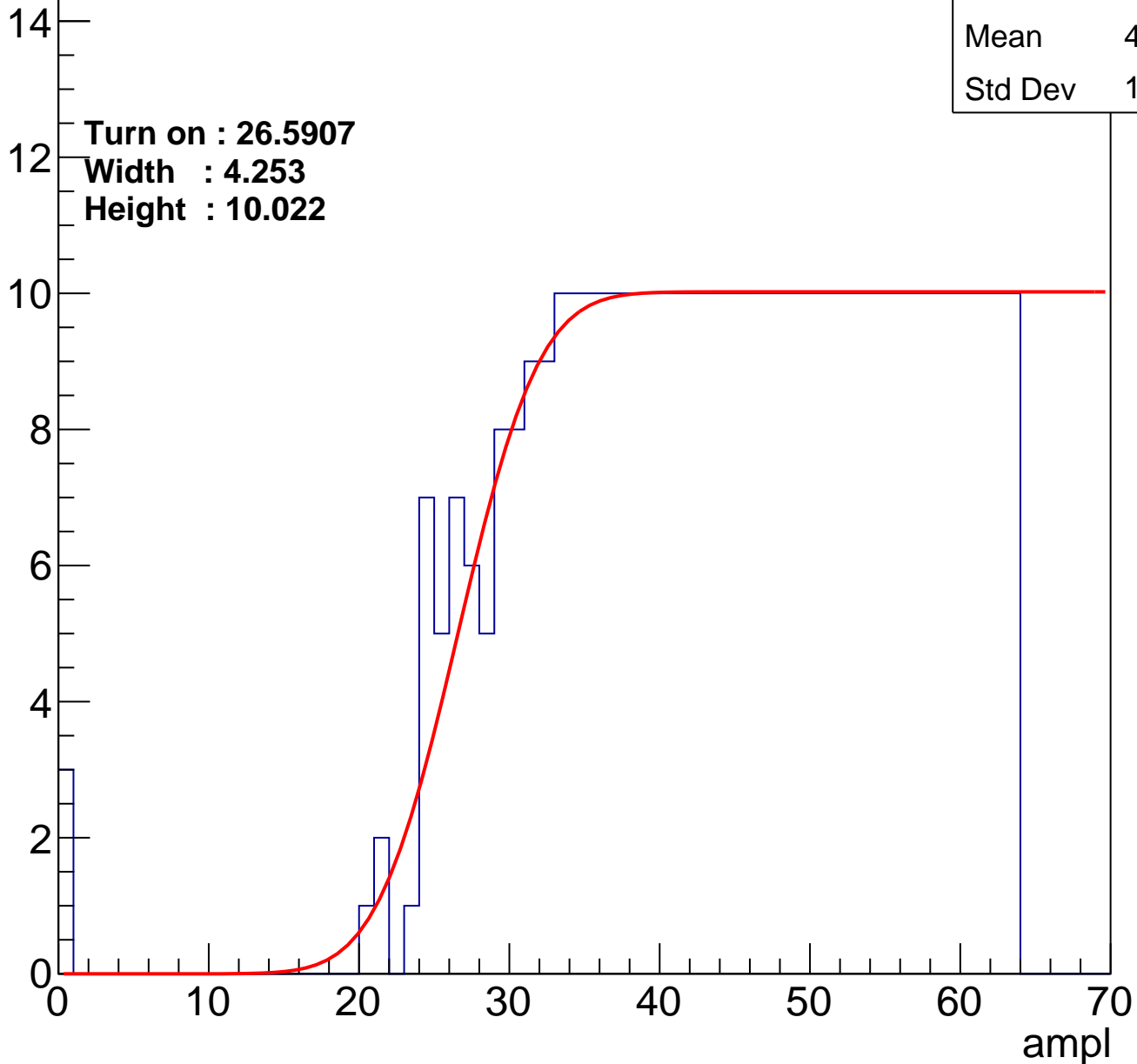
Entries	381
Mean	44.04
Std Dev	11.87

Turn on : 26.5907

Width : 4.253

Height : 10.022

Entry



B1L103S, U9-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.41
Std Dev	11.74

Turn on : 27.2878

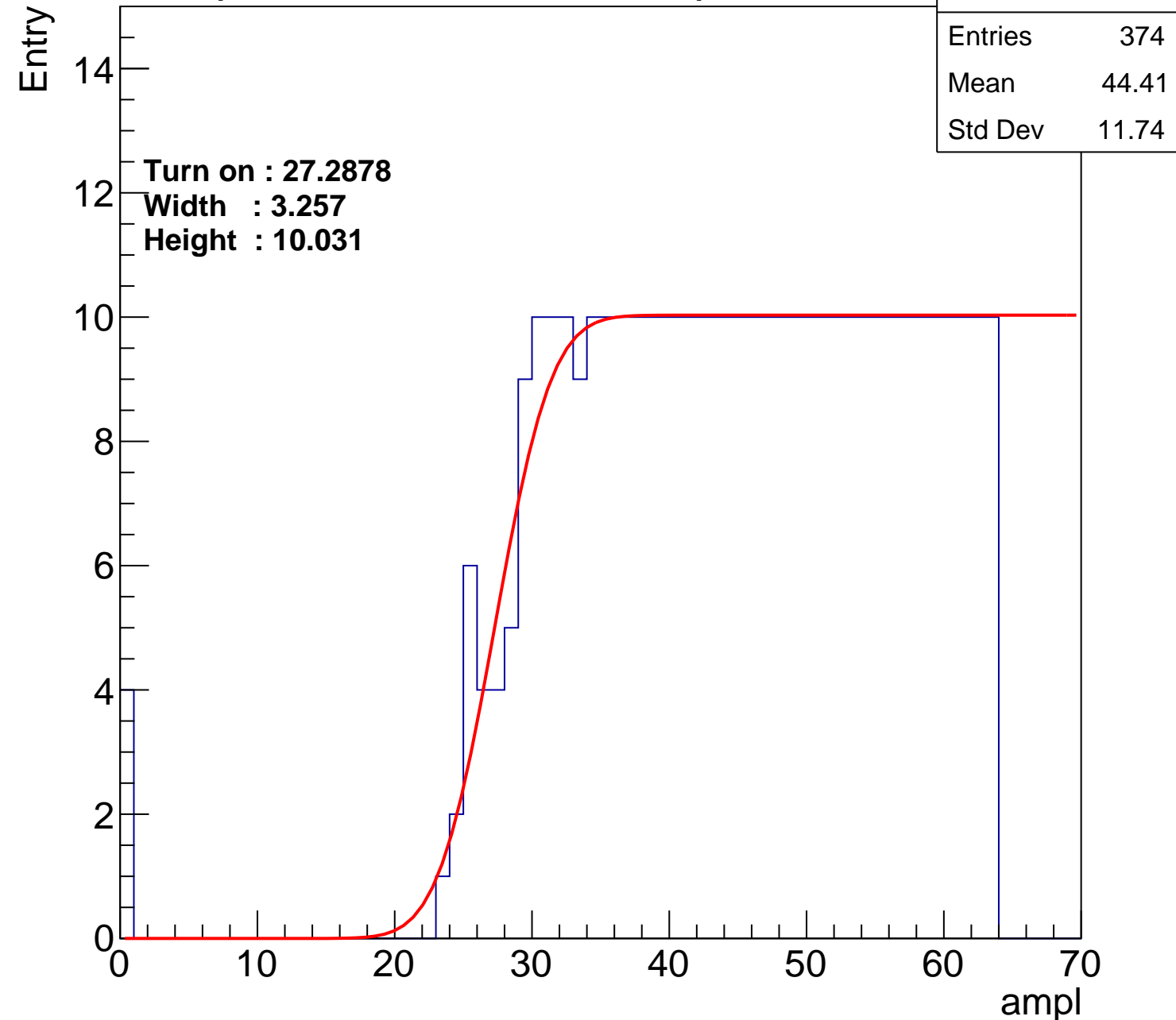
Width : 3.257

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch104

calib_packv5_042523_0143.root, FC#7, port C2

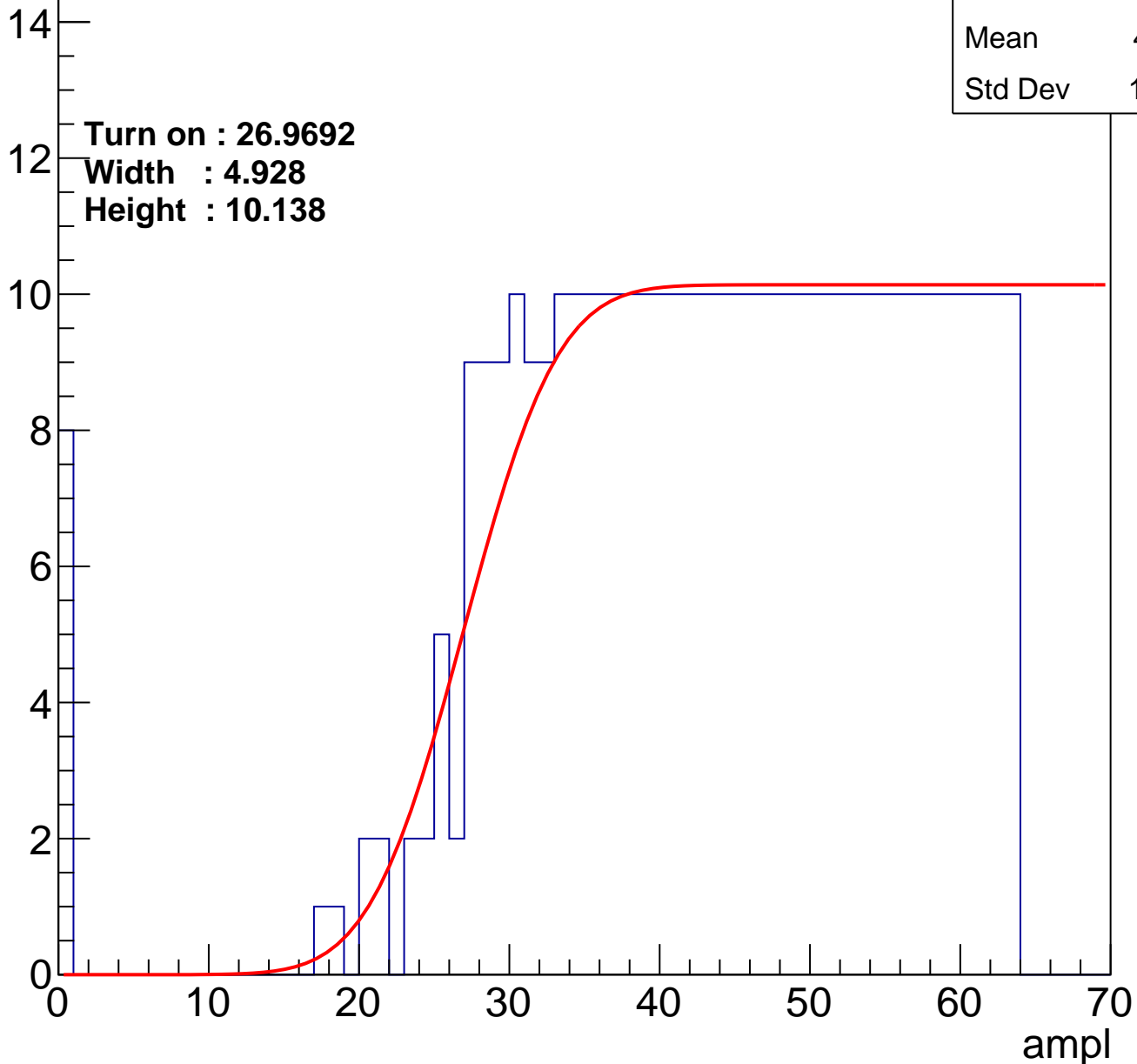
Entries	390
Mean	43.31
Std Dev	12.86

Turn on : 26.9692

Width : 4.928

Height : 10.138

Entry



B1L103S, U9-ch105

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.17
Std Dev	12

Turn on : 26.8696

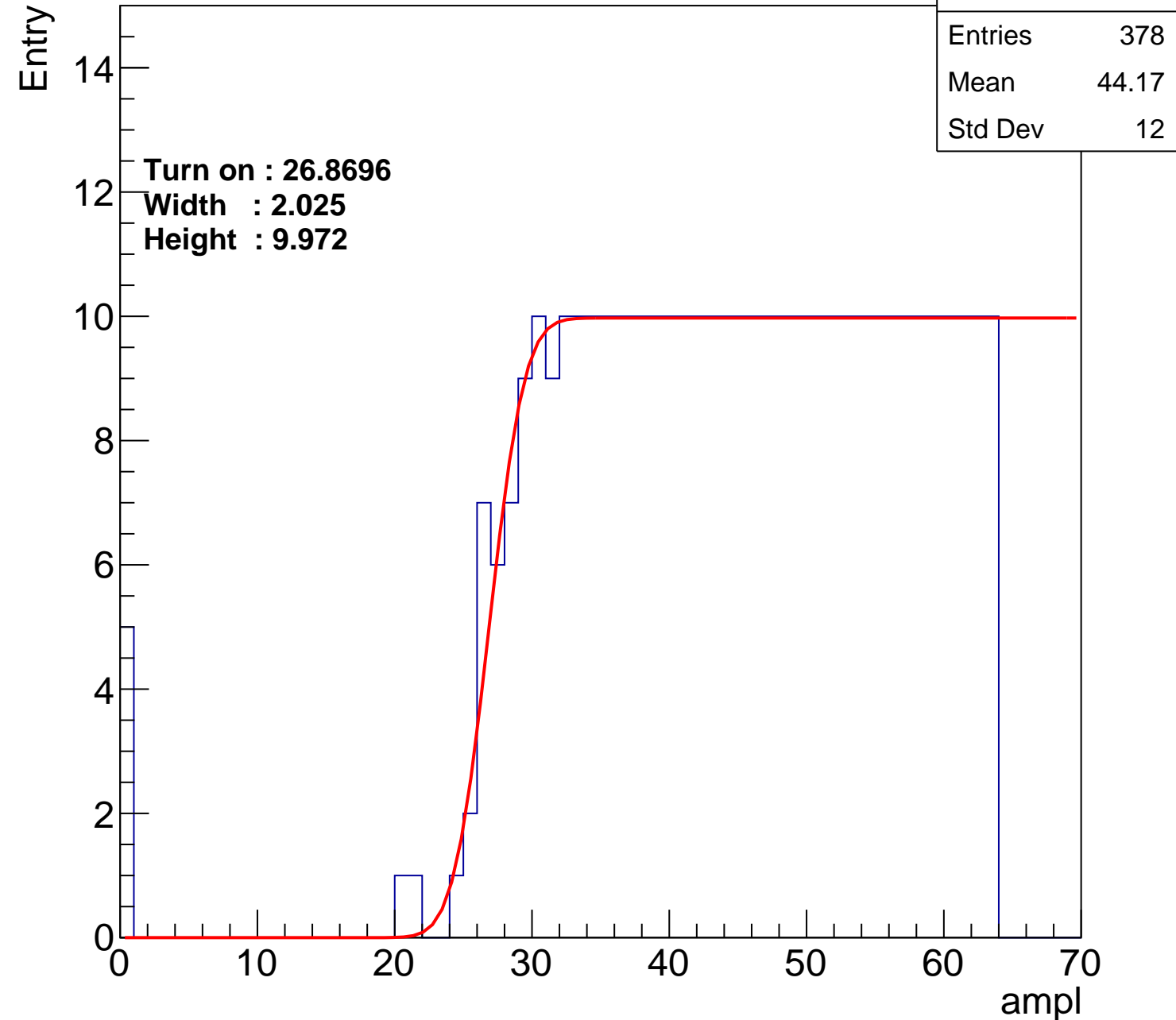
Width : 2.025

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch106

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.7
Std Dev	12.18

Turn on : 25.6630

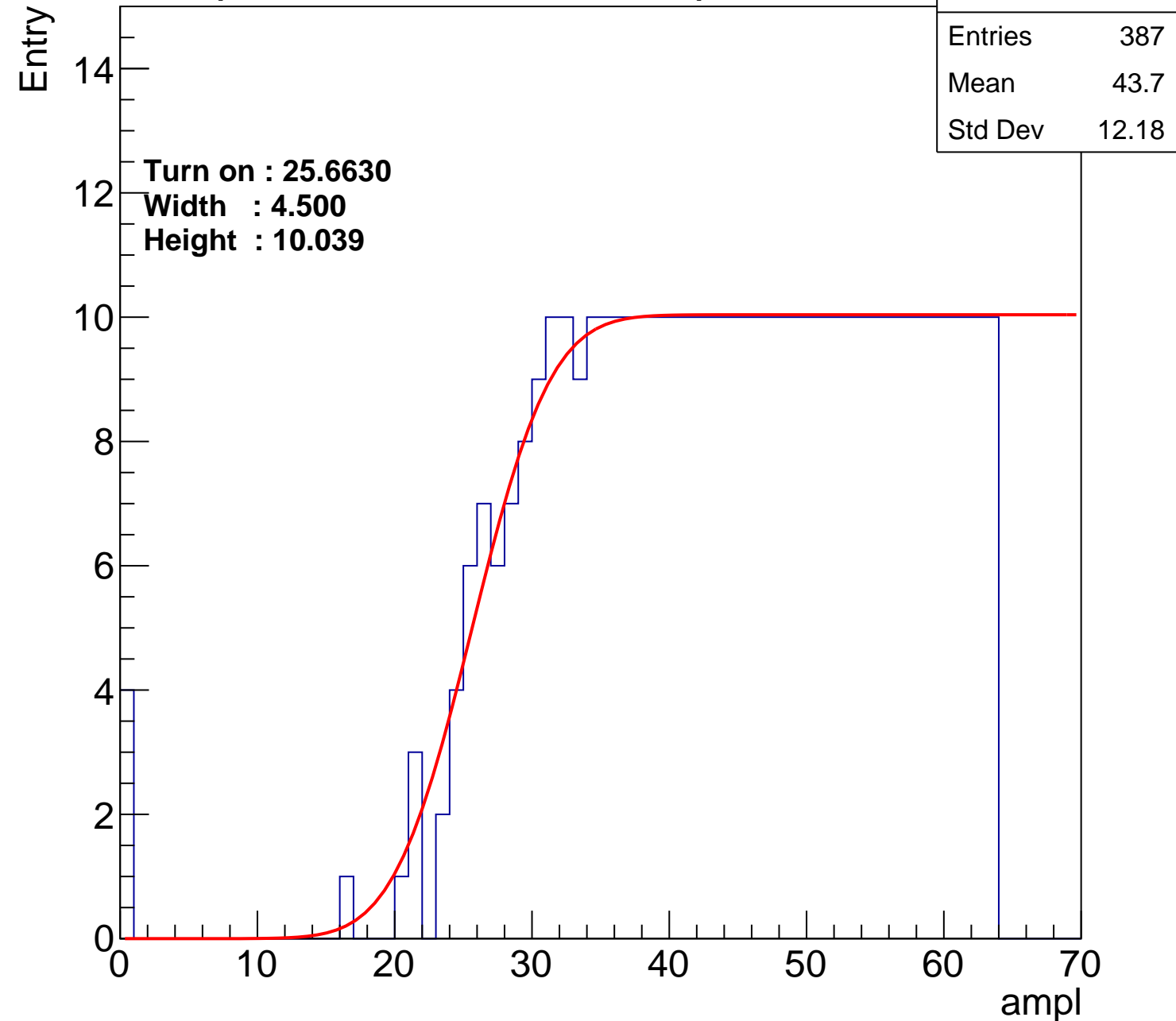
Width : 4.500

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.16
Std Dev	11.53

Turn on : 26.0922

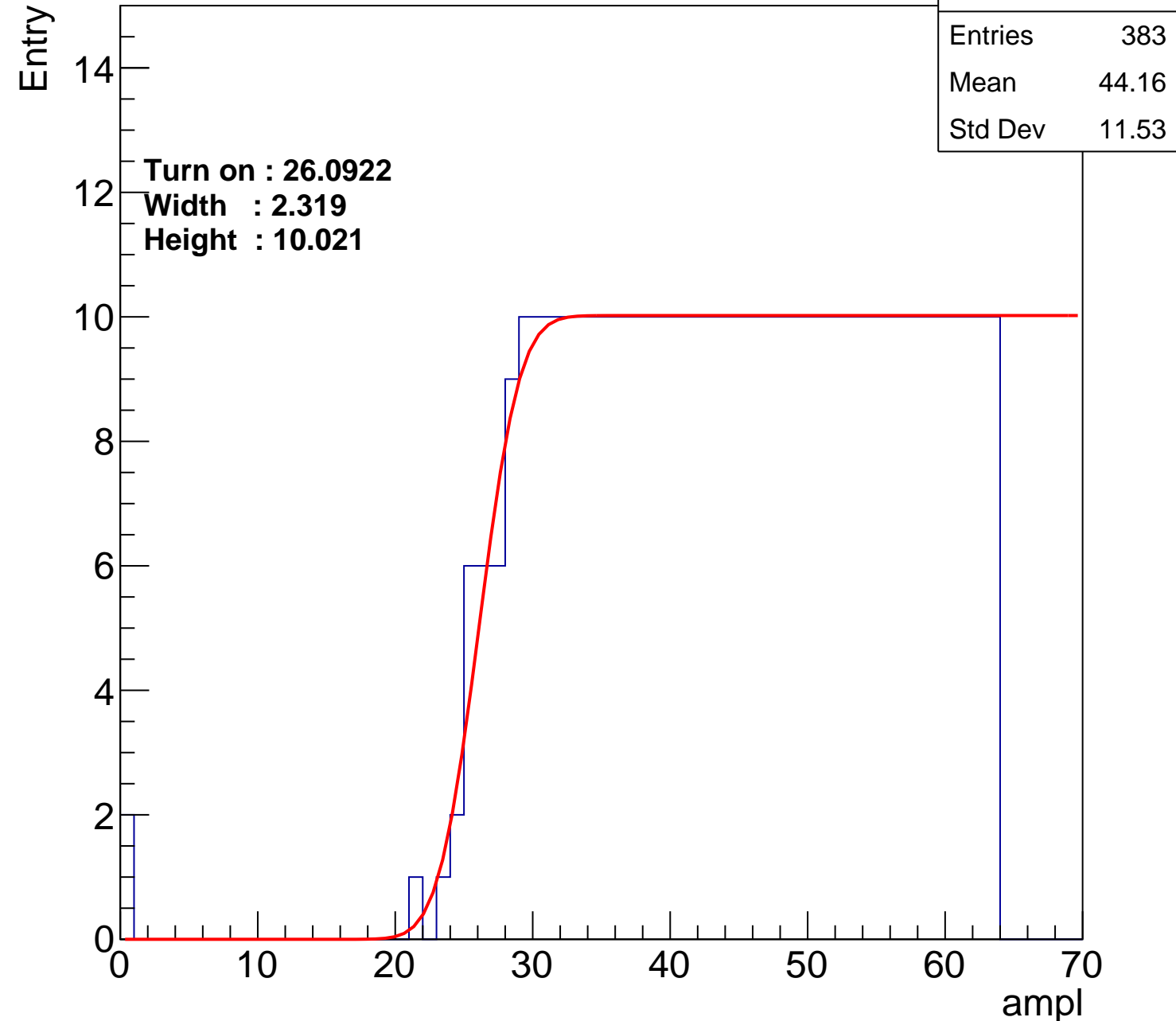
Width : 2.319

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch108

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 26.9793

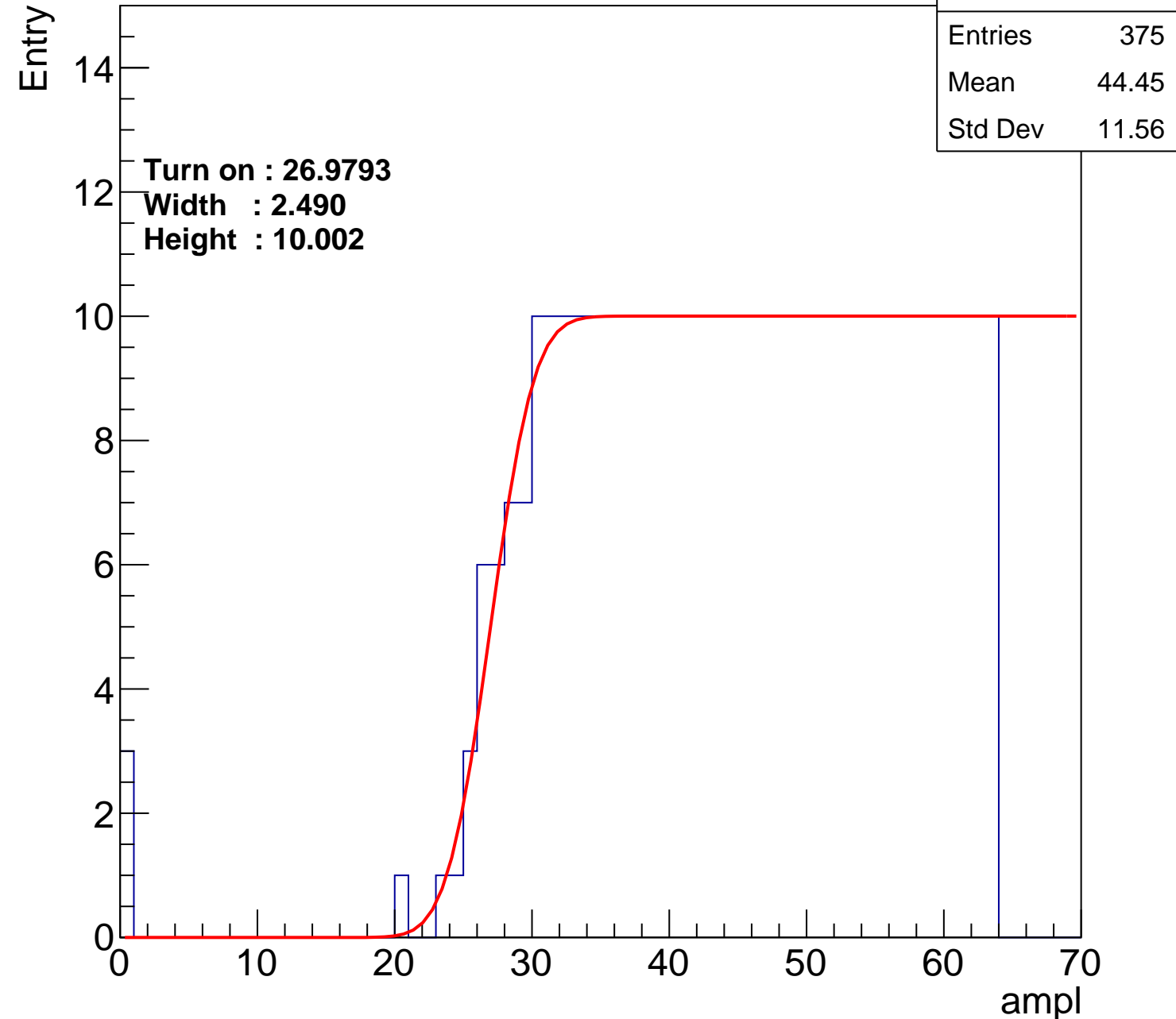
Width : 2.490

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch109

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.61
Std Dev	11.55

Turn on : 26.9768

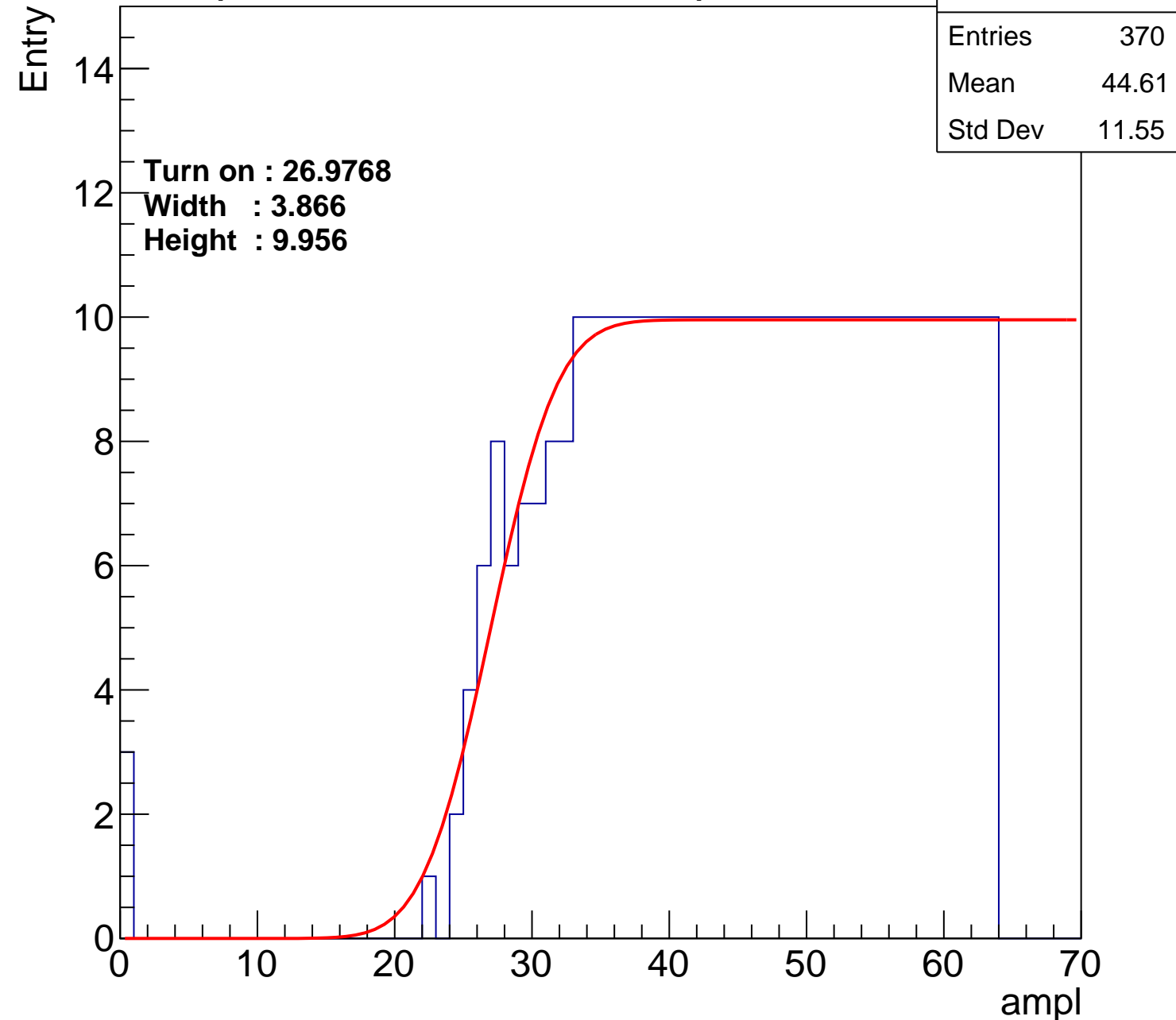
Width : 3.866

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch110

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.3
Std Dev	11.82

Turn on : 27.4838

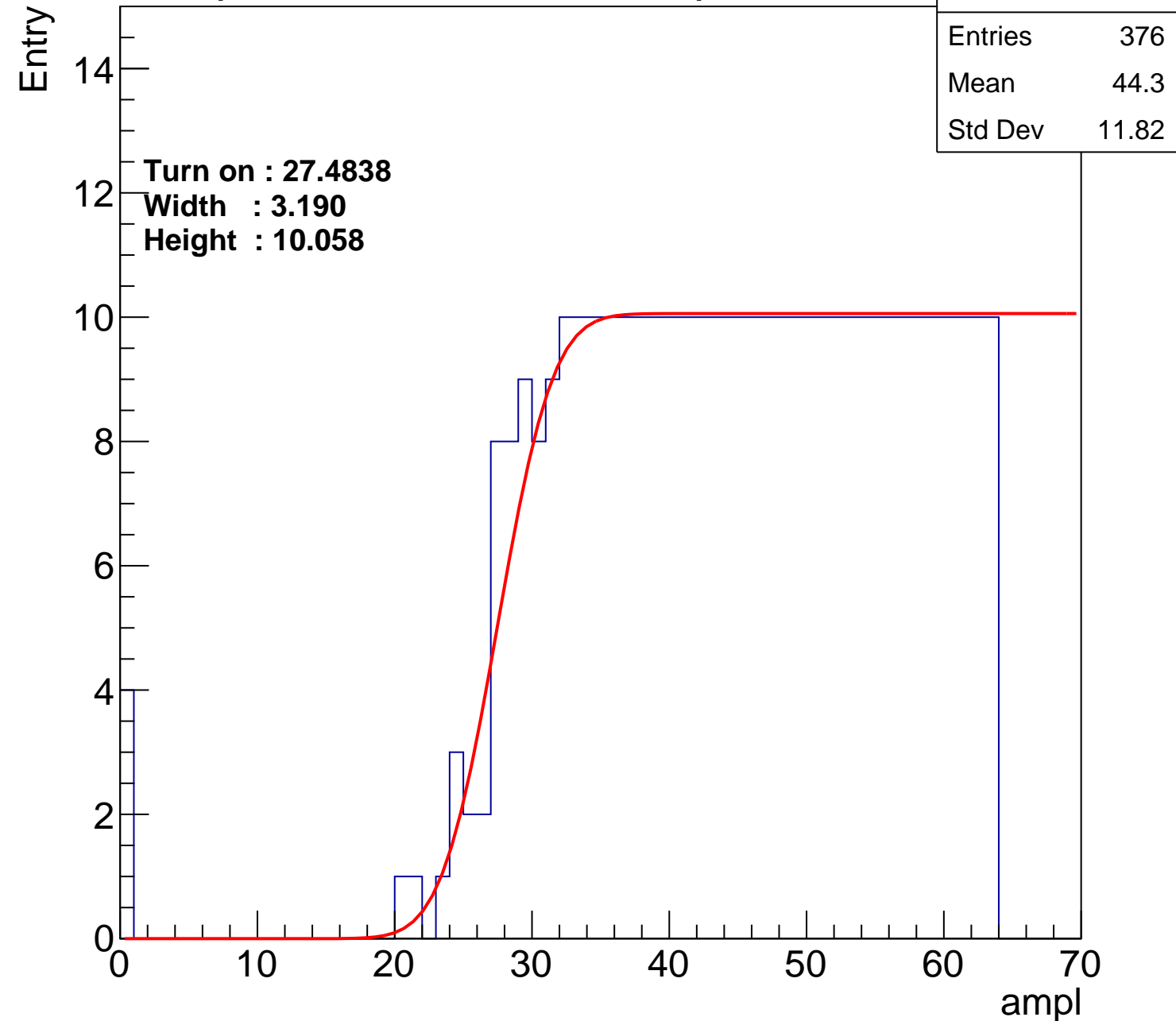
Width : 3.190

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch111

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.33
Std Dev	11.95

Turn on : 27.6186

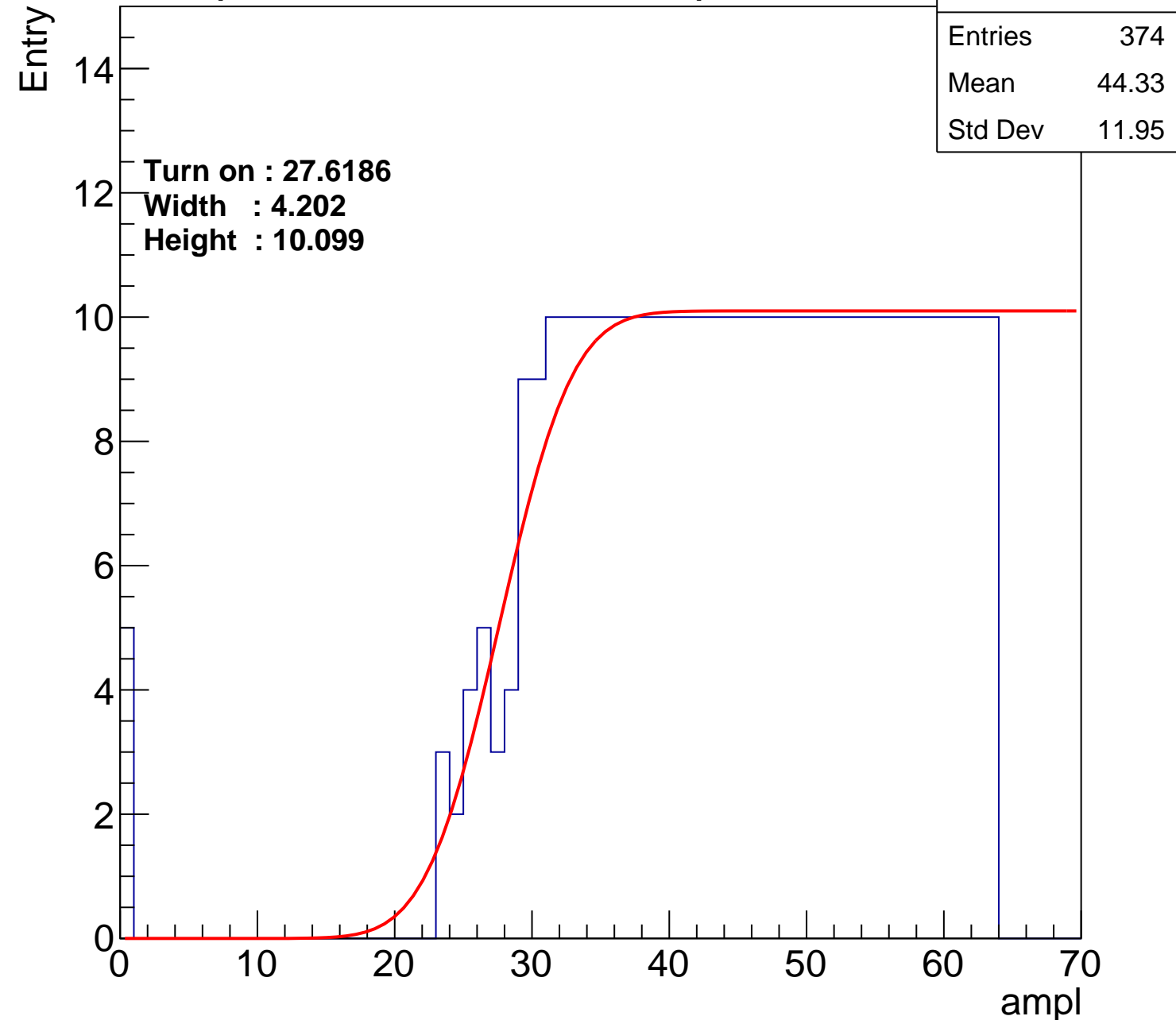
Width : 4.202

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.02
Std Dev	11.82

Turn on : 26.9046

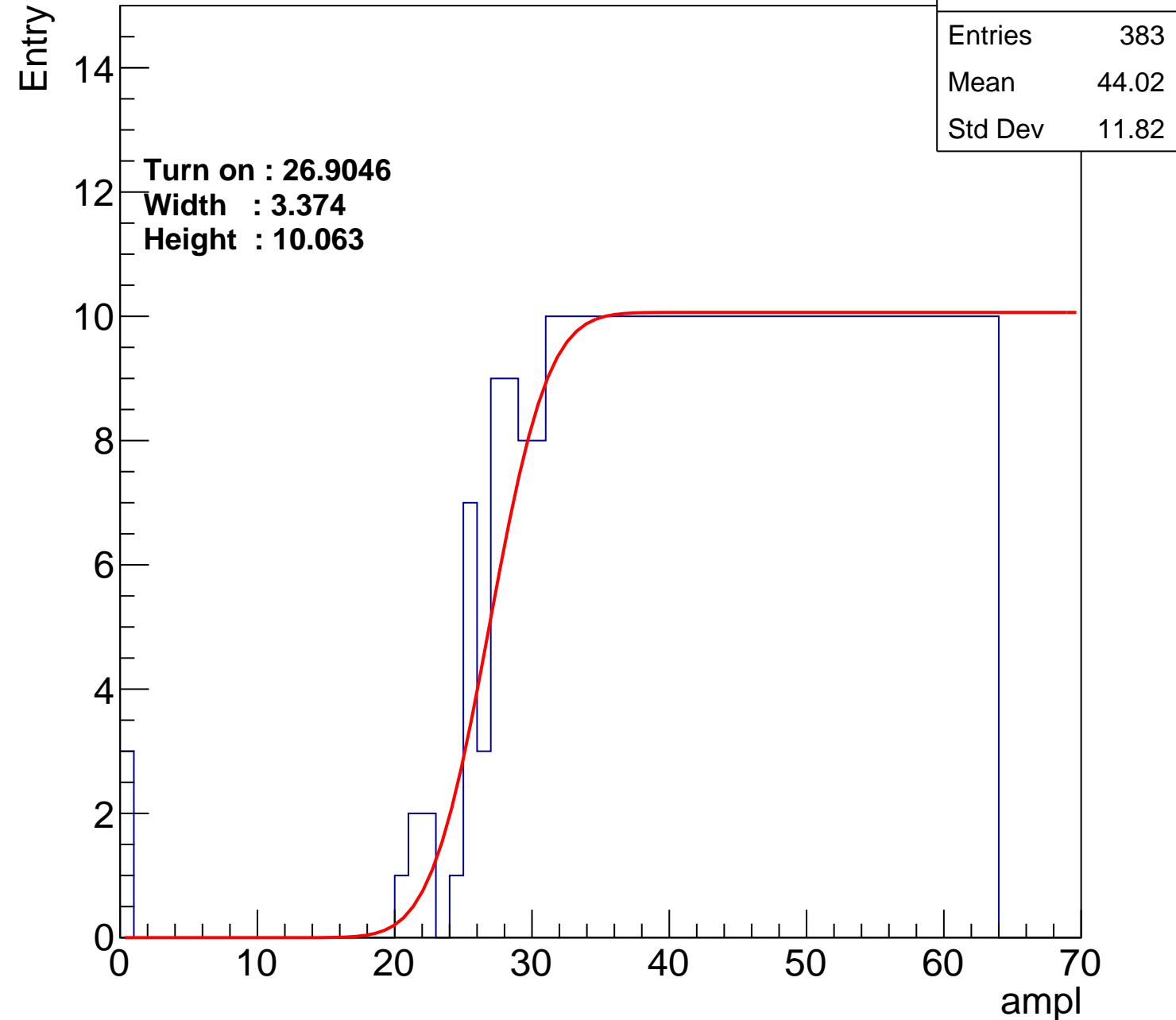
Width : 3.374

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch113

calib_packv5_042523_0143.root, FC#7, port C2

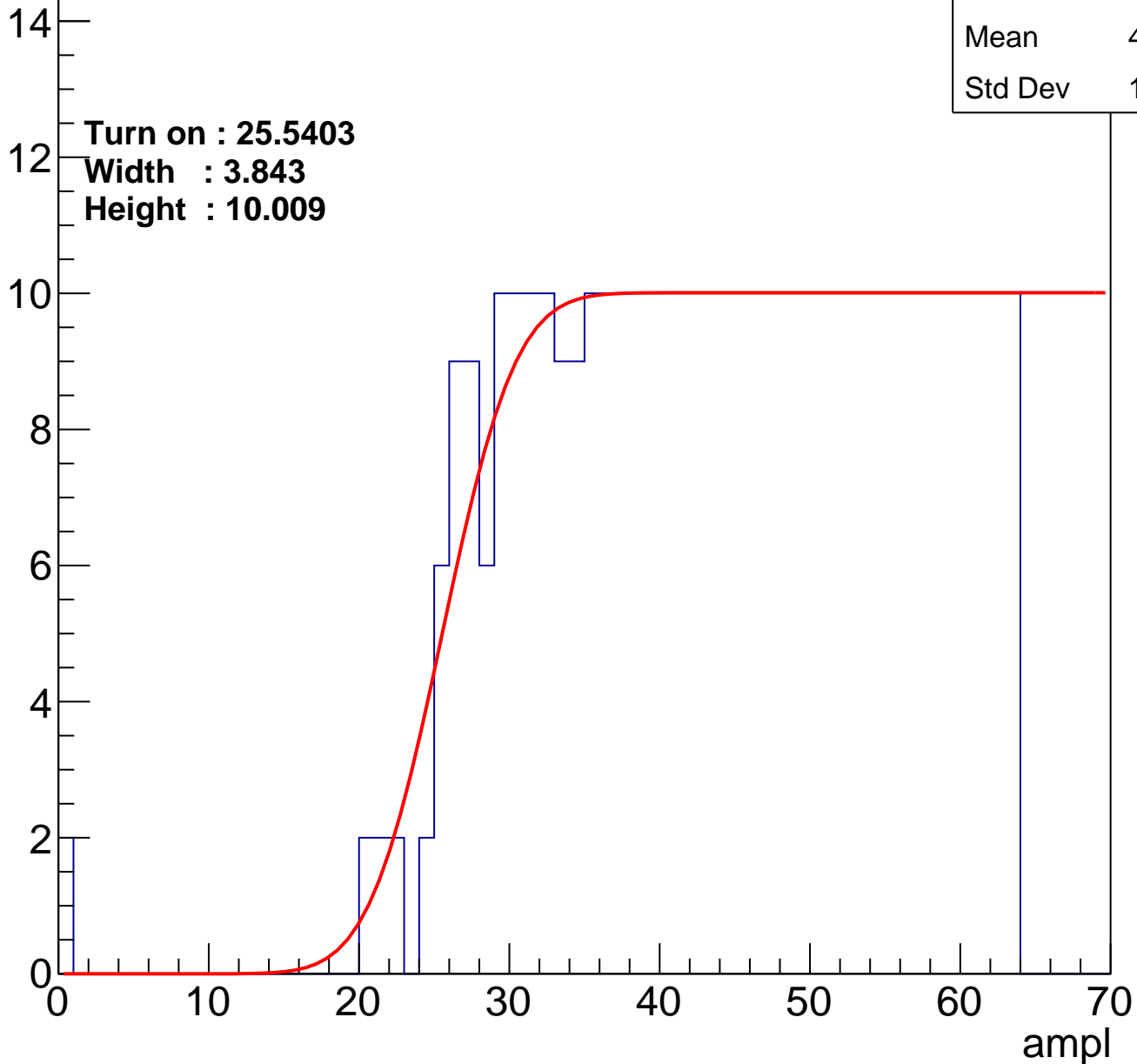
Entries	388
Mean	43.82
Std Dev	11.79

Turn on : 25.5403

Width : 3.843

Height : 10.009

Entry



B1L103S, U9-ch114

calib_packv5_042523_0143.root, FC#7, port C2

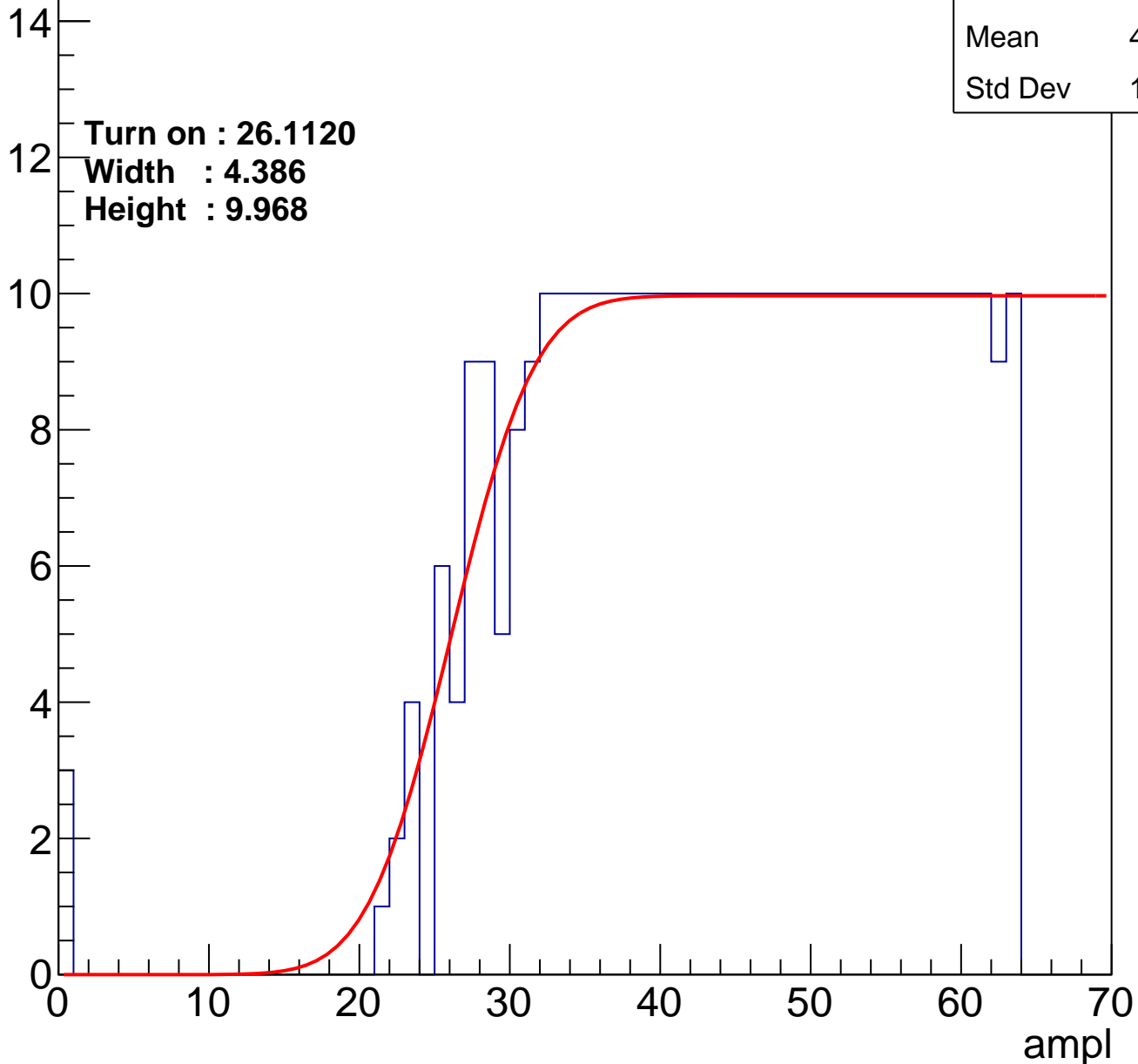
Entries	379
Mean	44.08
Std Dev	11.77

Turn on : 26.1120

Width : 4.386

Height : 9.968

Entry



B1L103S, U9-ch115

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.08
Std Dev	12.07

Turn on : 26.7789

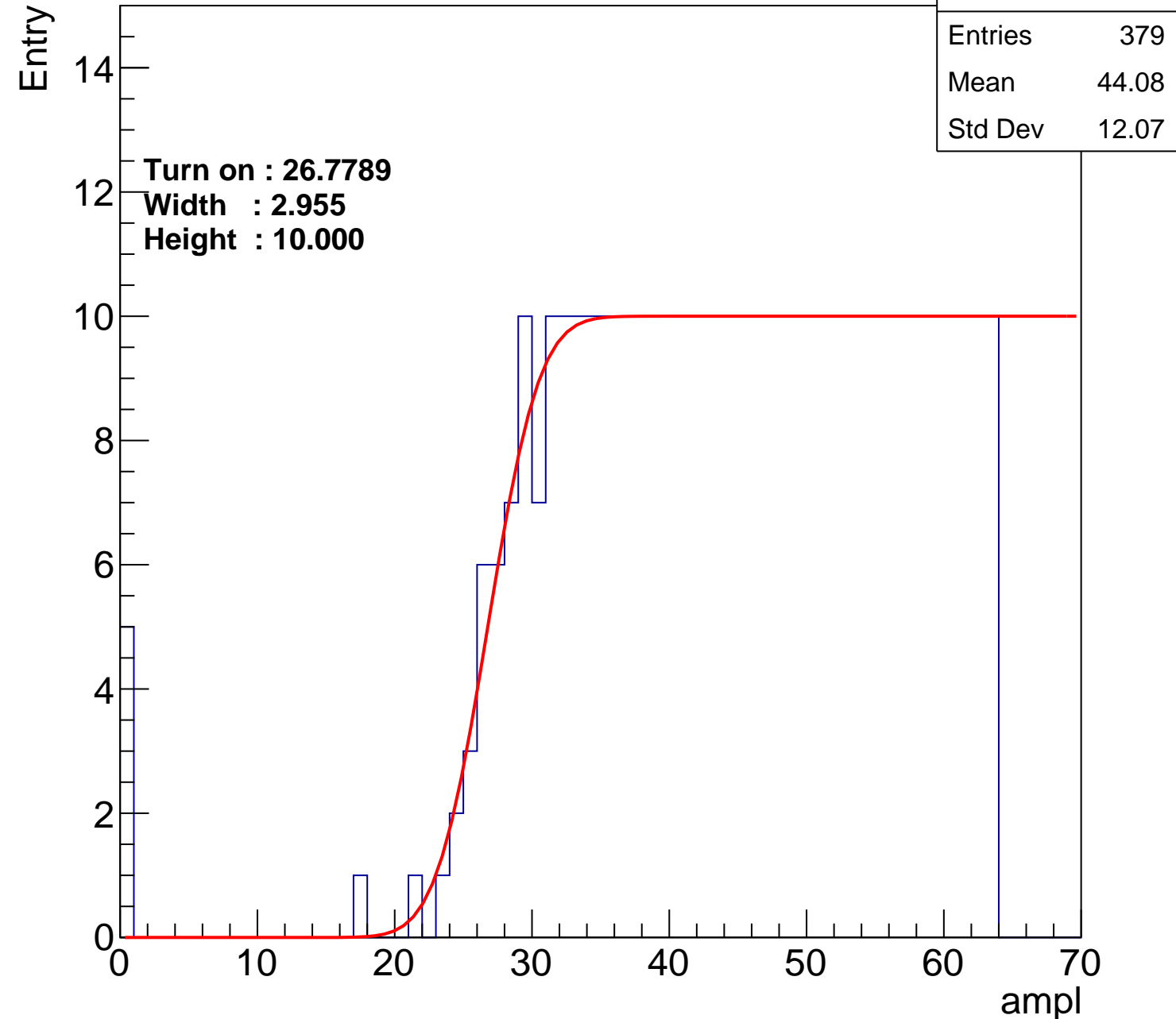
Width : 2.955

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch116

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.07
Std Dev	11.66

Turn on : 26.3885

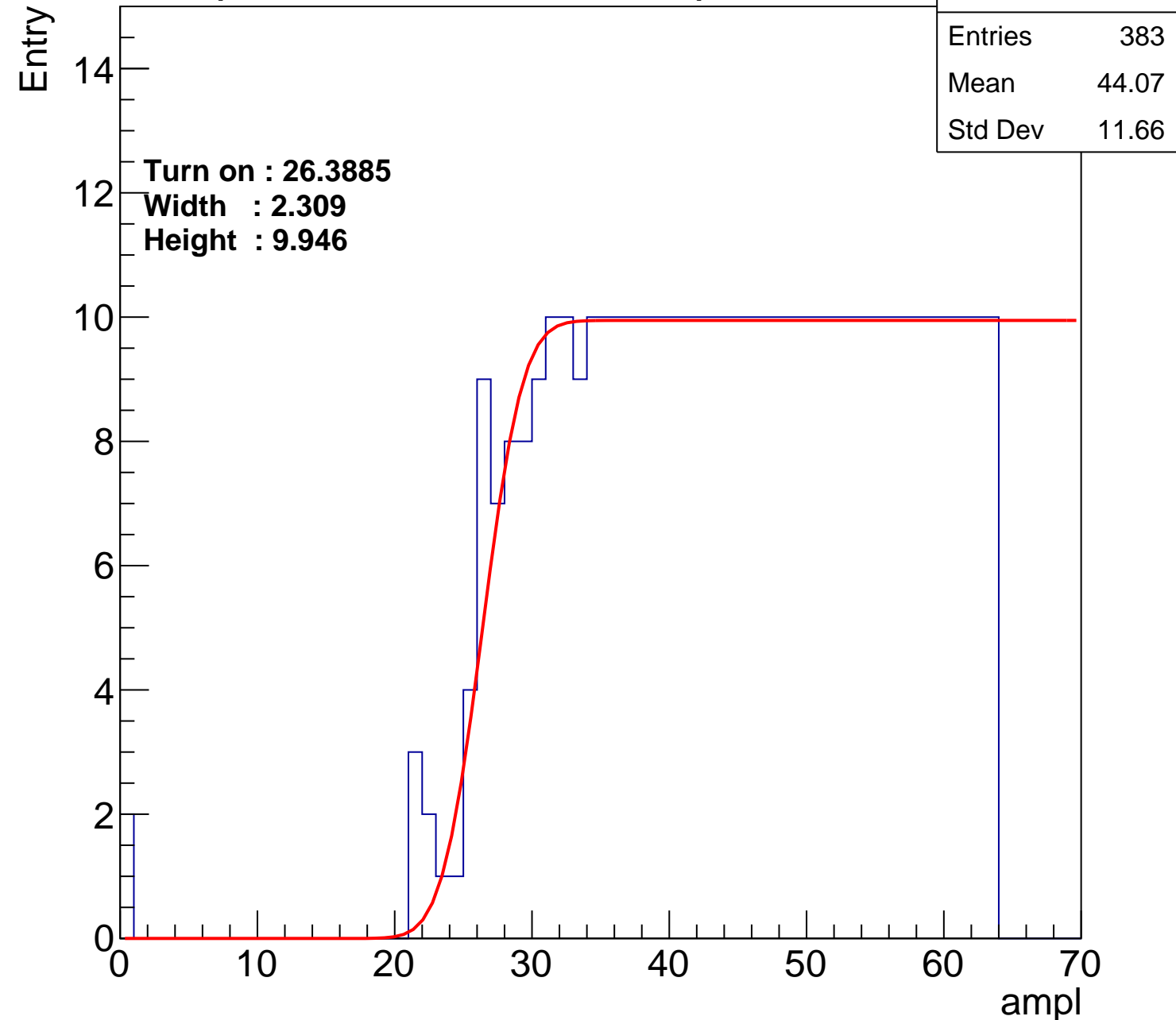
Width : 2.309

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.26
Std Dev	11.53

Turn on : 26.7371

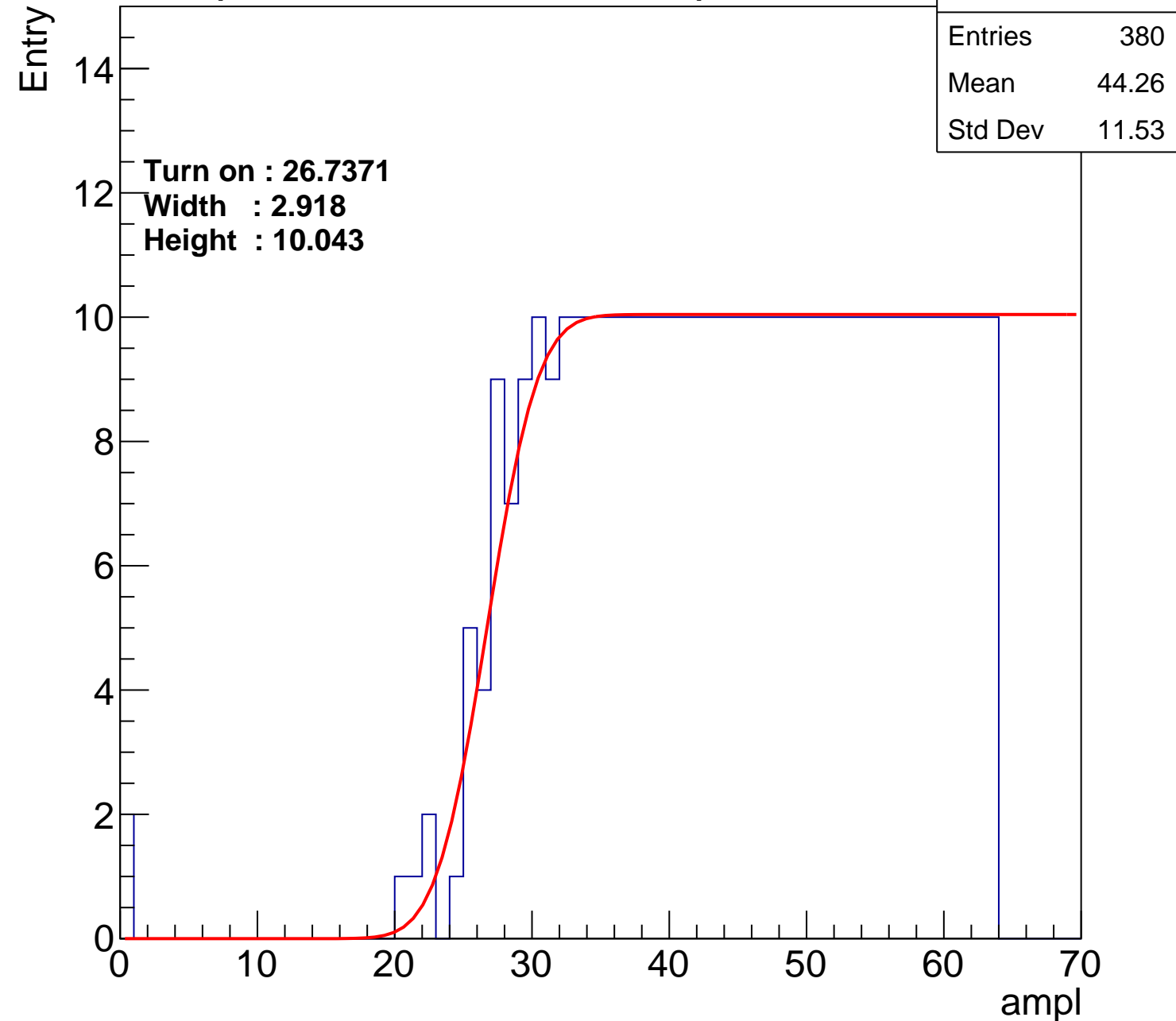
Width : 2.918

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch118

calib_packv5_042523_0143.root, FC#7, port C2

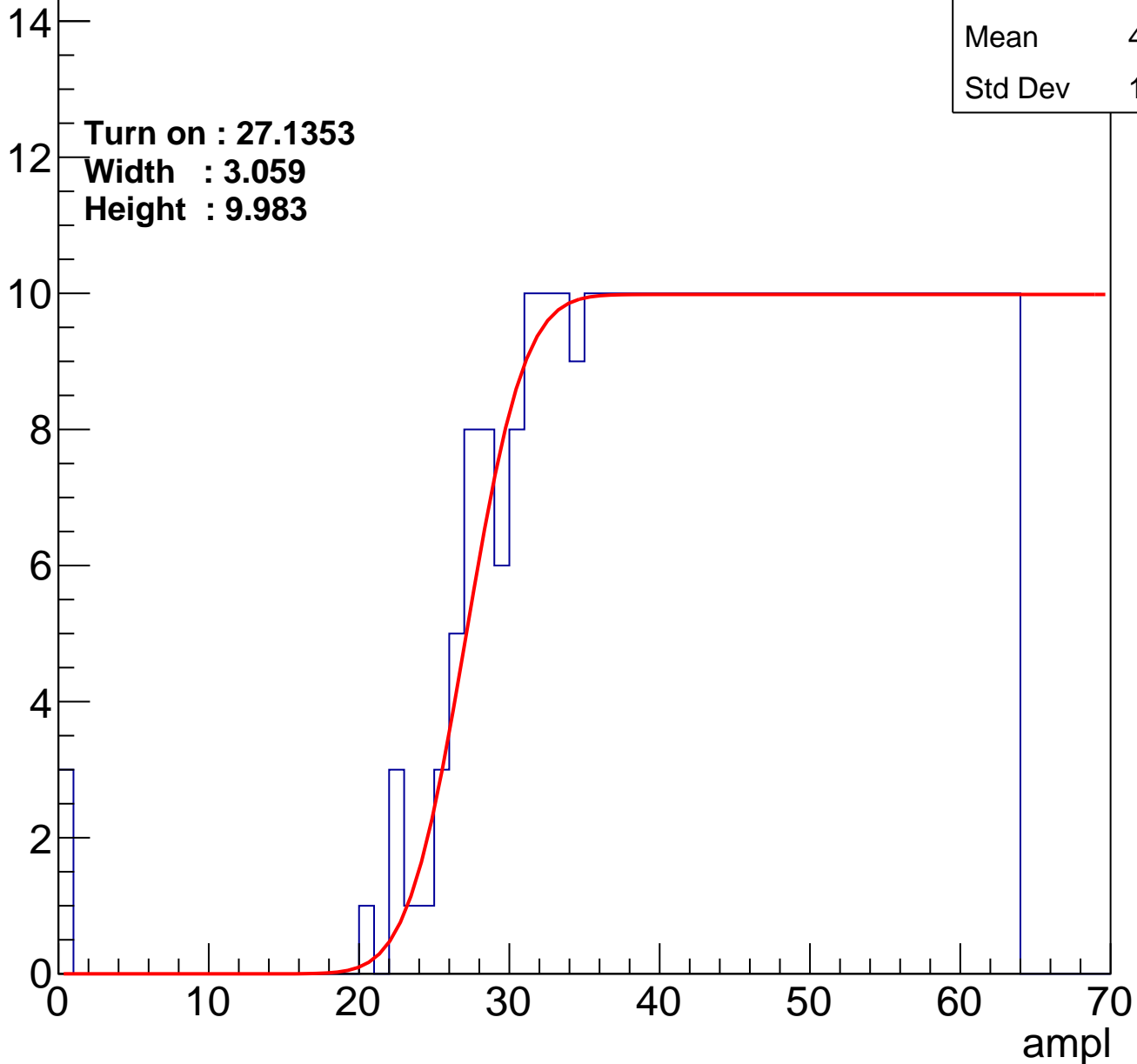
Entries	376
Mean	44.33
Std Dev	11.69

Turn on : 27.1353

Width : 3.059

Height : 9.983

Entry



B1L103S, U9-ch119

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	44.09
Std Dev	11.45

Turn on : 25.6360

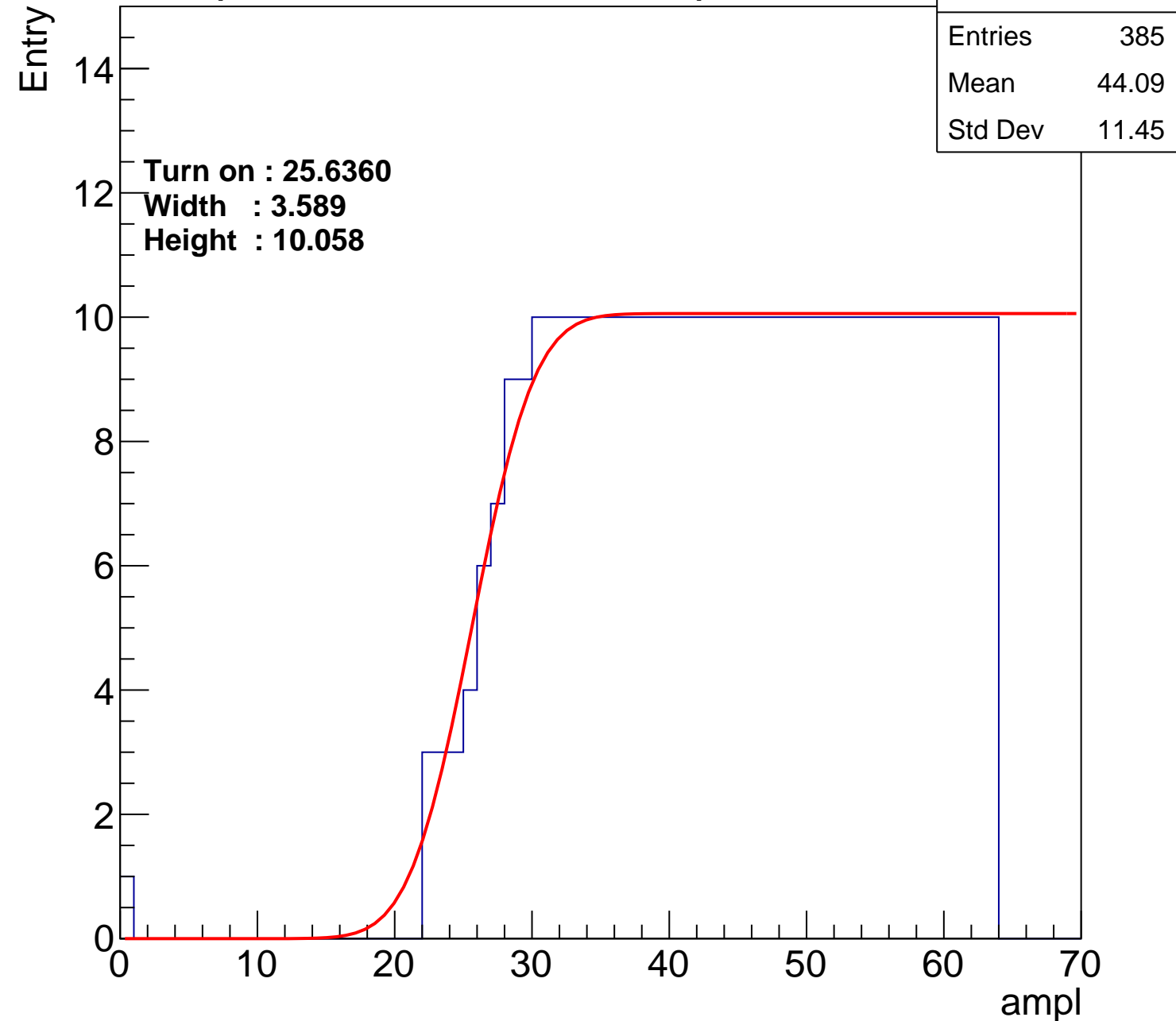
Width : 3.589

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch120

calib_packv5_042523_0143.root, FC#7, port C2

Entries	399
Mean	43.16
Std Dev	12.37

Turn on : 24.1931

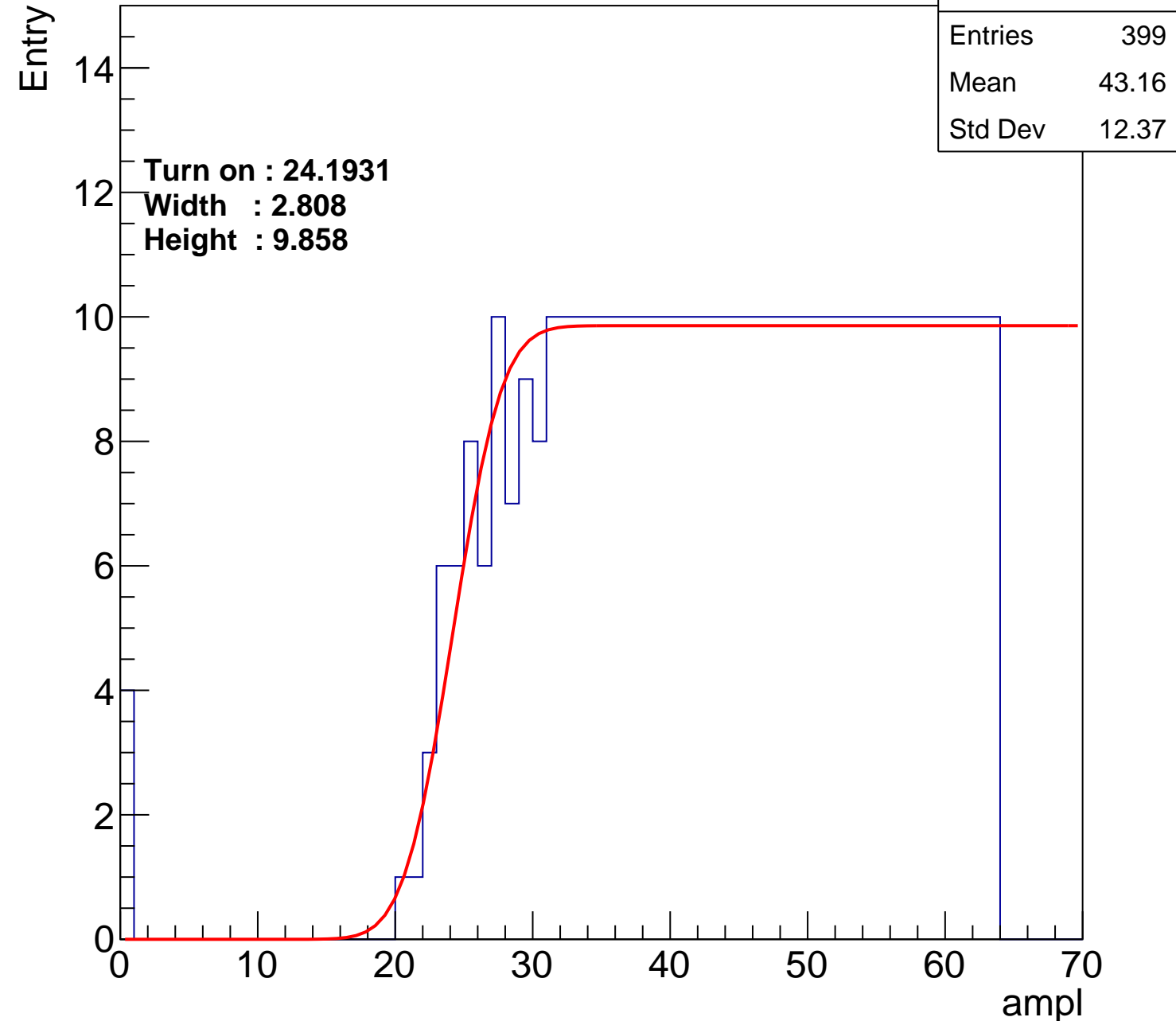
Width : 2.808

Height : 9.858

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch121

calib_packv5_042523_0143.root, FC#7, port C2

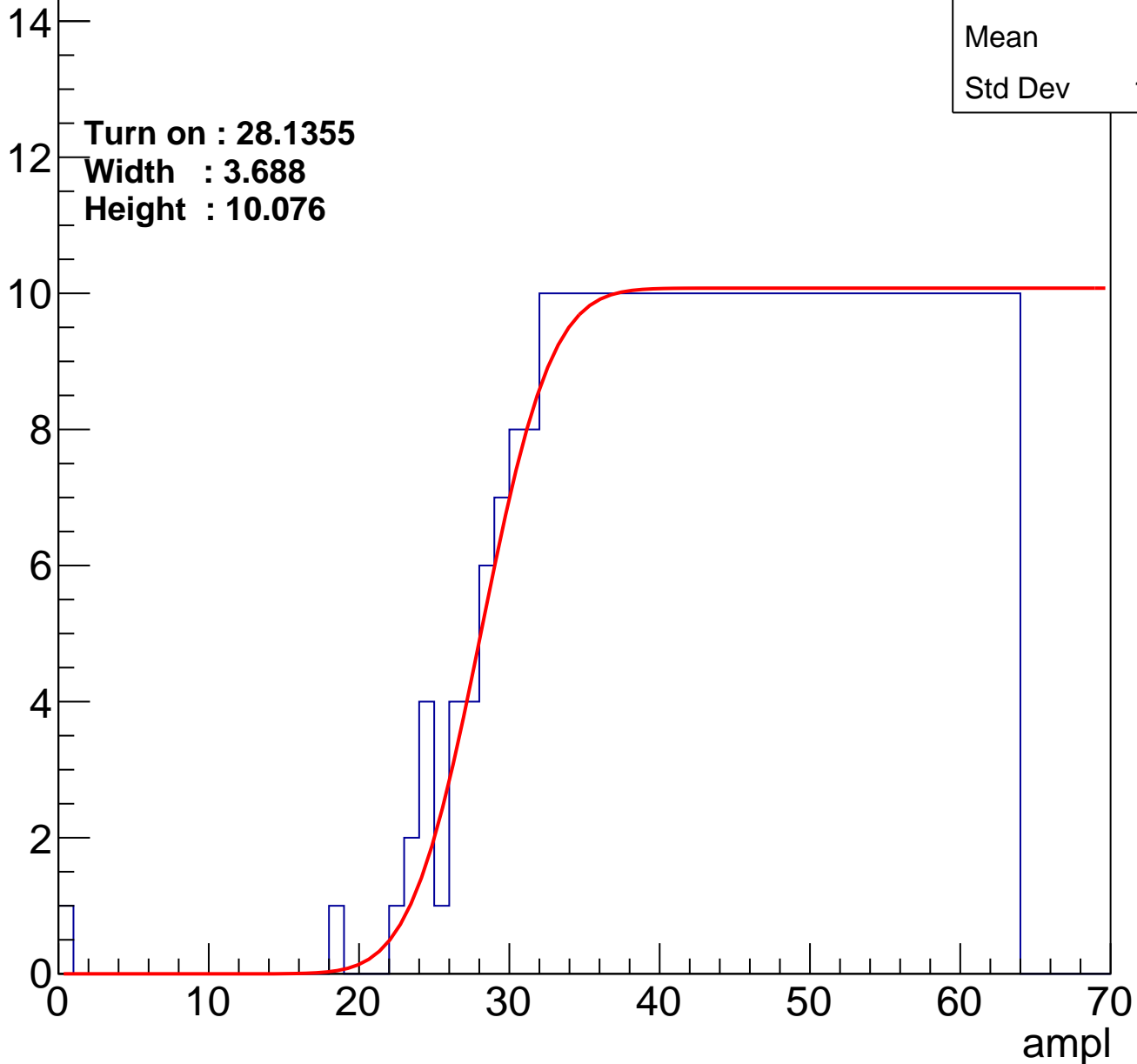
Entry

Entries	367
Mean	44.9
Std Dev	11.11

Turn on : 28.1355

Width : 3.688

Height : 10.076



B1L103S, U9-ch122

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.21
Std Dev	11.43

Turn on : 26.5012

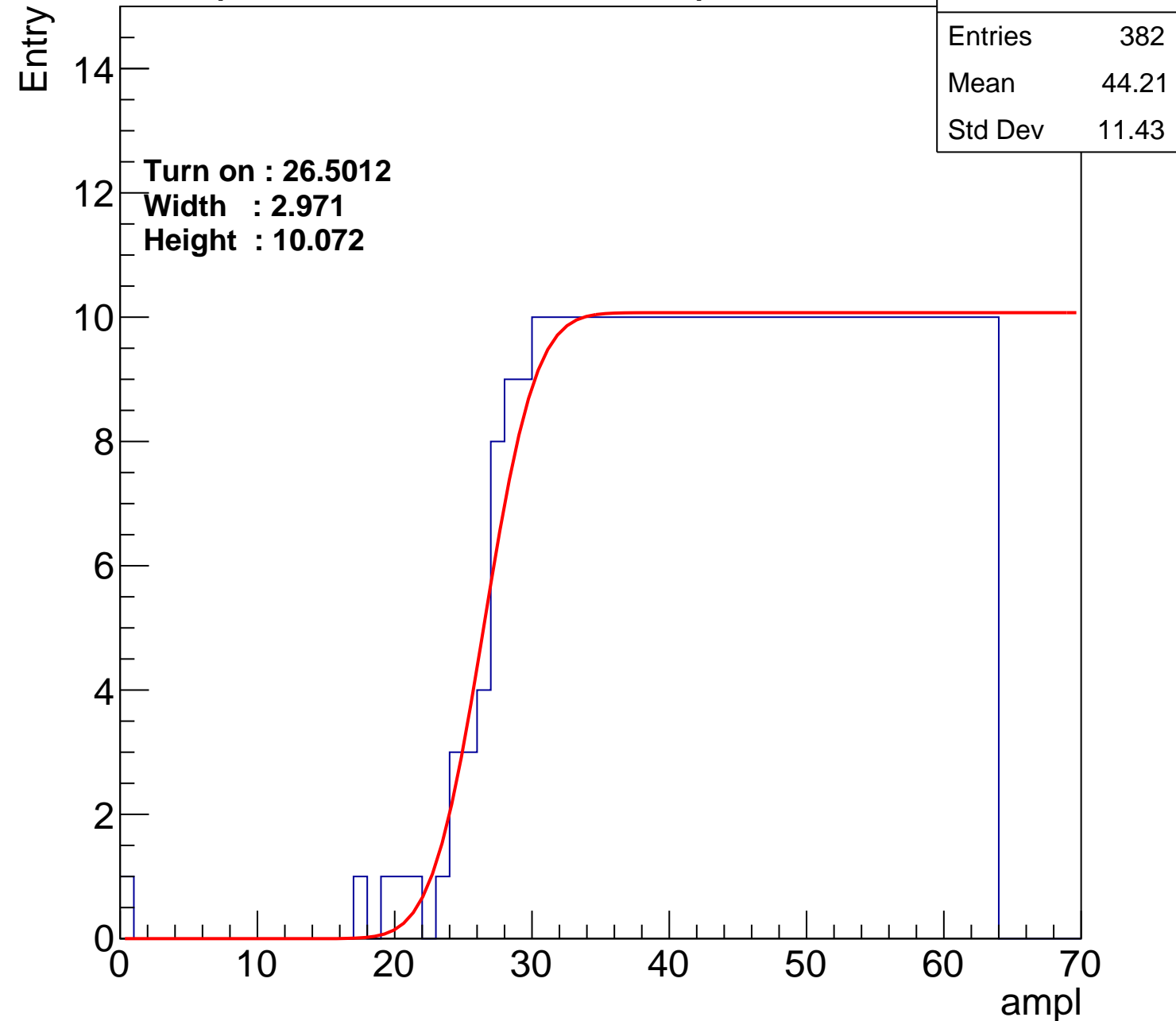
Width : 2.971

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch123

calib_packv5_042523_0143.root, FC#7, port C2

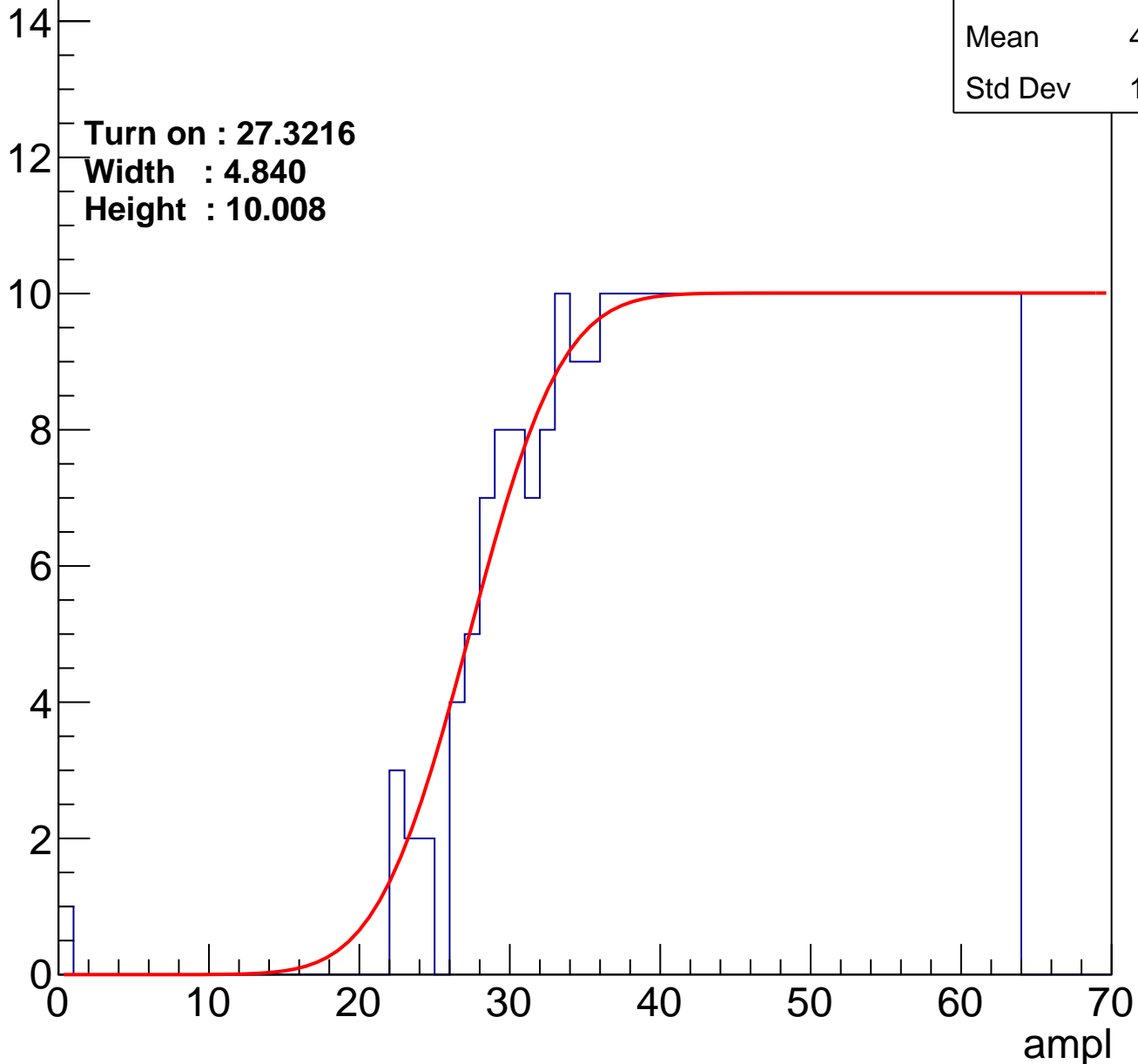
Entries	363
Mean	45.04
Std Dev	11.07

Turn on : 27.3216

Width : 4.840

Height : 10.008

Entry



B1L103S, U9-ch124

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.58
Std Dev	11.97

Turn on : 25.5648

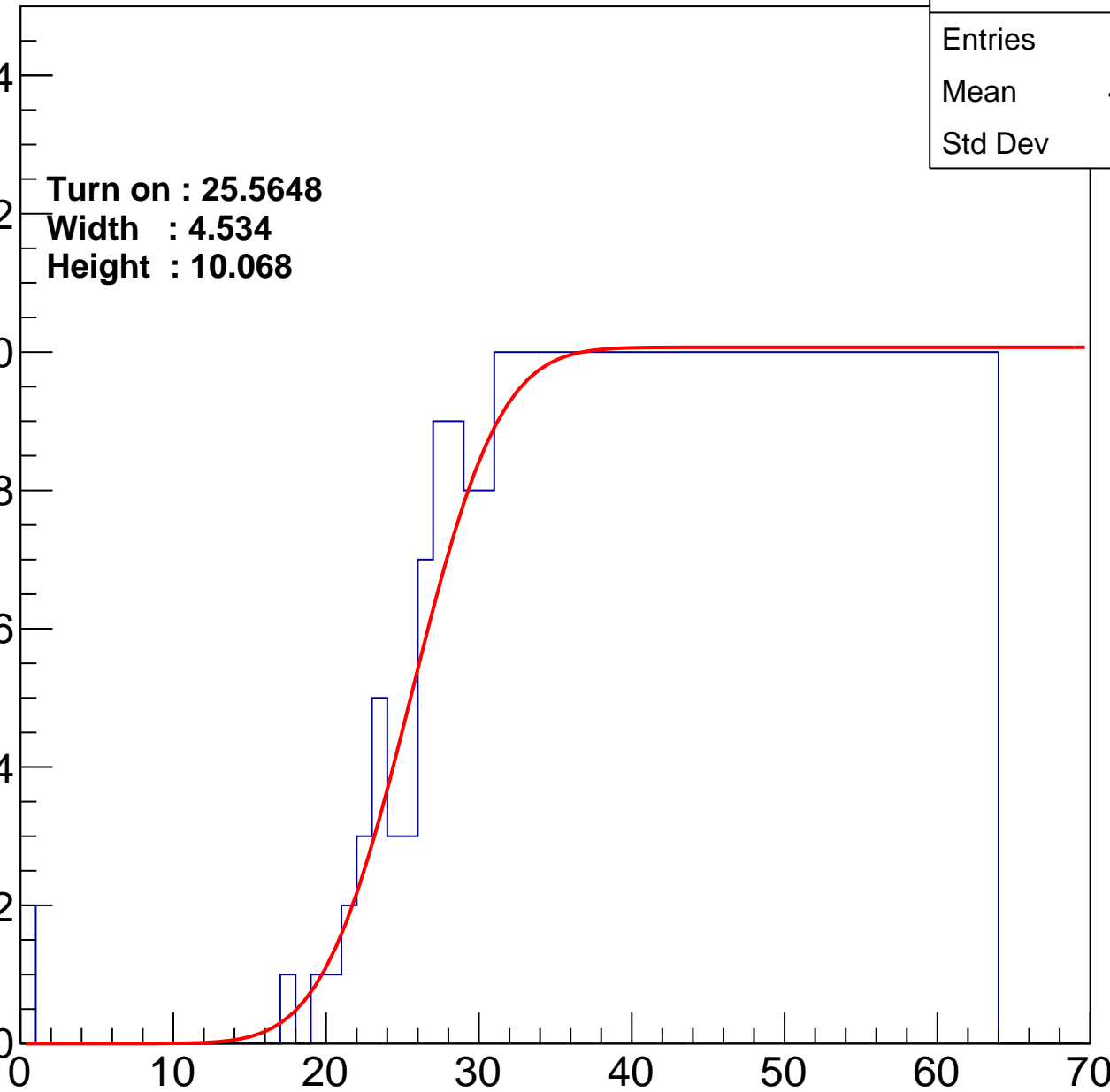
Width : 4.534

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch125

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.76
Std Dev	11.13

Turn on : 27.7907

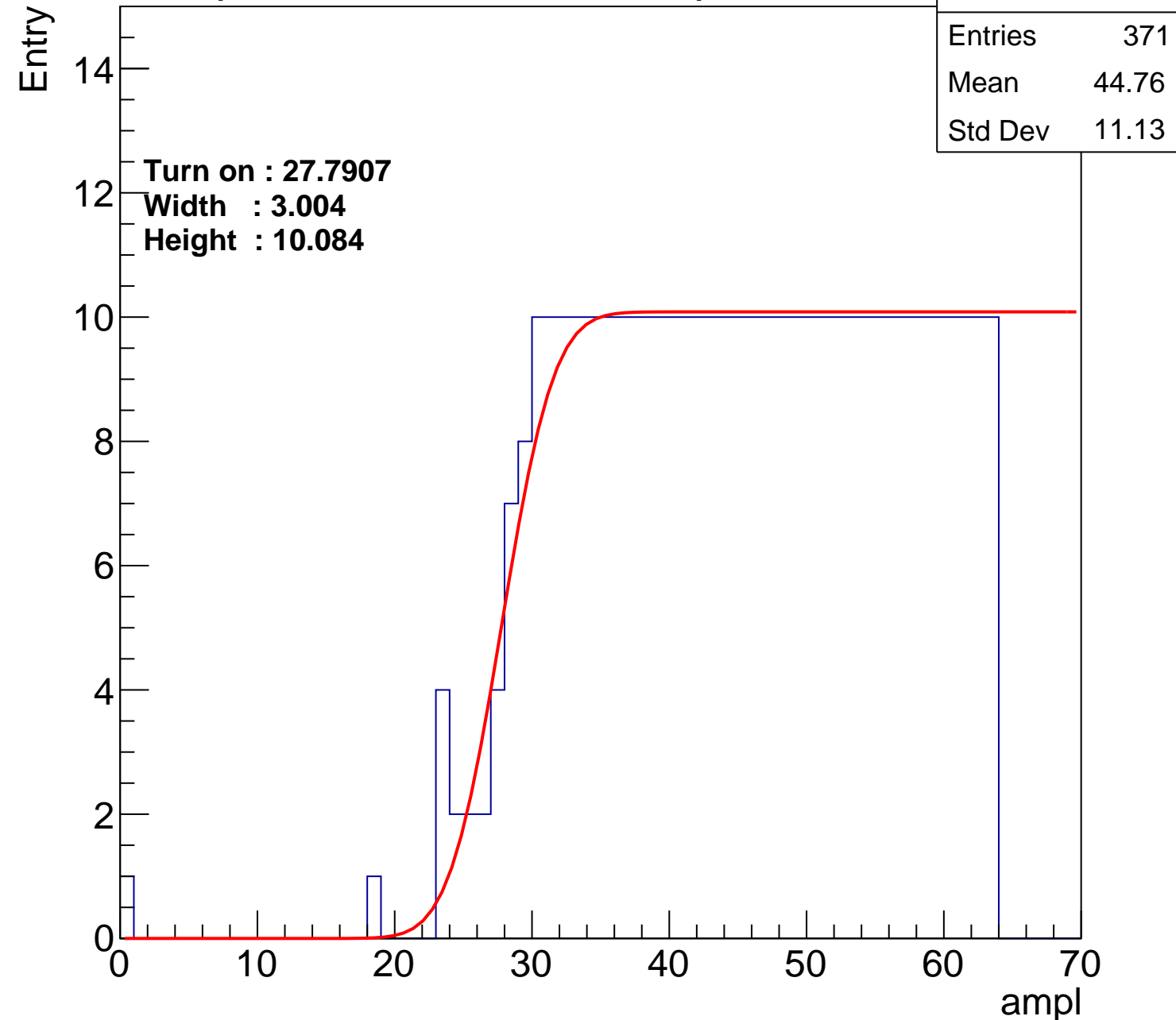
Width : 3.004

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch126

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.88
Std Dev	11.76

Turn on : 25.3584

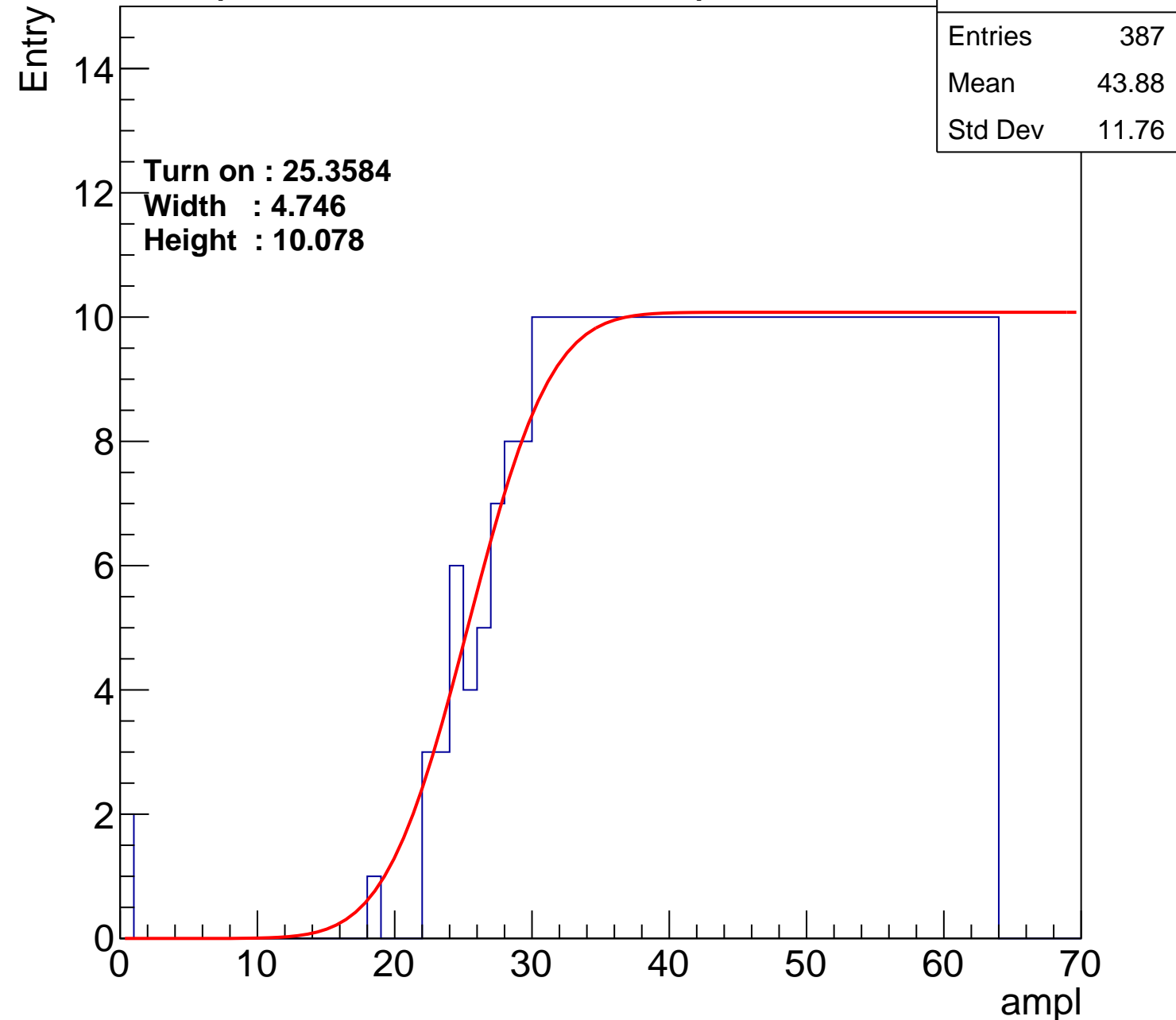
Width : 4.746

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U9-ch127

calib_packv5_042523_0143.root, FC#7, port C2

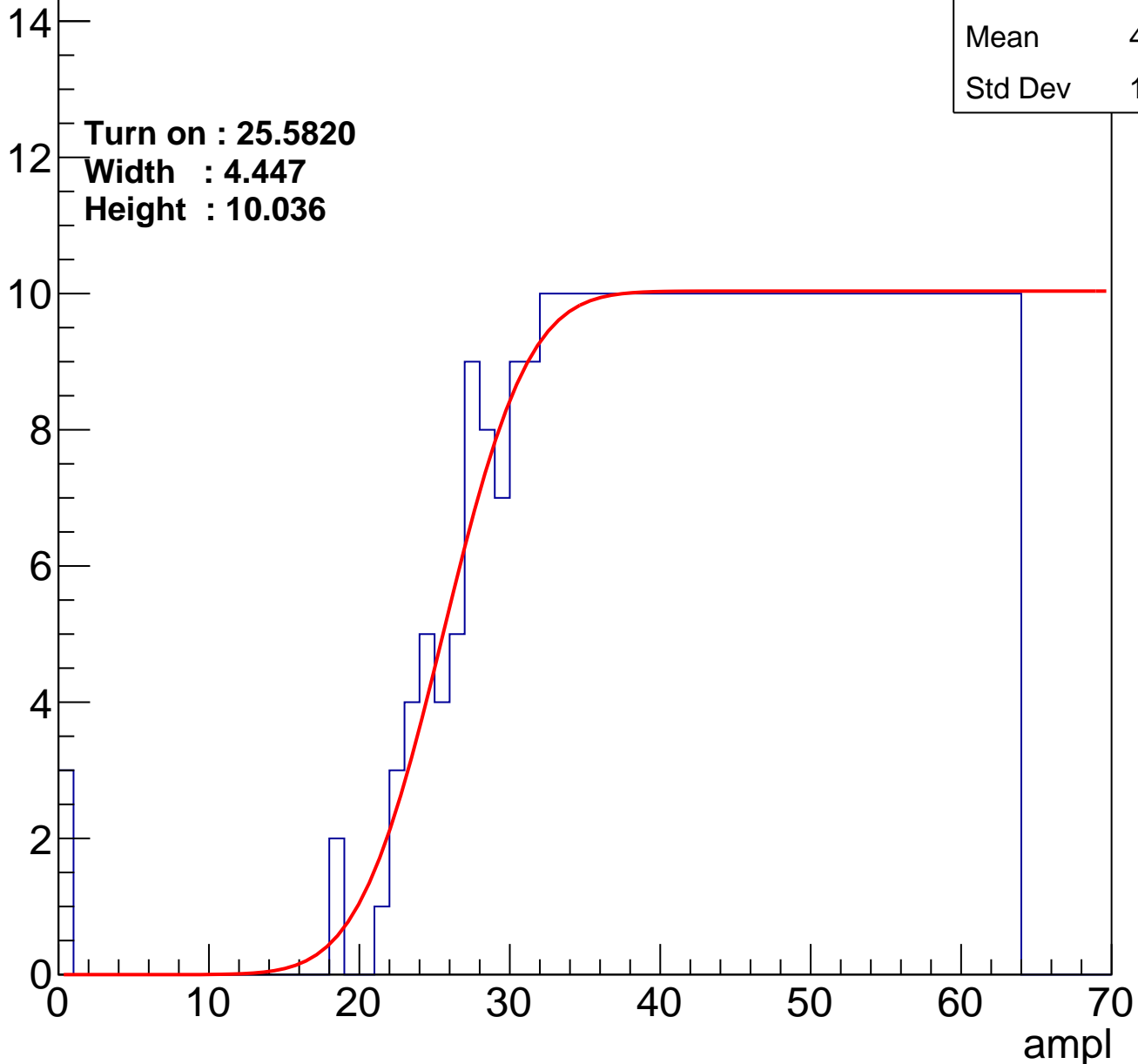
Entries	389
Mean	43.66
Std Dev	12.07

Turn on : 25.5820

Width : 4.447

Height : 10.036

Entry



B1L103S, U9-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.66
Std Dev	12.07

Turn on : 25.5820

Width : 4.447

Height : 10.036

Entry

