

B0L002S, U13-ch0

calib_packv5_042523_0143.root, FC#8, port C1

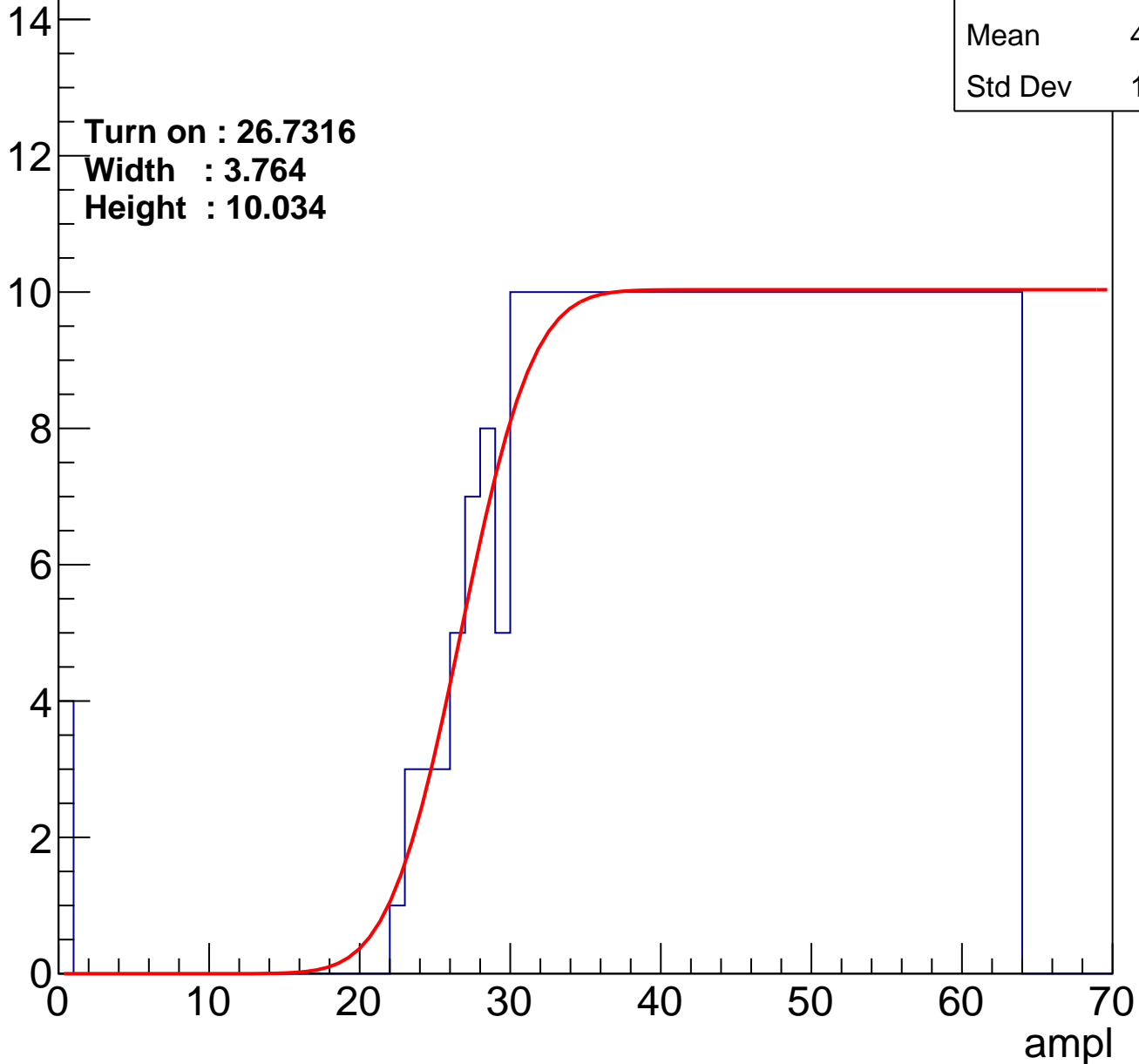
Entries	379
Mean	44.16
Std Dev	11.88

Turn on : 26.7316

Width : 3.764

Height : 10.034

Entry



B0L002S, U13-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.66
Std Dev	11.2

Turn on : 30.2922

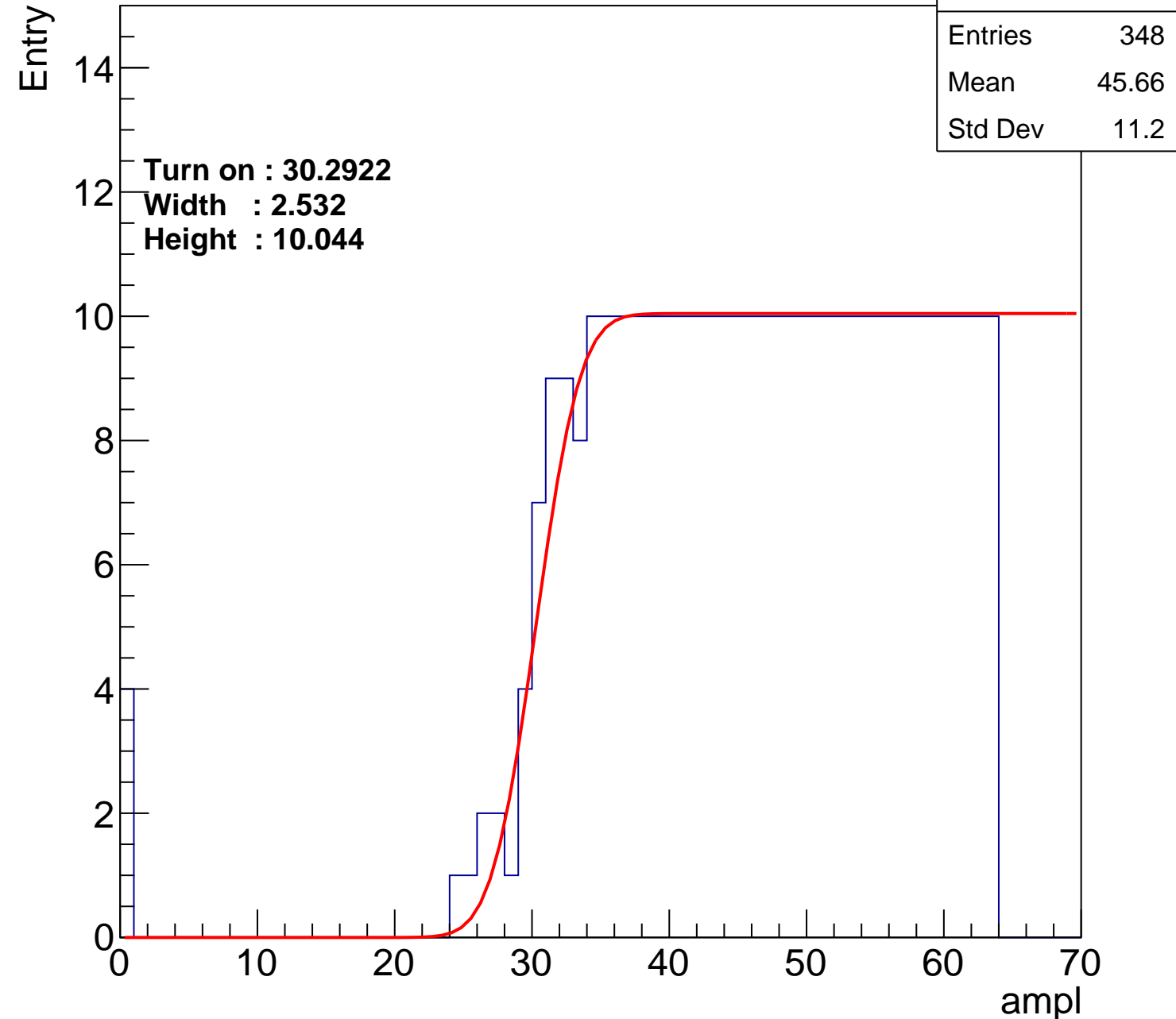
Width : 2.532

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.54
Std Dev	11.07

Turn on : 29.4889

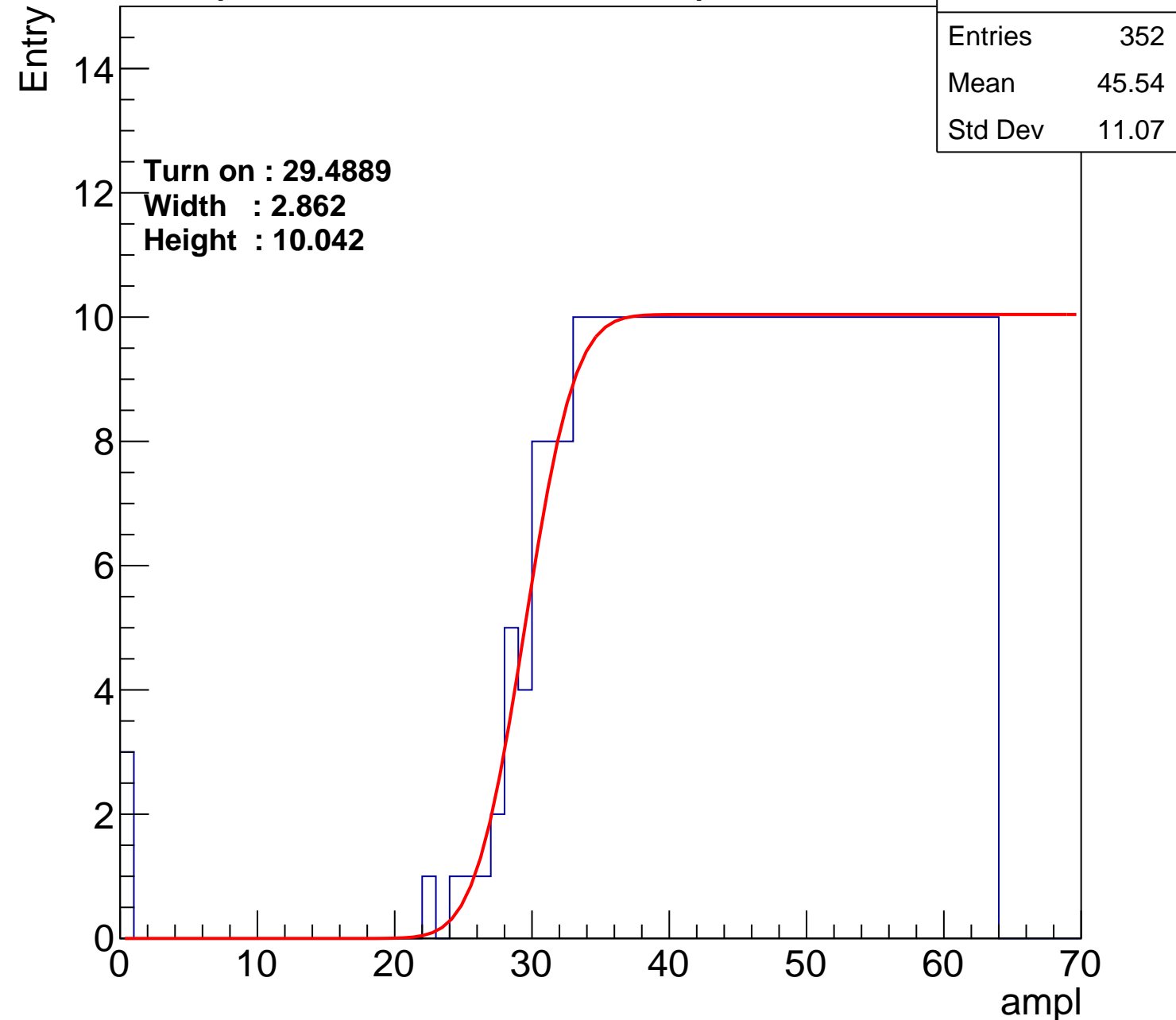
Width : 2.862

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
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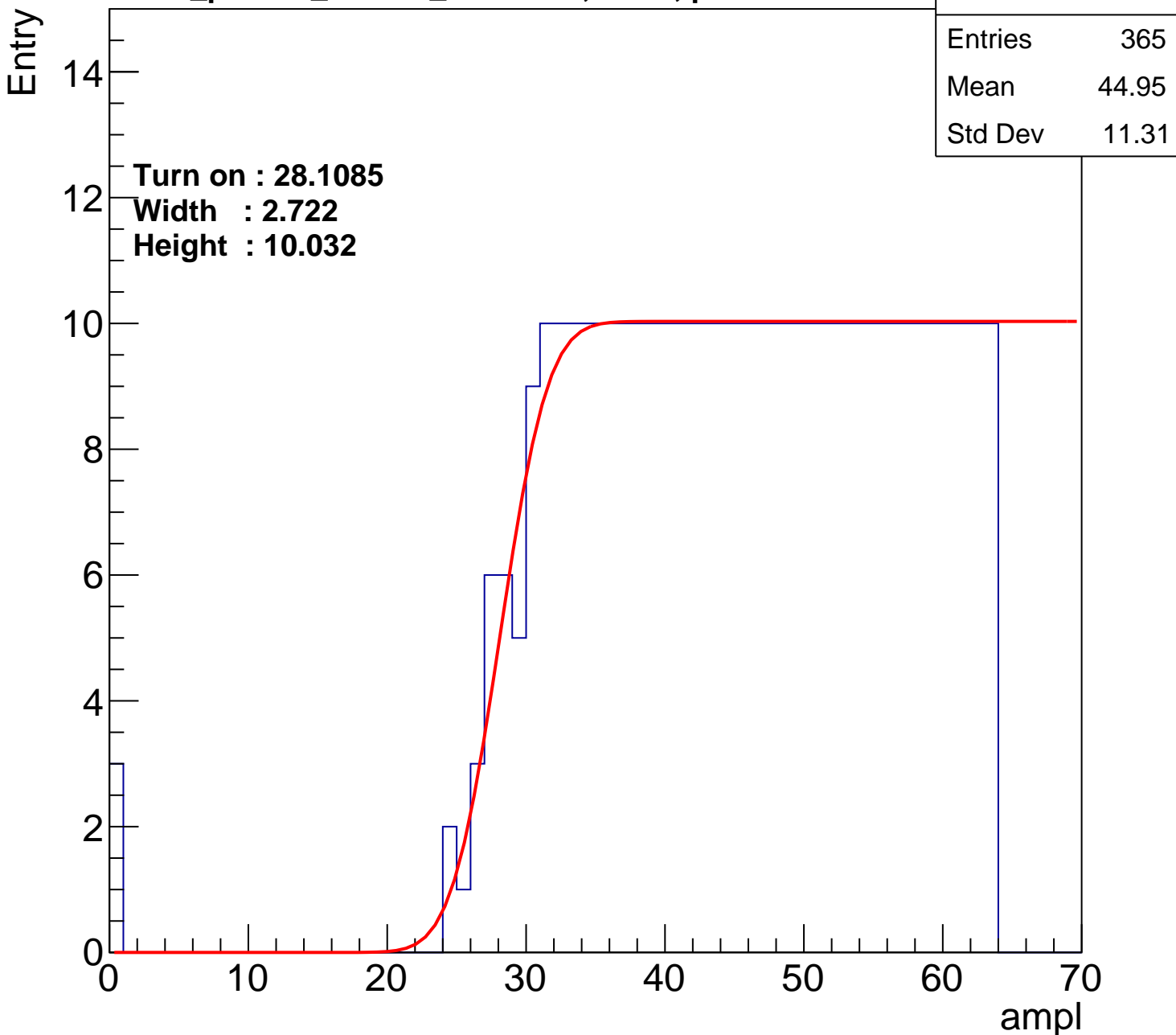
Mean	44.95
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Std Dev	11.31
---------	-------

Turn on : 28.1085

Width : 2.722

Height : 10.032



B0L002S, U13-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.81
Std Dev	10.48

Turn on : 29.3428

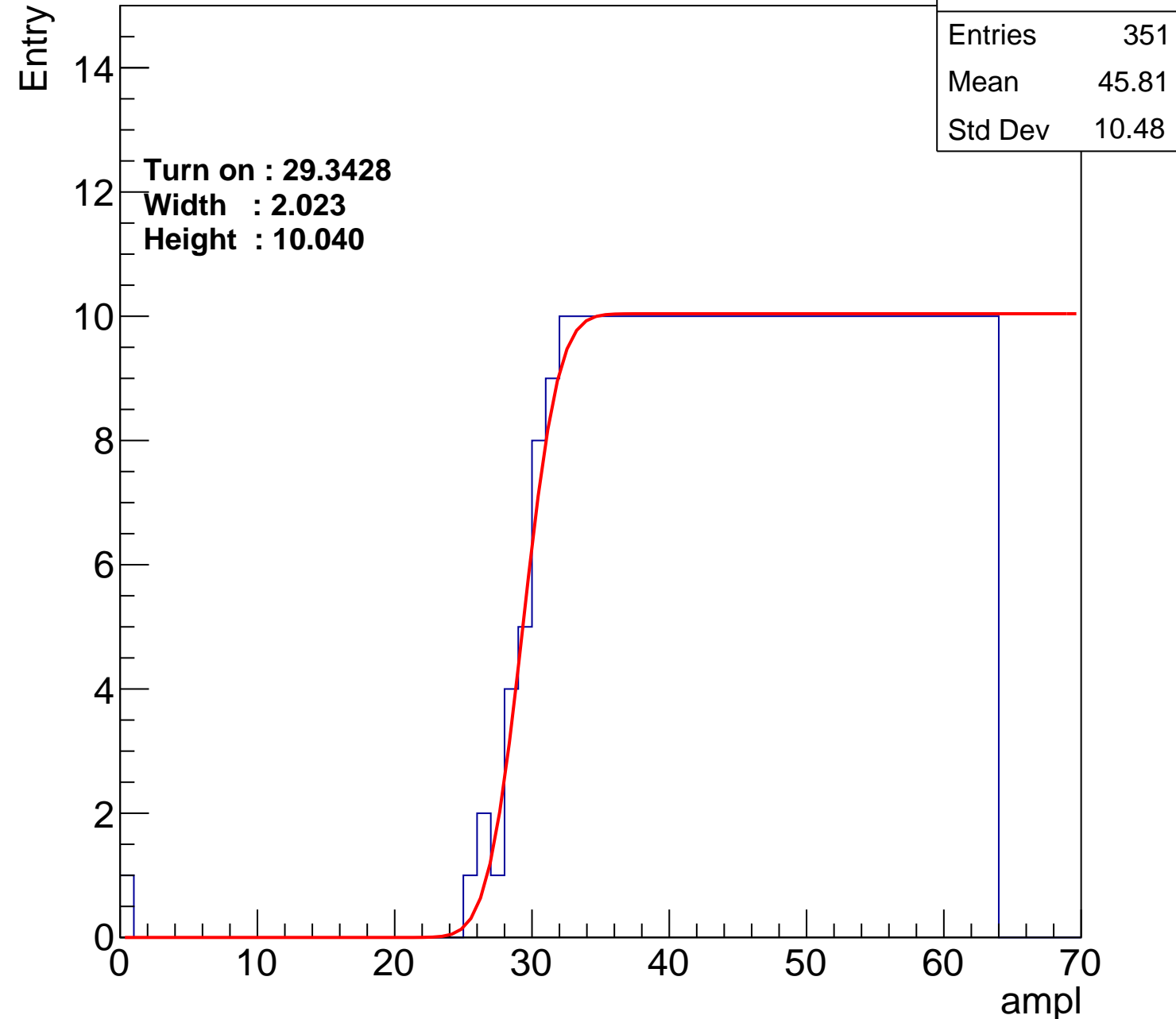
Width : 2.023

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch5

calib_packv5_042523_0143.root, FC#8, port C1

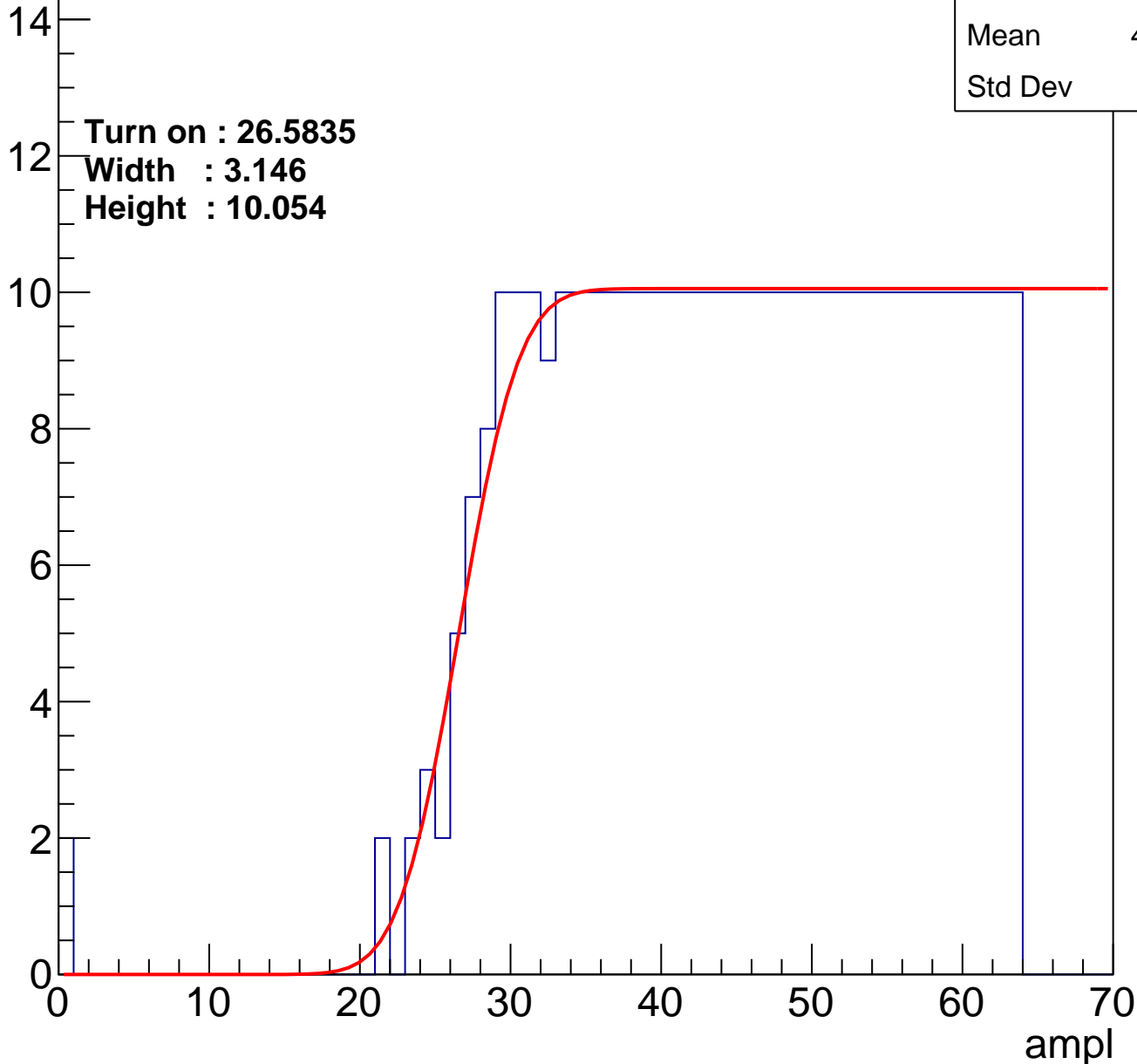
Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.5835

Width : 3.146

Height : 10.054

Entry



B0L002S, U13-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.72
Std Dev	11.12

Turn on : 27.0170

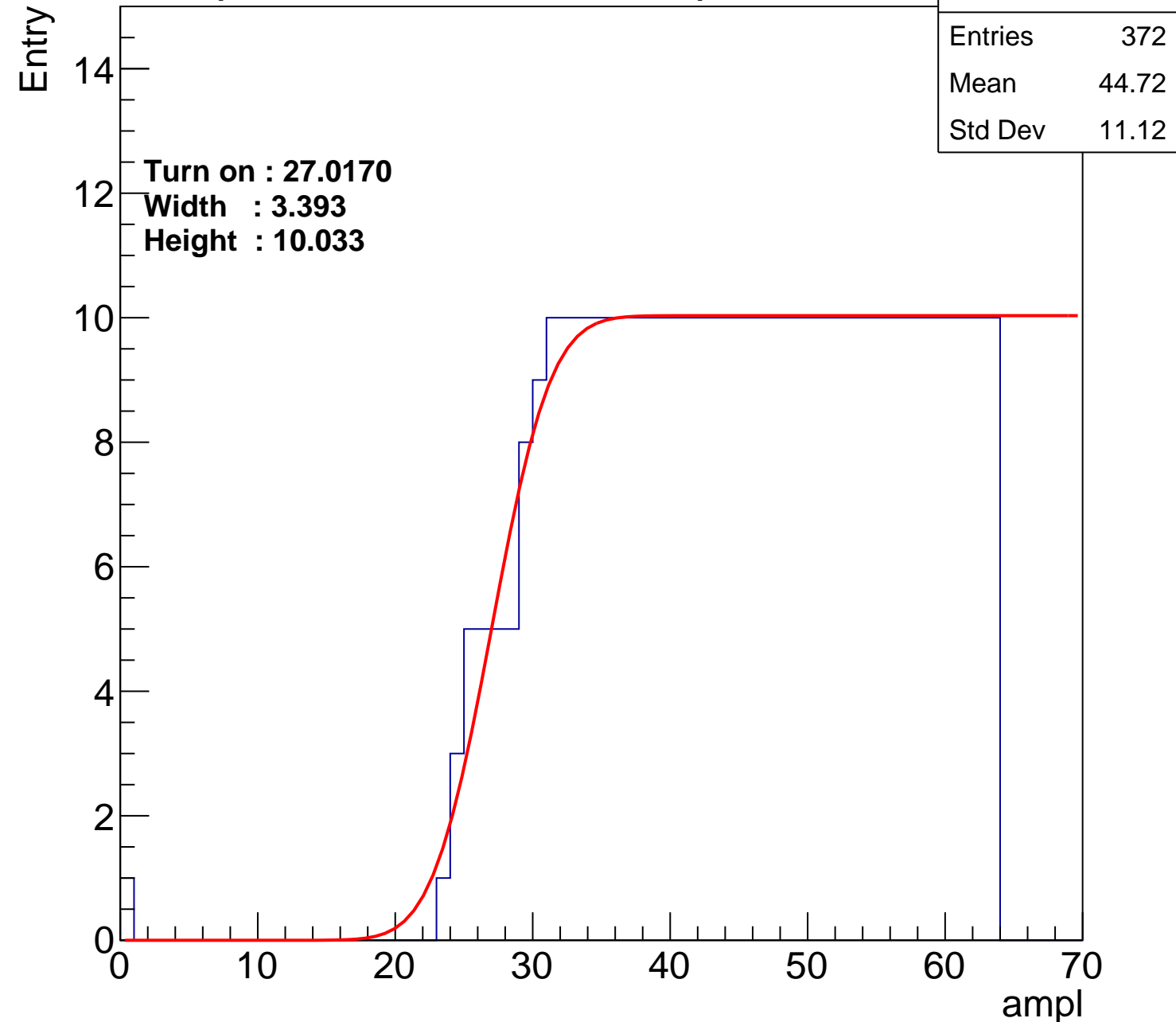
Width : 3.393

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.88
Std Dev	10.69

Turn on : 28.7652

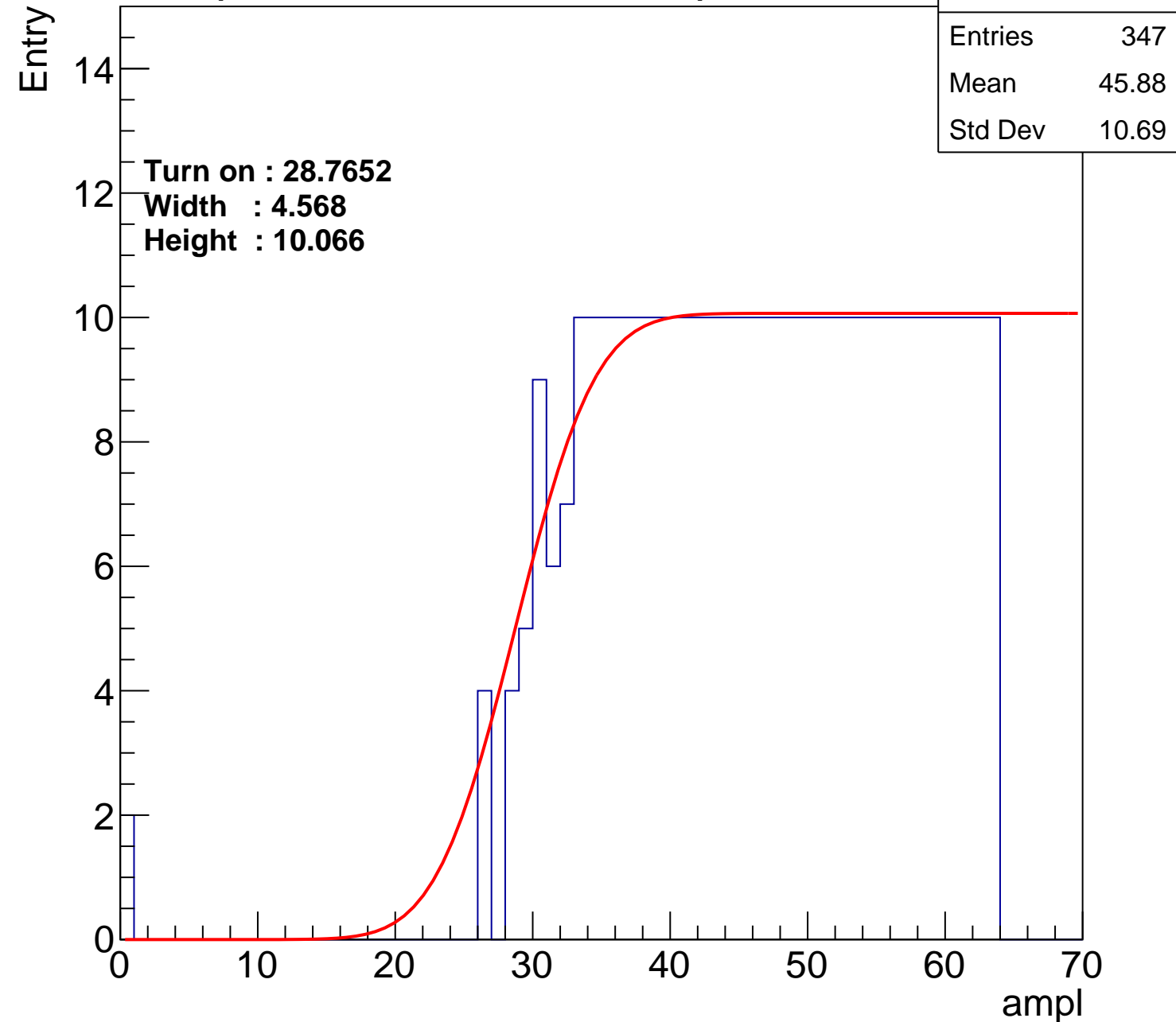
Width : 4.568

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.01
Std Dev	10.64

Turn on : 29.7272

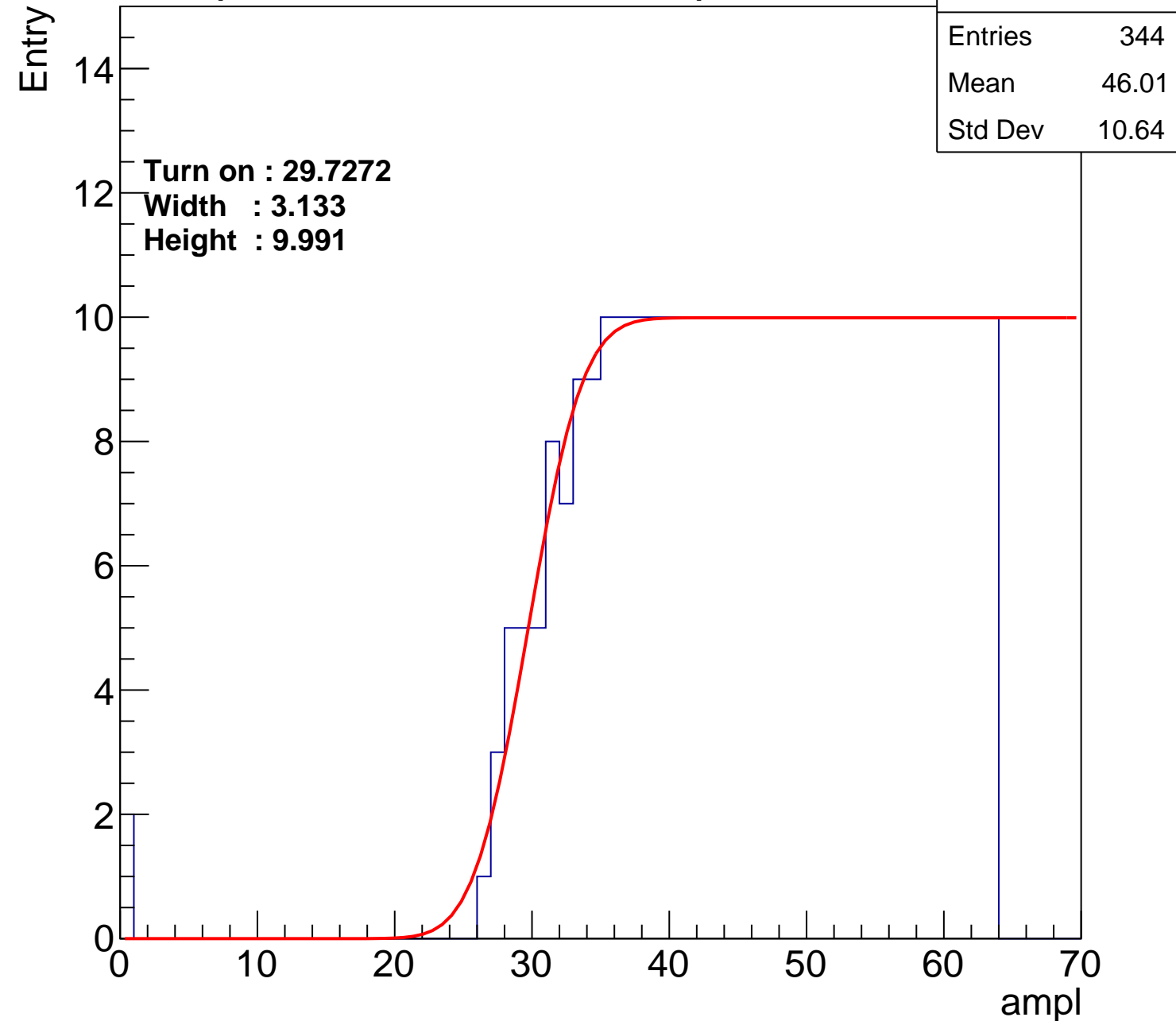
Width : 3.133

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch9

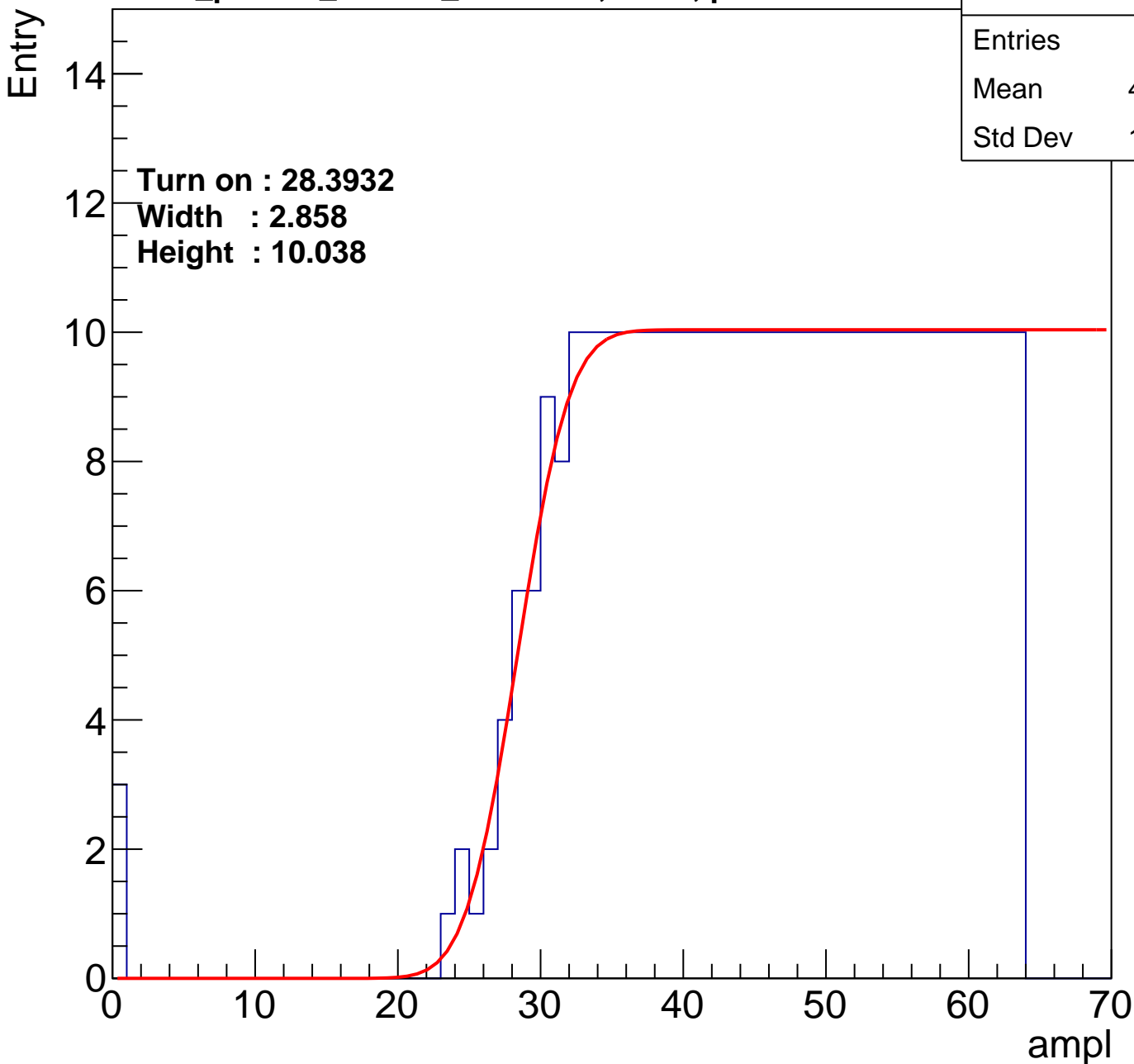
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.3932

Width : 2.858

Height : 10.038

Entries	362
Mean	45.07
Std Dev	11.27



B0L002S, U13-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 27.3474

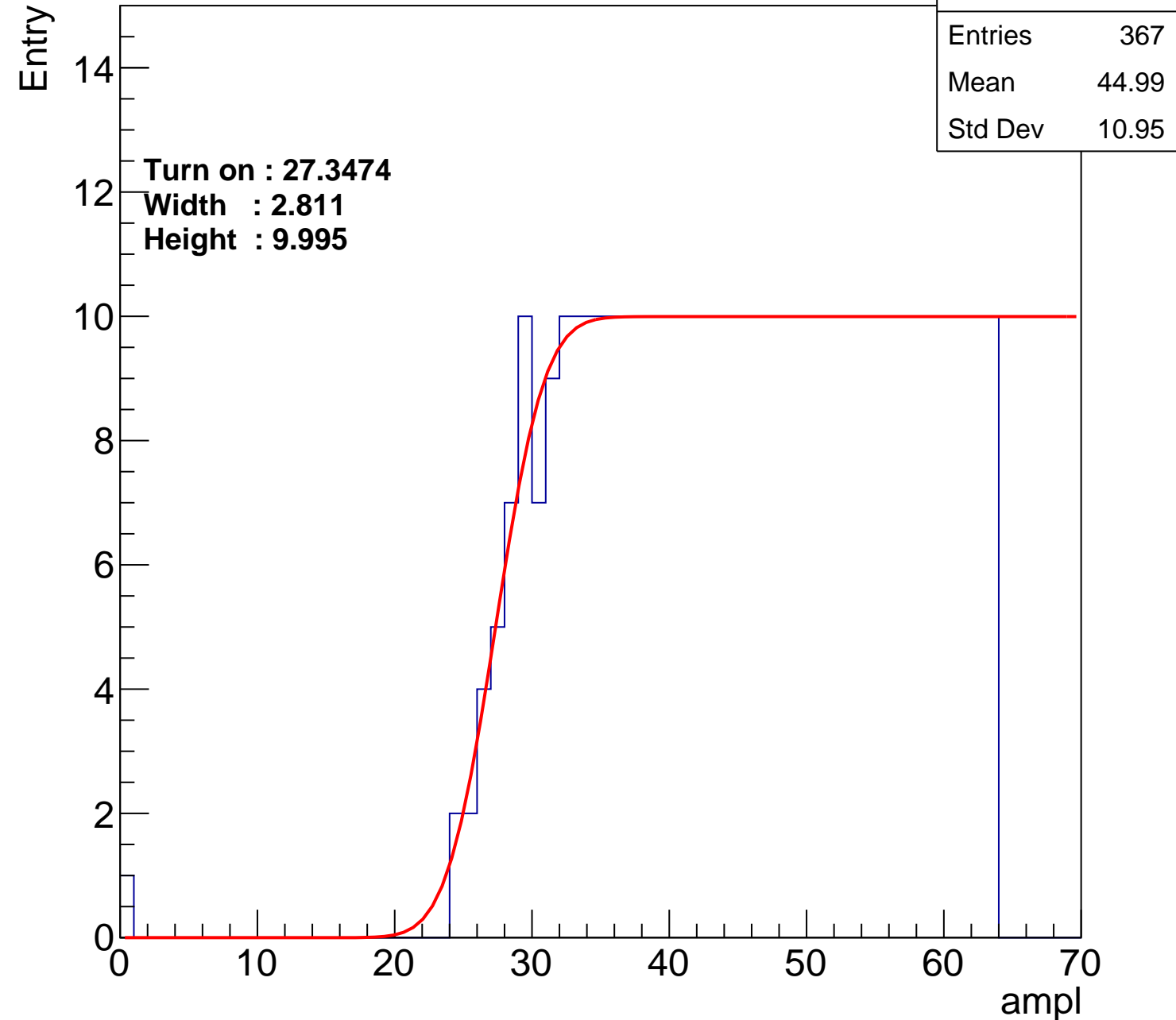
Width : 2.811

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.58
Std Dev	11.65

Turn on : 26.9751

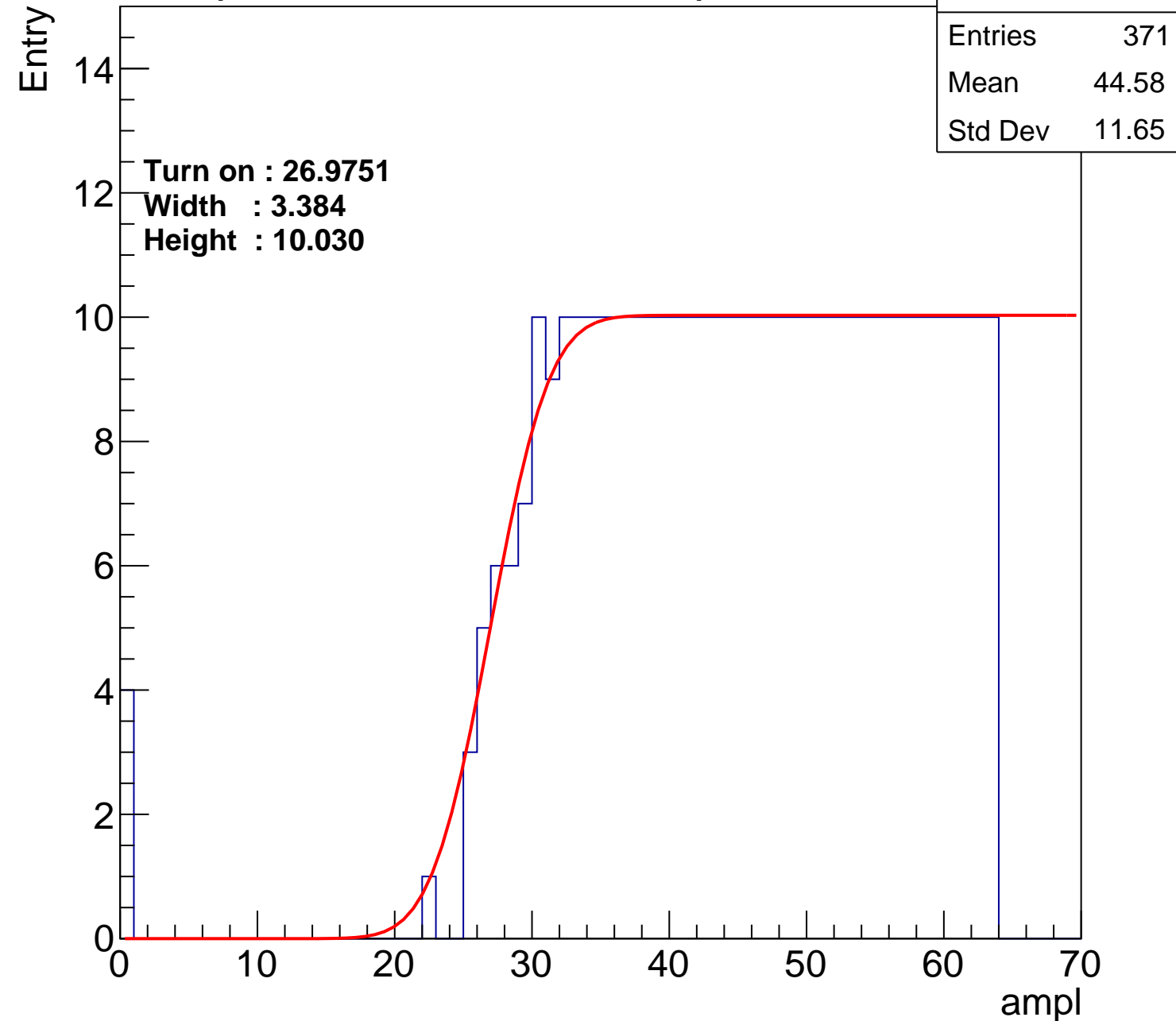
Width : 3.384

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.96
Std Dev	11.14

Turn on : 27.7114

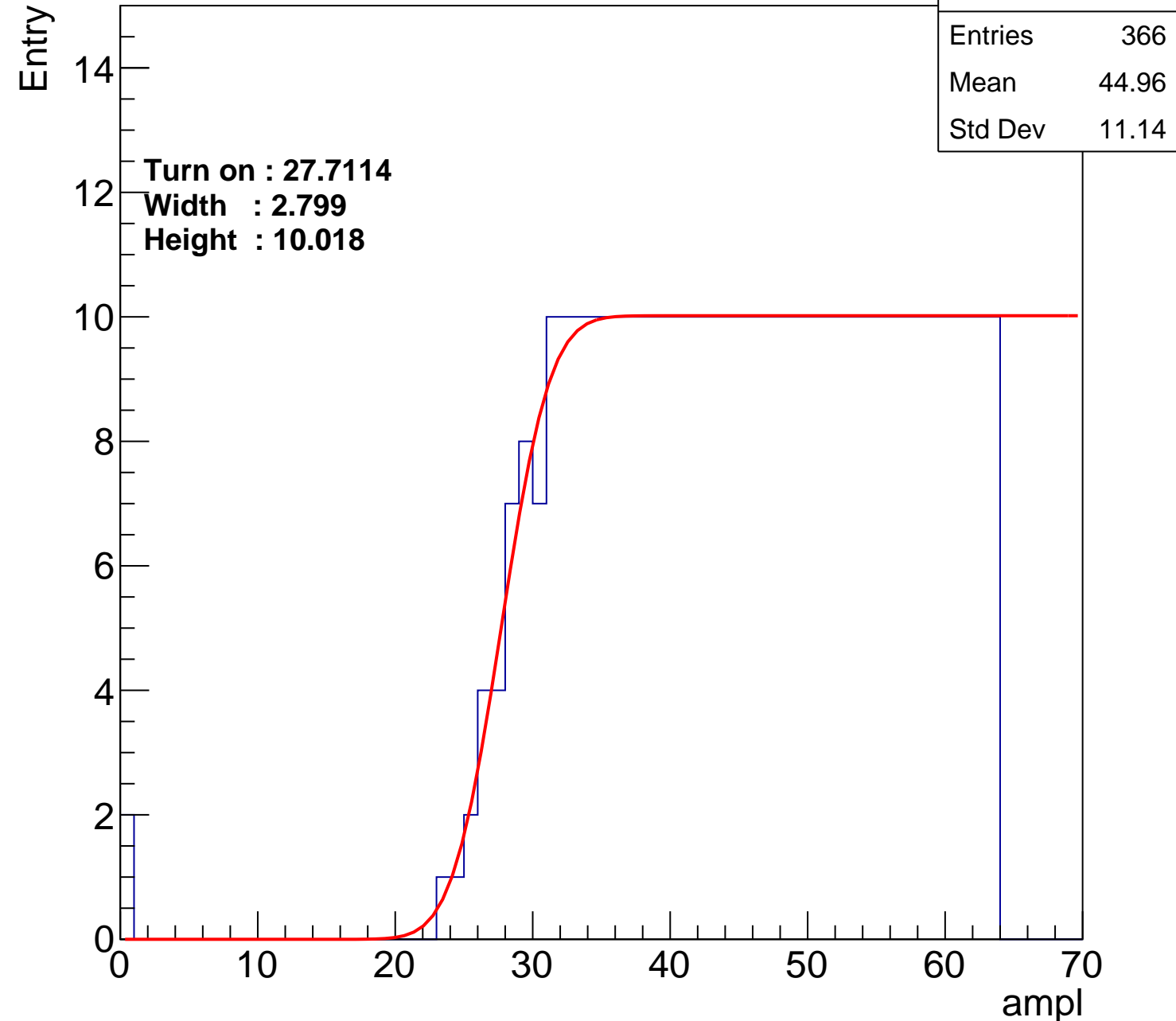
Width : 2.799

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch13

calib_packv5_042523_0143.root, FC#8, port C1

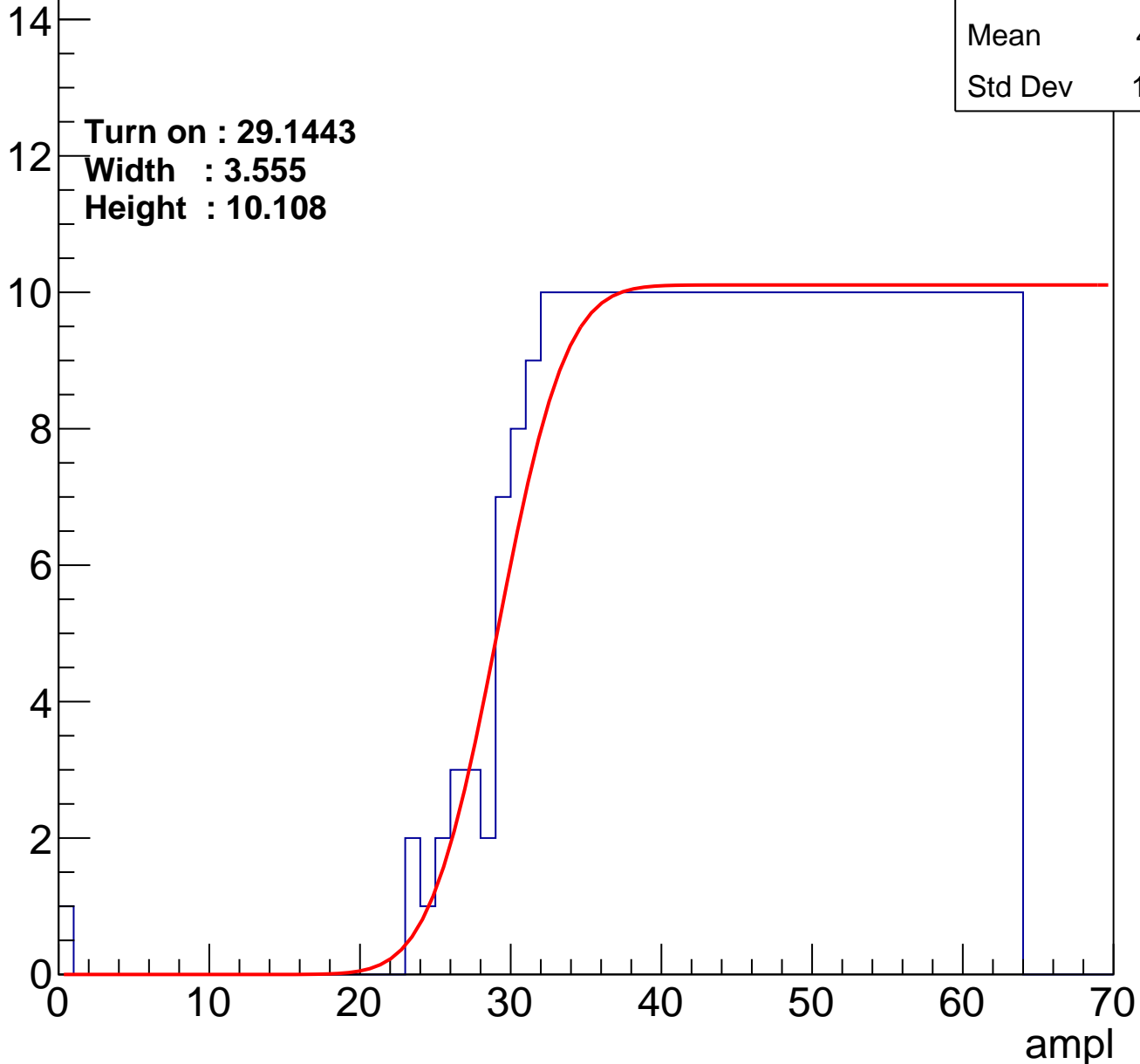
Entries	358
Mean	45.41
Std Dev	10.76

Turn on : 29.1443

Width : 3.555

Height : 10.108

Entry



B0L002S, U13-ch14

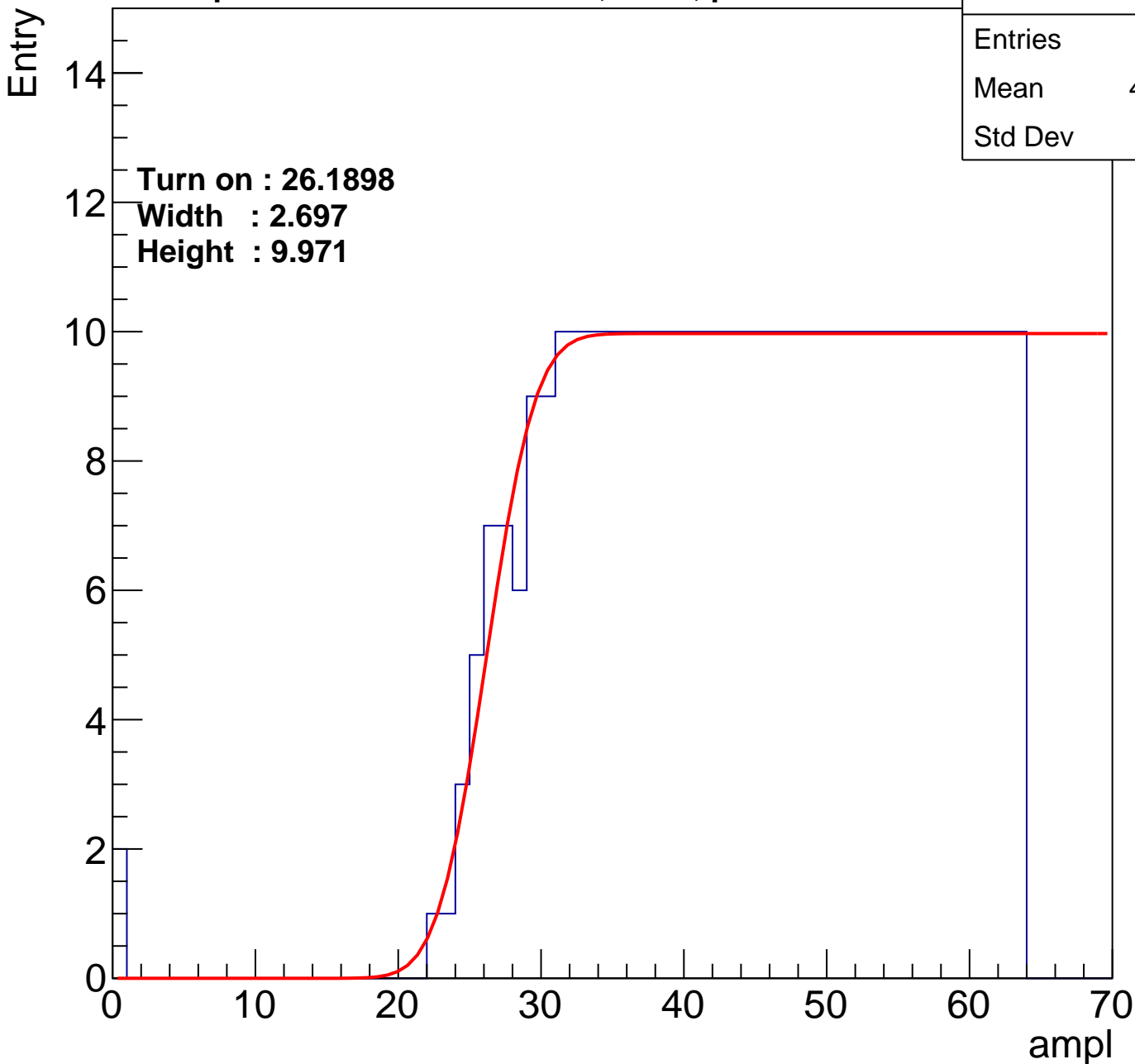
calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.1898

Width : 2.697

Height : 9.971



B0L002S, U13-ch15

calib_packv5_042523_0143.root, FC#8, port C1

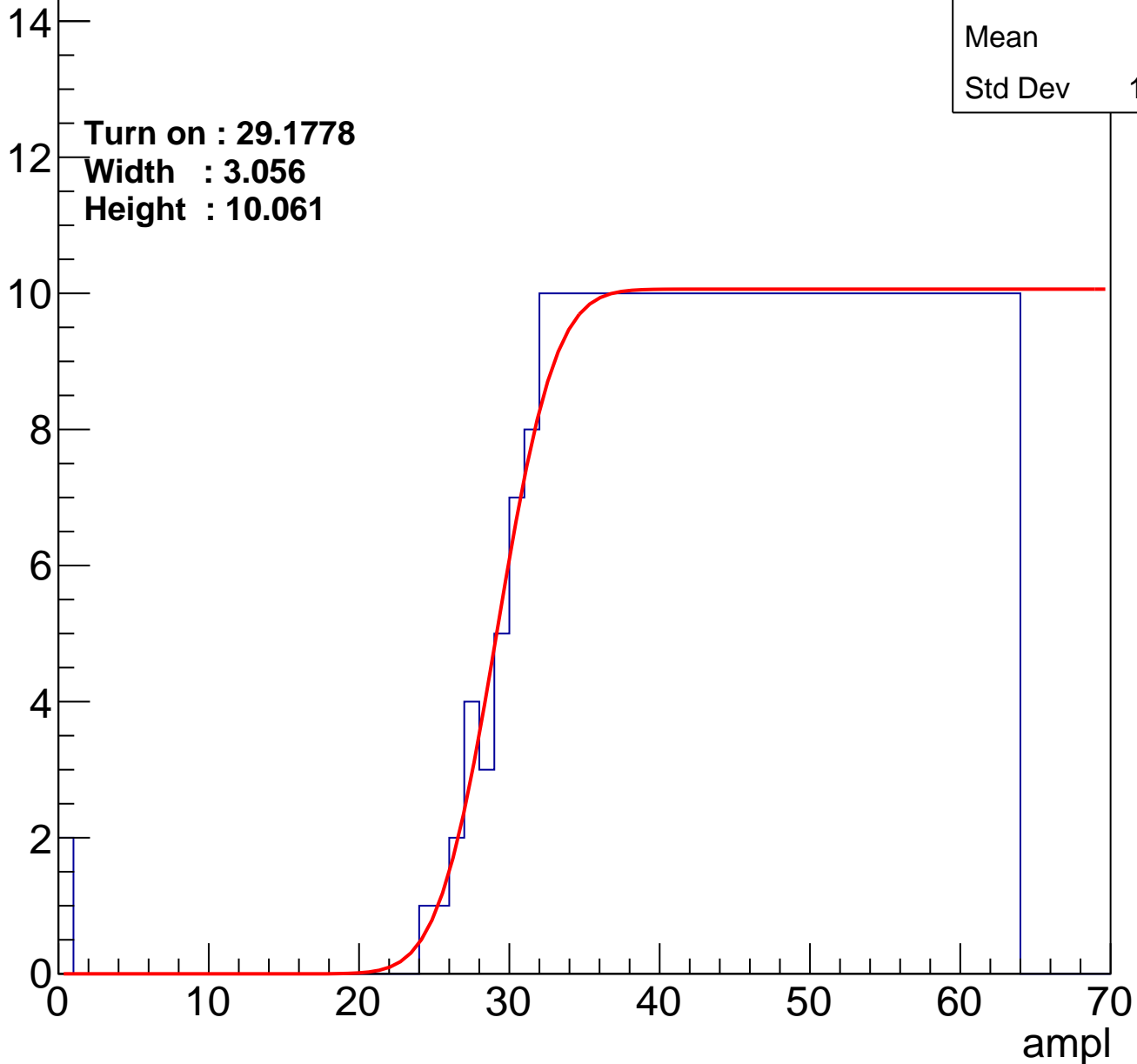
Entries	353
Mean	45.6
Std Dev	10.82

Turn on : 29.1778

Width : 3.056

Height : 10.061

Entry



B0L002S, U13-ch16

calib_packv5_042523_0143.root, FC#8, port C1

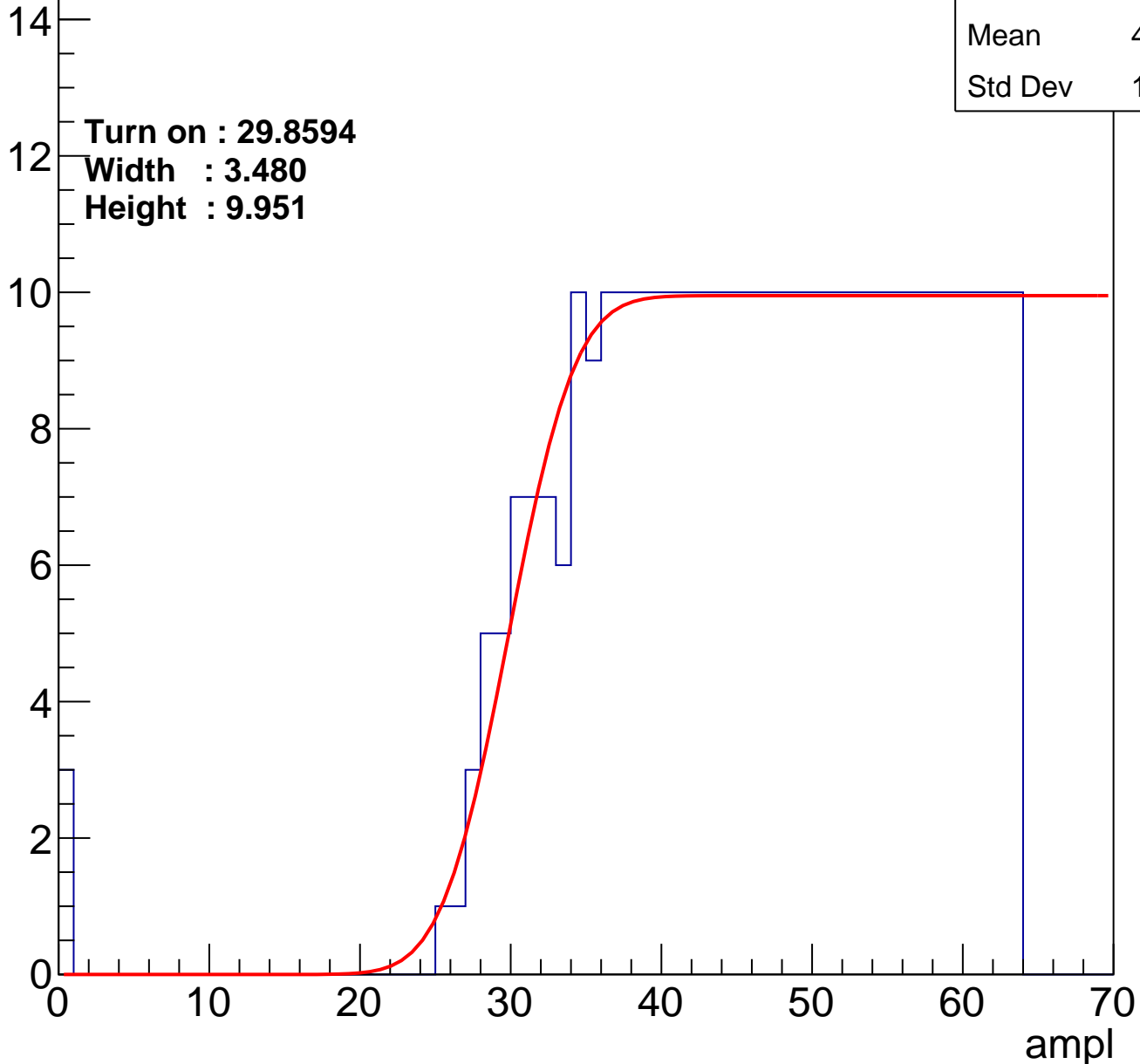
Entries	344
Mean	45.88
Std Dev	10.96

Turn on : 29.8594

Width : 3.480

Height : 9.951

Entry



B0L002S, U13-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	43.91
Std Dev	12.12

Turn on : 26.2523

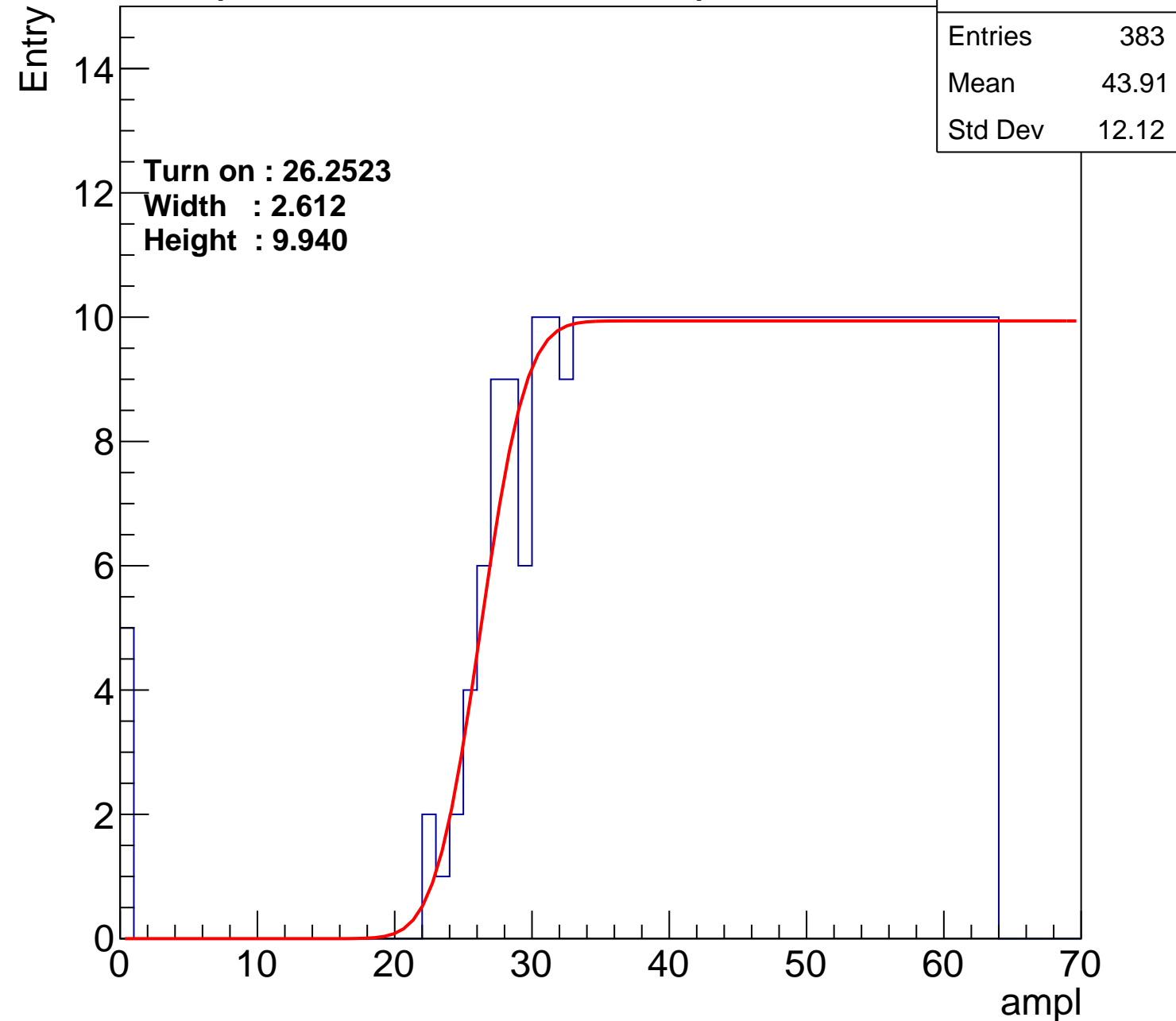
Width : 2.612

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.34
Std Dev	11.5

Turn on : 26.4748

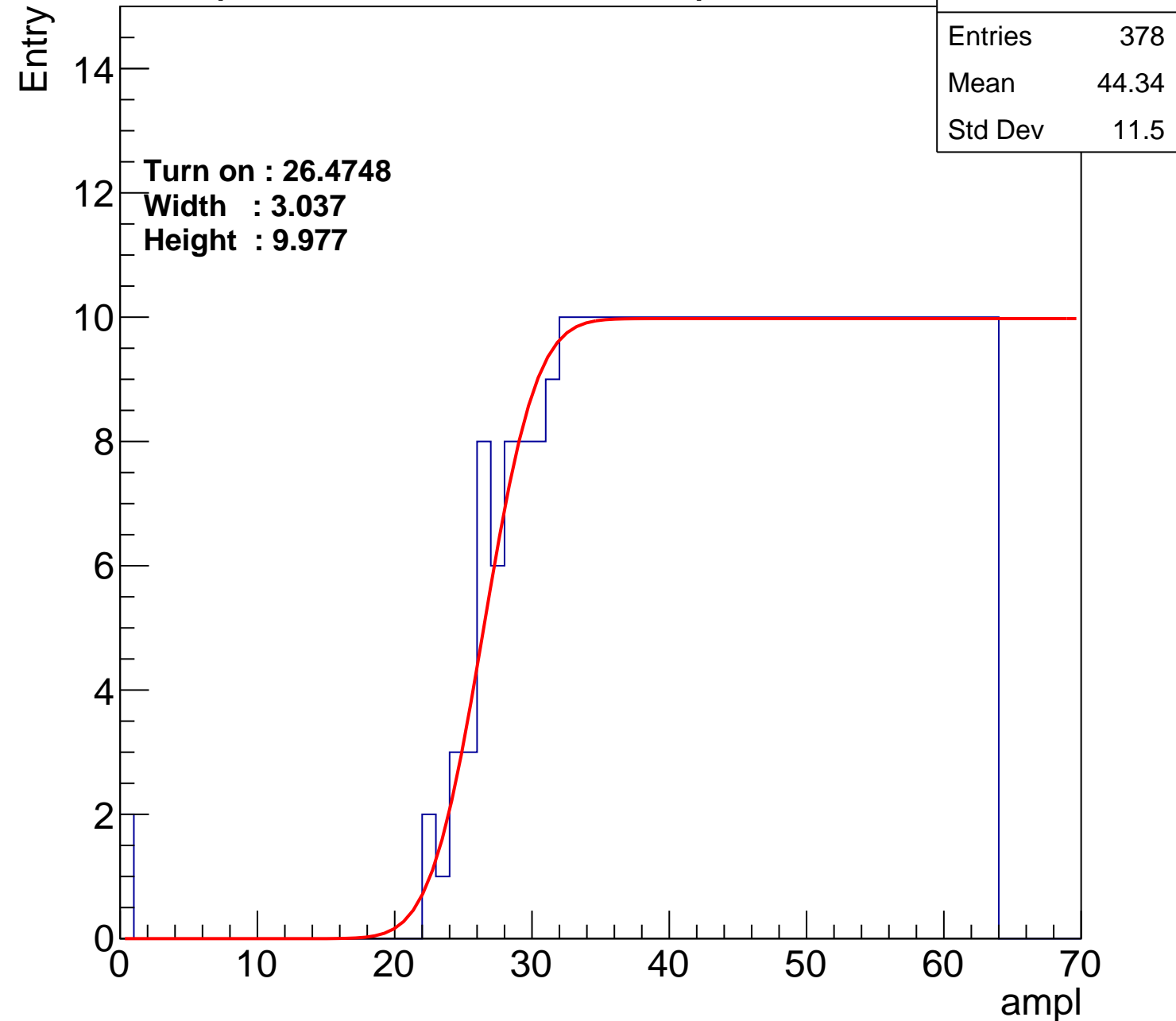
Width : 3.037

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.2
Std Dev	11.23

Turn on : 28.9586

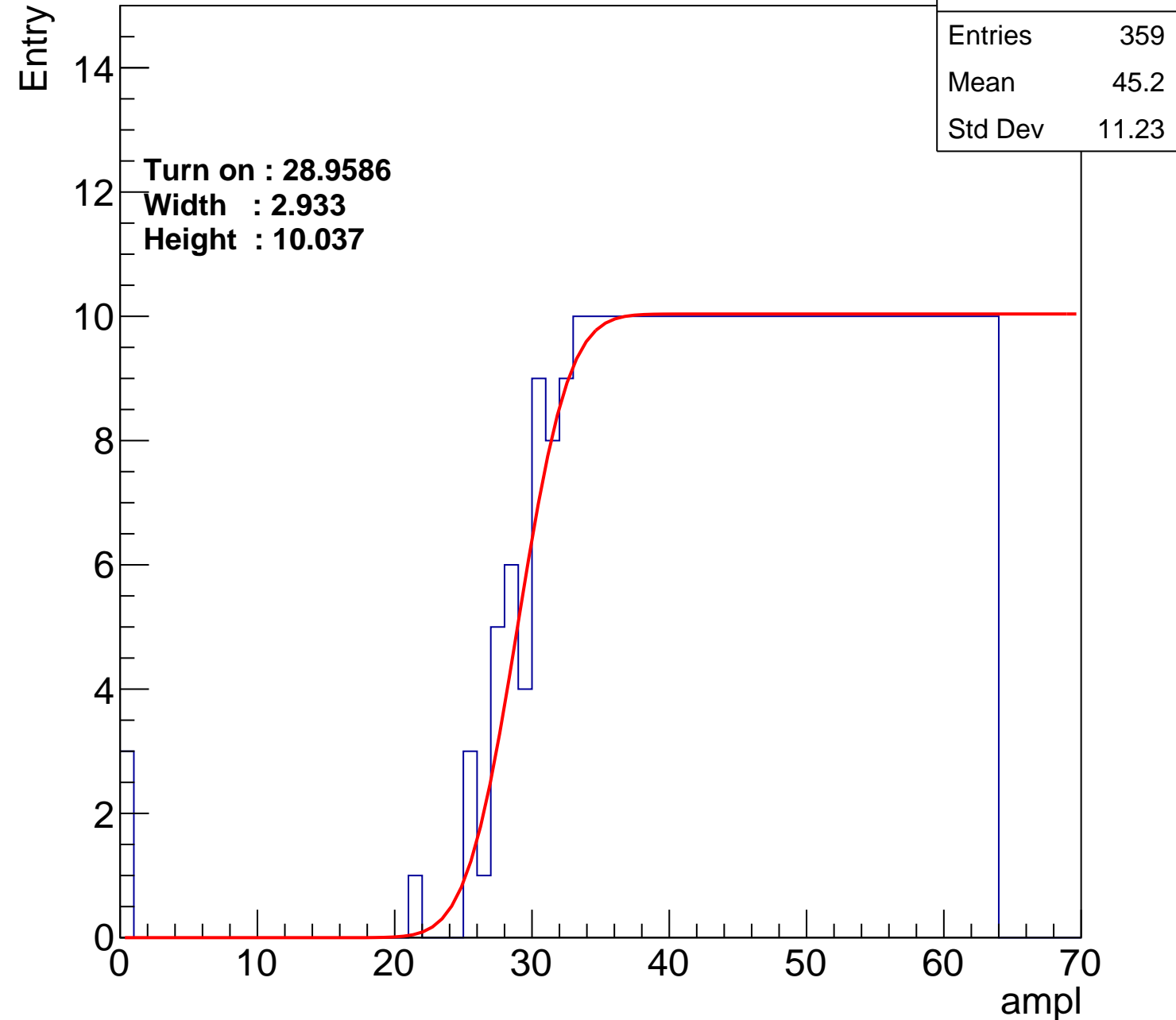
Width : 2.933

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.82
Std Dev	10.74

Turn on : 29.9732

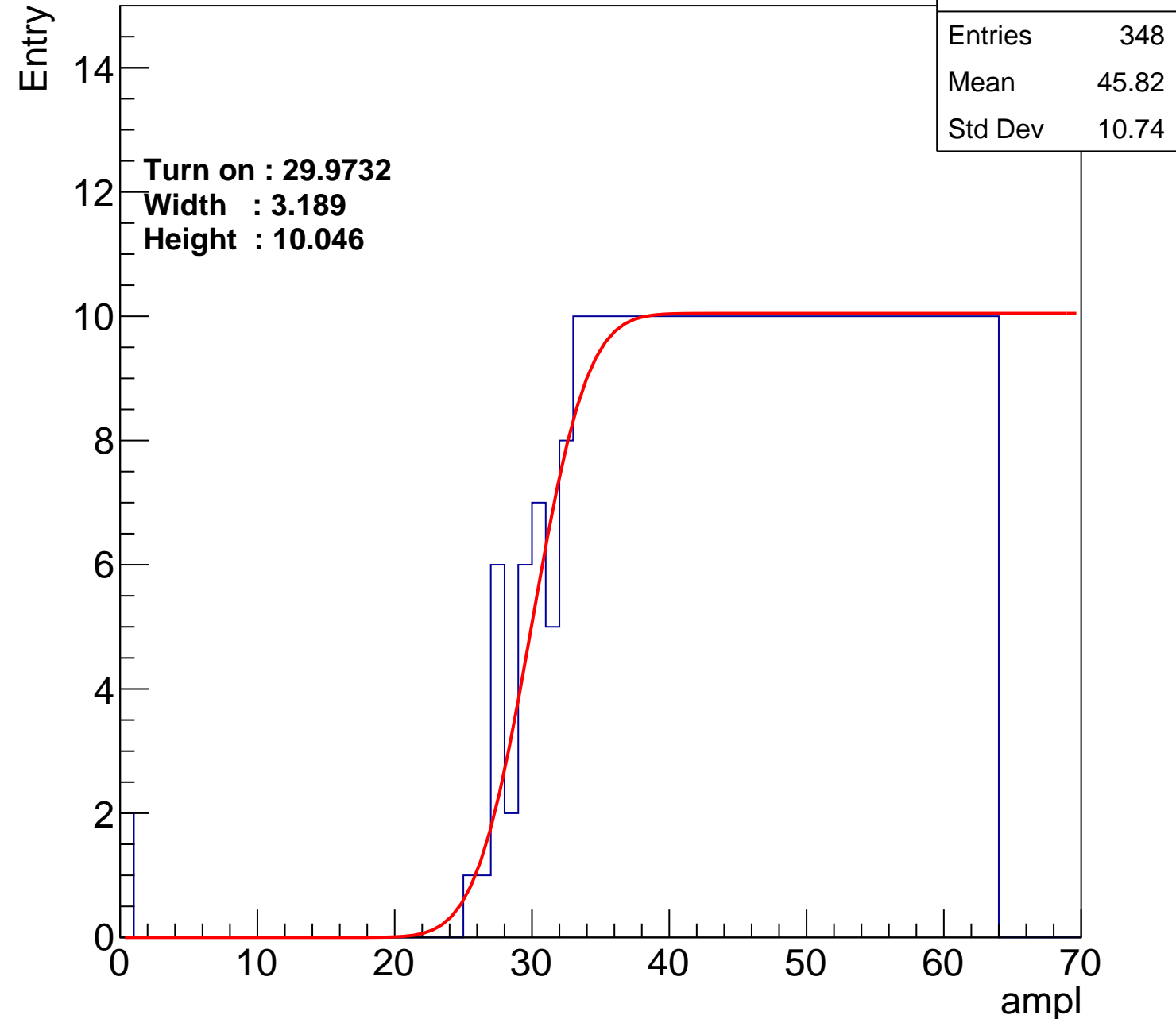
Width : 3.189

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch21

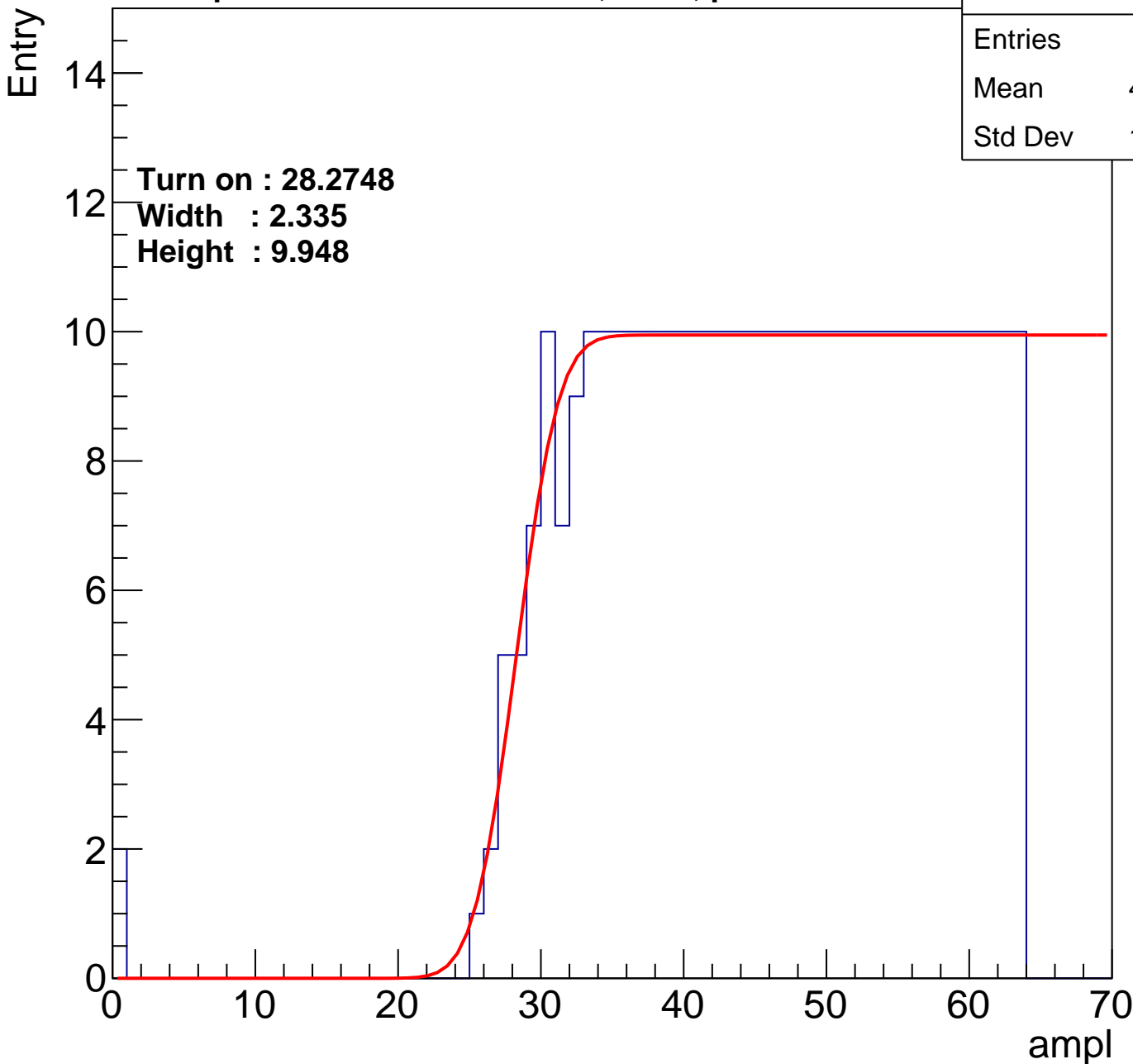
calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.36
Std Dev	10.92

Turn on : 28.2748

Width : 2.335

Height : 9.948



B0L002S, U13-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.4
Std Dev	10.77

Turn on : 28.5500

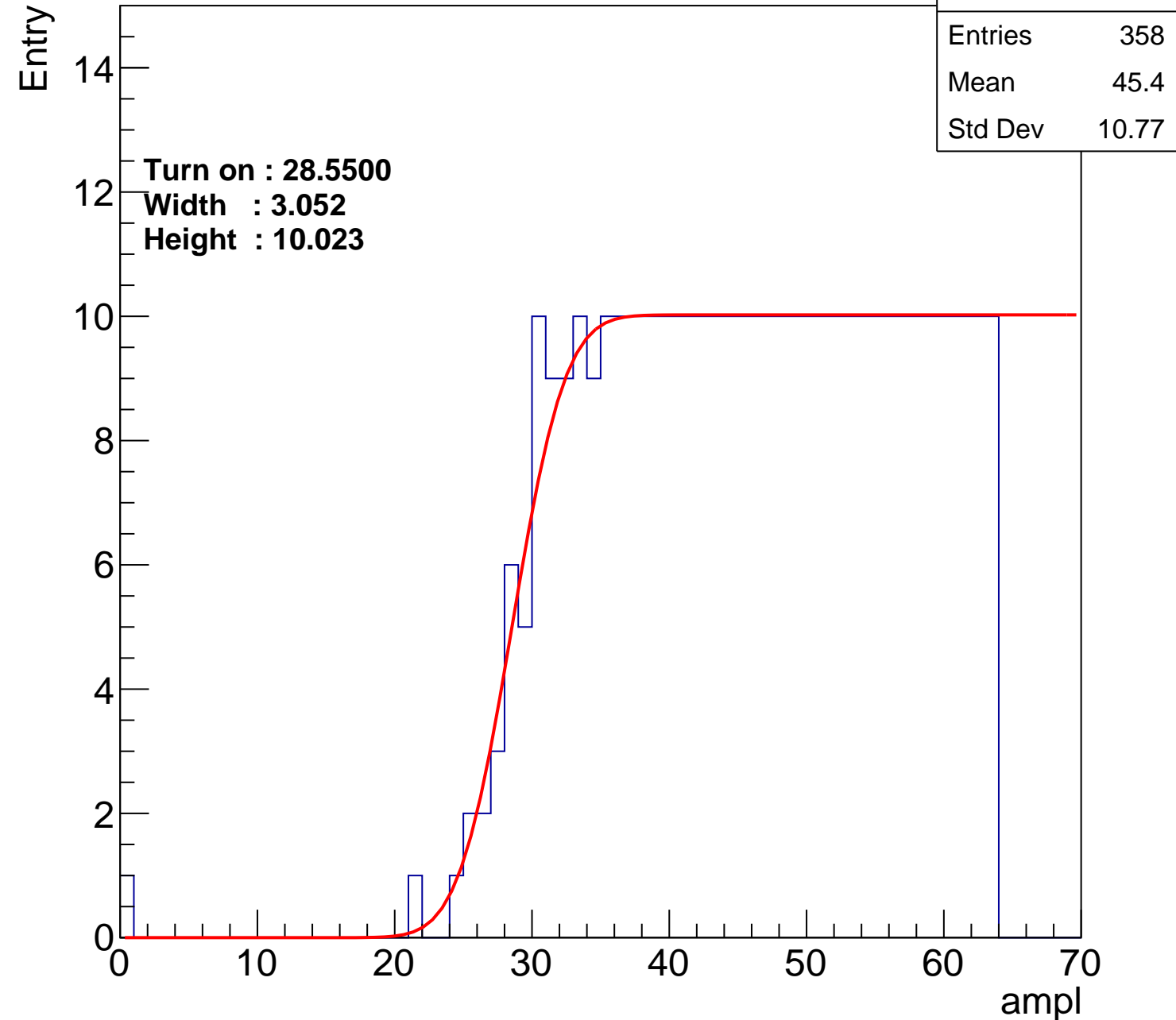
Width : 3.052

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.09
Std Dev	10.4

Turn on : 30.0380

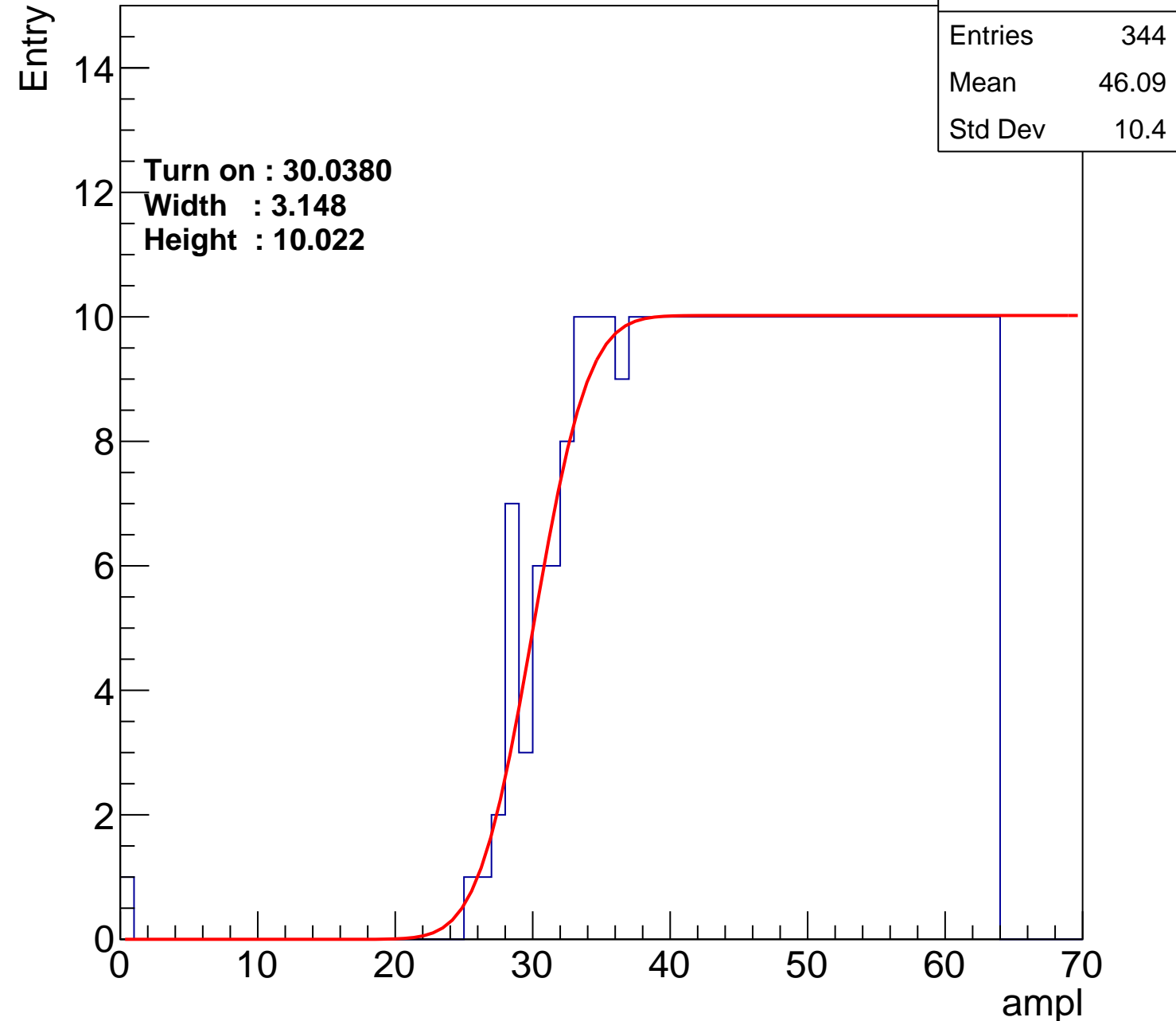
Width : 3.148

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.2935

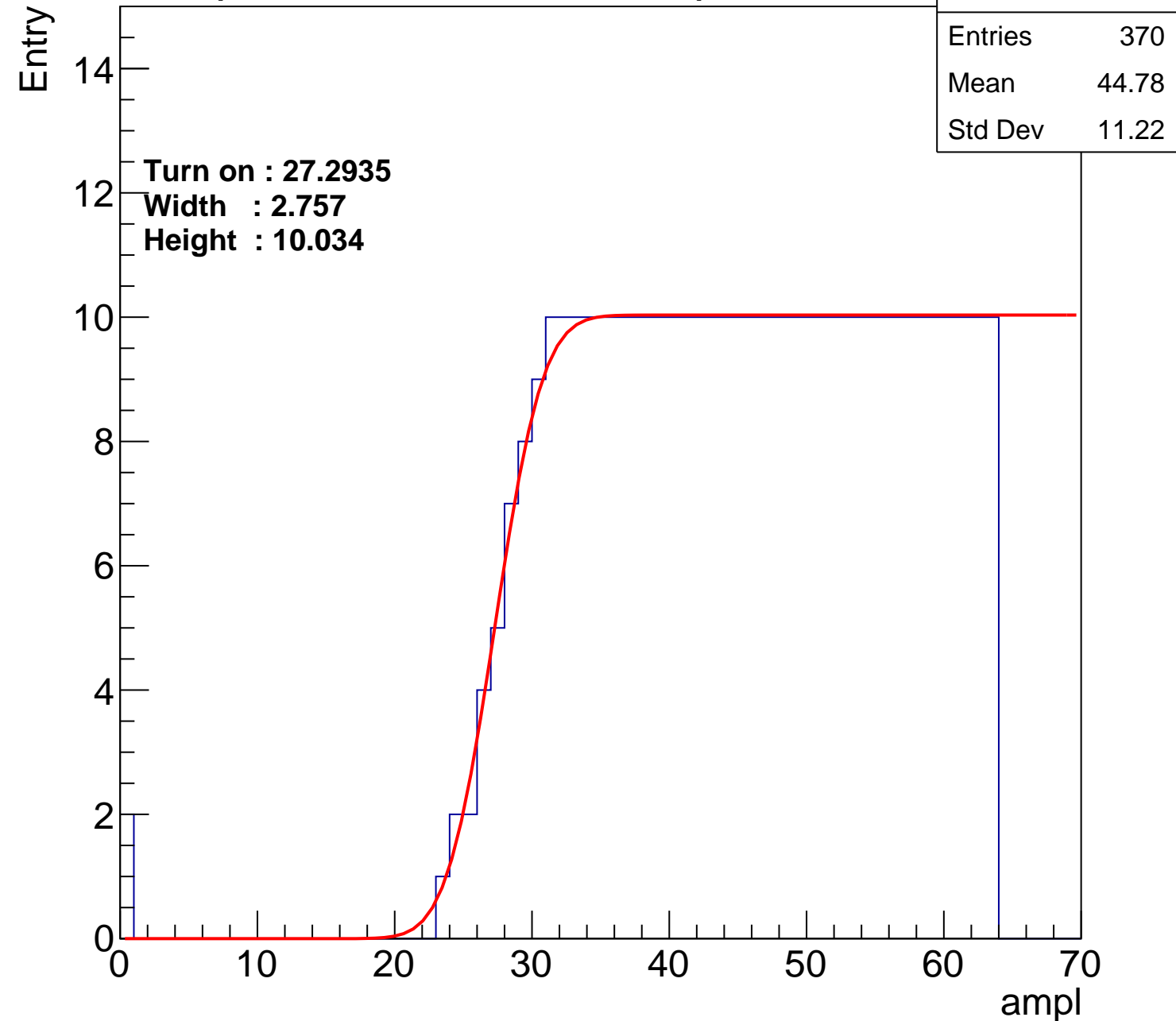
Width : 2.757

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.05
Std Dev	11.11

Turn on : 28.0618

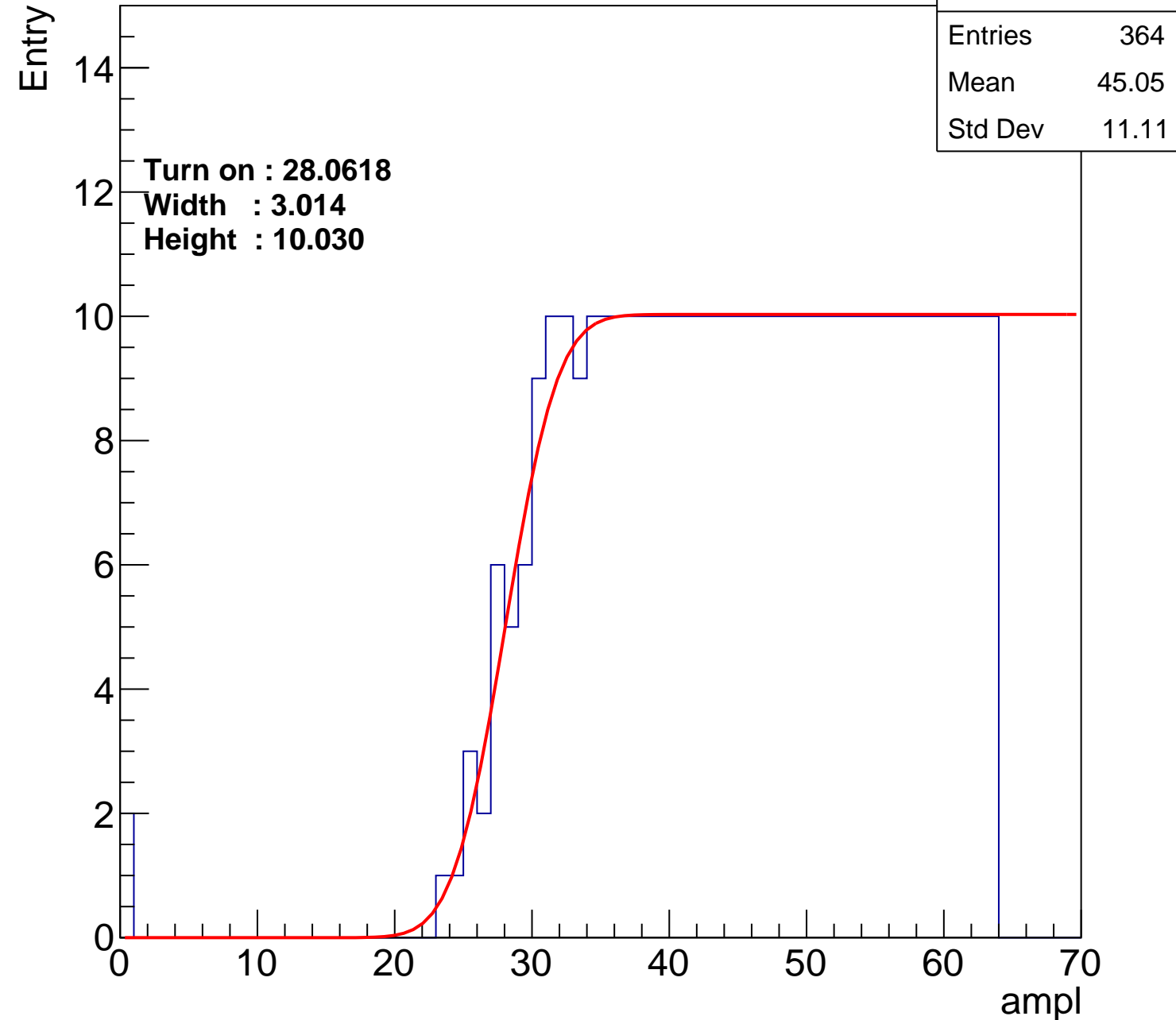
Width : 3.014

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 28.1061

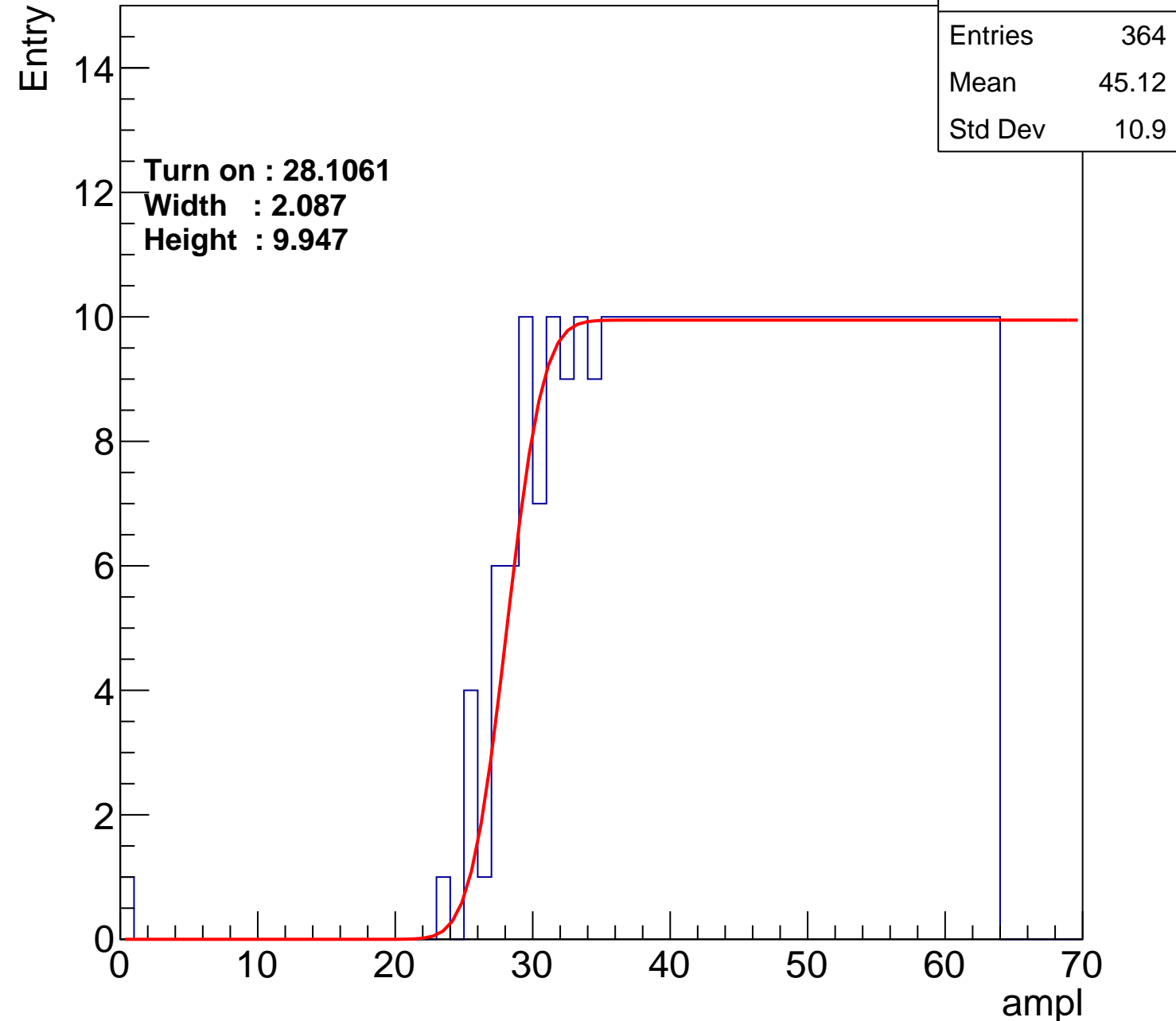
Width : 2.087

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch27

calib_packv5_042523_0143.root, FC#8, port C1

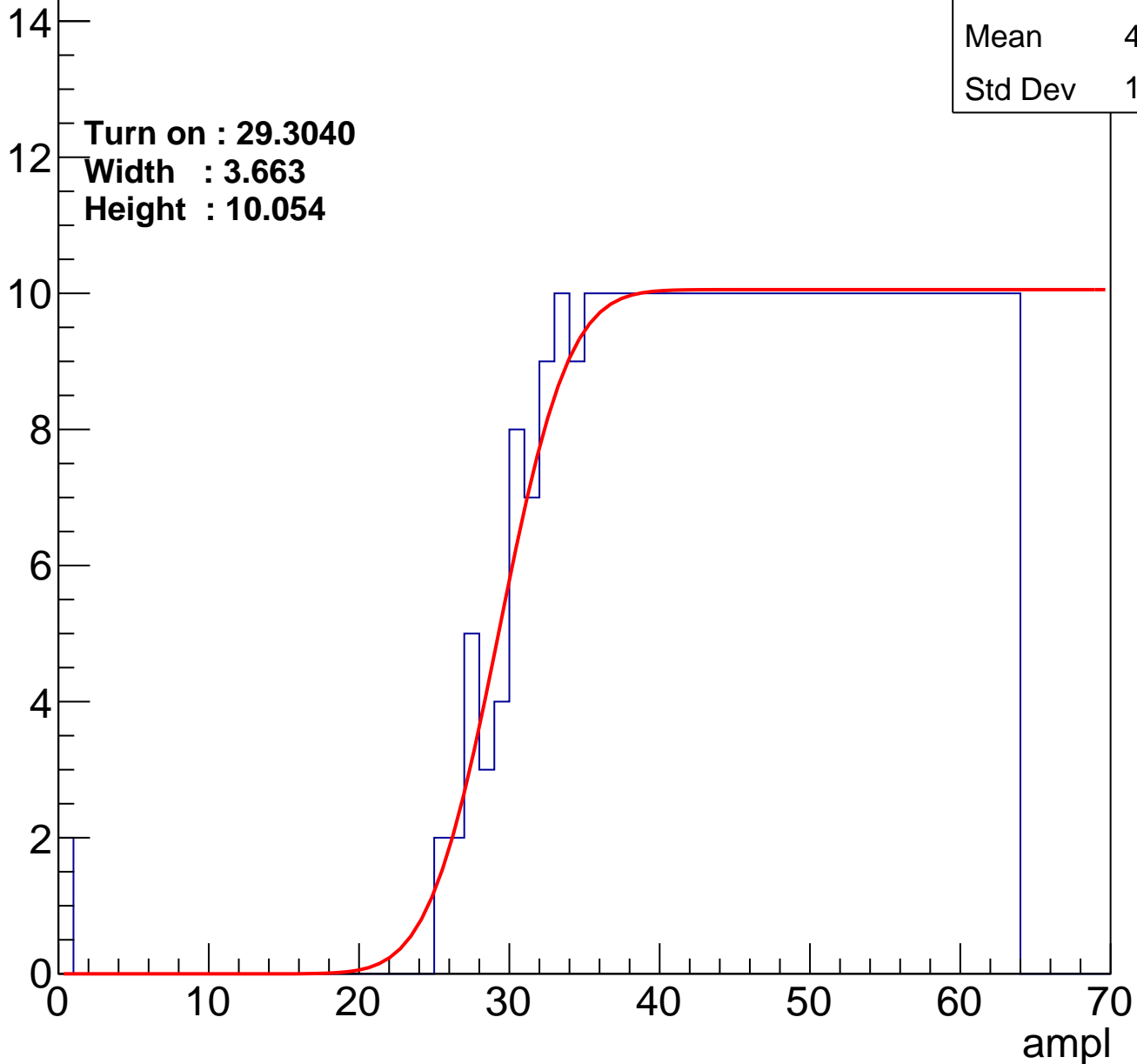
Entries	351
Mean	45.66
Std Dev	10.82

Turn on : 29.3040

Width : 3.663

Height : 10.054

Entry

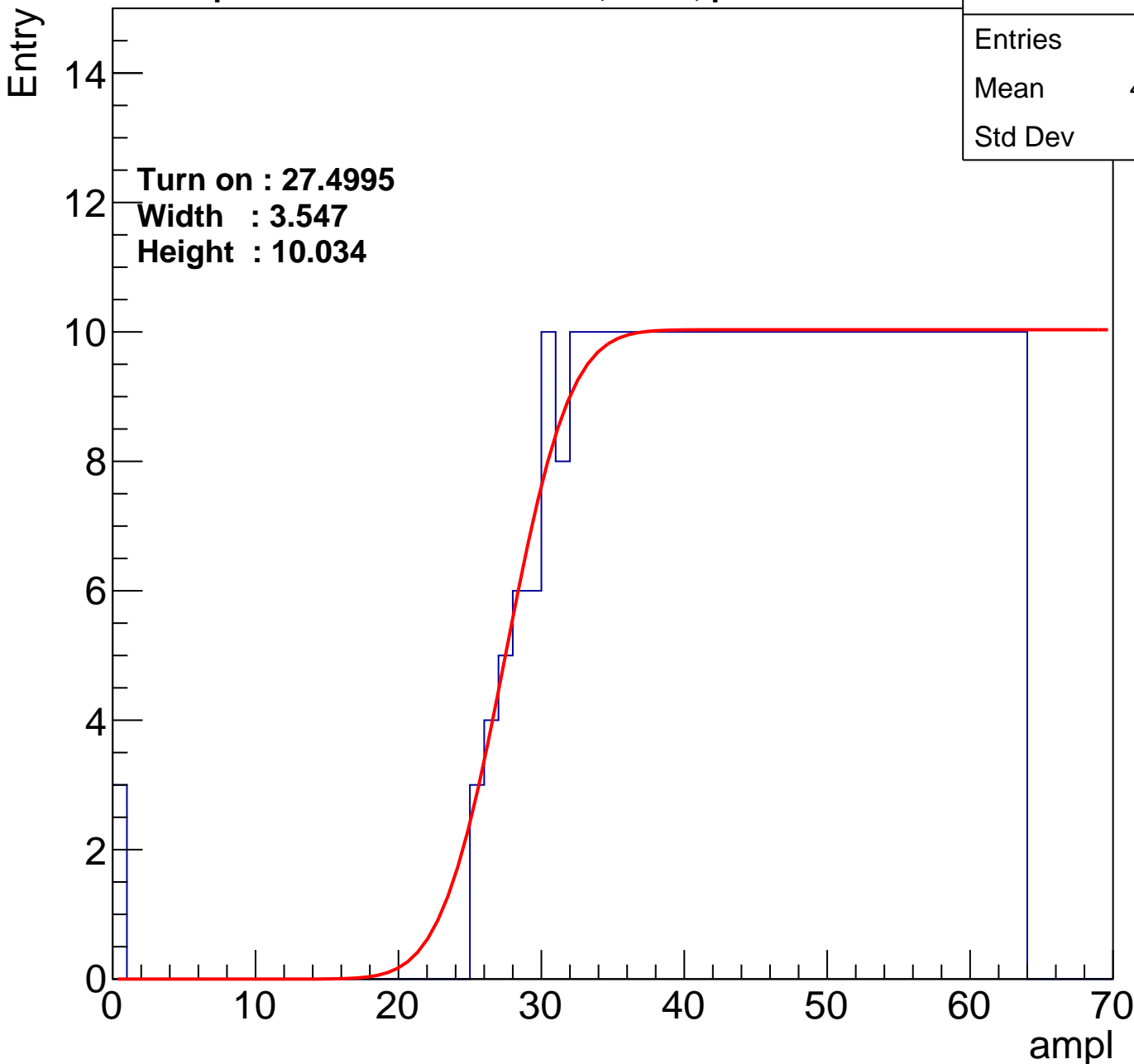


calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.94
Std Dev	11.31

Std Dev	11.31
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Height : 10.034



B0L002S, U13-ch29

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.68
Std Dev	10.8

Turn on : 29.2693

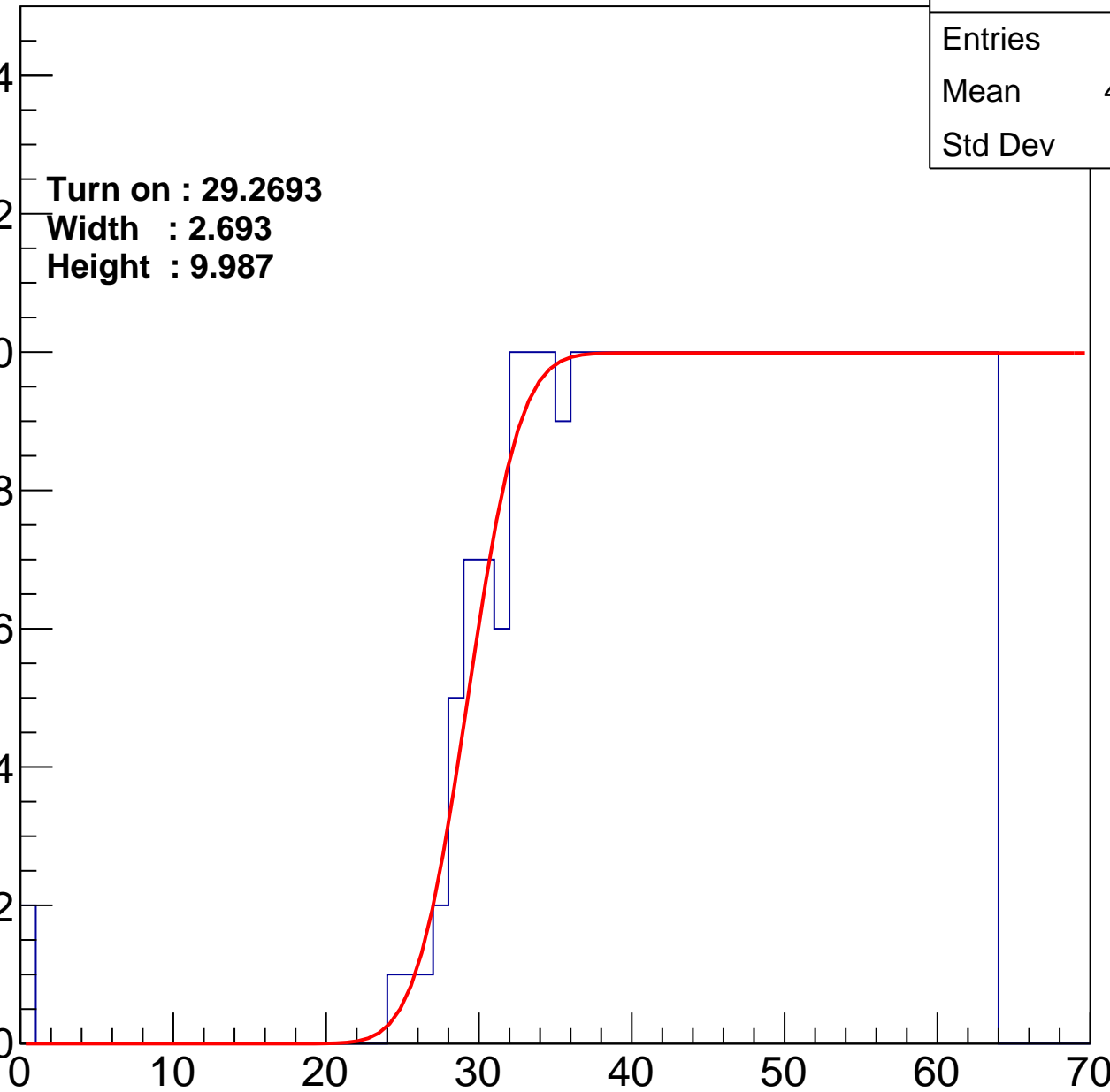
Width : 2.693

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45
Std Dev	11

Turn on : 27.8832

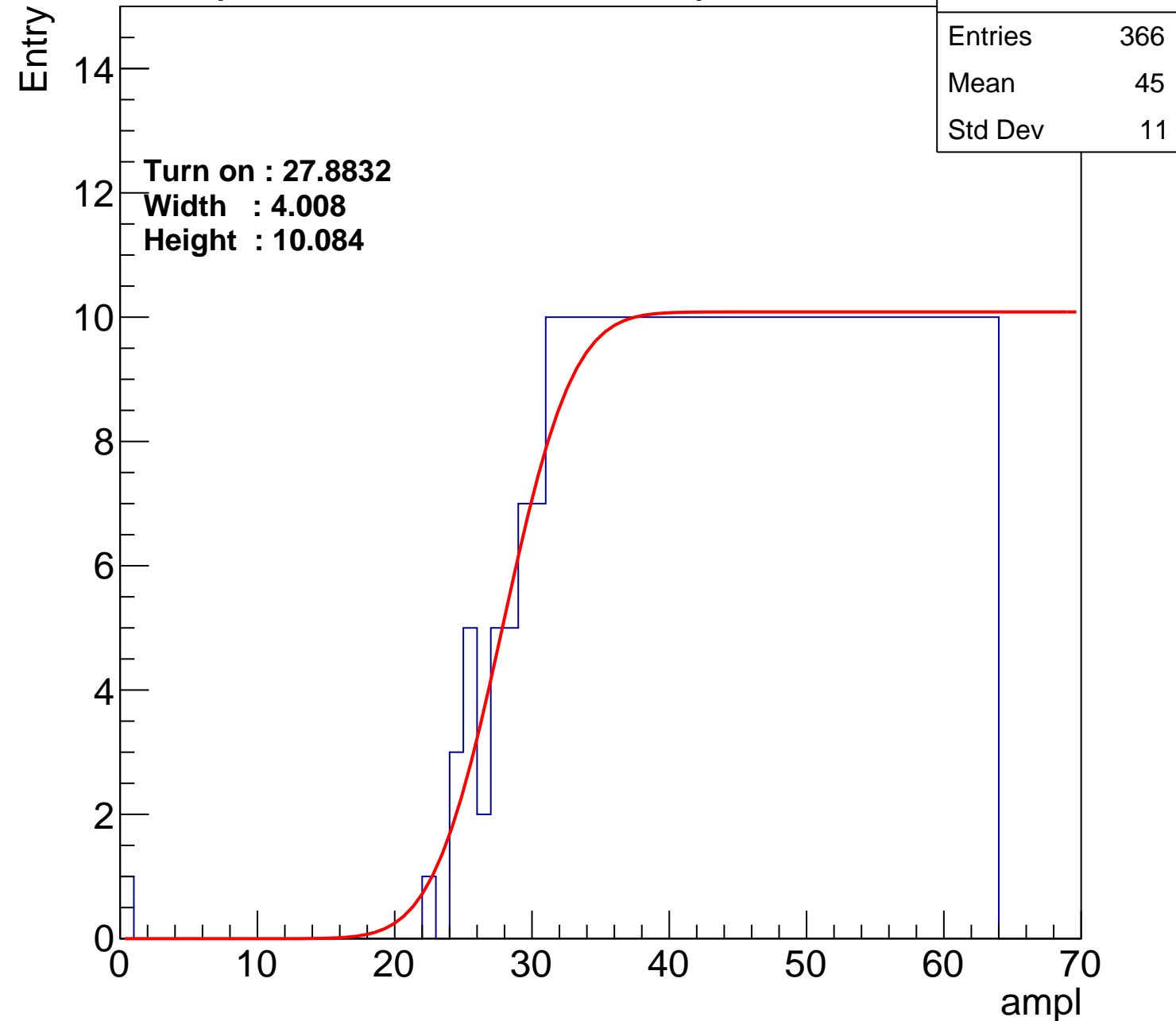
Width : 4.008

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch31

calib_packv5_042523_0143.root, FC#8, port C1

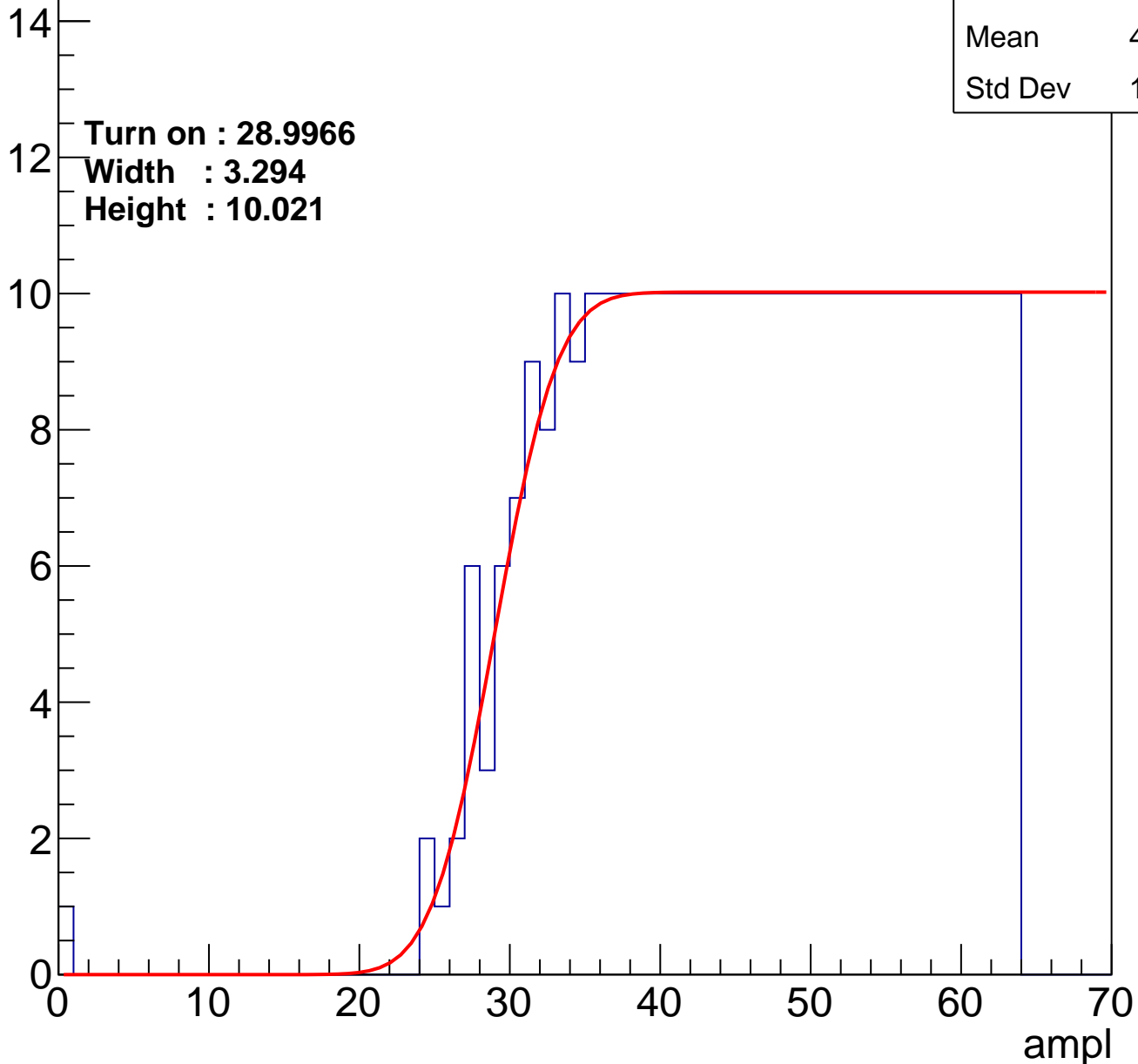
Entries	354
Mean	45.58
Std Dev	10.69

Turn on : 28.9966

Width : 3.294

Height : 10.021

Entry



B0L002S, U13-ch32

calib_packv5_042523_0143.root, FC#8, port C1

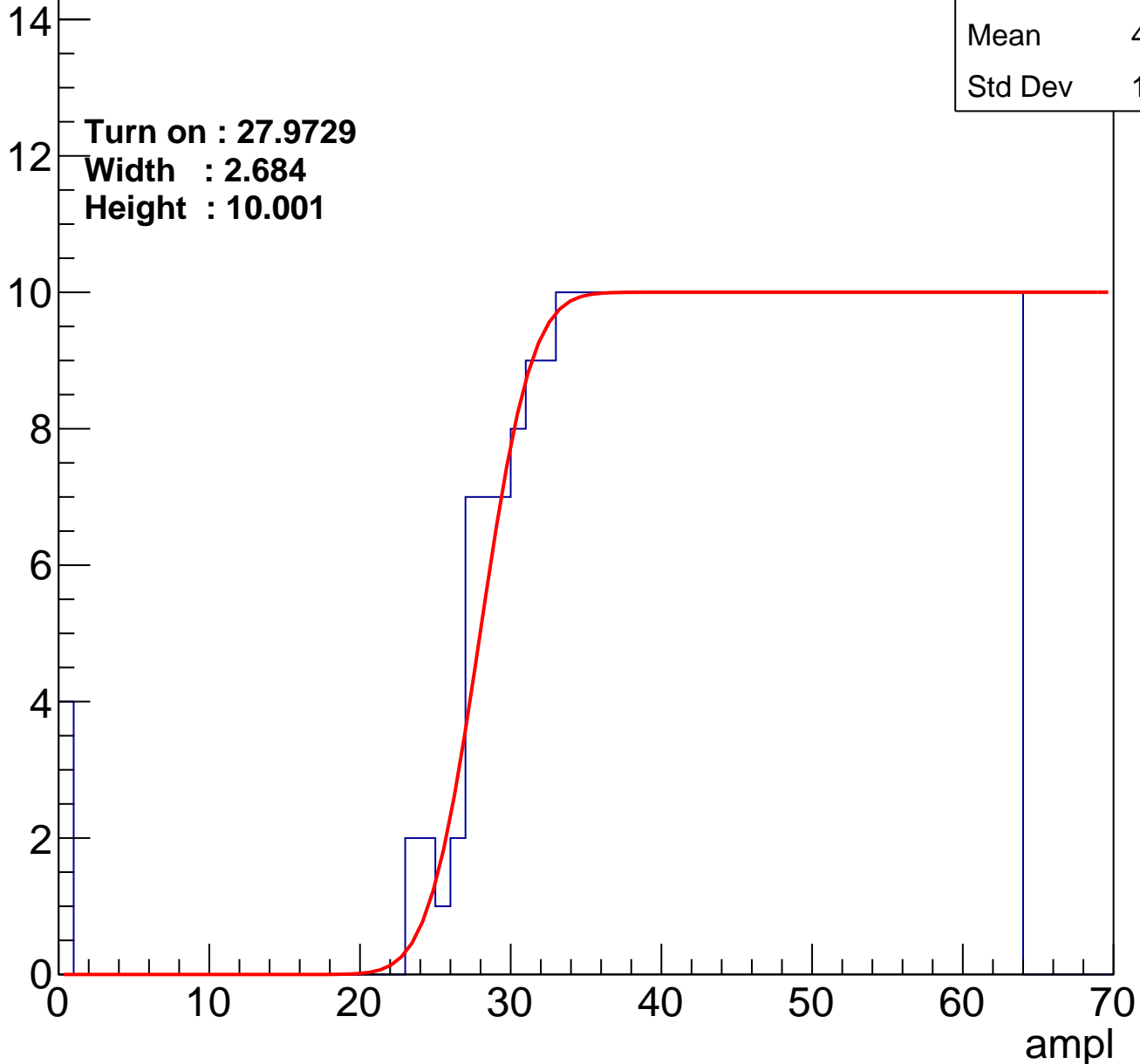
Entries	368
Mean	44.69
Std Dev	11.63

Turn on : 27.9729

Width : 2.684

Height : 10.001

Entry



B0L002S, U13-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 26.9191

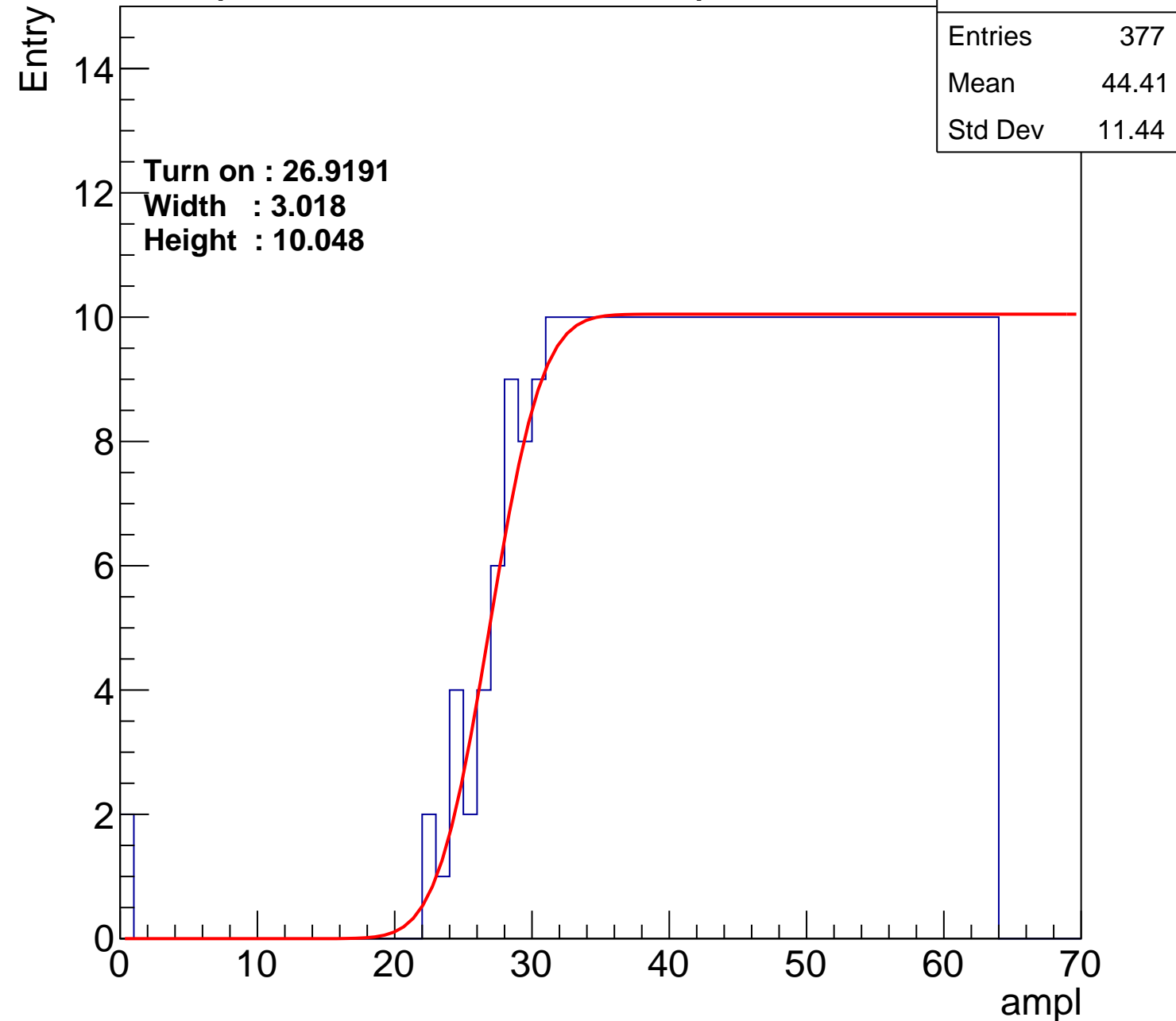
Width : 3.018

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	340
Mean	46.22
Std Dev	10.53

Turn on : 30.7455

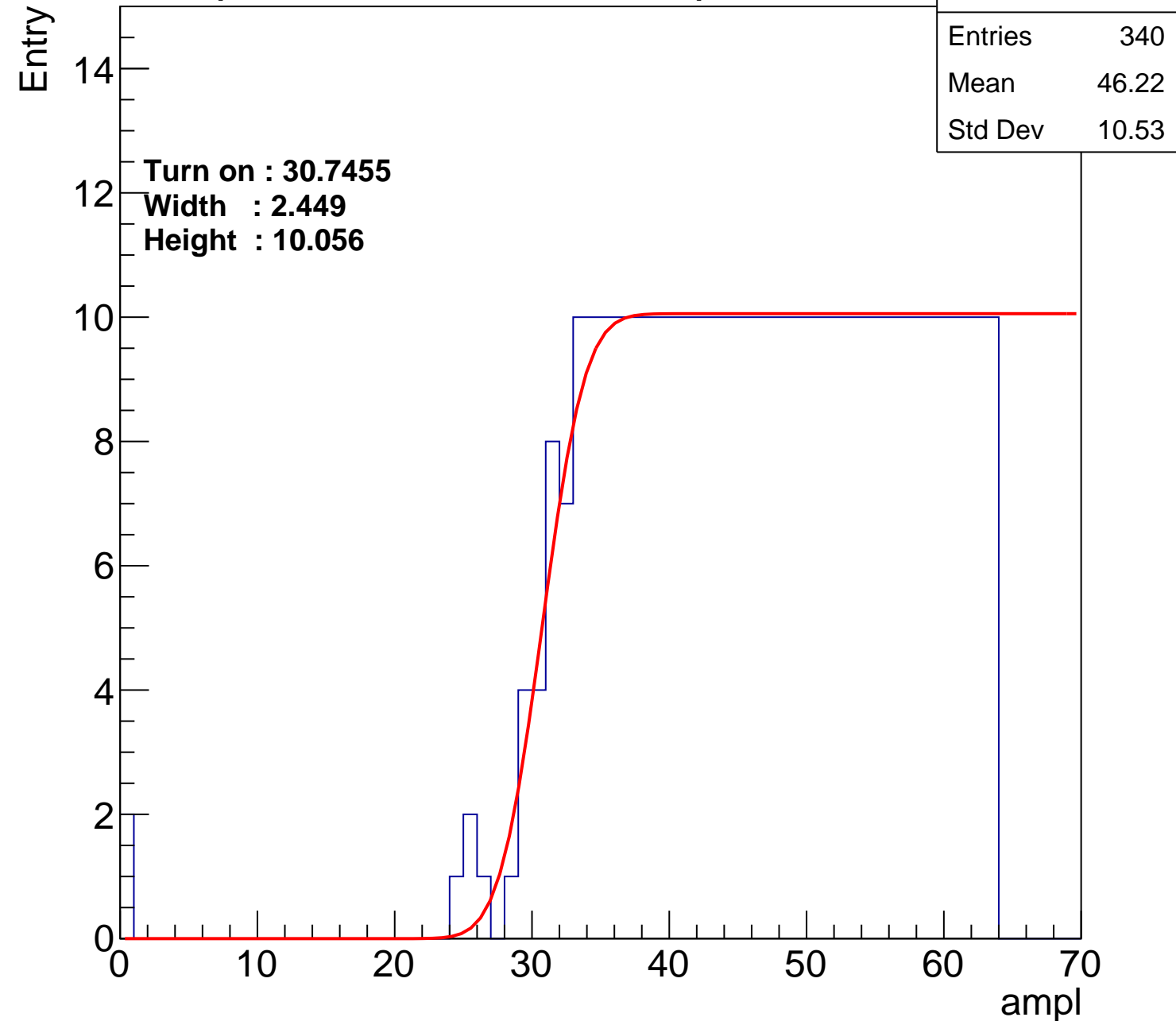
Width : 2.449

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.84
Std Dev	10.54

Turn on : 29.8774

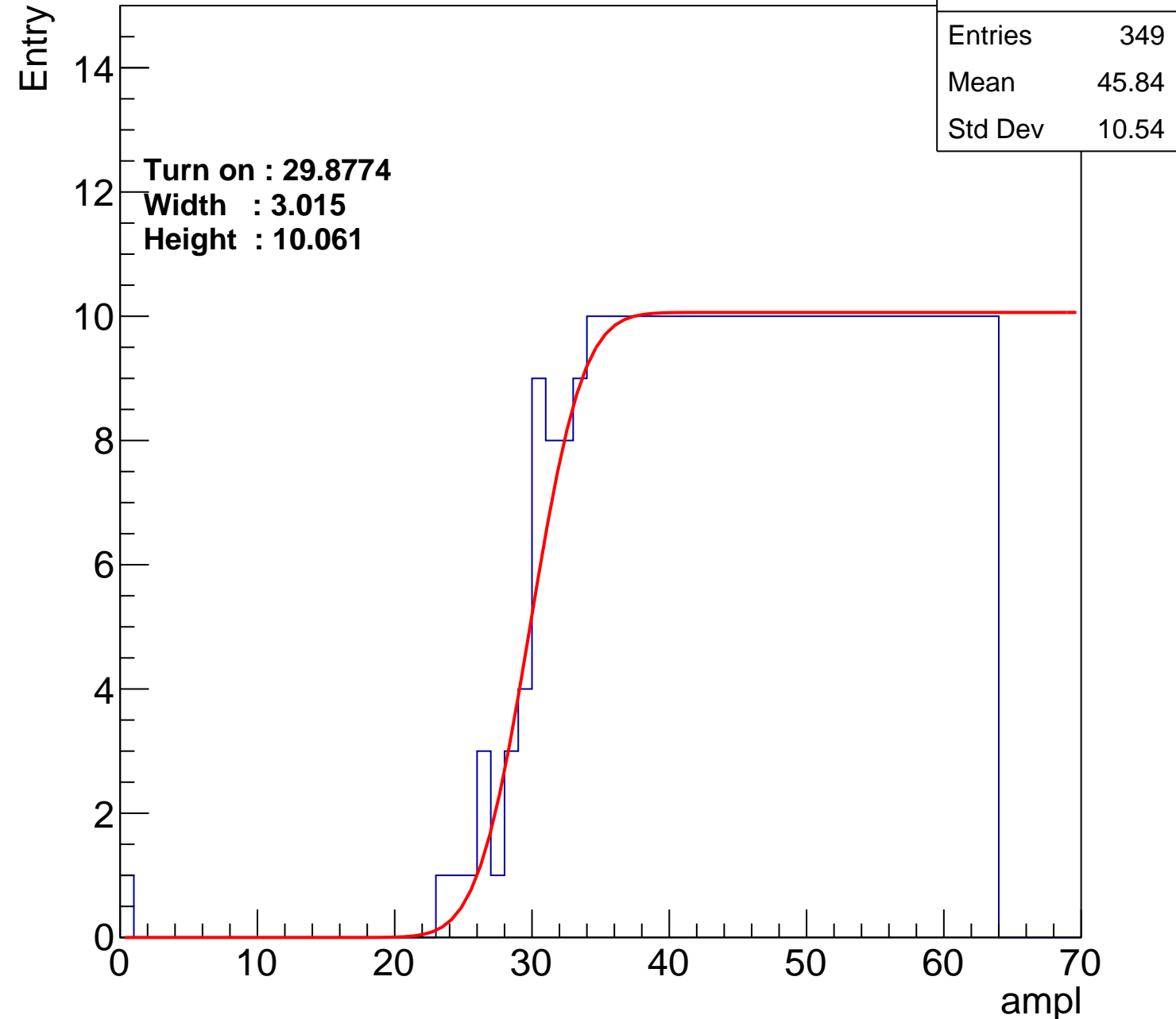
Width : 3.015

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.74
Std Dev	10.8

Turn on : 29.5048

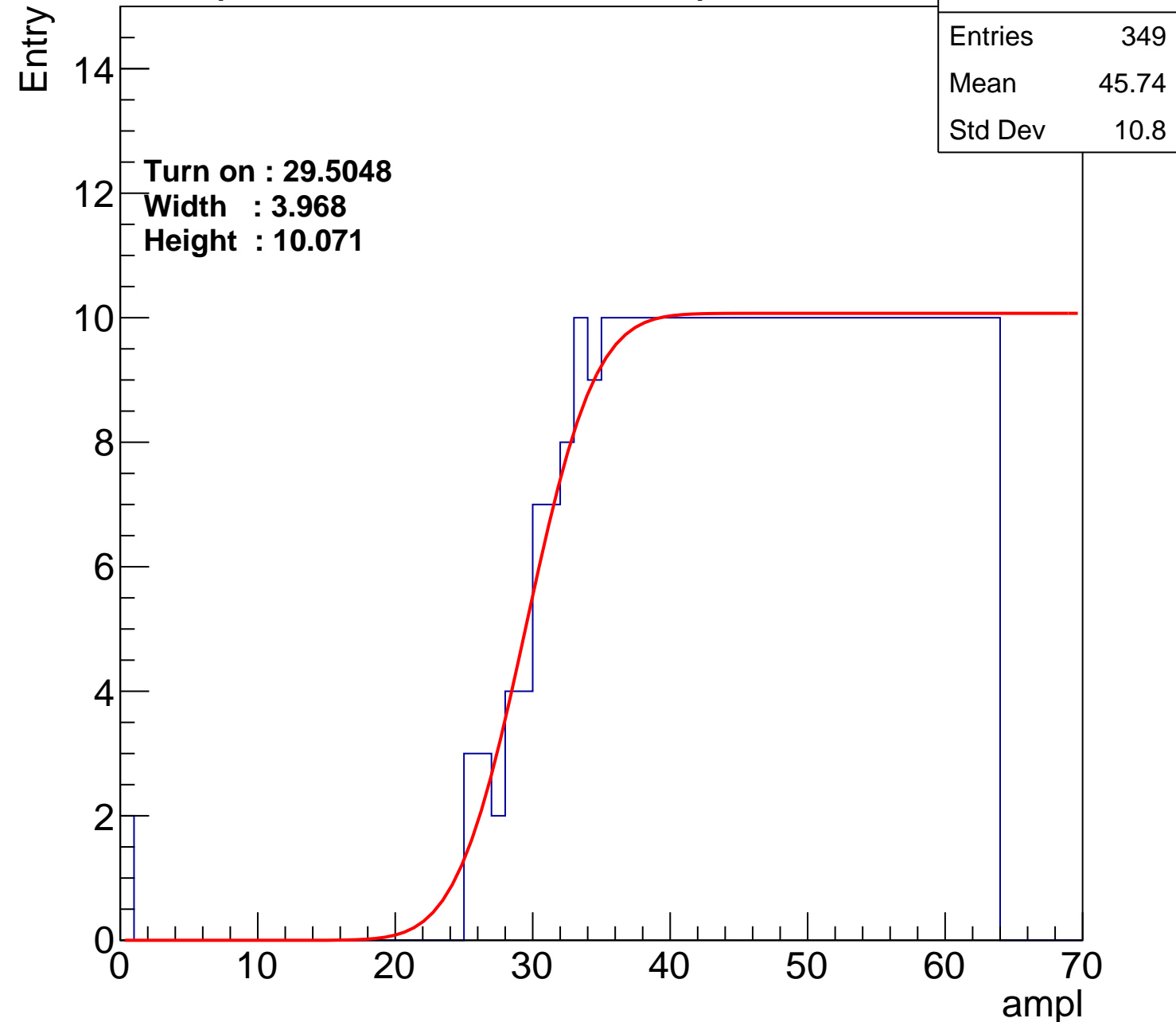
Width : 3.968

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.93
Std Dev	11.34

Turn on : 28.0616

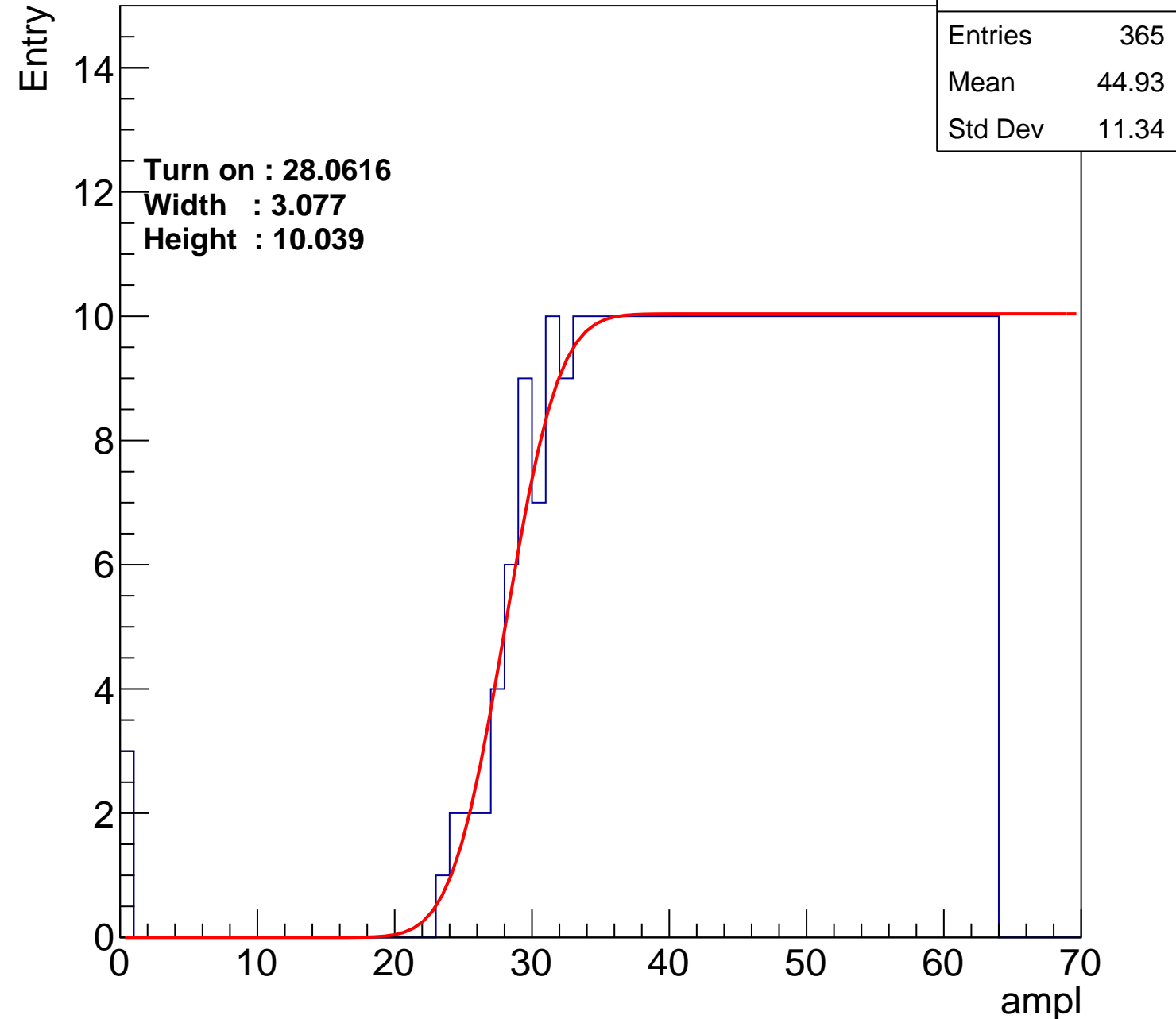
Width : 3.077

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.55
Std Dev	11.96

Turn on : 27.8172

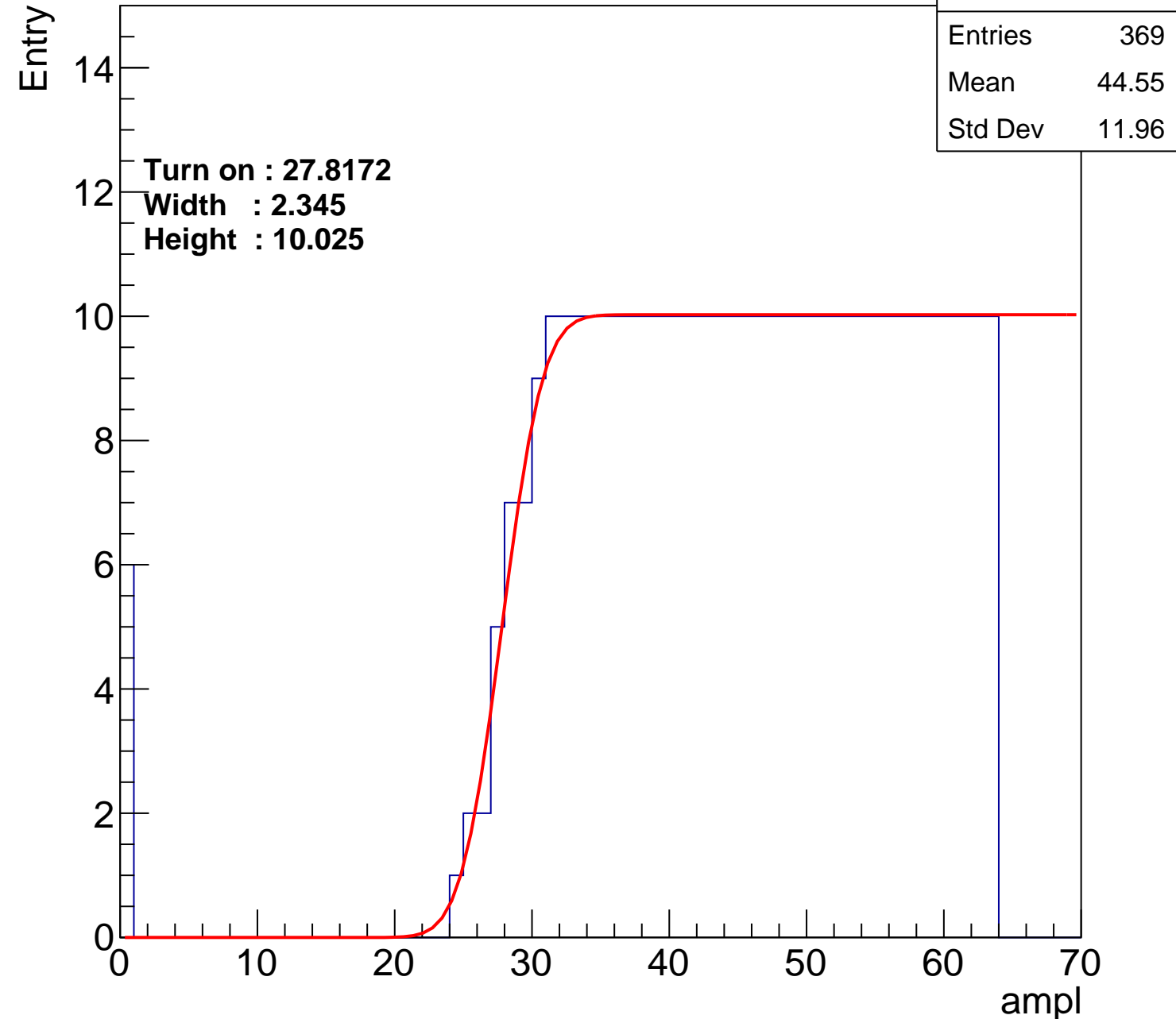
Width : 2.345

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.05
Std Dev	10.86

Turn on : 30.6853

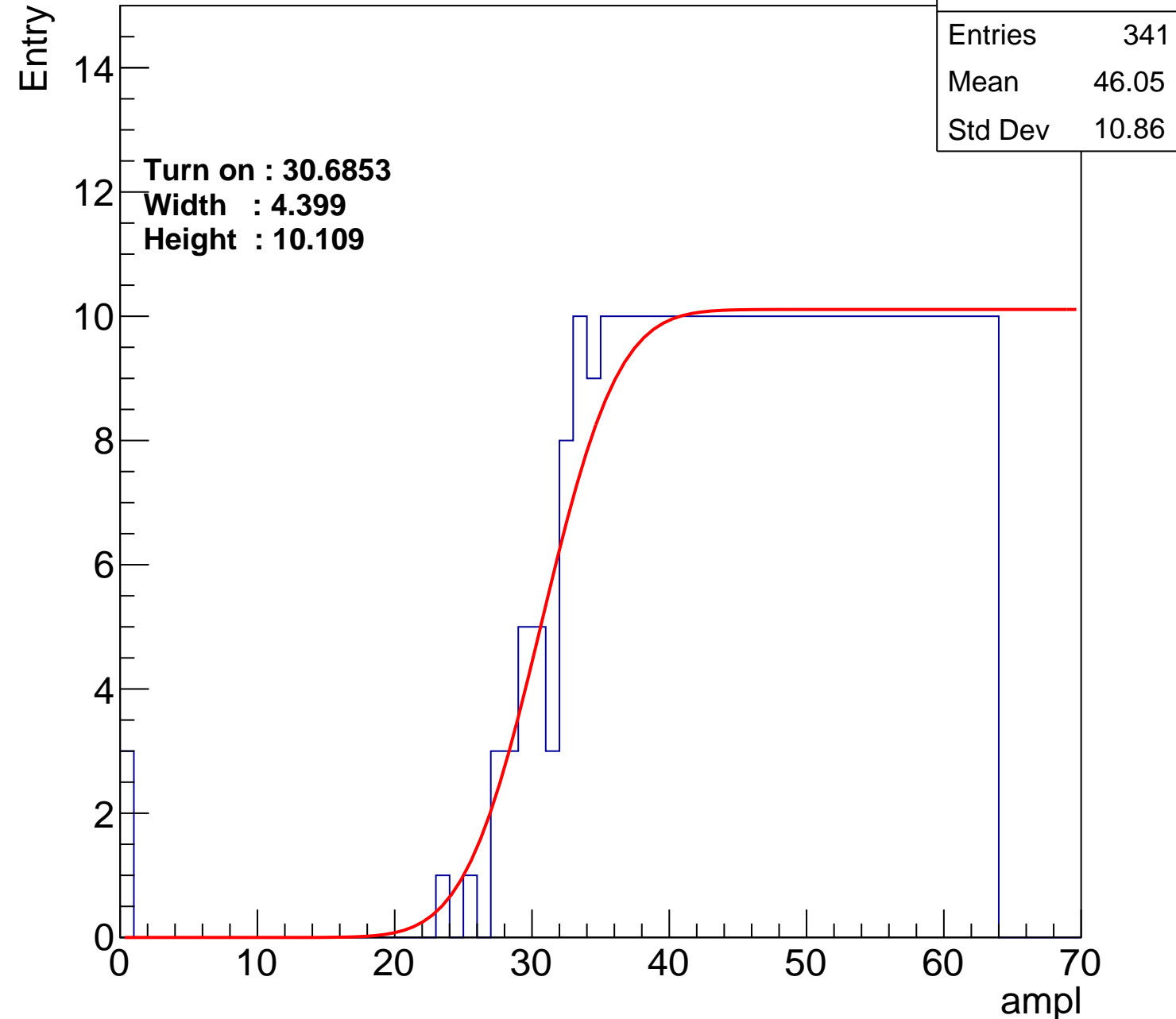
Width : 4.399

Height : 10.109

Entry

14
12
10
8
6
4
2
0

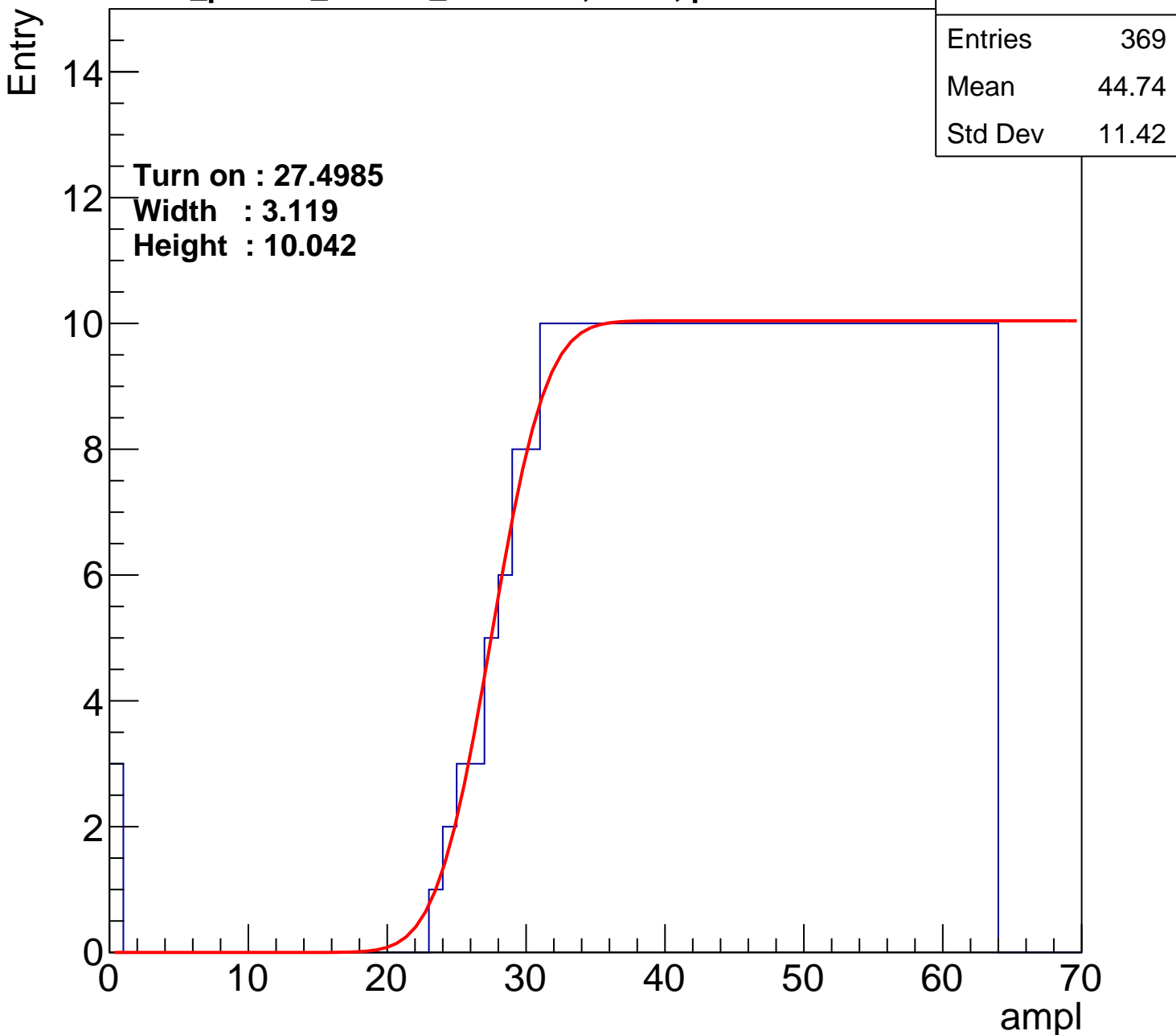
ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.4985
Width : 3.119
Height : 10.042



B0L002S, U13-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.88
Std Dev	11.22

Turn on : 27.8841

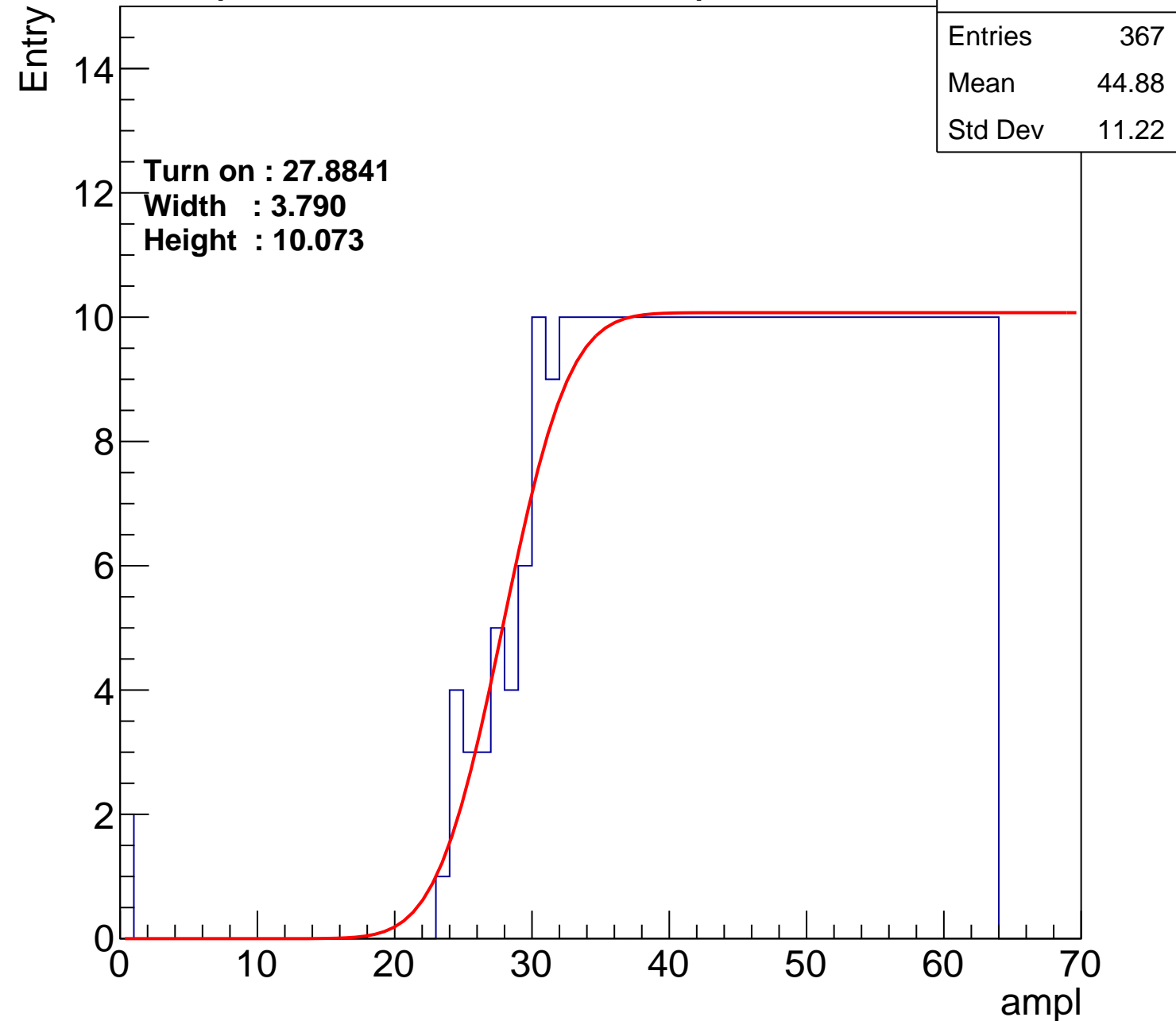
Width : 3.790

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.49
Std Dev	10.89

Turn on : 29.0030

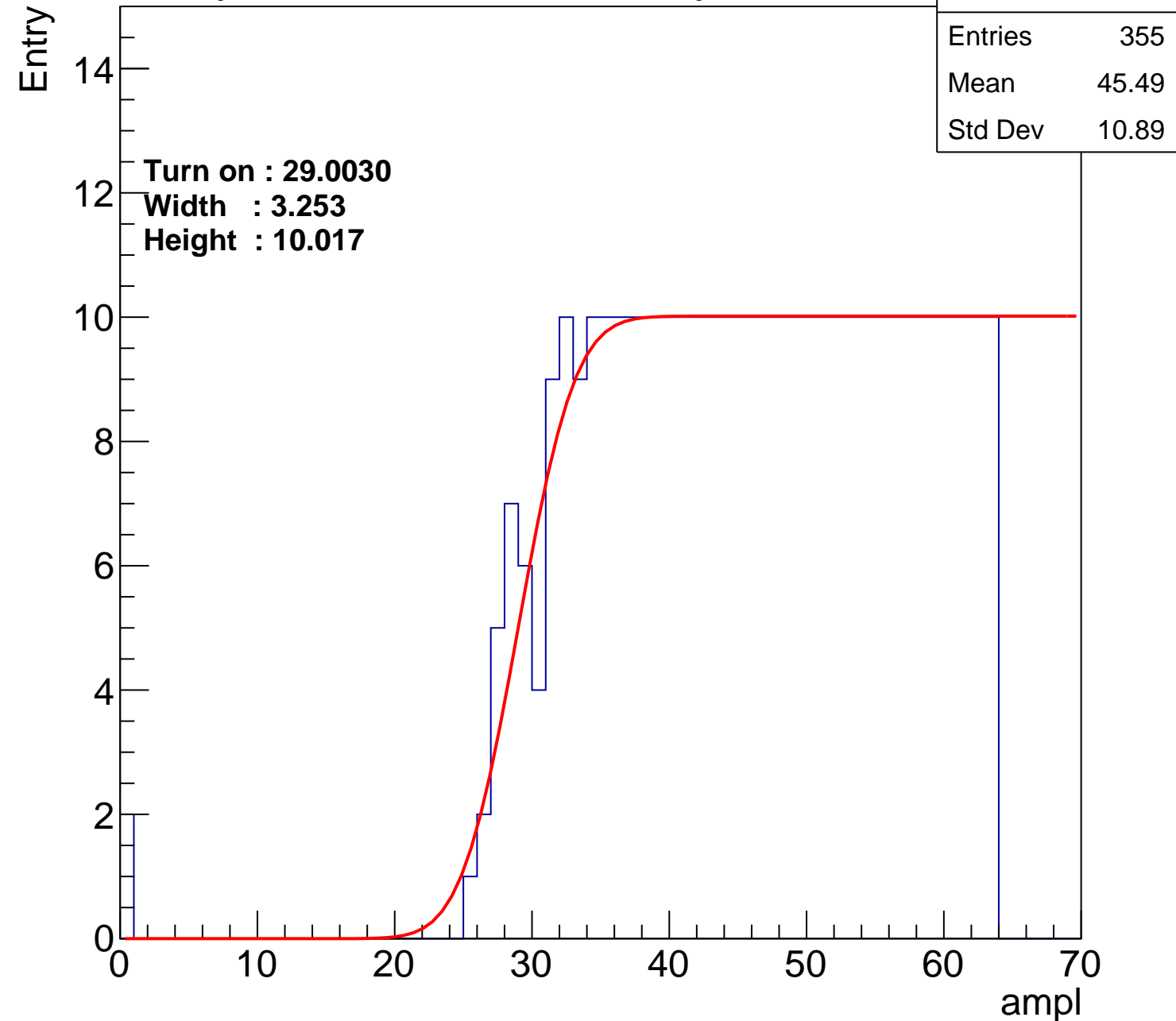
Width : 3.253

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.8
Std Dev	10.77

Turn on : 29.4512

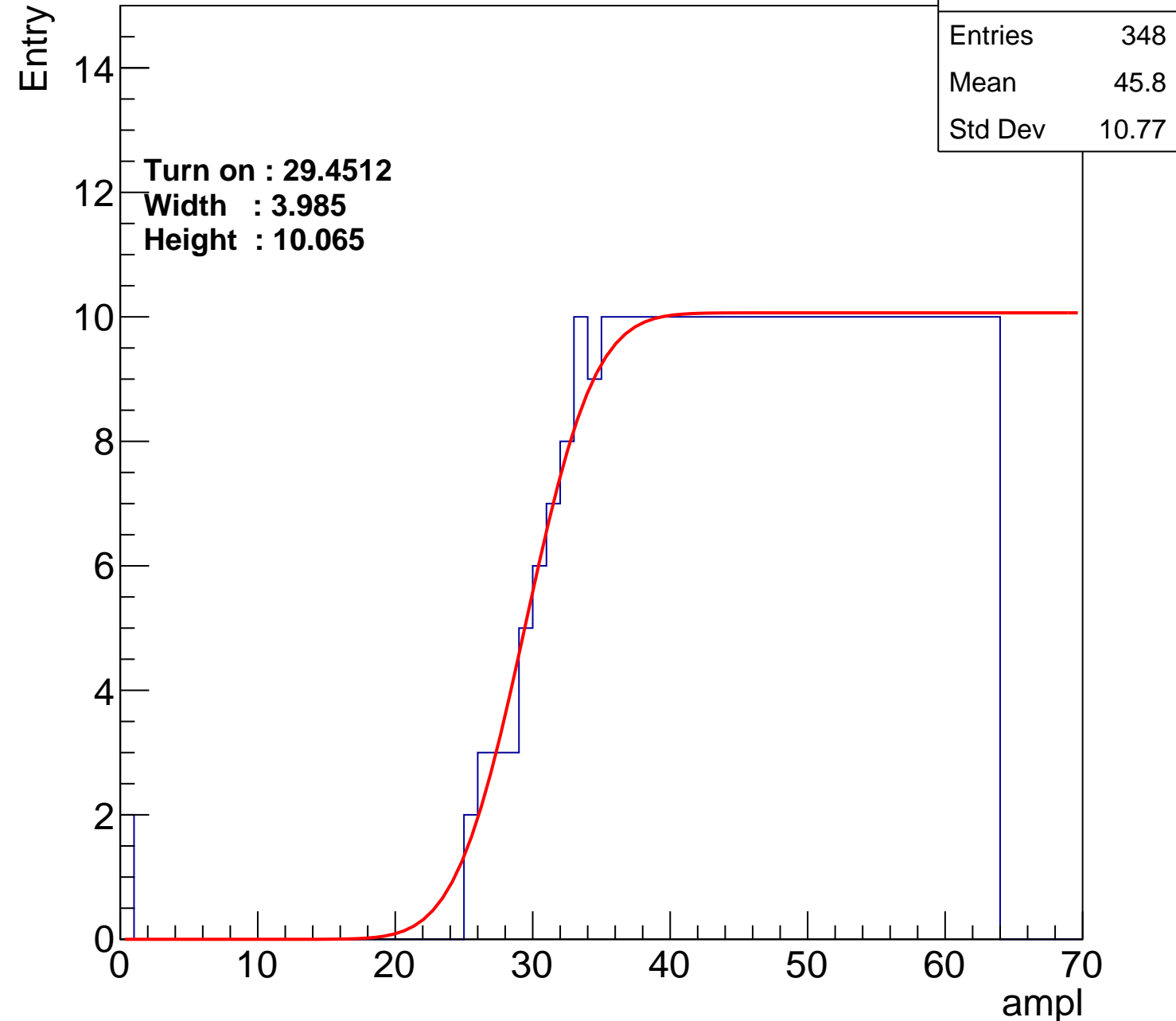
Width : 3.985

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch44

calib_packv5_042523_0143.root, FC#8, port C1

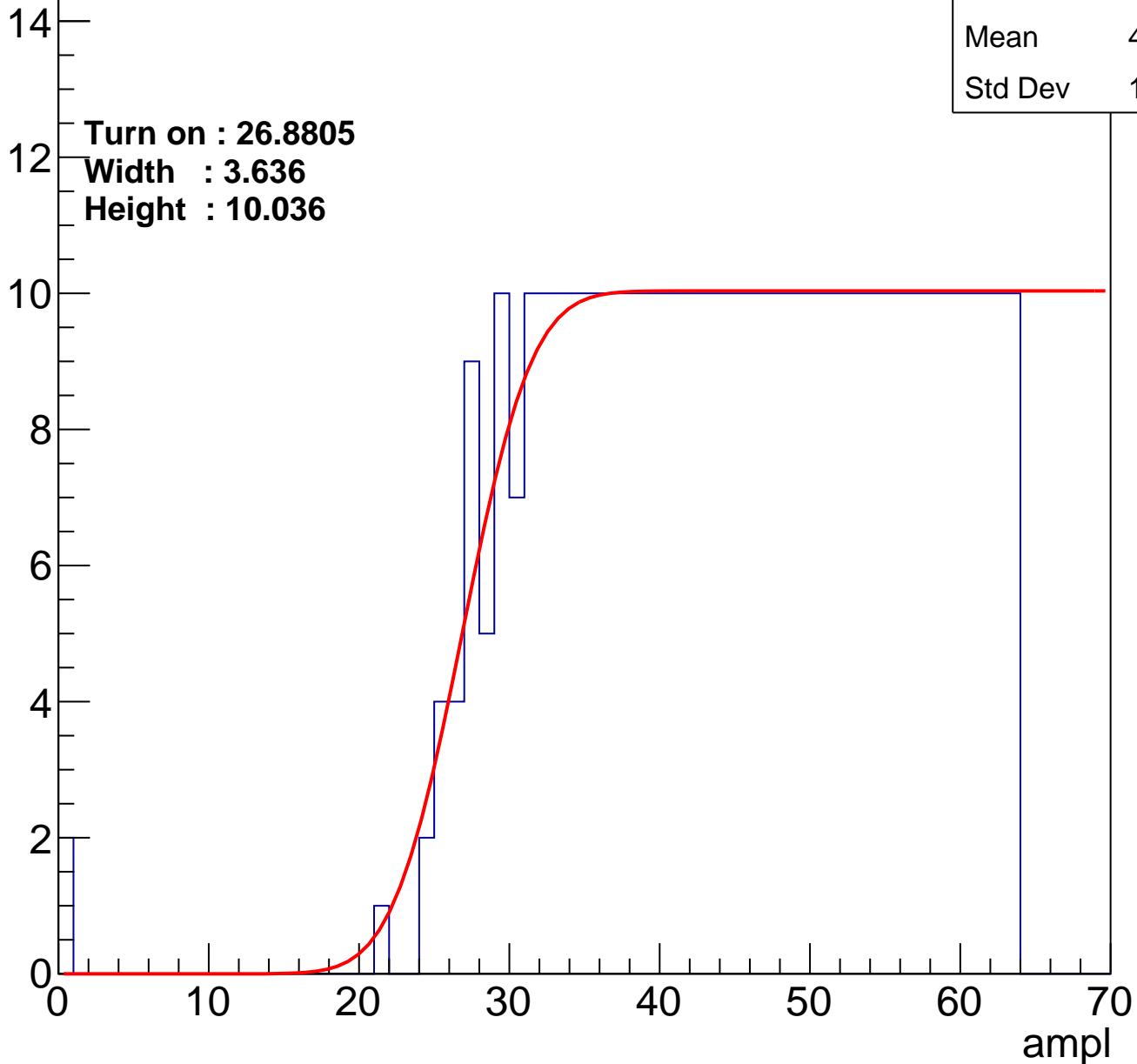
Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 26.8805

Width : 3.636

Height : 10.036

Entry



B0L002S, U13-ch45

calib_packv5_042523_0143.root, FC#8, port C1

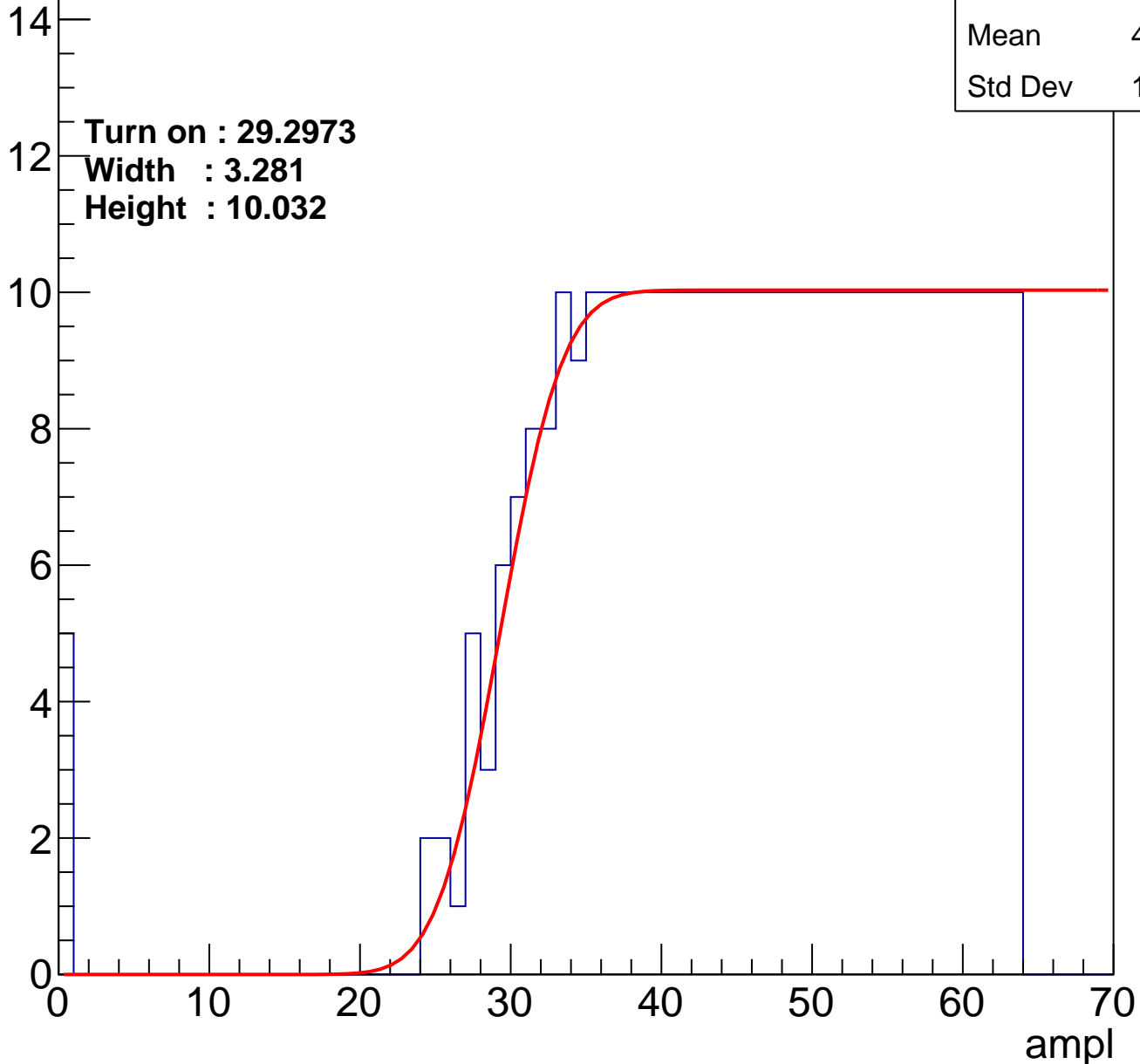
Entries	356
Mean	45.16
Std Dev	11.63

Turn on : 29.2973

Width : 3.281

Height : 10.032

Entry



B0L002S, U13-ch46

calib_packv5_042523_0143.root, FC#8, port C1

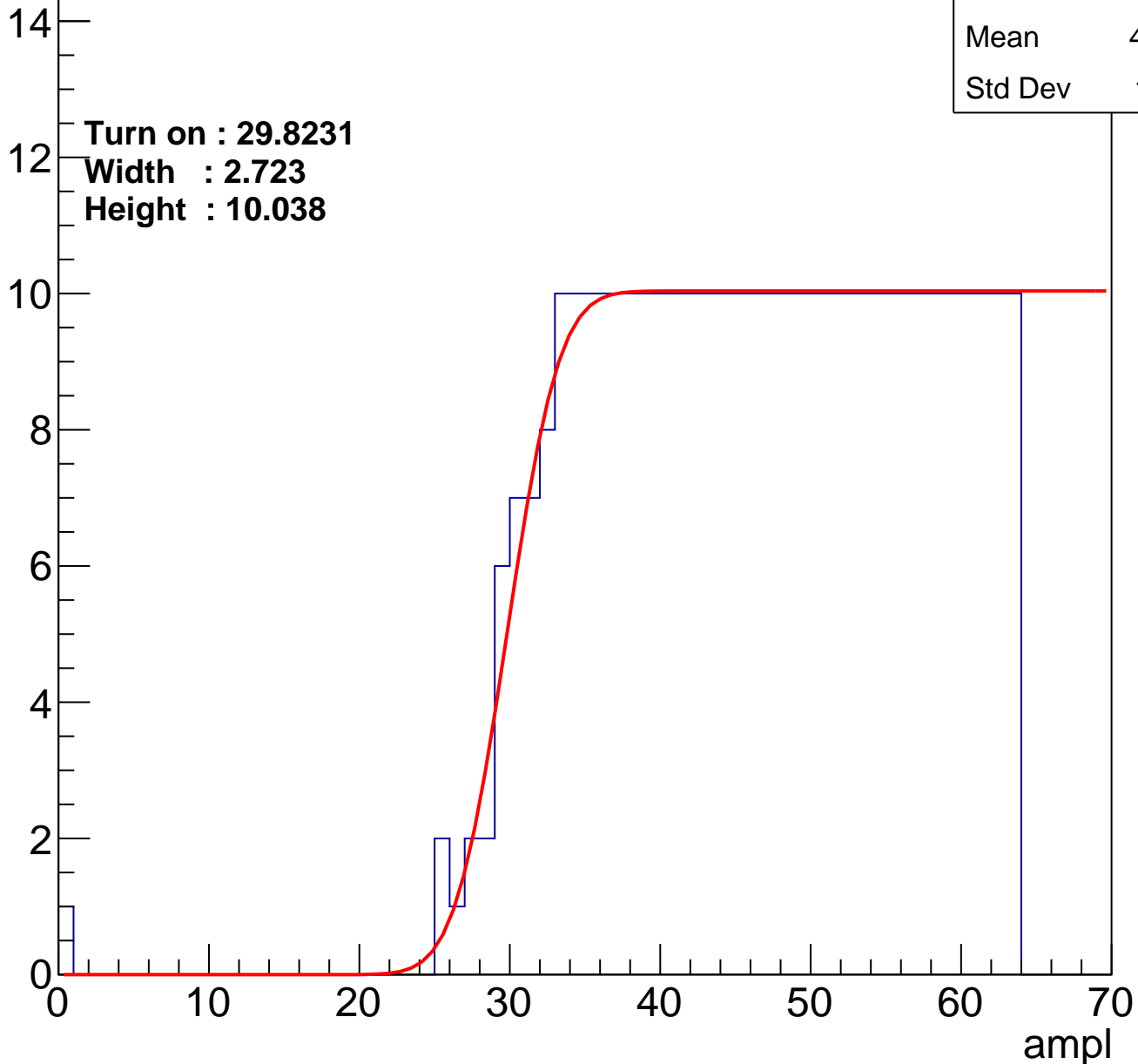
Entries	346
Mean	46.02
Std Dev	10.41

Turn on : 29.8231

Width : 2.723

Height : 10.038

Entry



B0L002S, U13-ch47

calib_packv5_042523_0143.root, FC#8, port C1

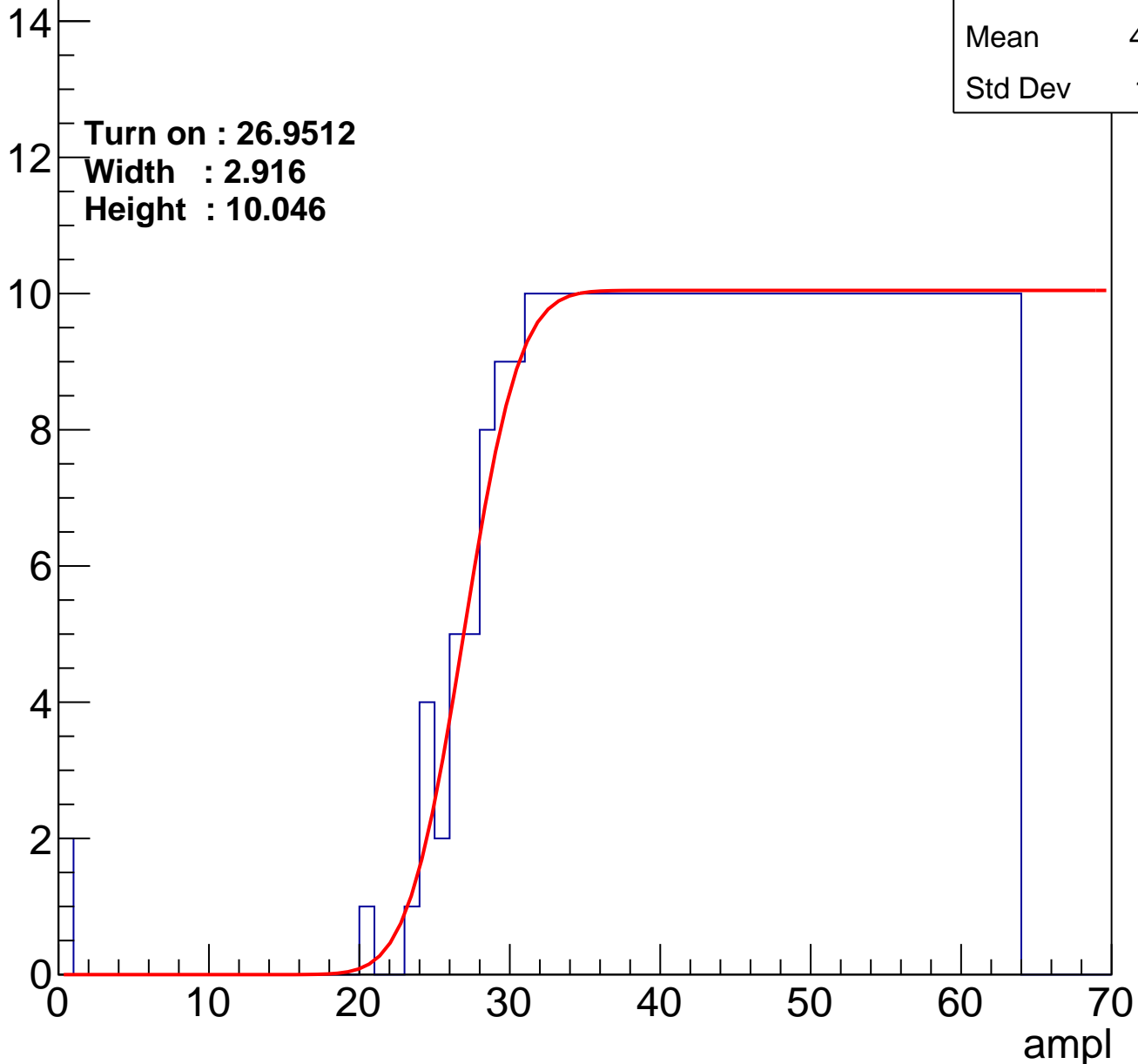
Entries	376
Mean	44.47
Std Dev	11.41

Turn on : 26.9512

Width : 2.916

Height : 10.046

Entry



B0L002S, U13-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.93
Std Dev	10.67

Turn on : 30.0432

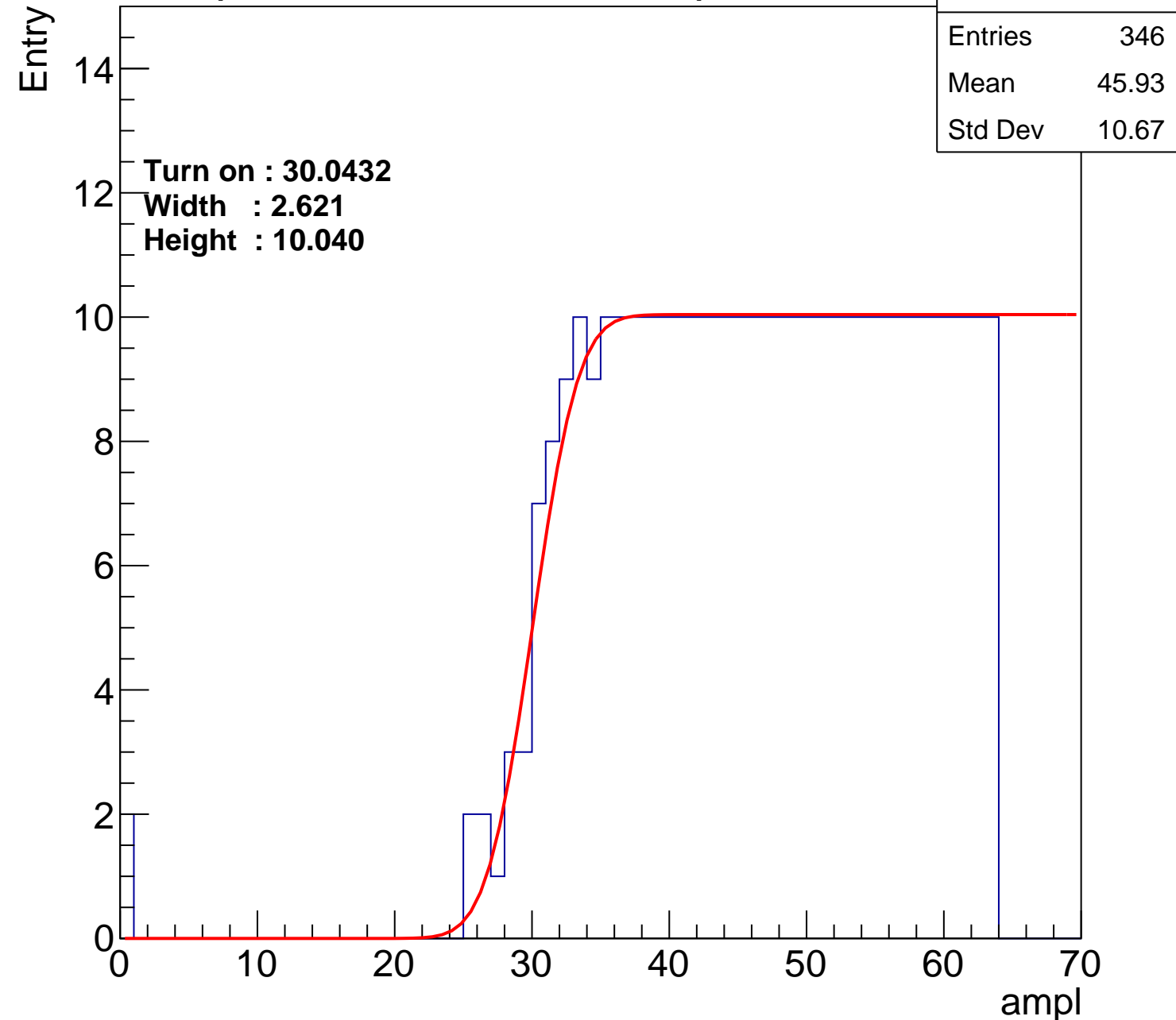
Width : 2.621

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.98
Std Dev	11.3

Turn on : 27.5784

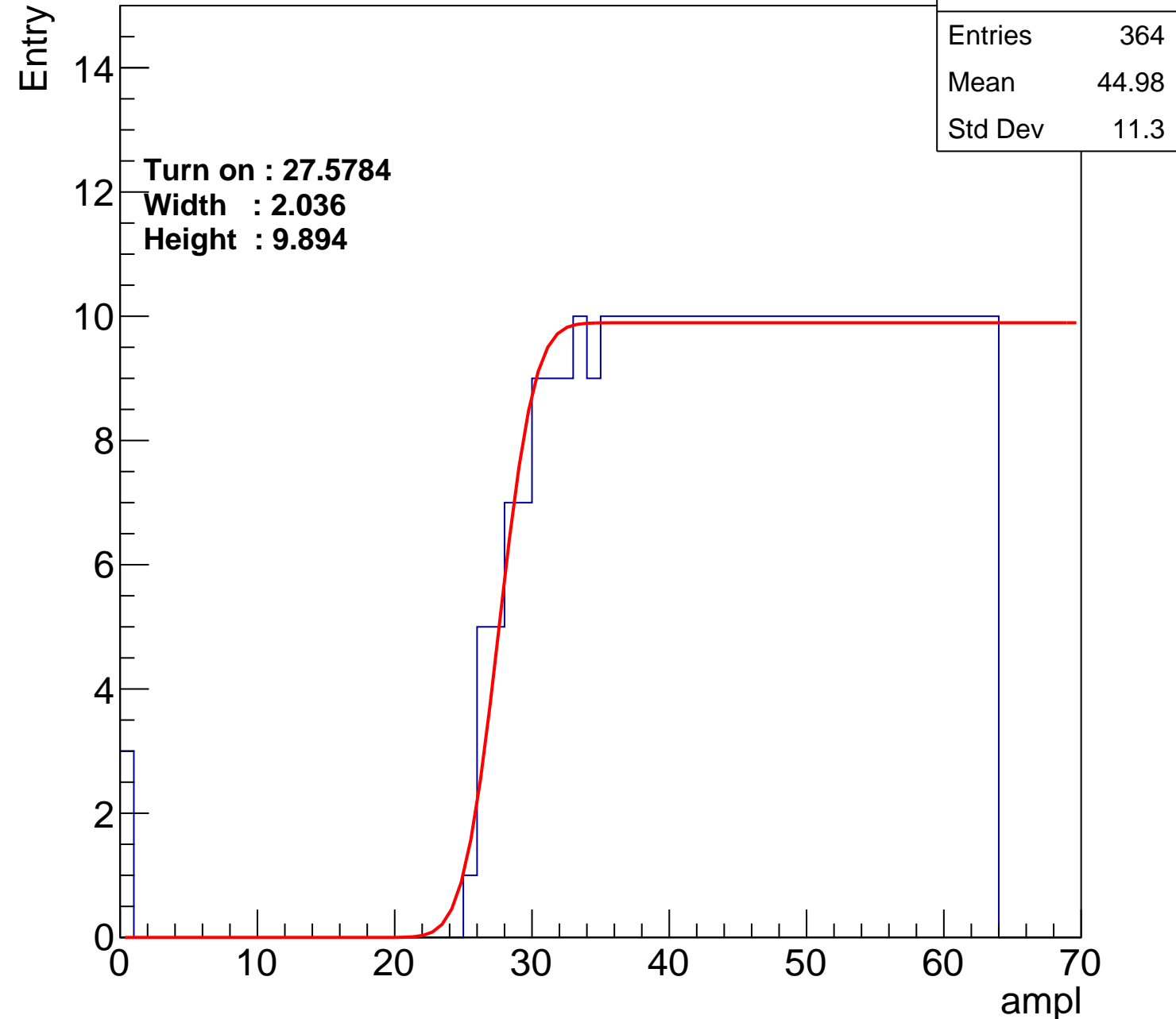
Width : 2.036

Height : 9.894

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	340
Mean	46.09
Std Dev	10.85

Turn on : 30.5100

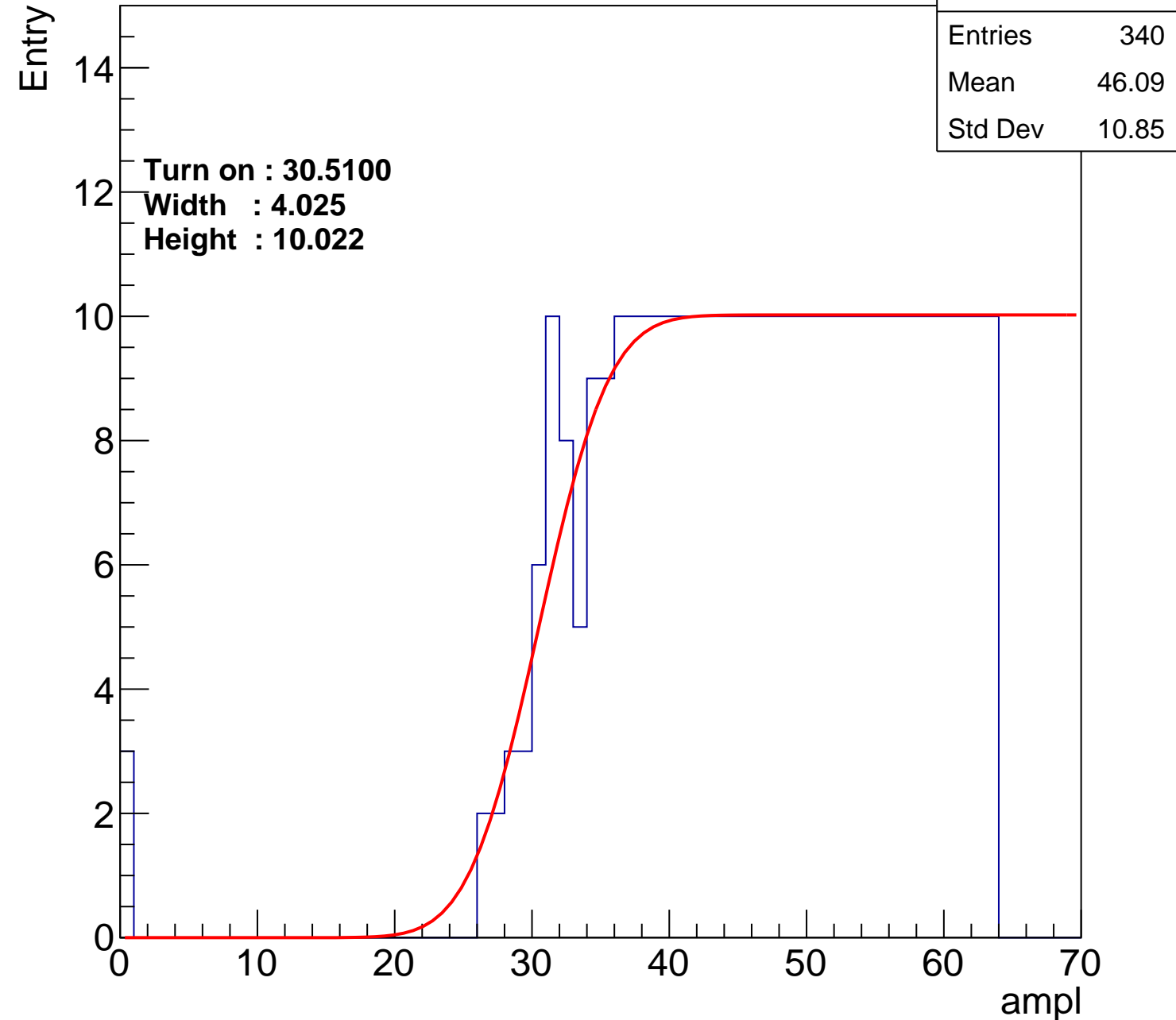
Width : 4.025

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch51

calib_packv5_042523_0143.root, FC#8, port C1

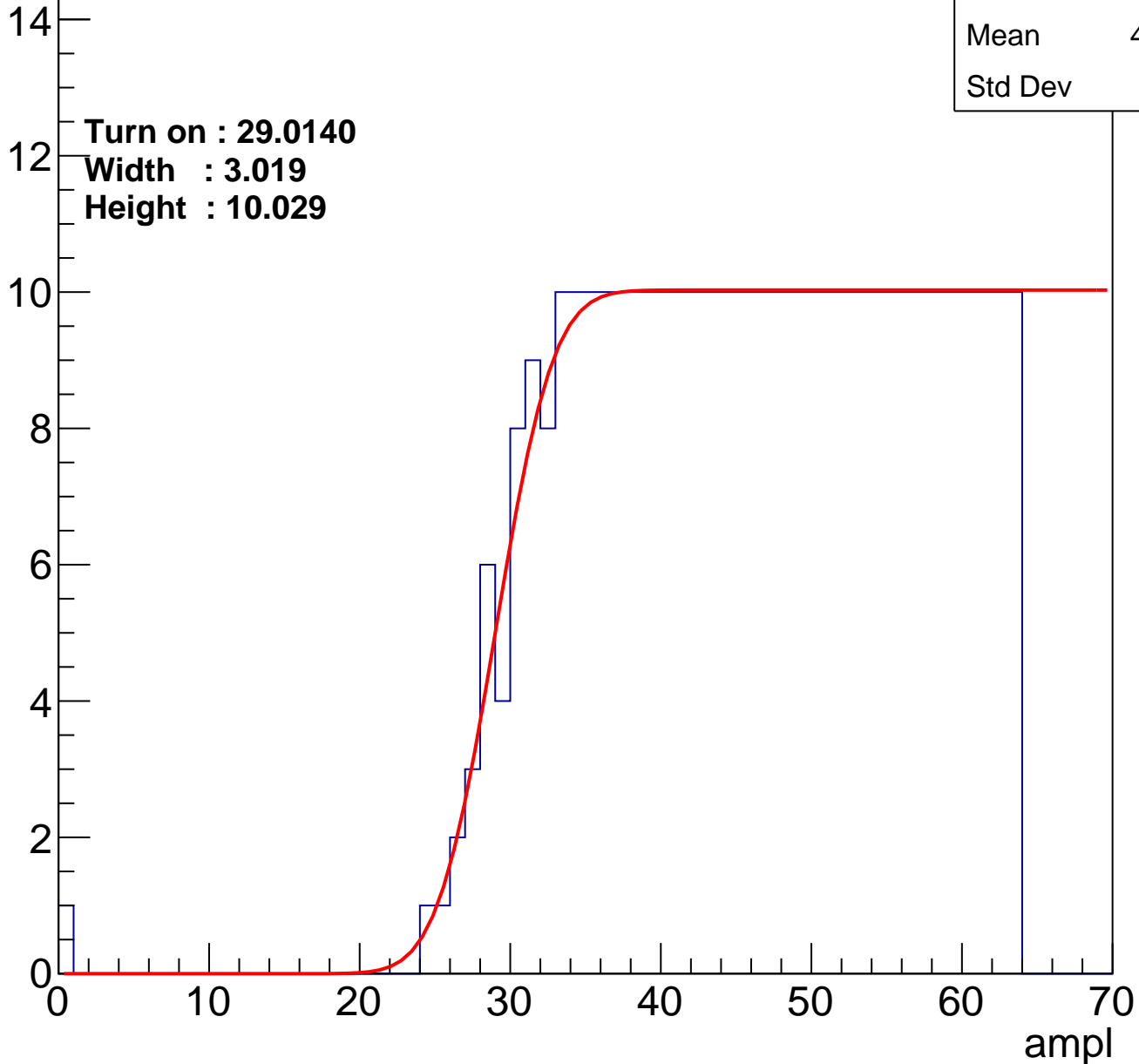
Entries	353
Mean	45.67
Std Dev	10.6

Turn on : 29.0140

Width : 3.019

Height : 10.029

Entry



B0L002S, U13-ch52

calib_packv5_042523_0143.root, FC#8, port C1

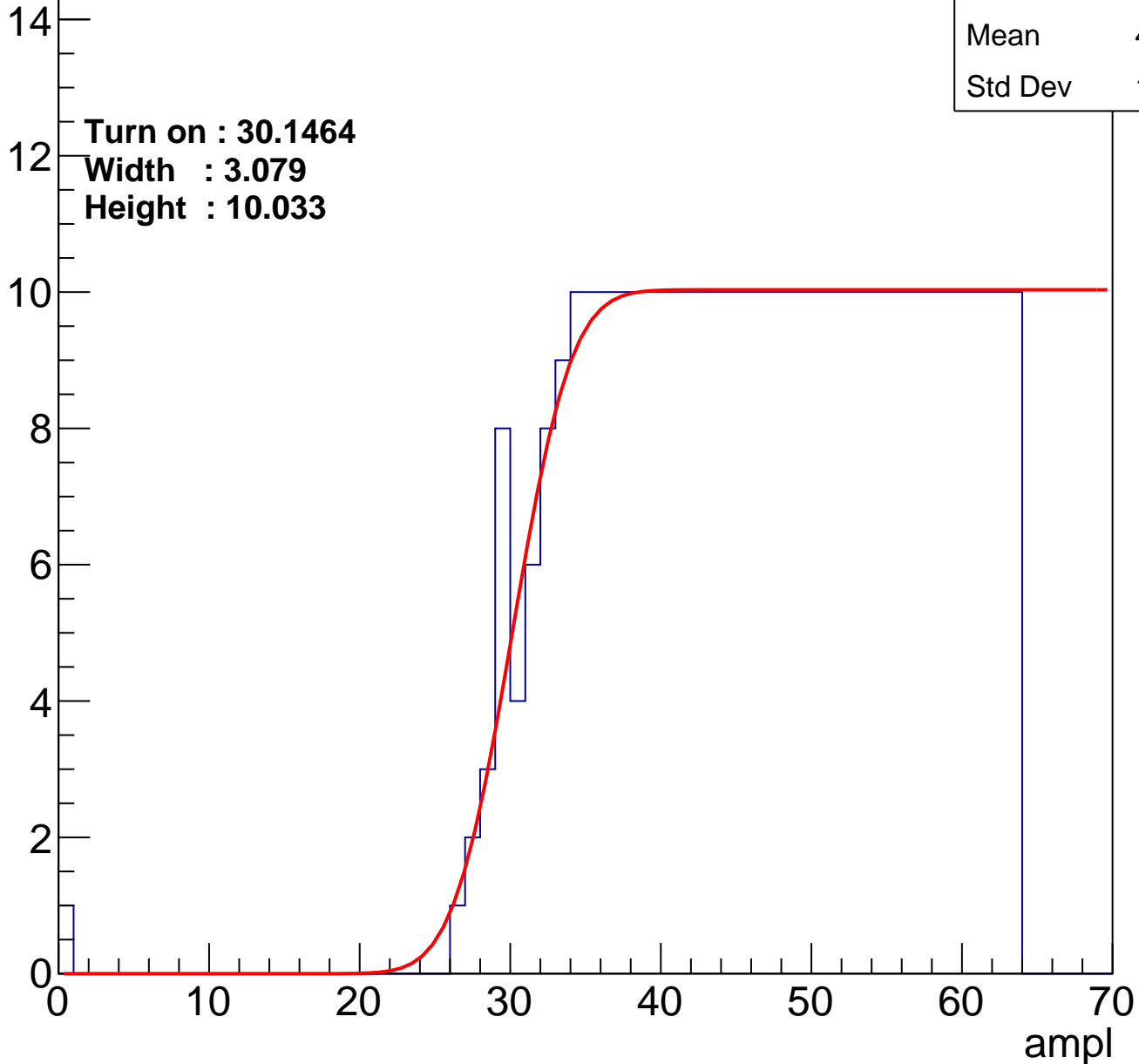
Entries	342
Mean	46.21
Std Dev	10.31

Turn on : 30.1464

Width : 3.079

Height : 10.033

Entry



B0L002S, U13-ch53

calib_packv5_042523_0143.root, FC#8, port C1

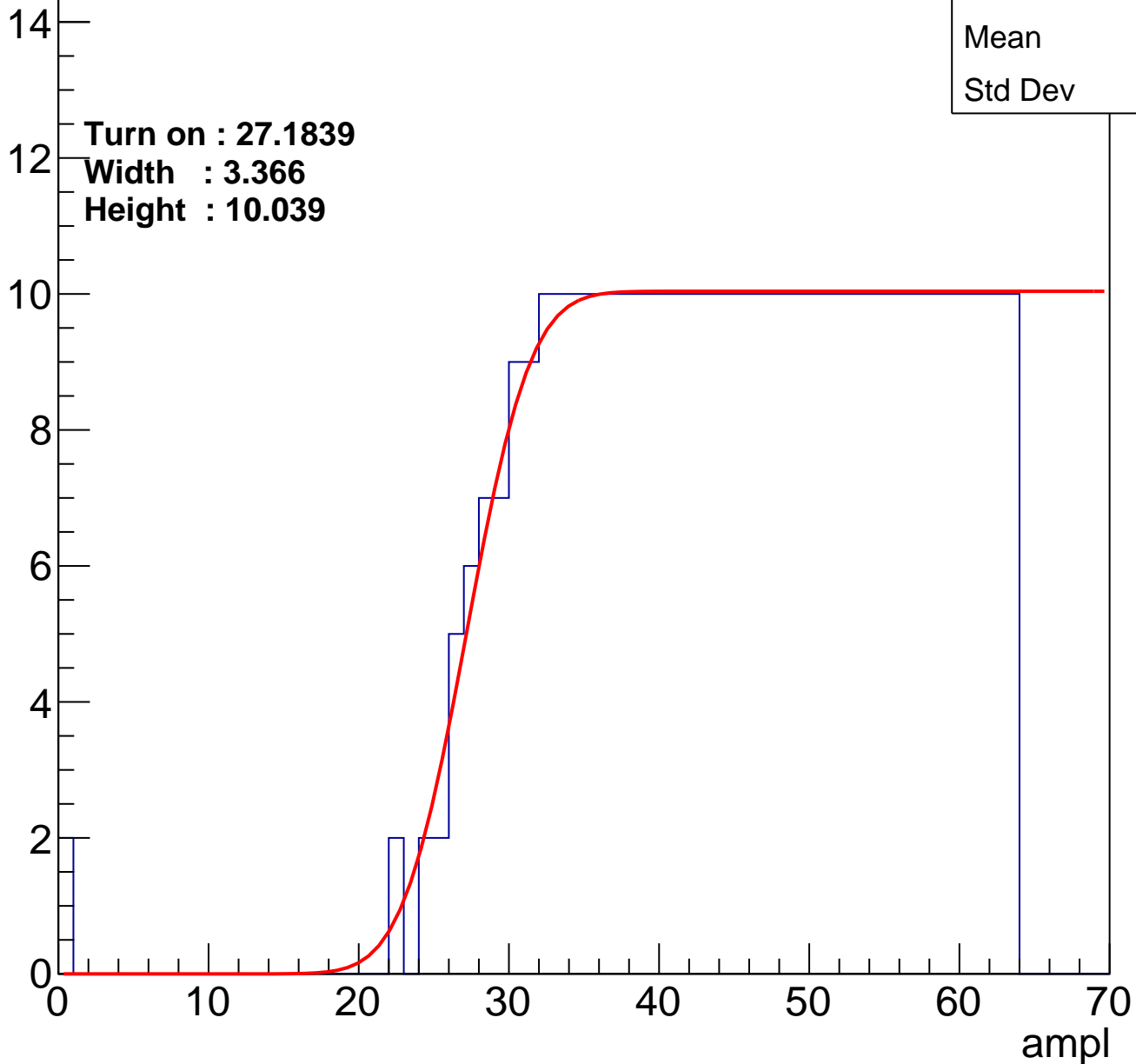
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.1839

Width : 3.366

Height : 10.039

Entry



B0L002S, U13-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.23
Std Dev	10.83

Turn on : 28.0215

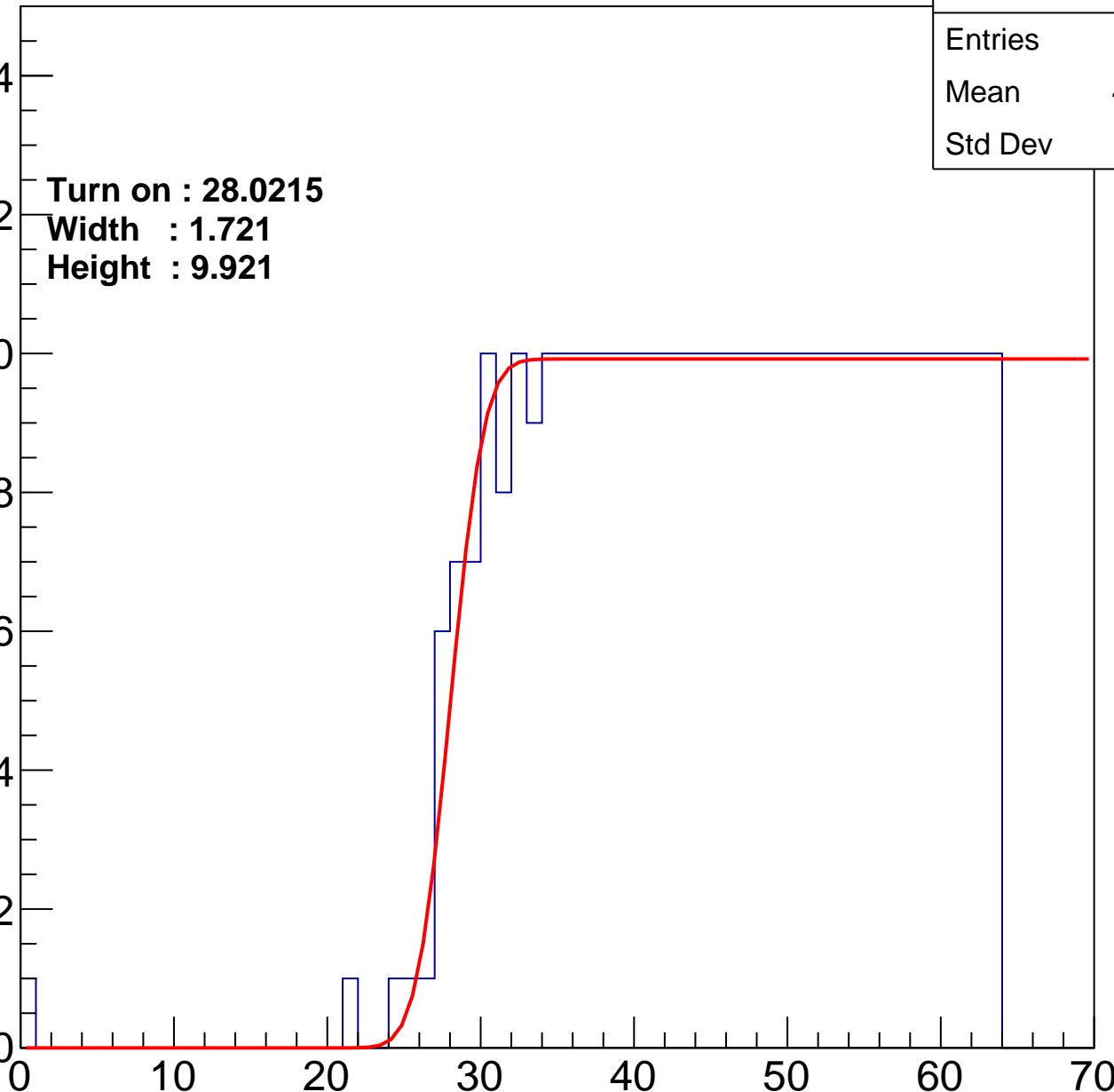
Width : 1.721

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch55

calib_packv5_042523_0143.root, FC#8, port C1

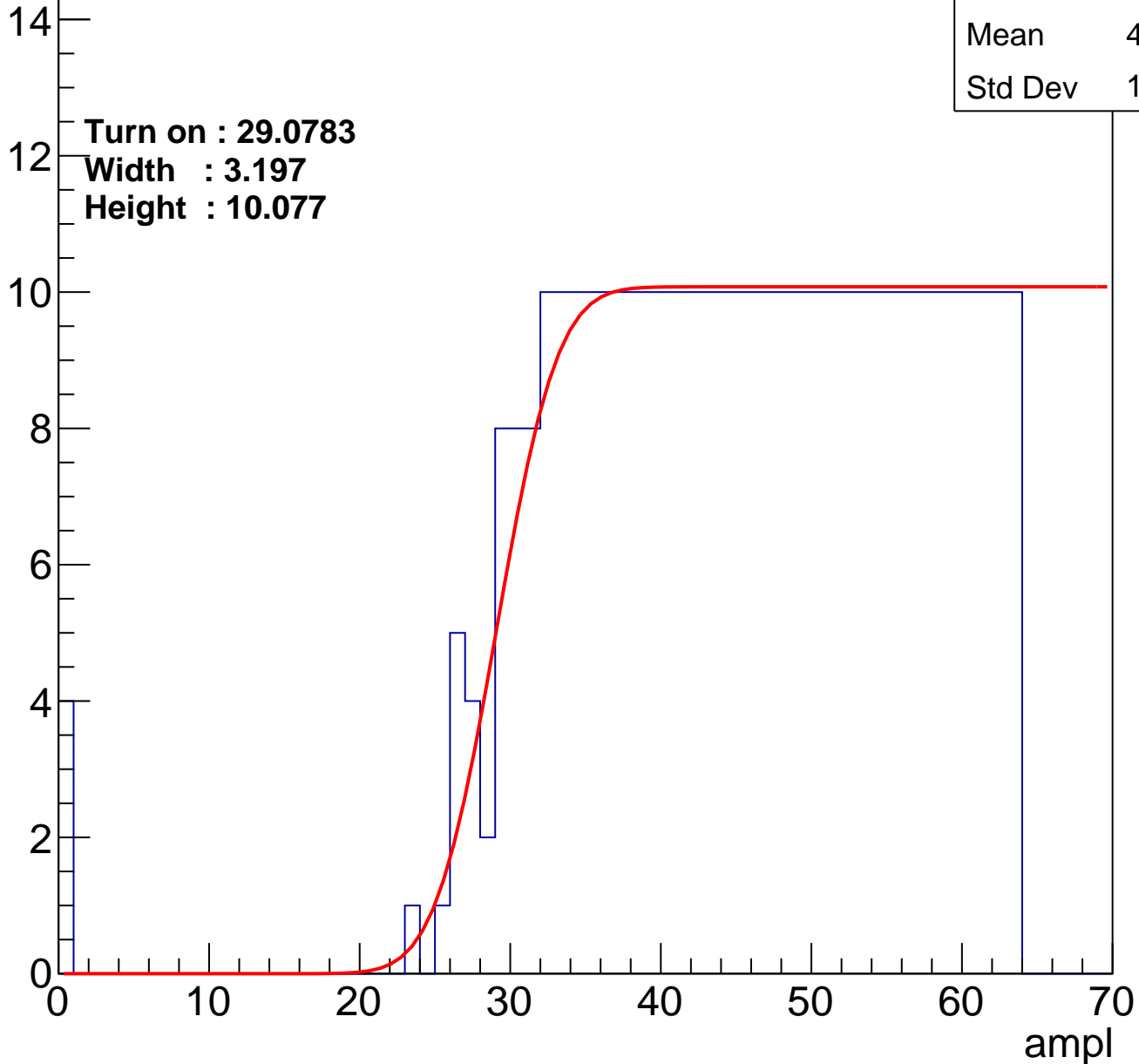
Entries	361
Mean	45.05
Std Dev	11.45

Turn on : 29.0783

Width : 3.197

Height : 10.077

Entry



B0L002S, U13-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.91
Std Dev	10.53

Turn on : 29.0607

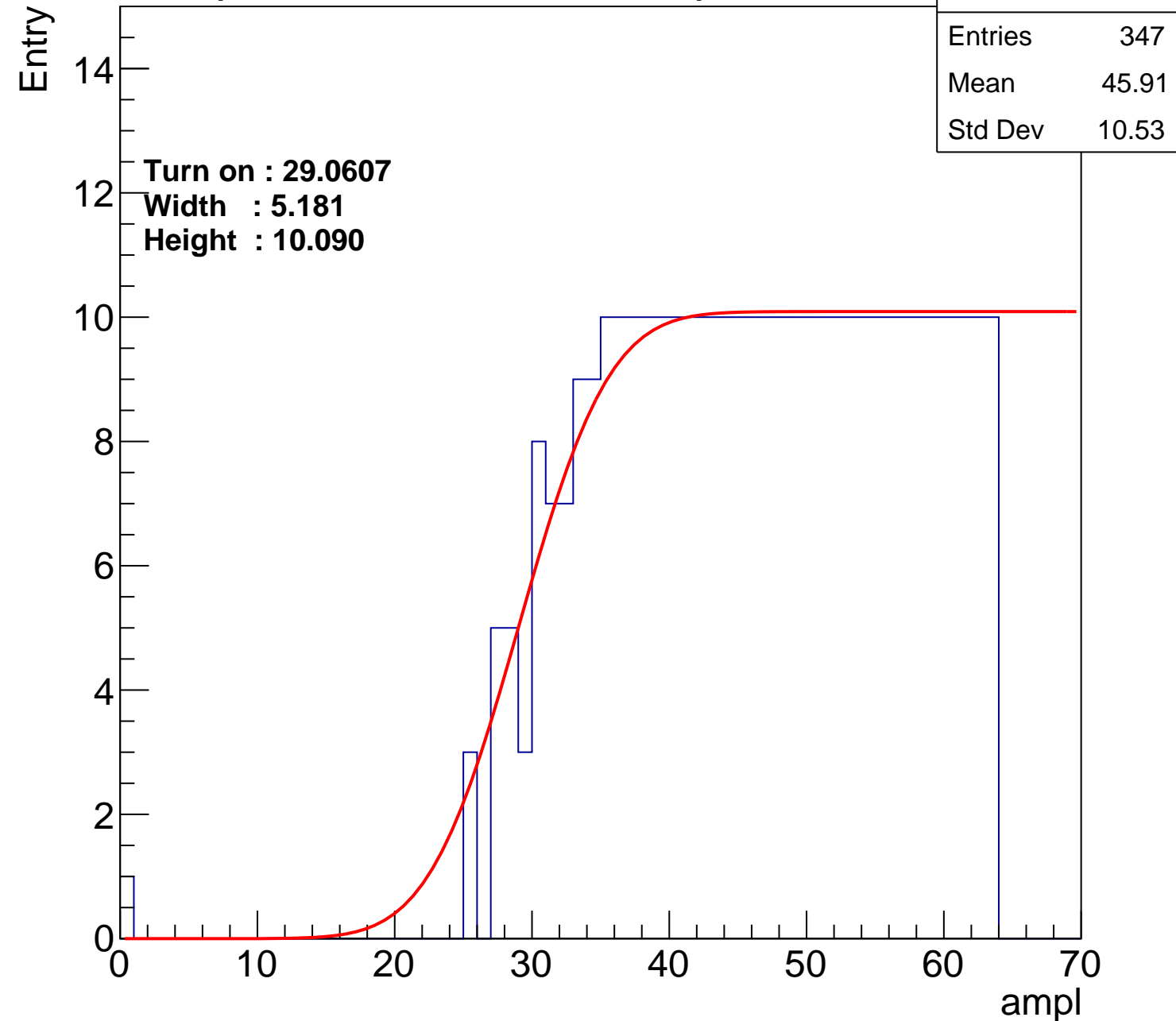
Width : 5.181

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.71
Std Dev	11.43

Turn on : 27.9777

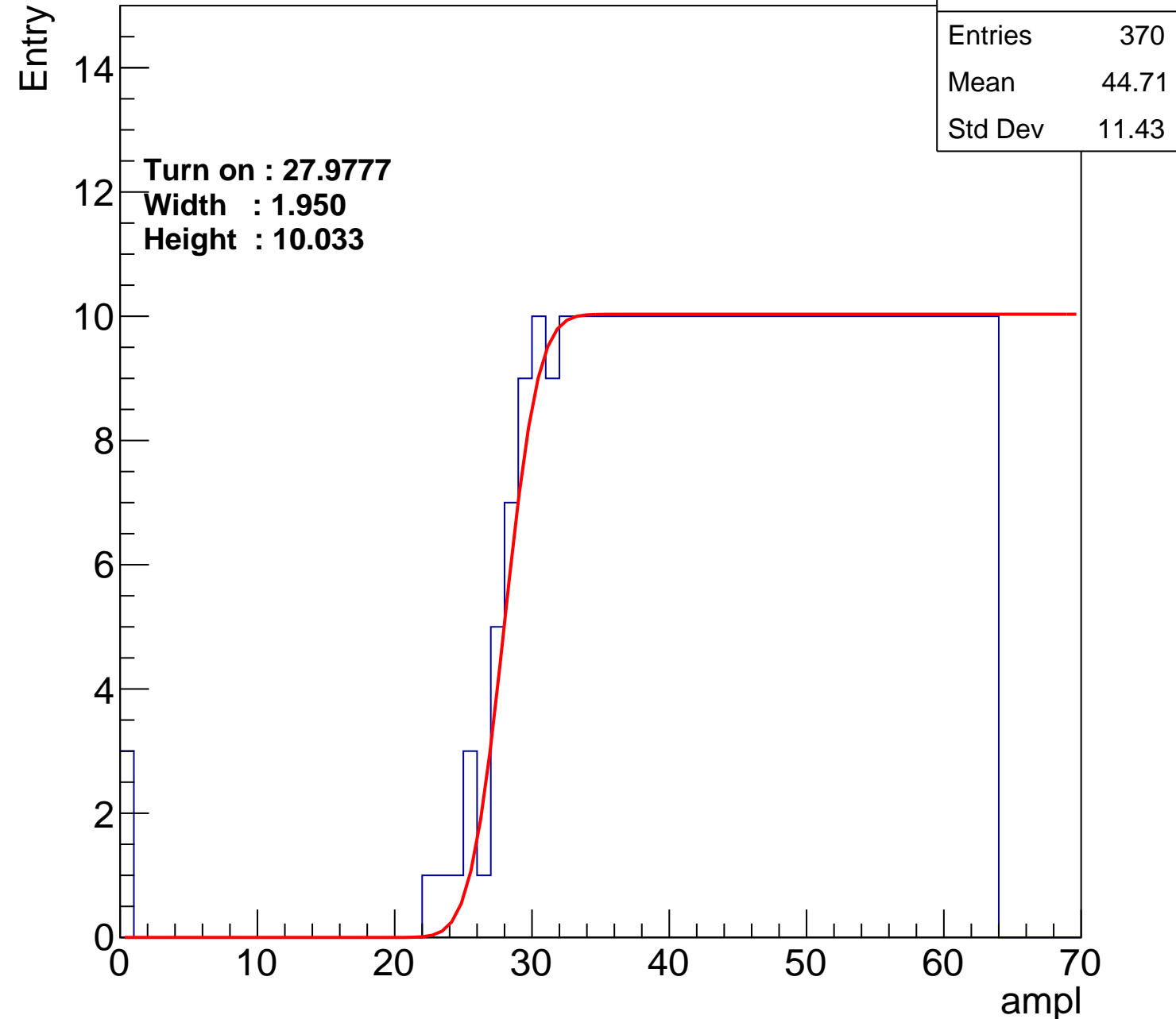
Width : 1.950

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.63
Std Dev	11.8

Turn on : 27.1256

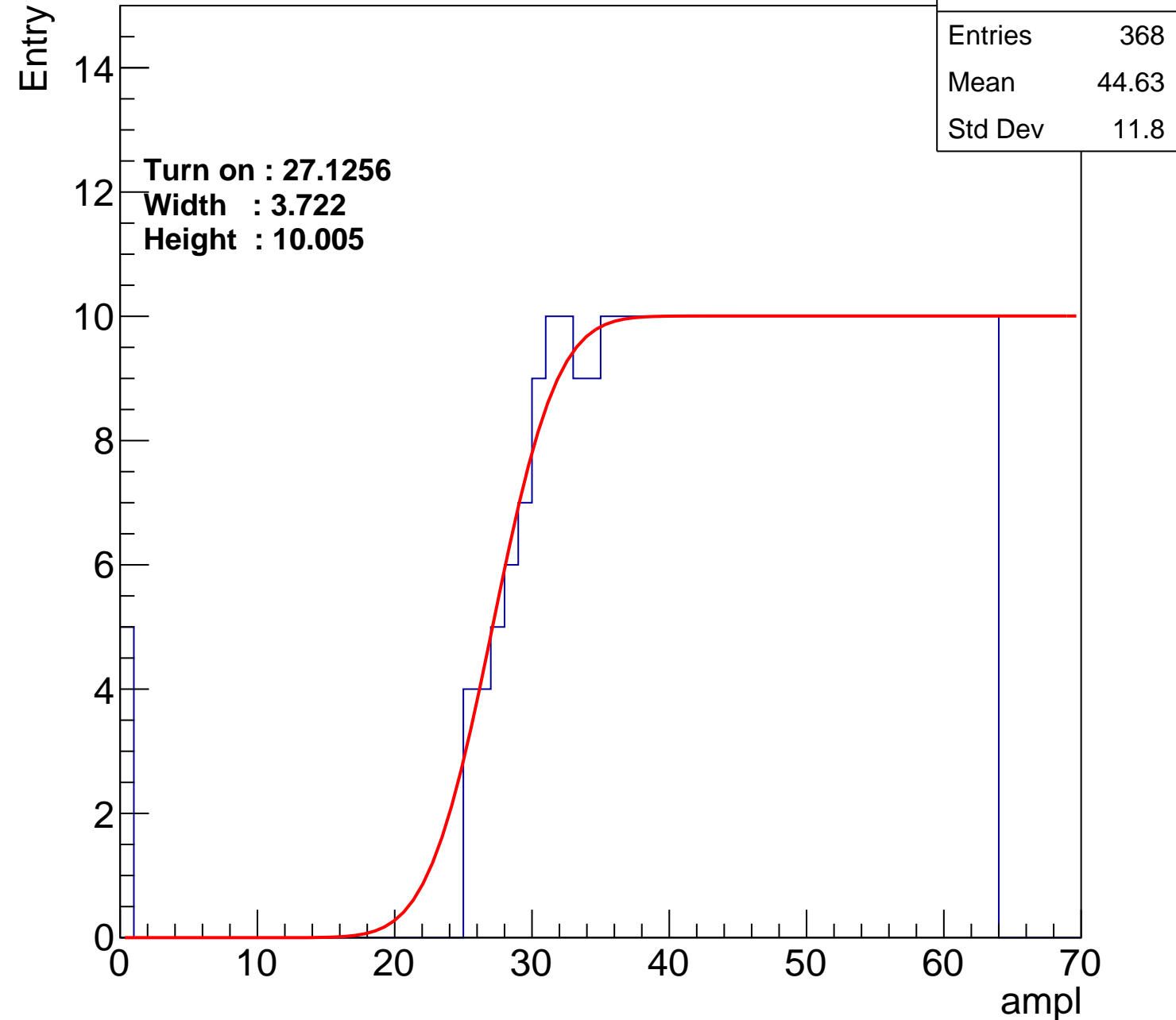
Width : 3.722

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.92
Std Dev	11.15

Turn on : 27.6448

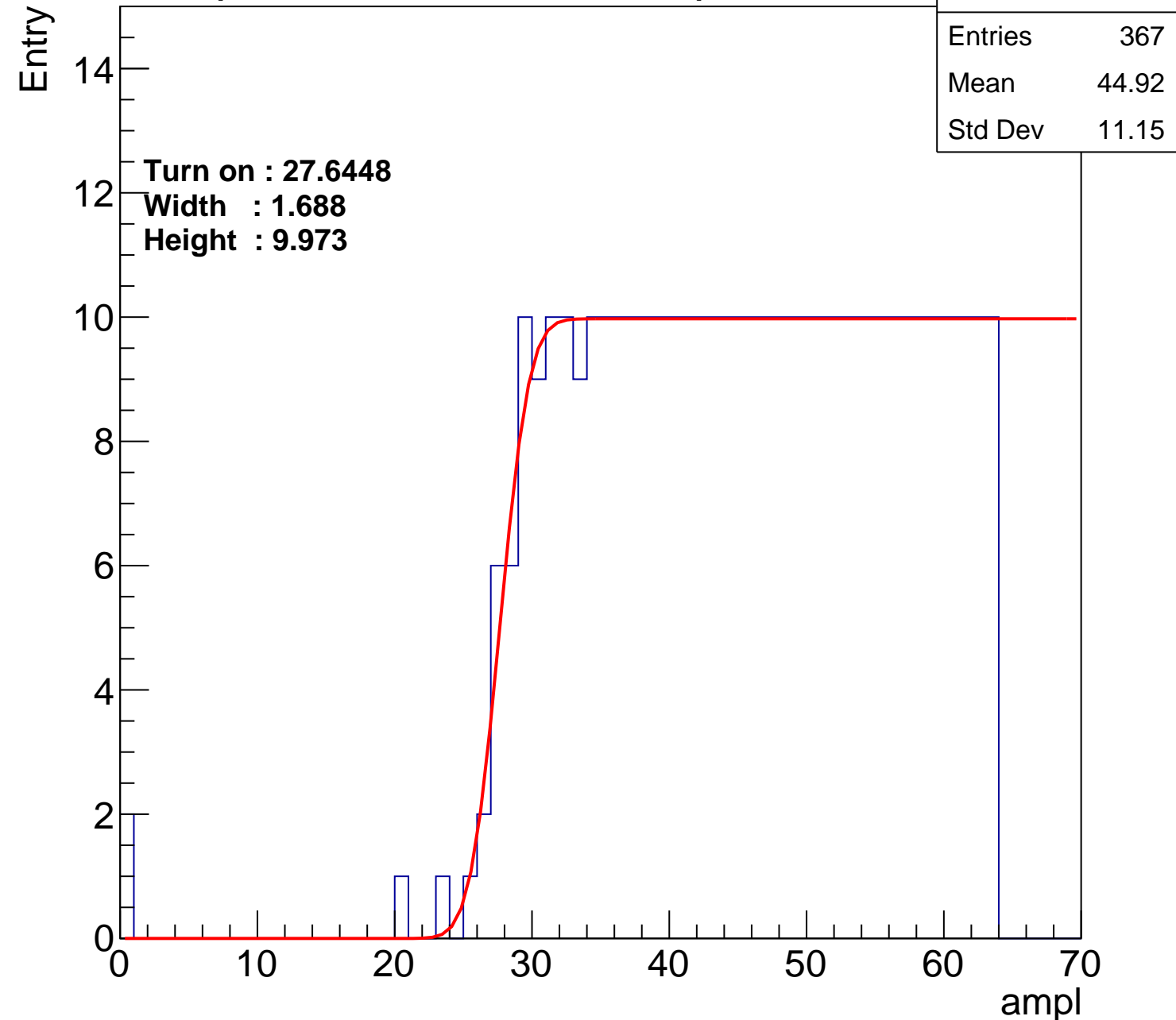
Width : 1.688

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch60

calib_packv5_042523_0143.root, FC#8, port C1

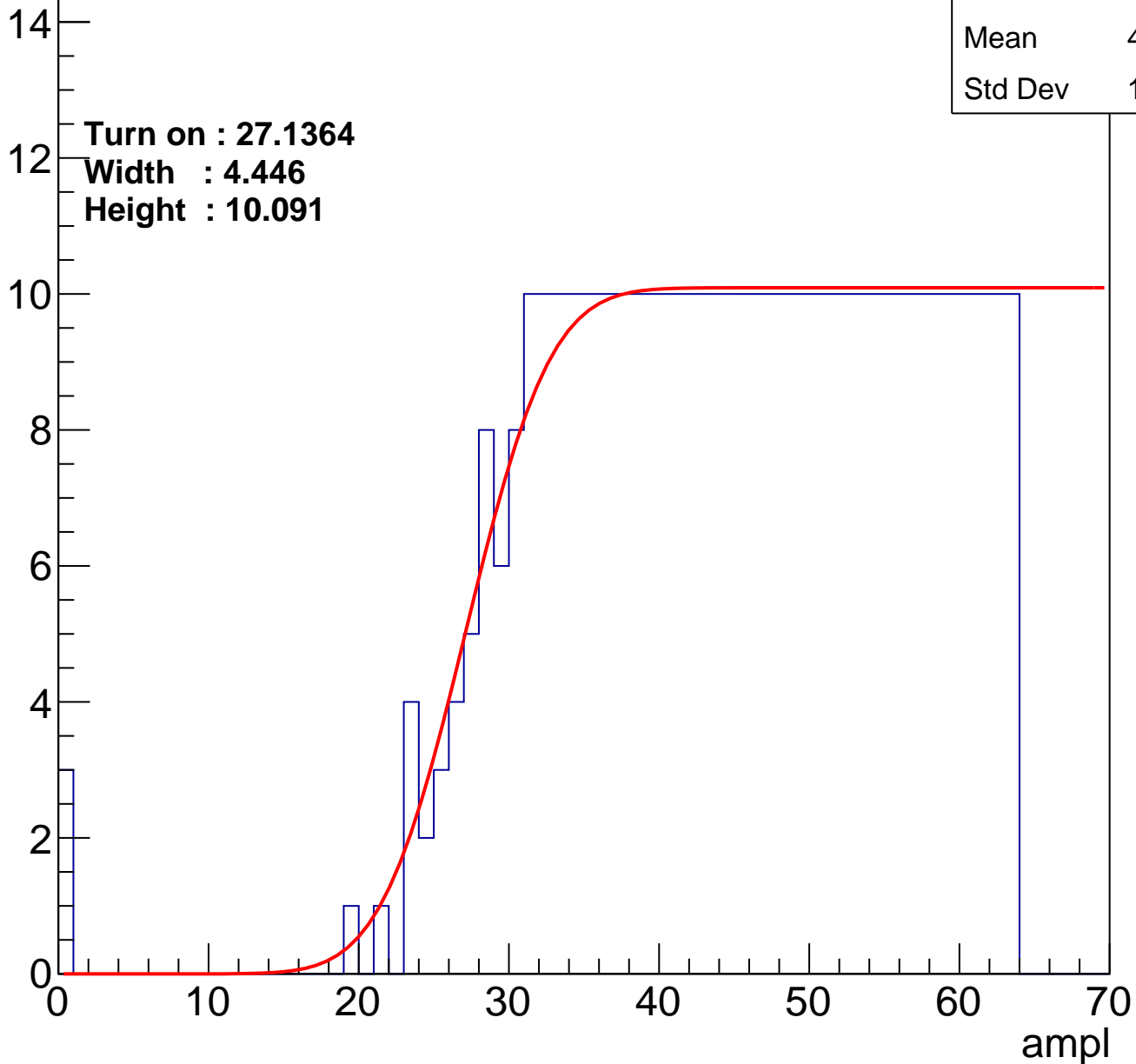
Entries	375
Mean	44.38
Std Dev	11.68

Turn on : 27.1364

Width : 4.446

Height : 10.091

Entry



B0L002S, U13-ch61

calib_packv5_042523_0143.root, FC#8, port C1

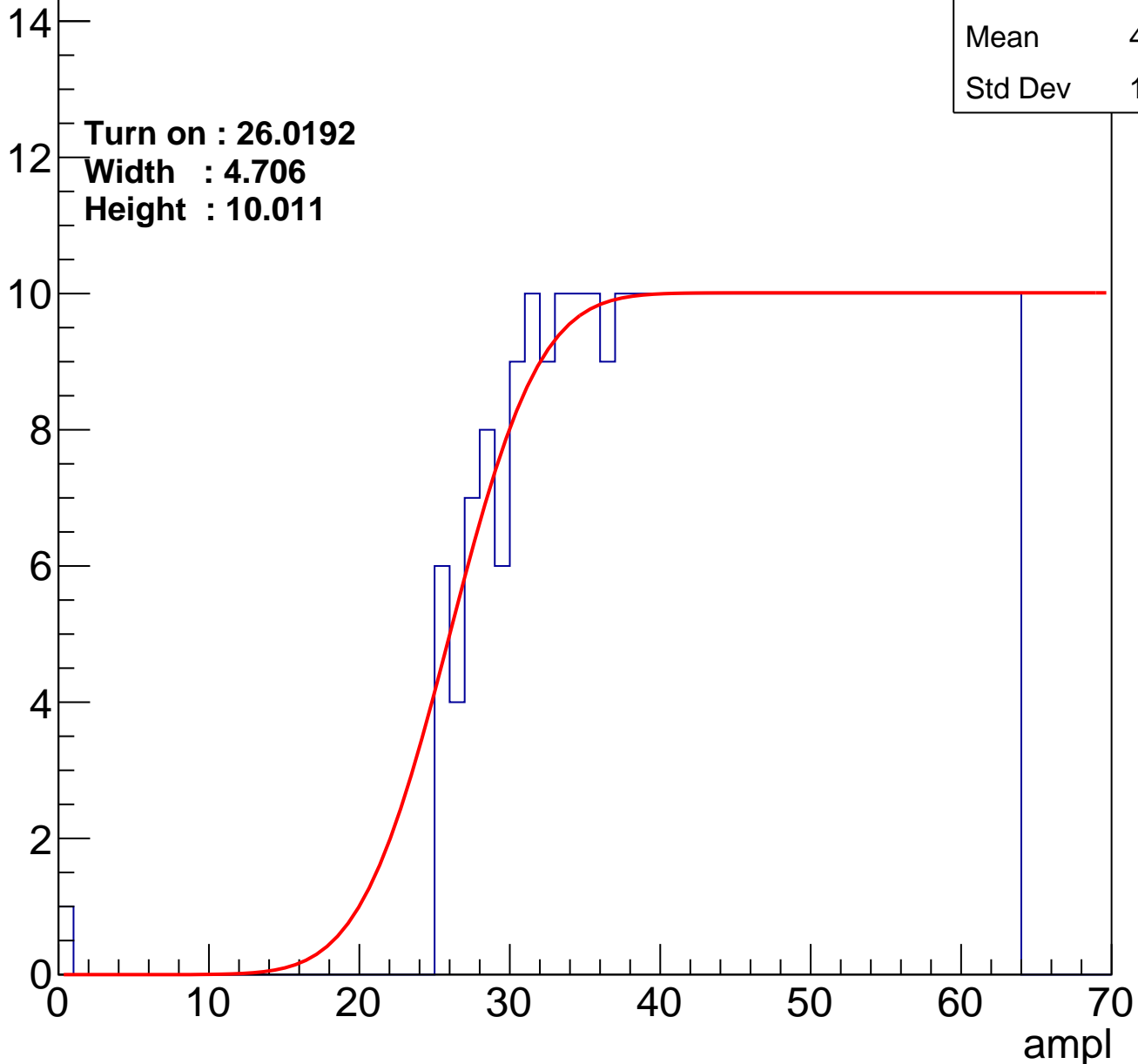
Entries	369
Mean	44.86
Std Dev	11.04

Turn on : 26.0192

Width : 4.706

Height : 10.011

Entry



B0L002S, U13-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.79
Std Dev	10.57

Turn on : 29.3656

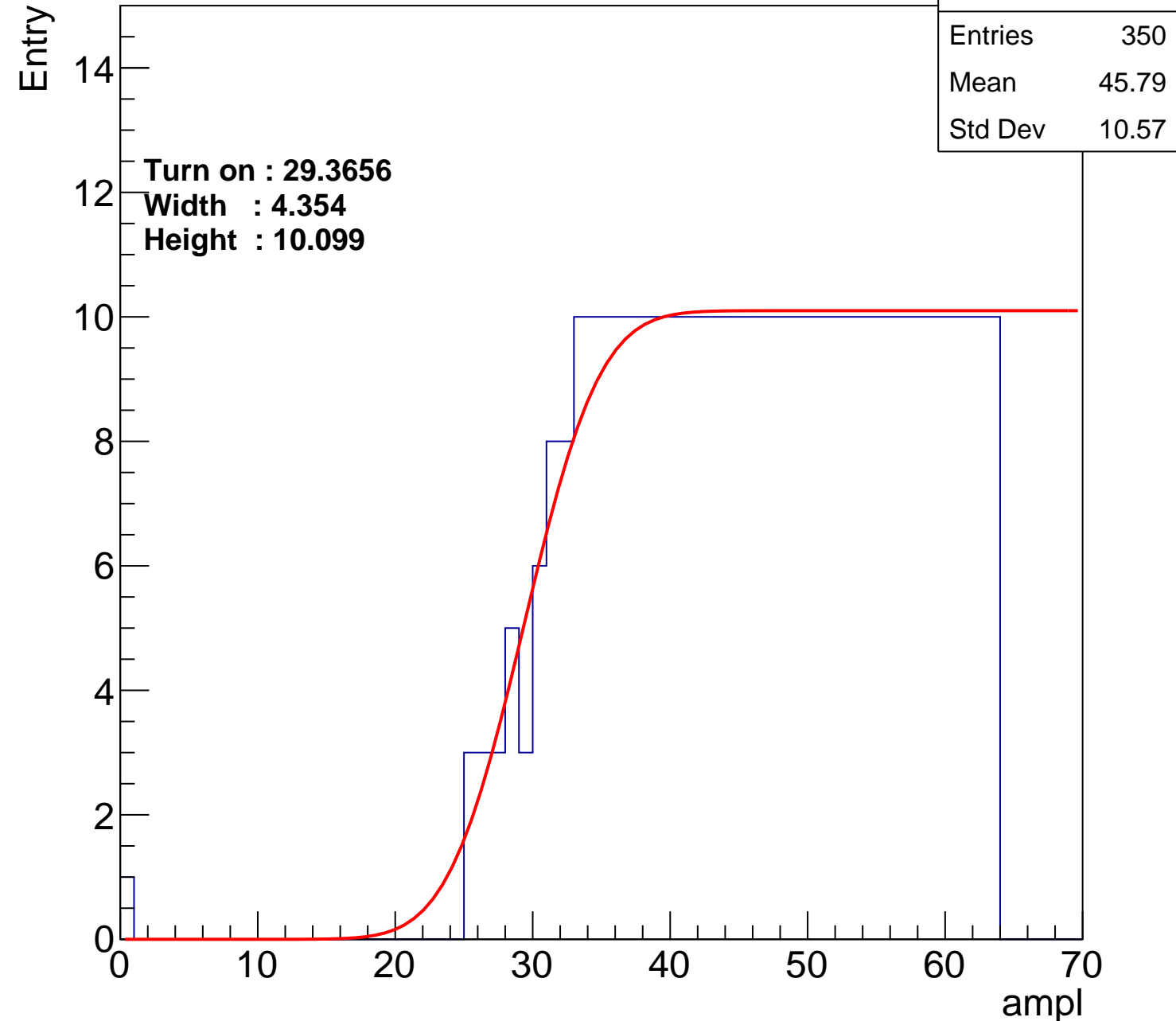
Width : 4.354

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.33
Std Dev	10.93

Turn on : 28.3909

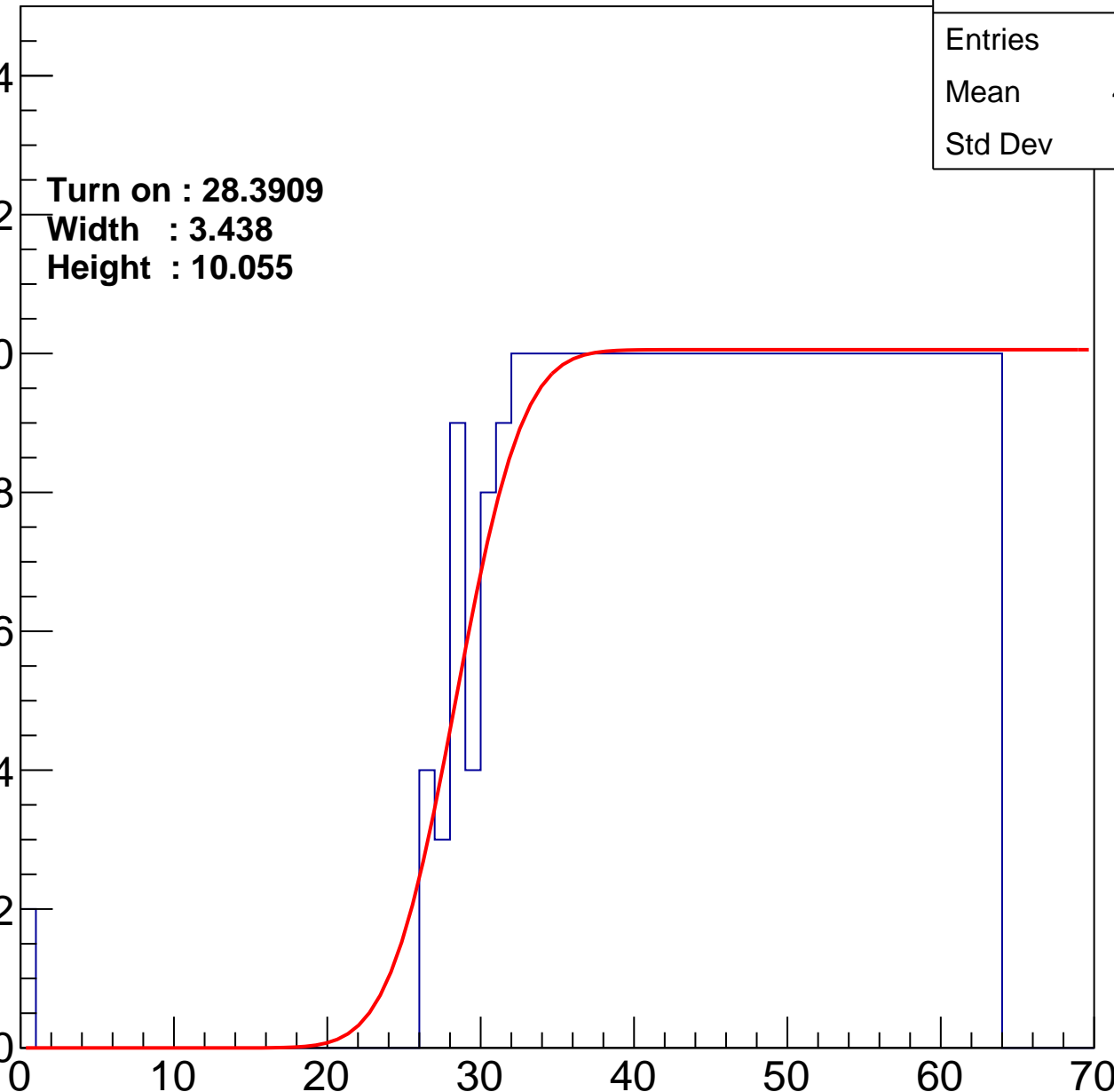
Width : 3.438

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
Mean	46.11
Std Dev	10.55

Mean	46.11
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Std Dev	10.55
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Width : 2.847

Height : 10.076



B0L002S, U13-ch65

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.16
Std Dev	11.44

Turn on : 28.7957

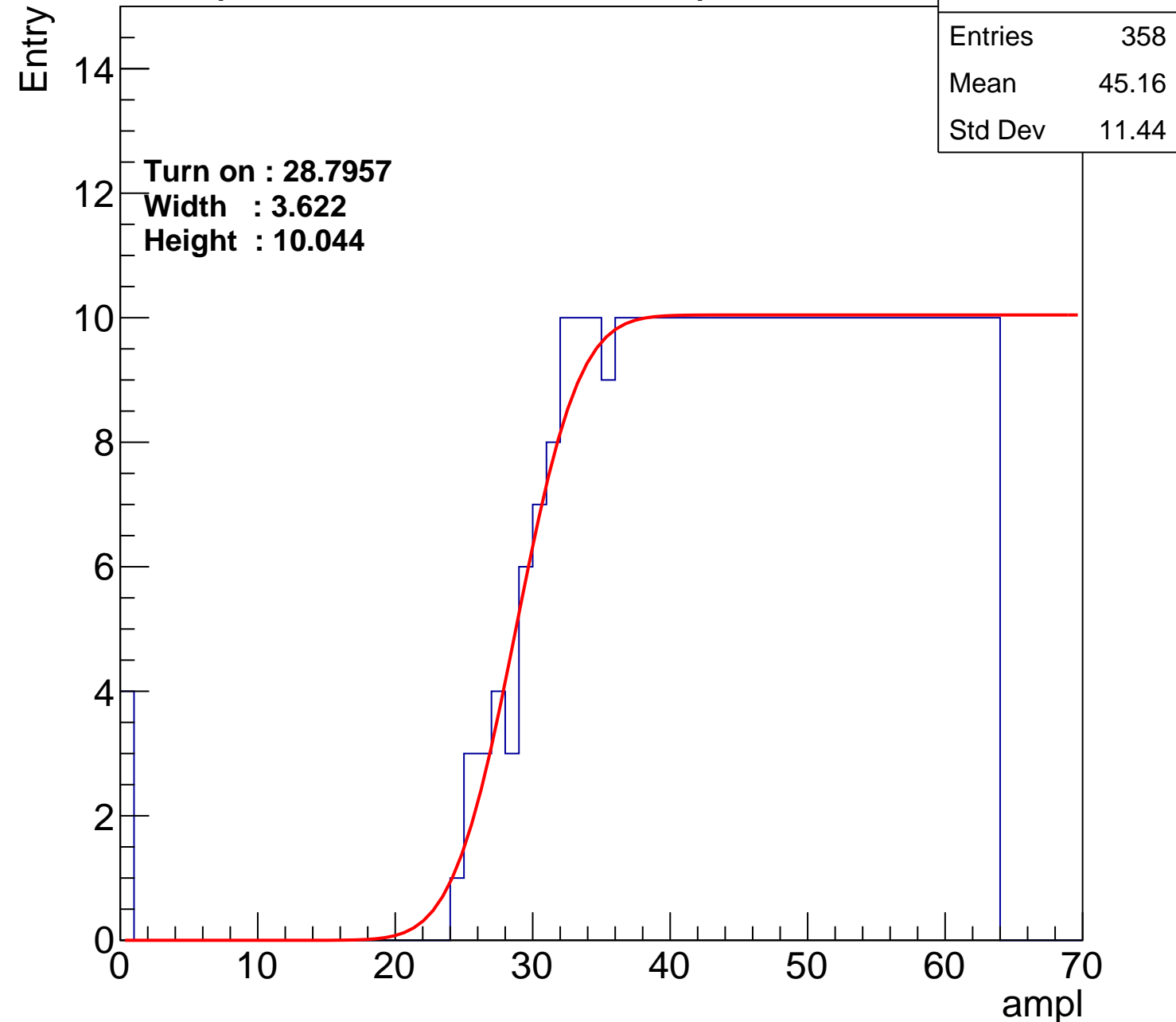
Width : 3.622

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 26.9557

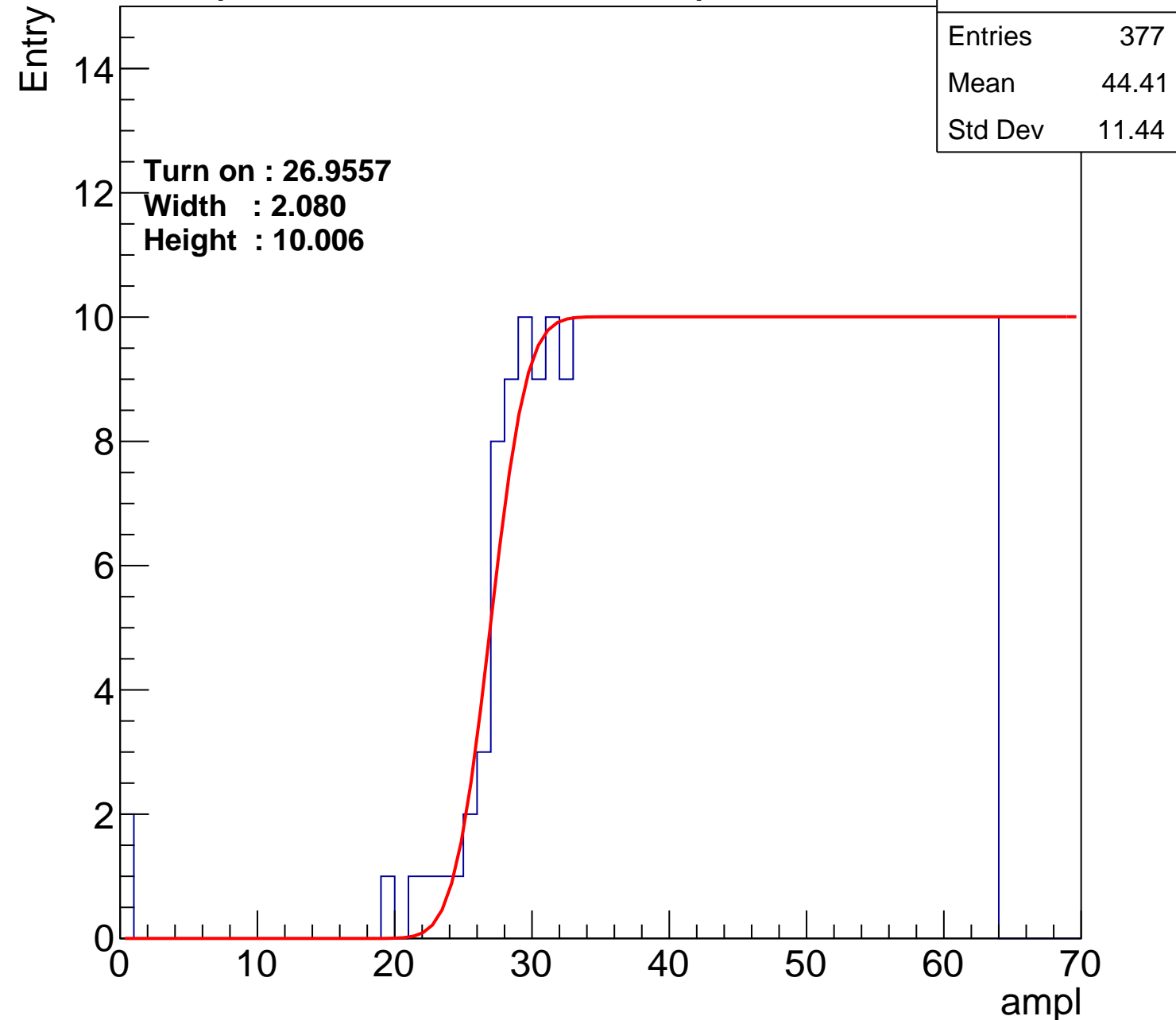
Width : 2.080

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch67

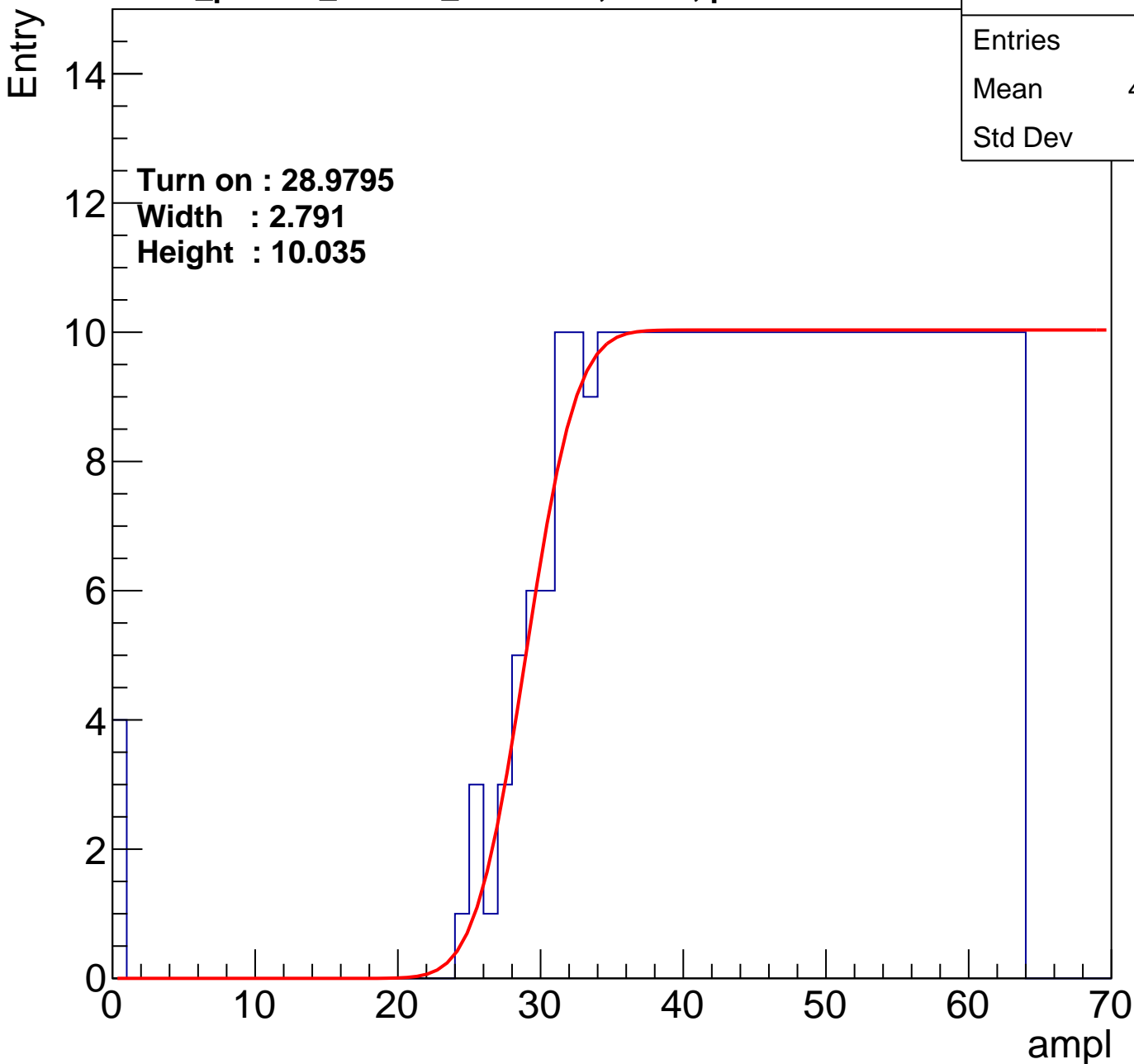
calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.19
Std Dev	11.4

Turn on : 28.9795

Width : 2.791

Height : 10.035



B0L002S, U13-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.7548

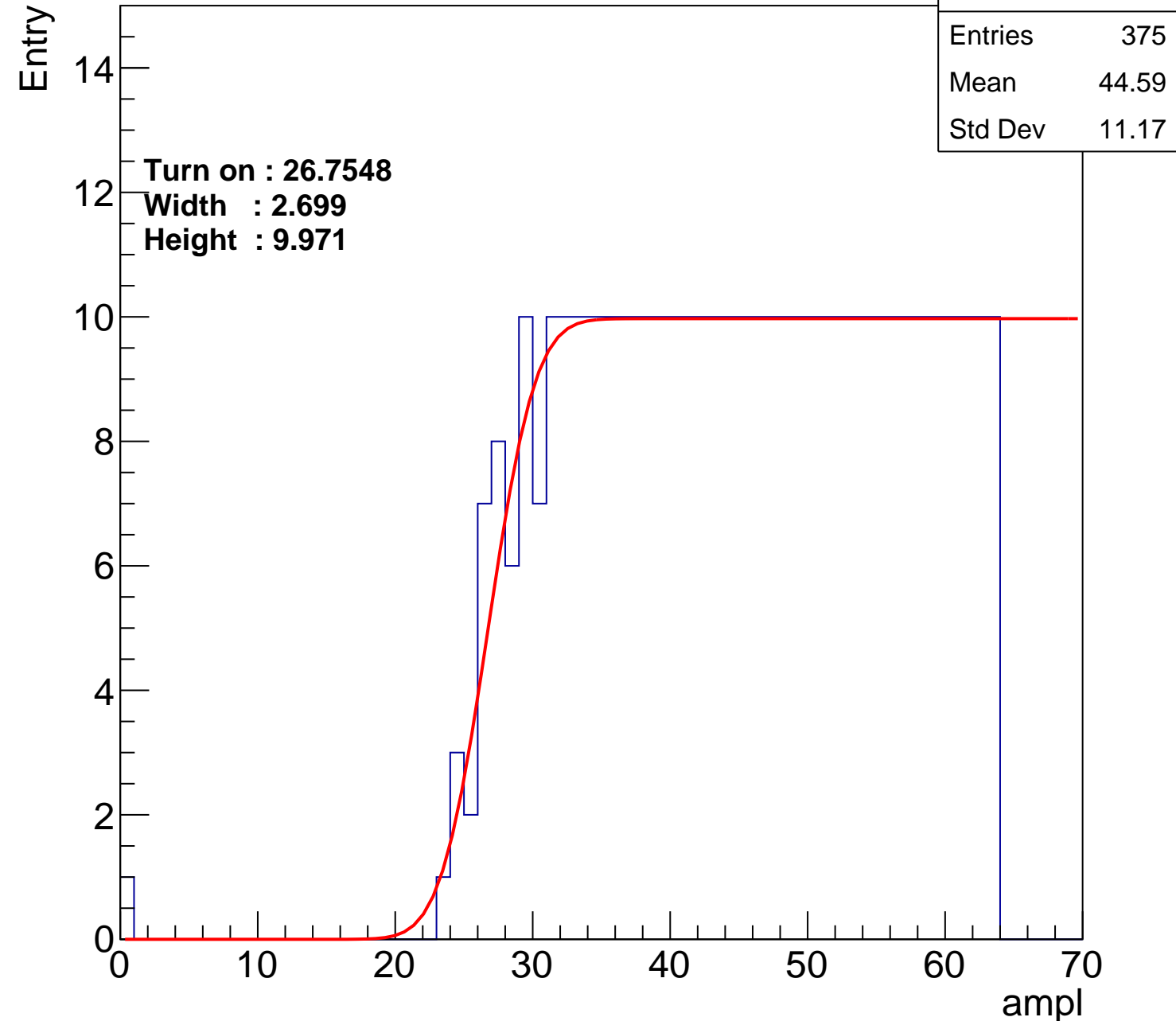
Width : 2.699

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch69

calib_packv5_042523_0143.root, FC#8, port C1

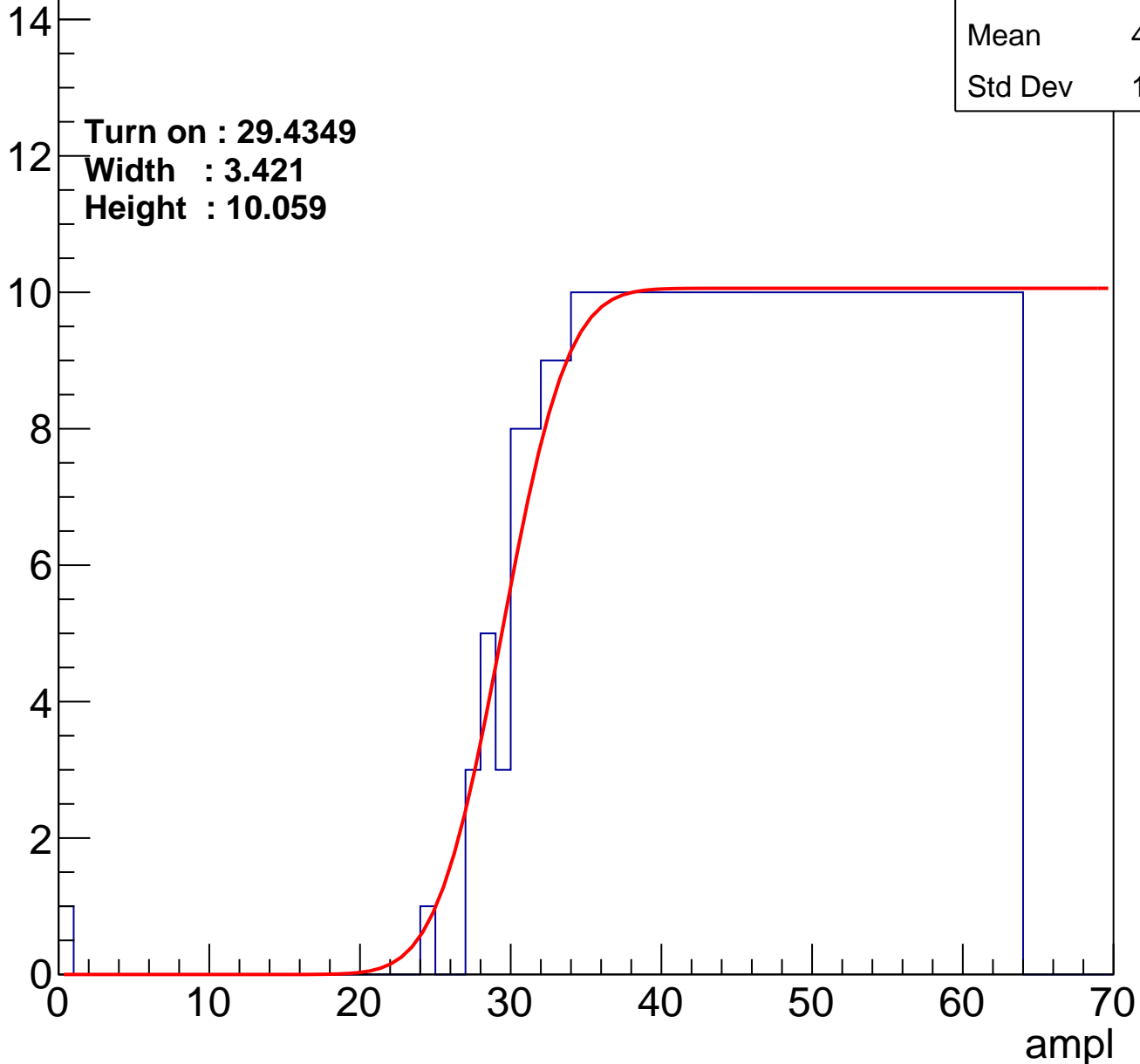
Entries	347
Mean	45.98
Std Dev	10.42

Turn on : 29.4349

Width : 3.421

Height : 10.059

Entry



B0L002S, U13-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.65
Std Dev	10.63

Turn on : 28.5243

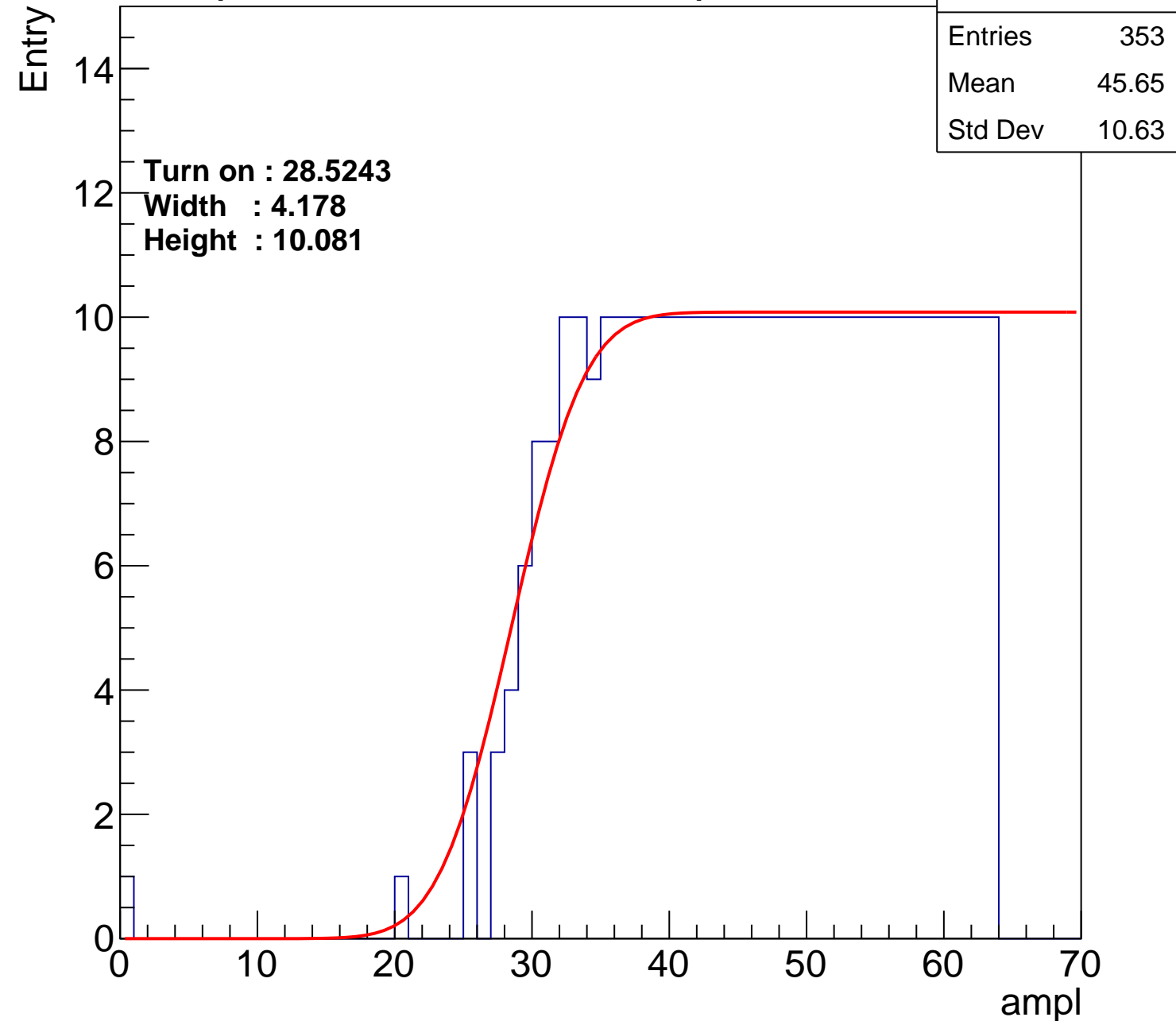
Width : 4.178

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.58
Std Dev	10.63

Turn on : 28.3593

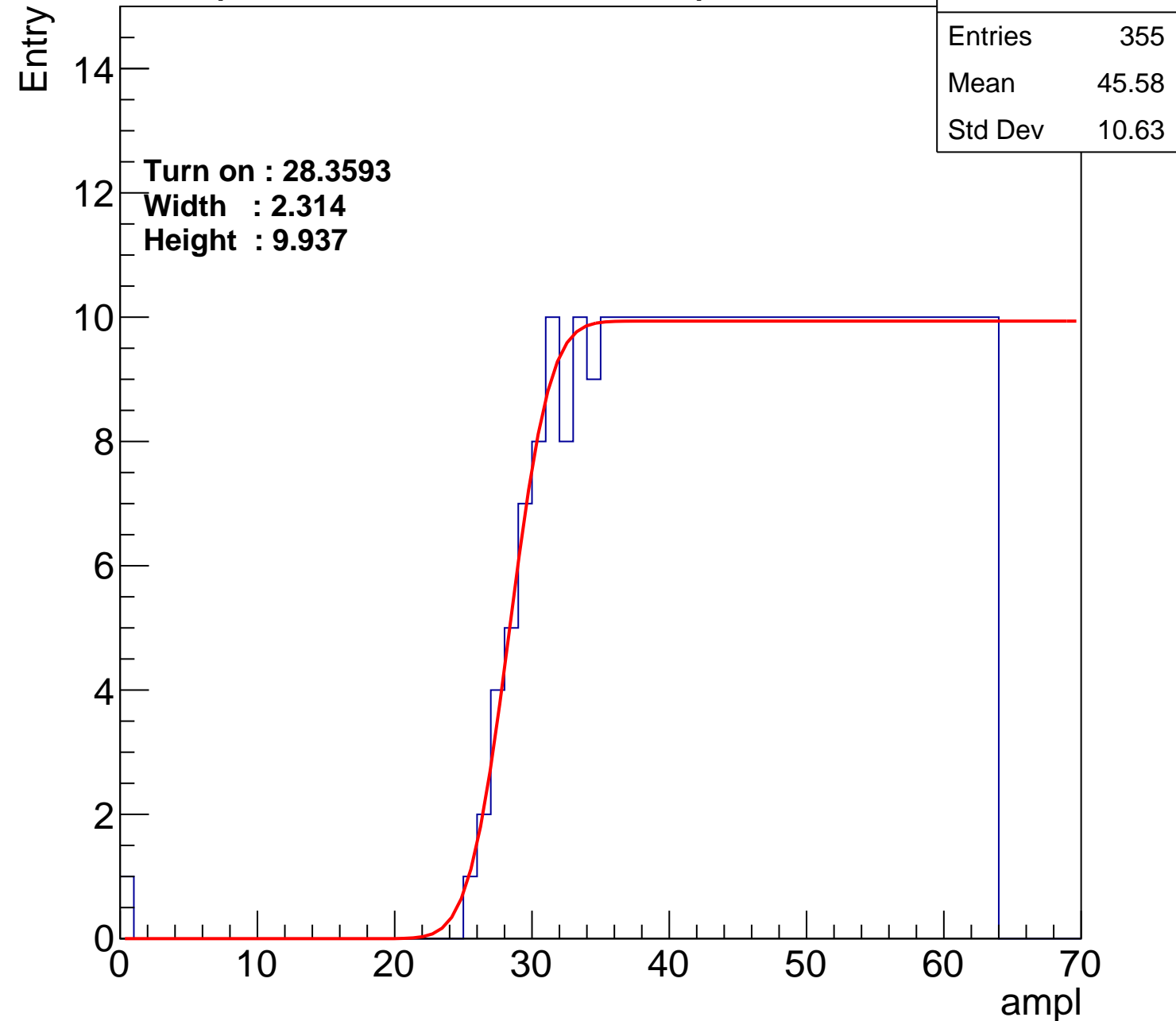
Width : 2.314

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
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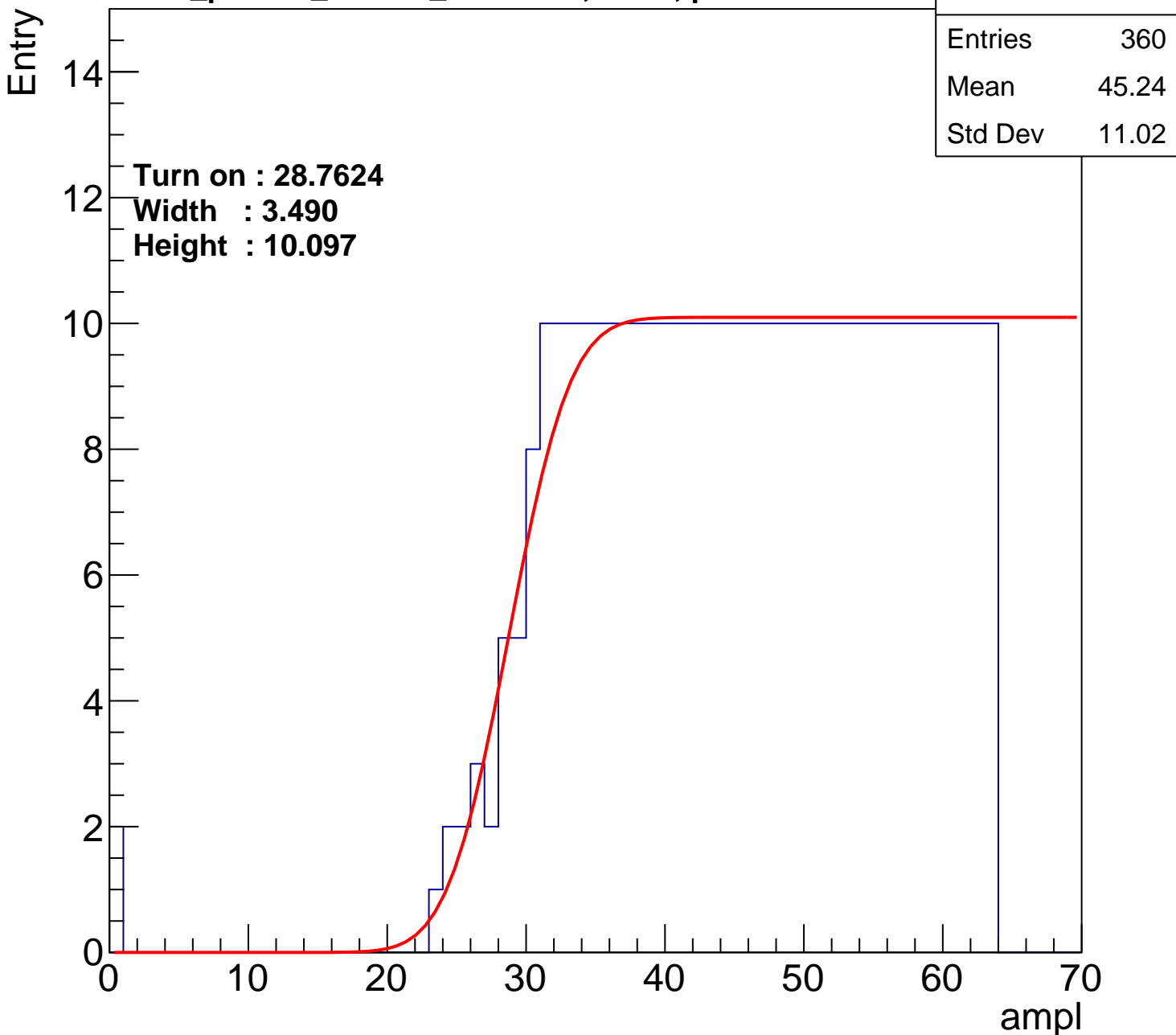
Mean	45.24
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Std Dev	11.02
---------	-------

Turn on : 28.7624

Width : 3.490

Height : 10.097



B0L002S, U13-ch73

calib_packv5_042523_0143.root, FC#8, port C1

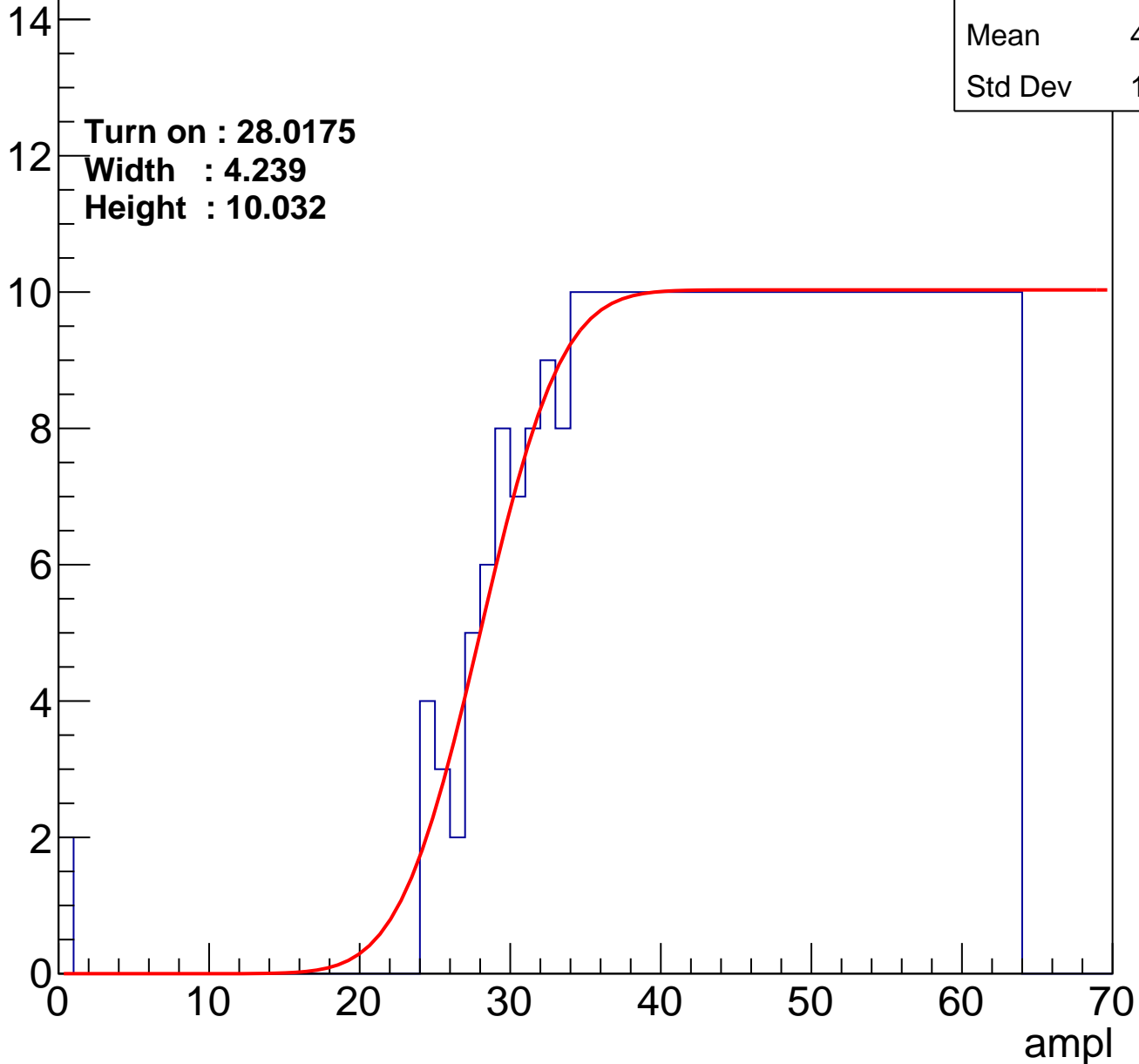
Entries	362
Mean	45.08
Std Dev	11.16

Turn on : 28.0175

Width : 4.239

Height : 10.032

Entry



B0L002S, U13-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.64
Std Dev	11.34

Turn on : 27.0371

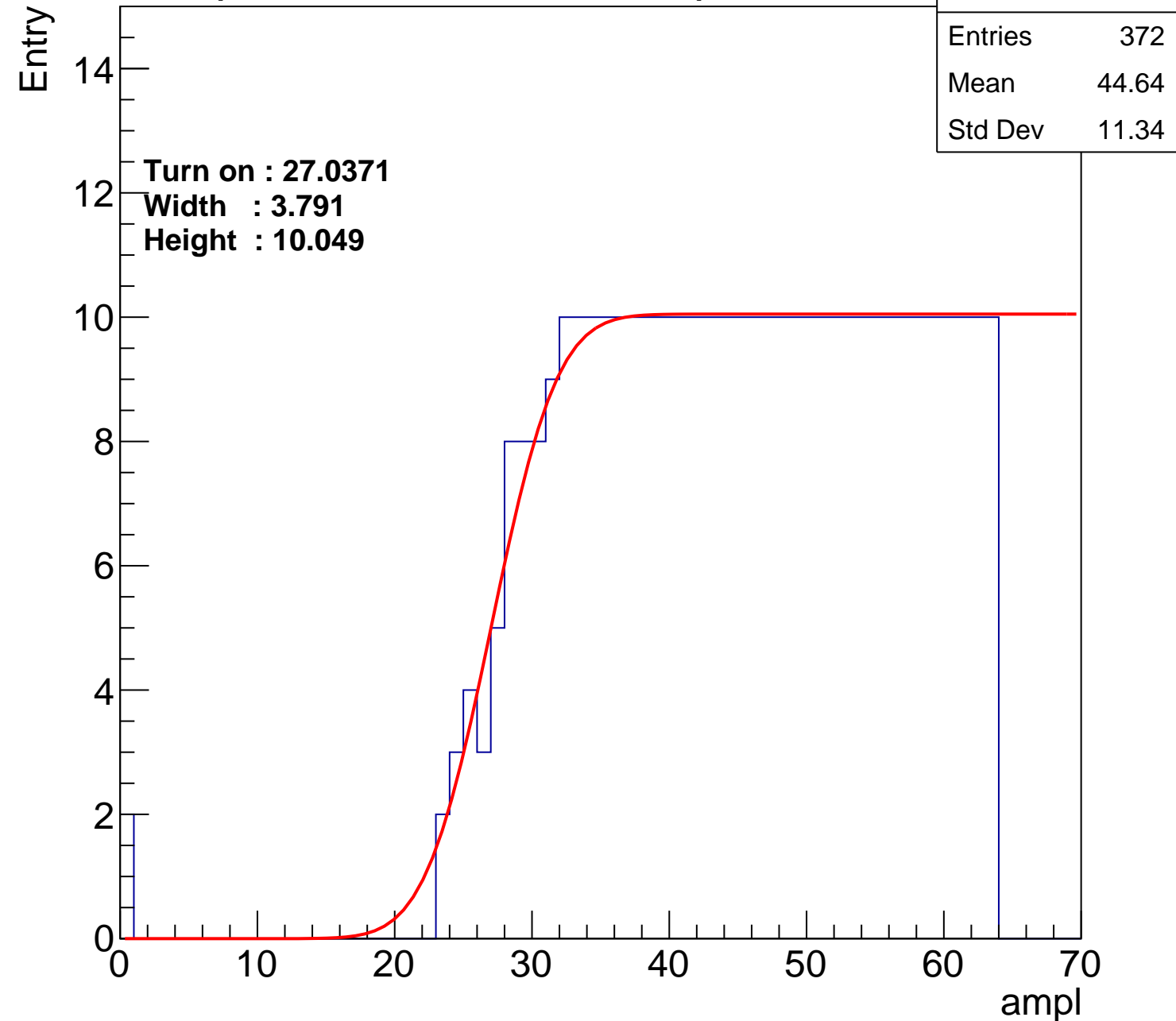
Width : 3.791

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch75

calib_packv5_042523_0143.root, FC#8, port C1

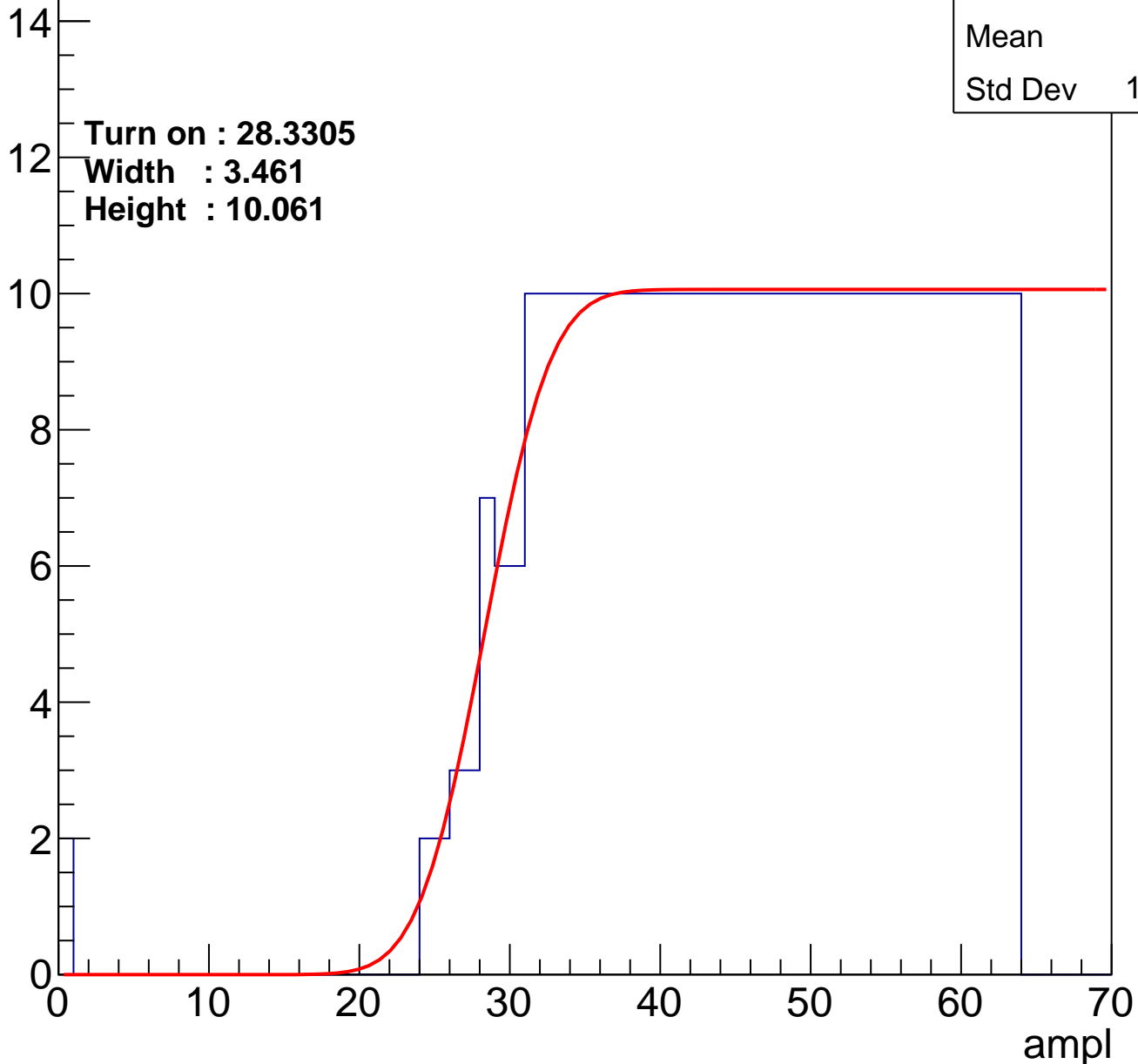
Entries	361
Mean	45.2
Std Dev	11.03

Turn on : 28.3305

Width : 3.461

Height : 10.061

Entry



B0L002S, U13-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.76
Std Dev	10.5

Turn on : 29.0079

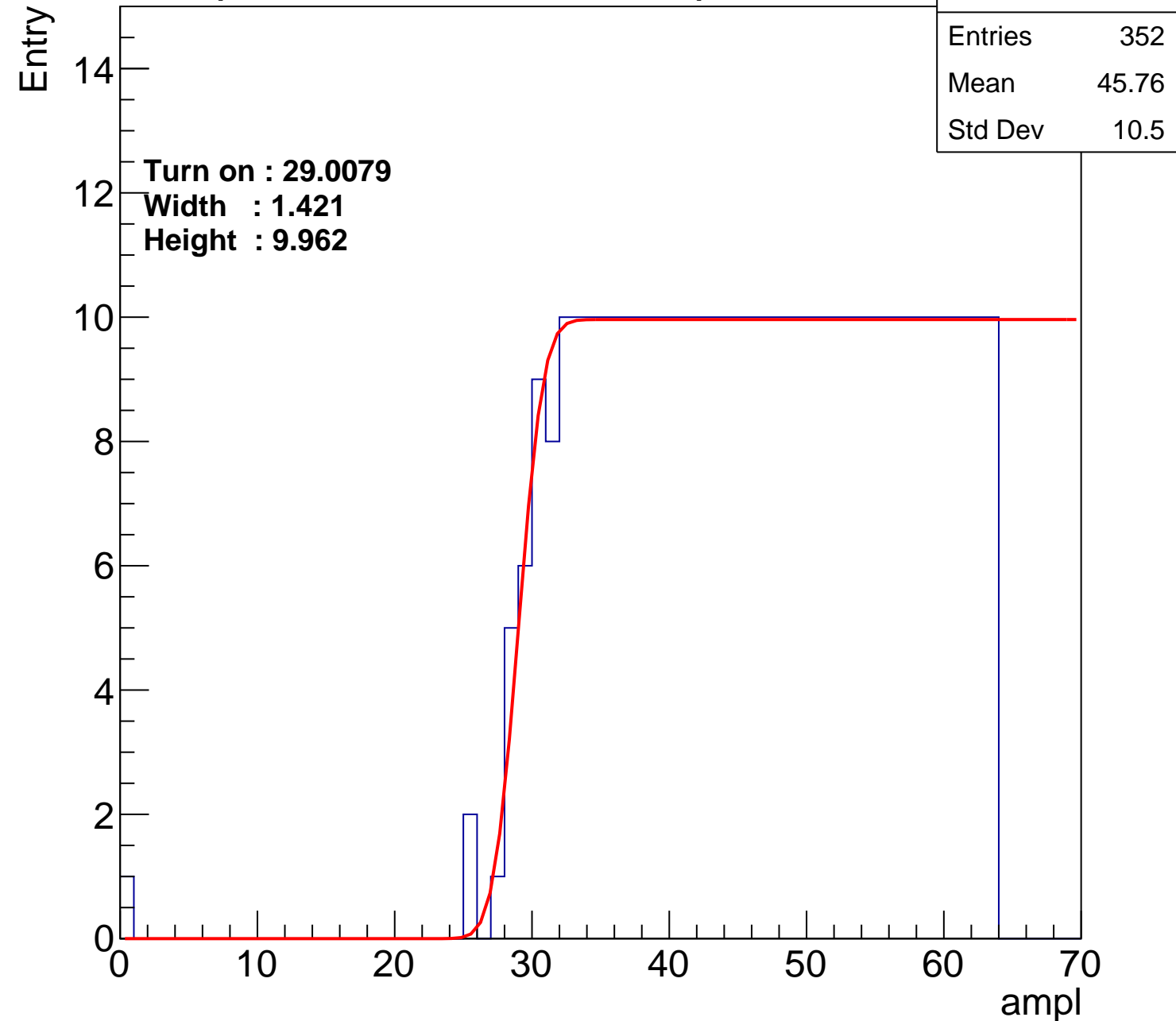
Width : 1.421

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch77

calib_packv5_042523_0143.root, FC#8, port C1

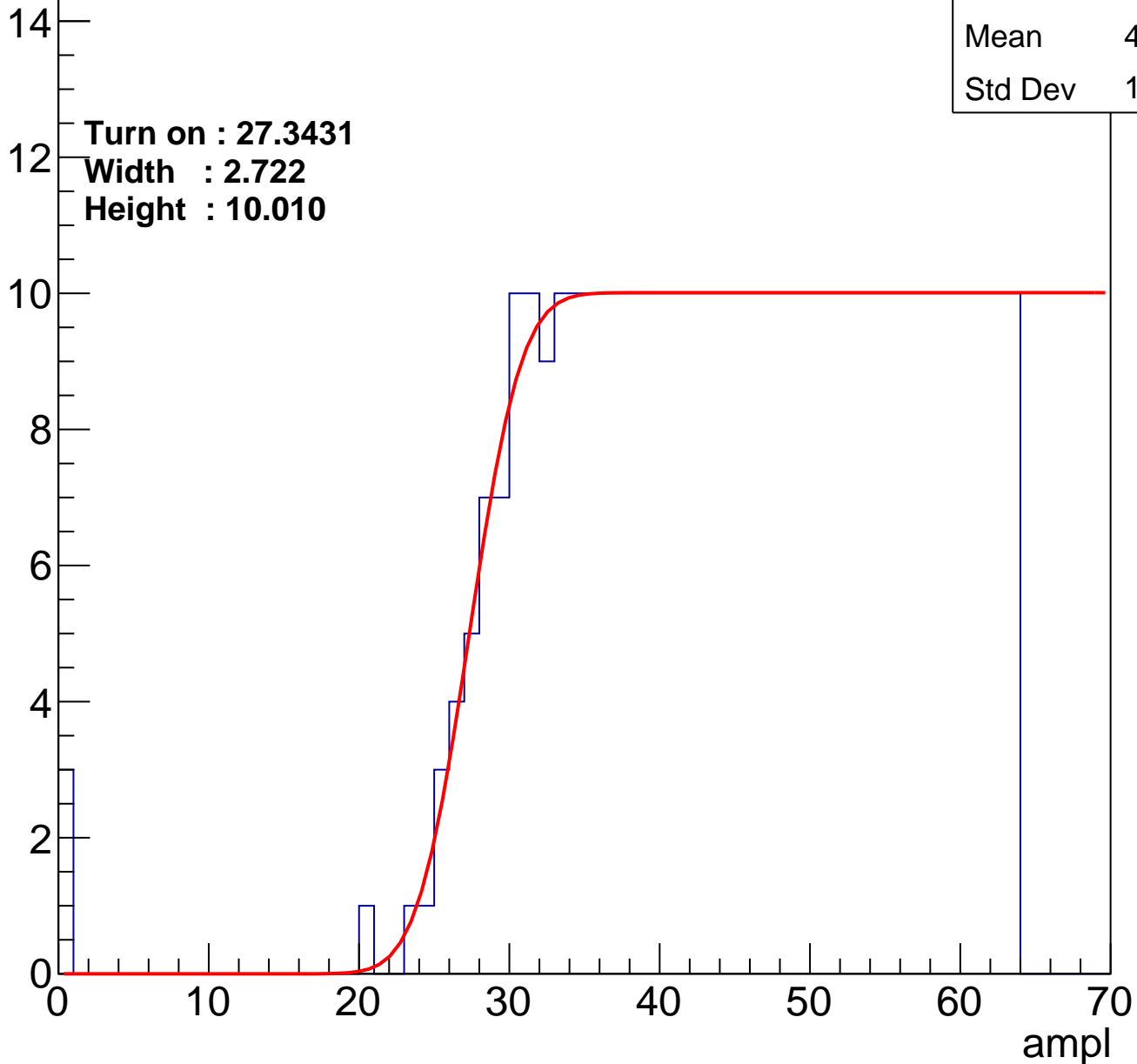
Entries	371
Mean	44.63
Std Dev	11.49

Turn on : 27.3431

Width : 2.722

Height : 10.010

Entry



B0L002S, U13-ch78

calib_packv5_042523_0143.root, FC#8, port C1

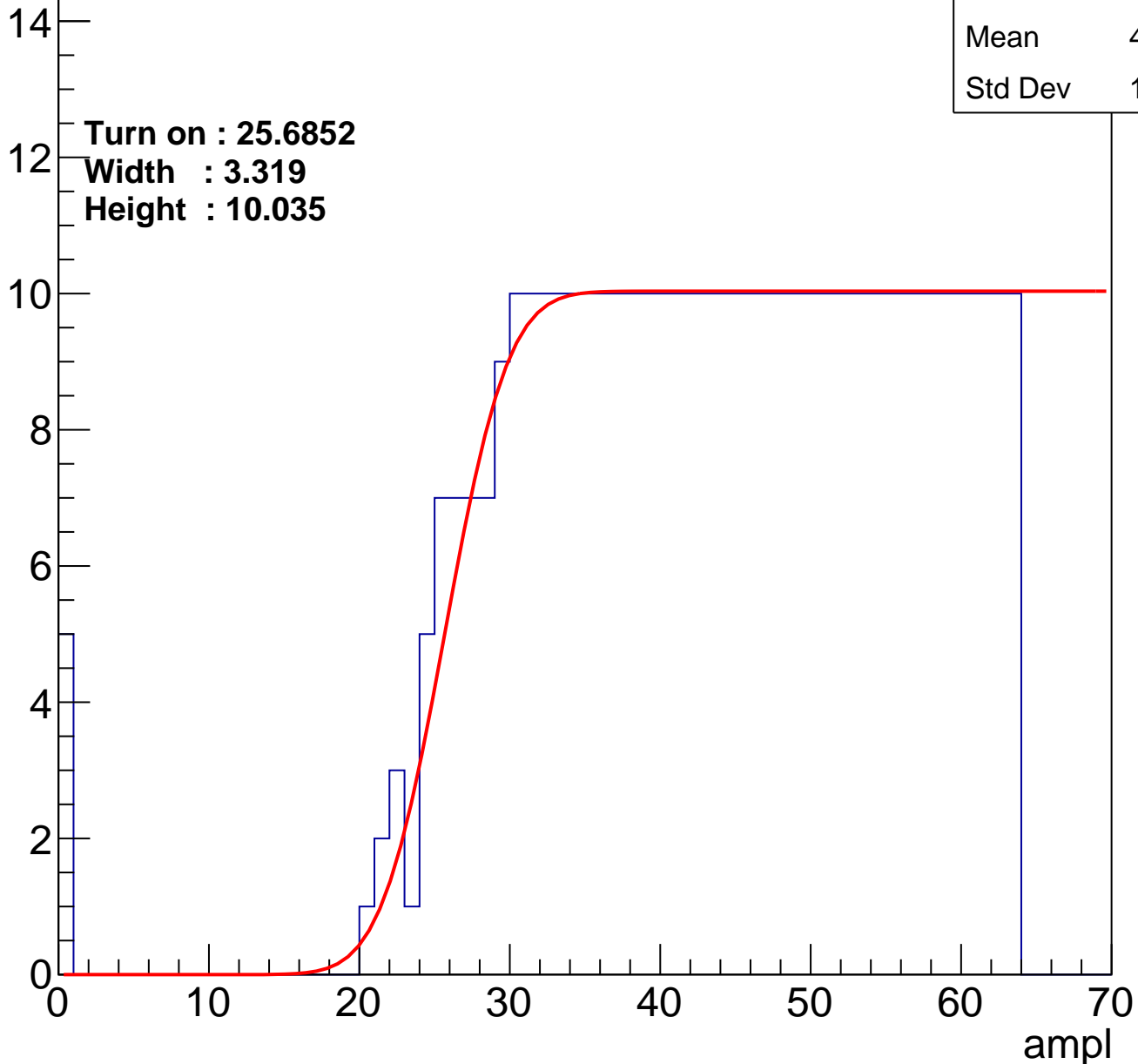
Entries	394
Mean	43.36
Std Dev	12.39

Turn on : 25.6852

Width : 3.319

Height : 10.035

Entry



B0L002S, U13-ch79

calib_packv5_042523_0143.root, FC#8, port C1

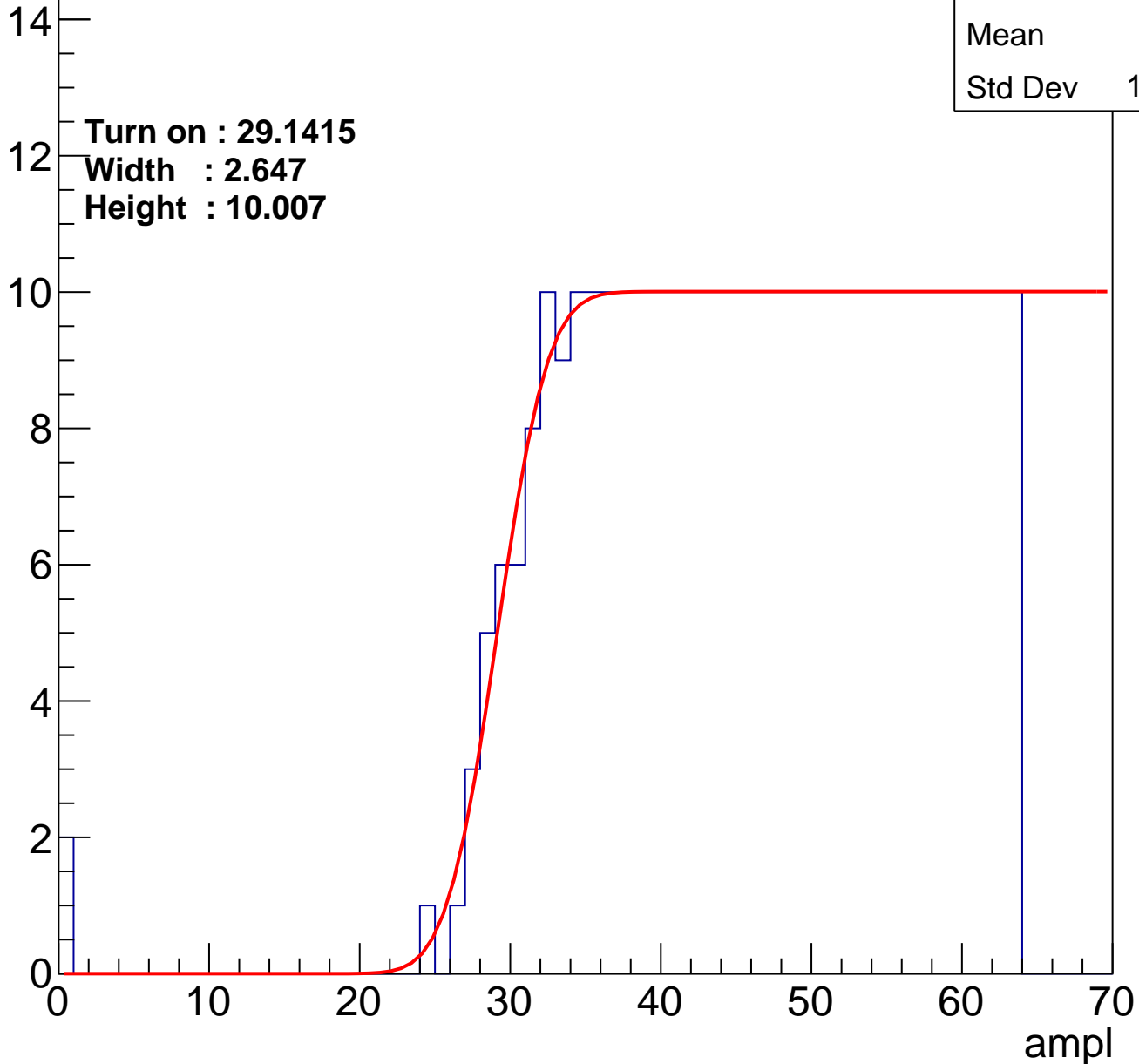
Entries	351
Mean	45.7
Std Dev	10.77

Turn on : 29.1415

Width : 2.647

Height : 10.007

Entry



B0L002S, U13-ch80

calib_packv5_042523_0143.root, FC#8, port C1

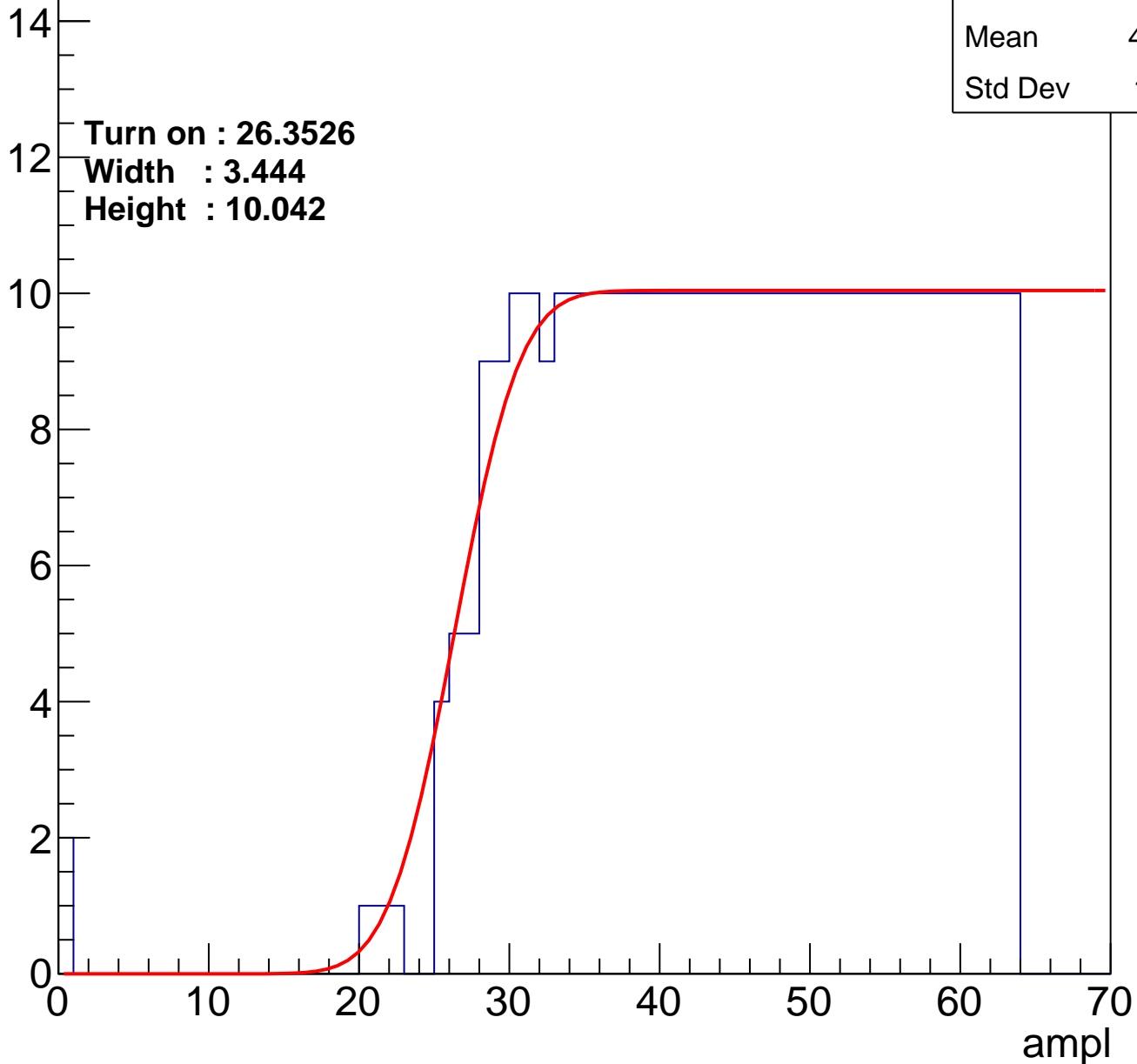
Entries	376
Mean	44.47
Std Dev	11.41

Turn on : 26.3526

Width : 3.444

Height : 10.042

Entry



B0L002S, U13-ch81

calib_packv5_042523_0143.root, FC#8, port C1

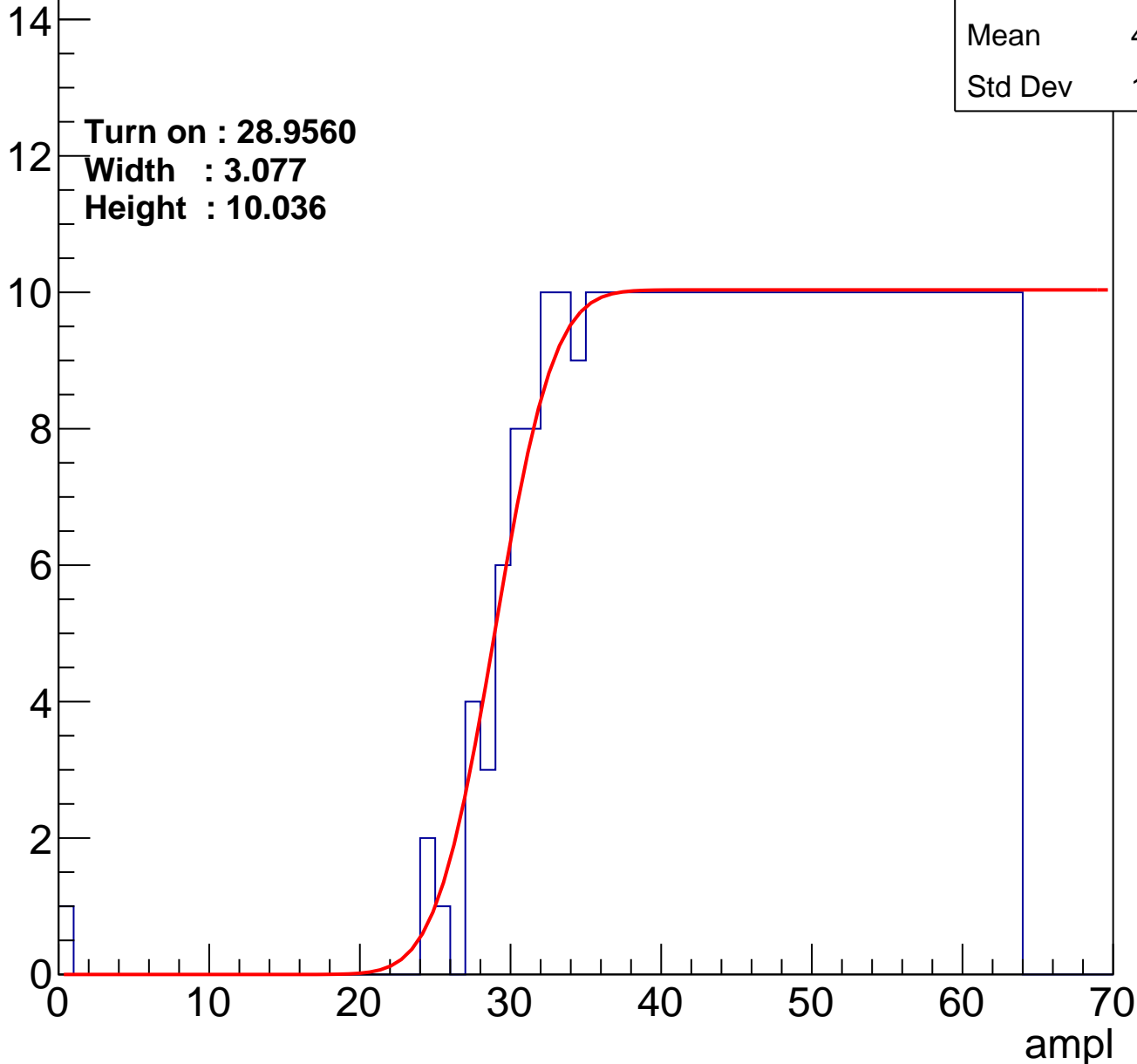
Entry

Entries	352
Mean	45.72
Std Dev	10.57

Turn on : 28.9560

Width : 3.077

Height : 10.036



B0L002S, U13-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.07
Std Dev	11.66

Turn on : 26.0246

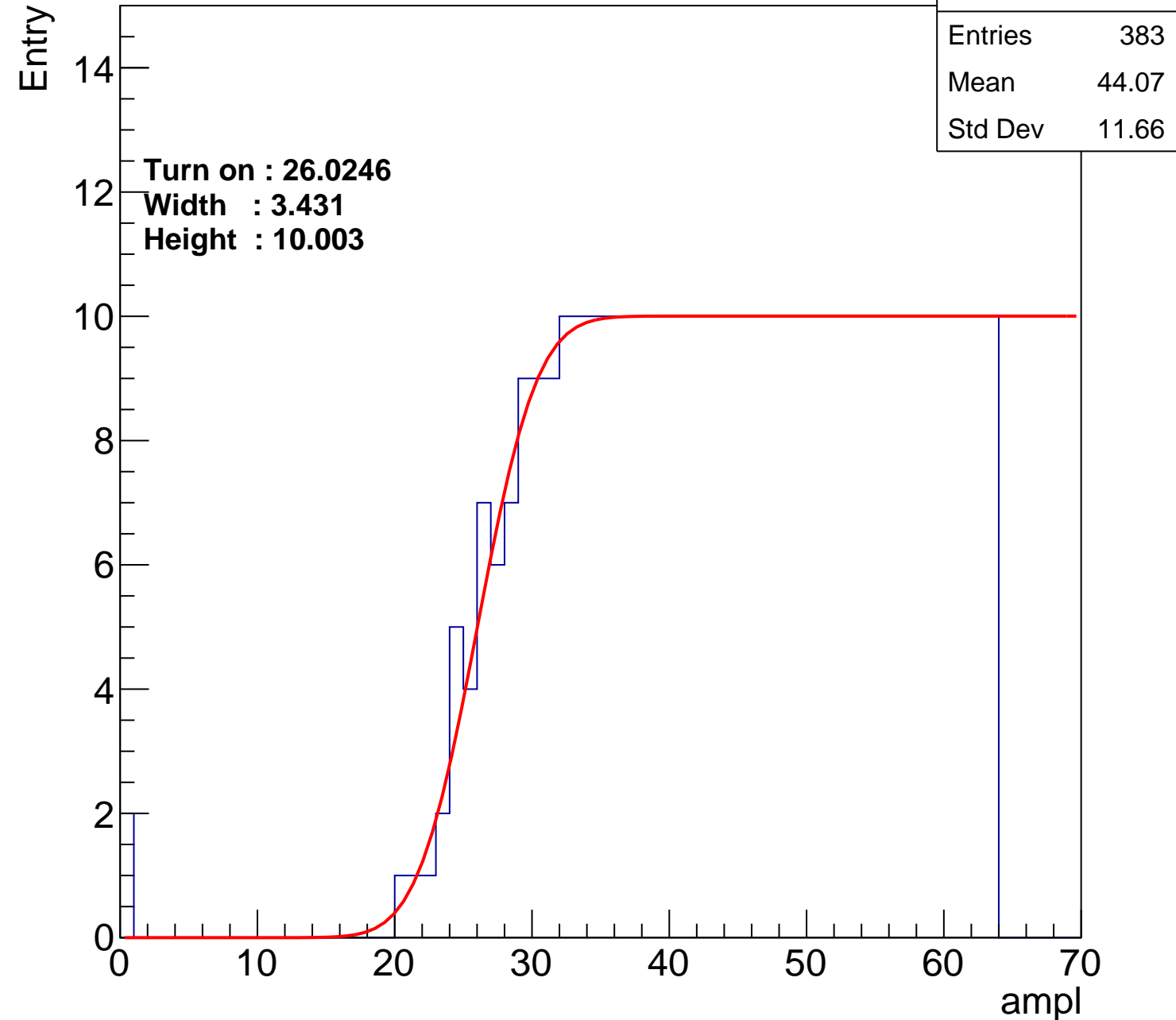
Width : 3.431

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.07
Std Dev	11.75

Turn on : 29.2248

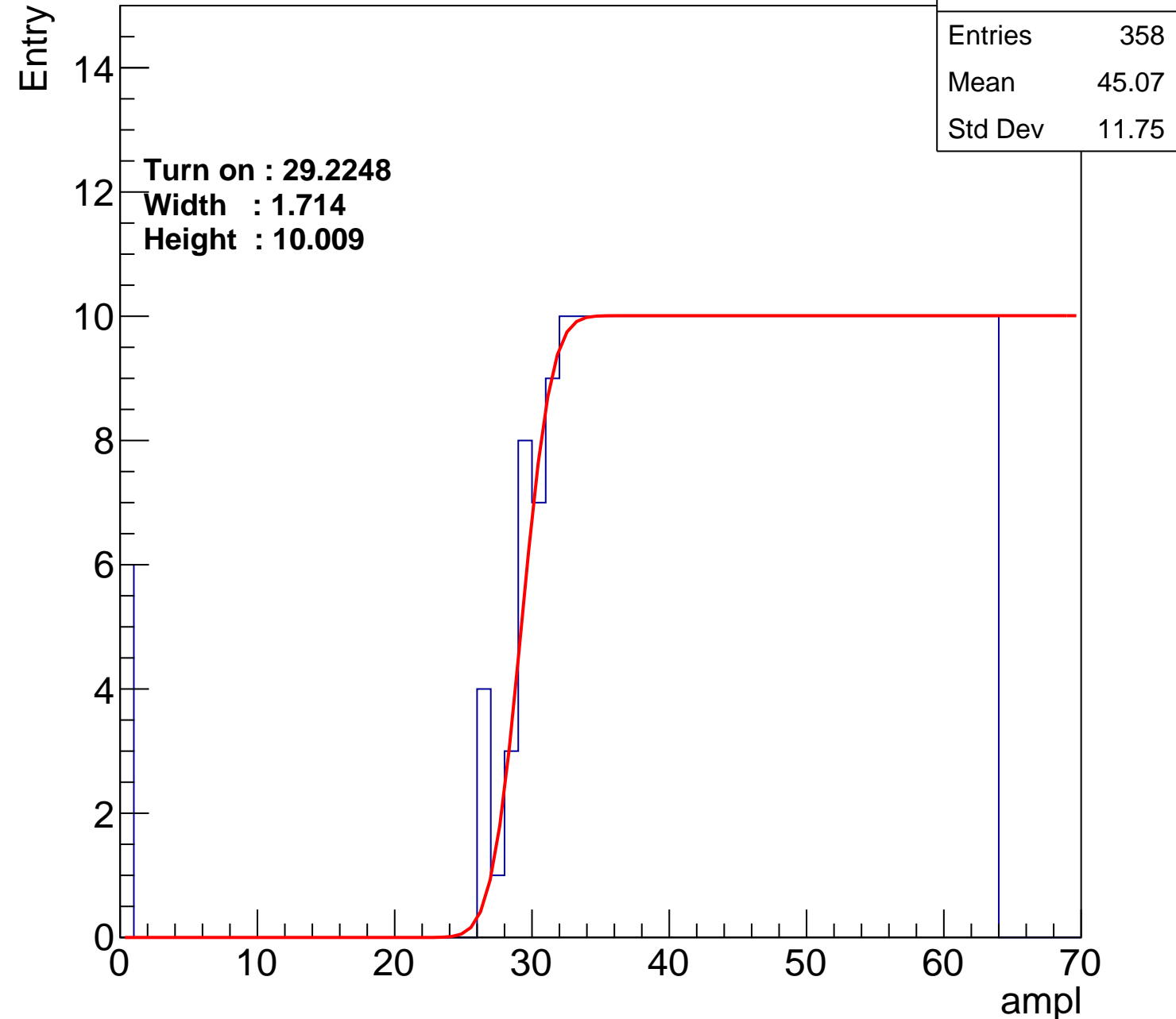
Width : 1.714

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.9
Std Dev	11.24

Mean	44.9
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Std Dev	11.24
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Width : 3.996

Height : 10.101



B0L002S, U13-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.81
Std Dev	11.37

Turn on : 27.5195

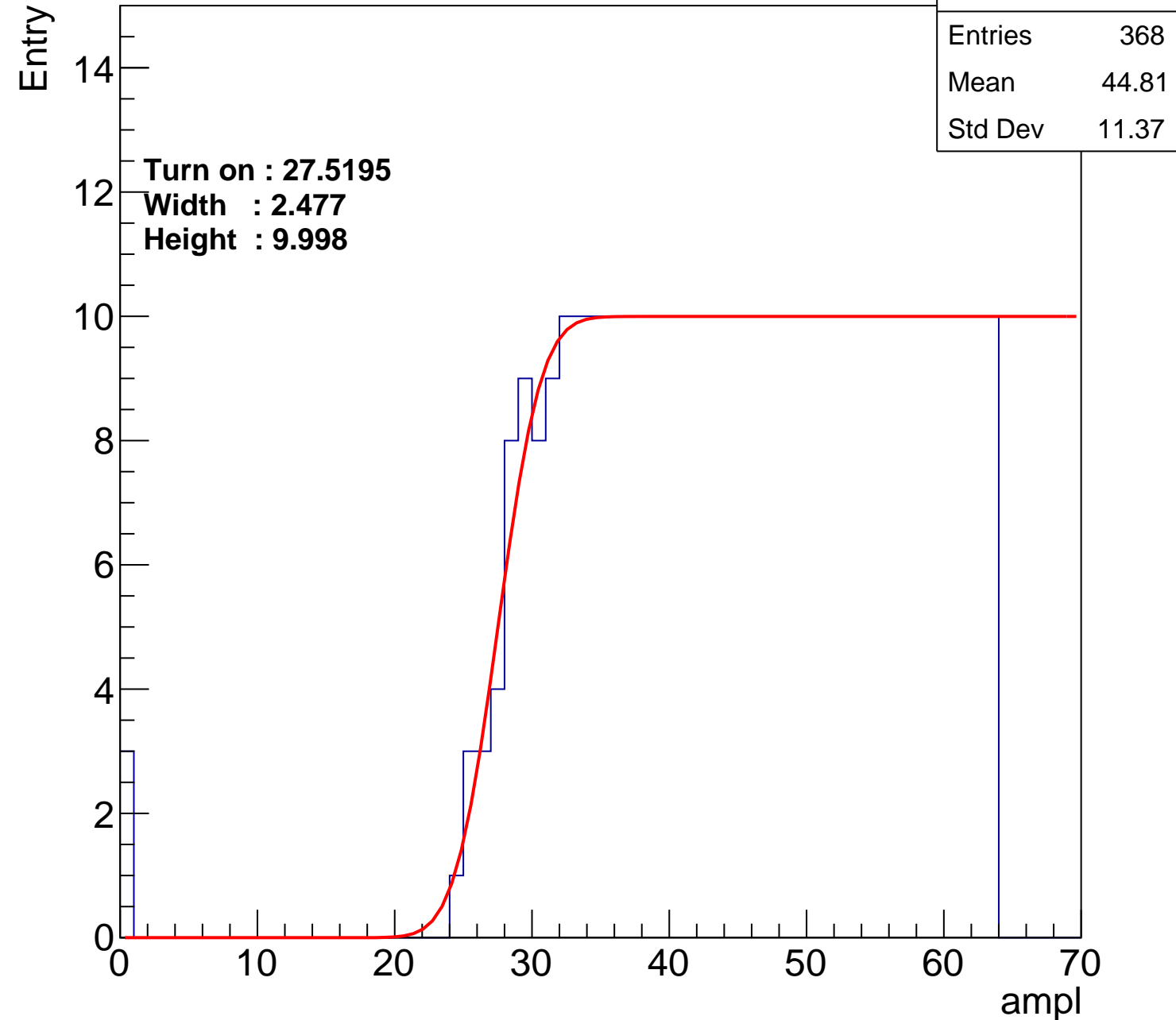
Width : 2.477

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.43
Std Dev	11.26

Turn on : 26.5436

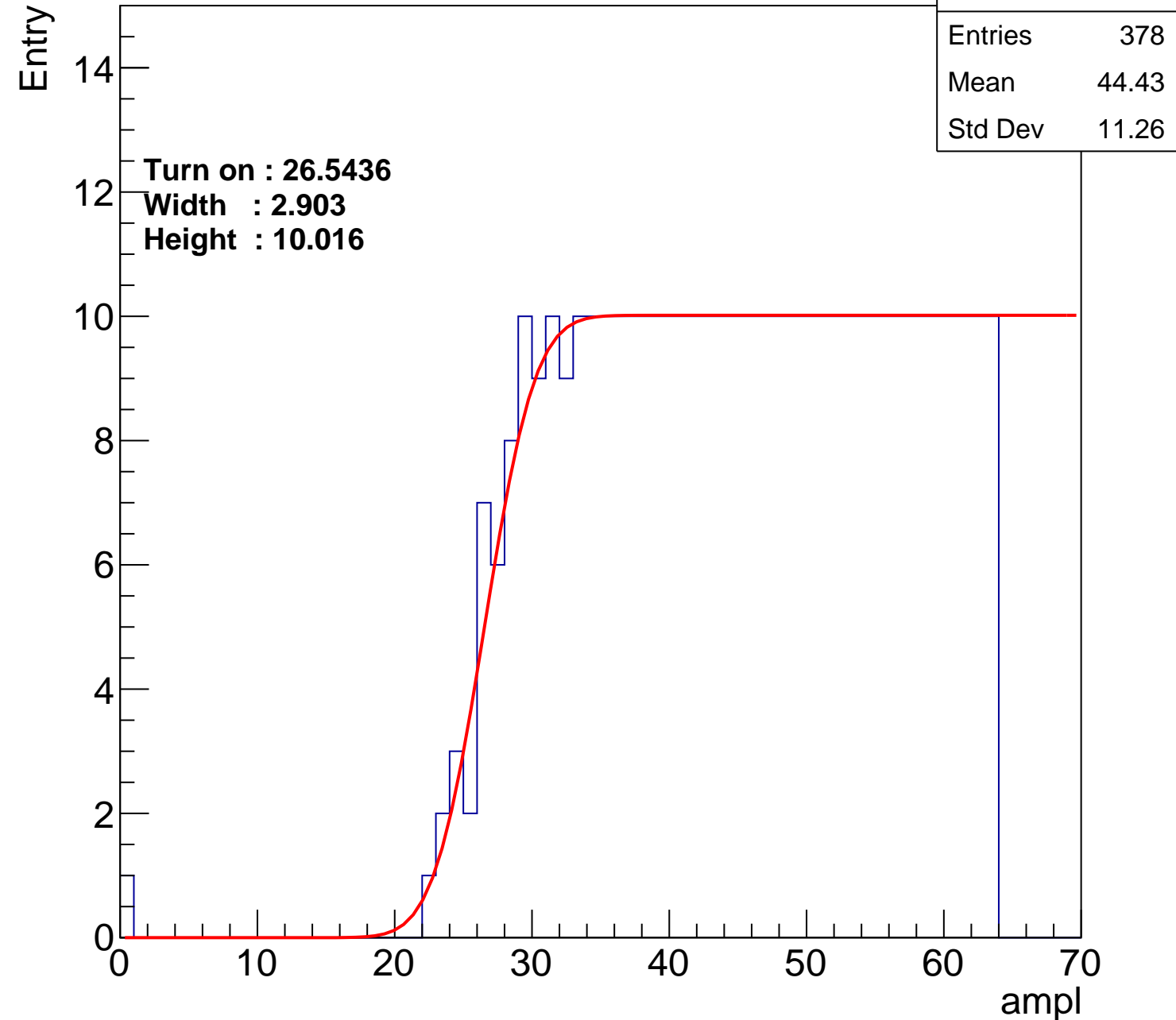
Width : 2.903

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch87

calib_packv5_042523_0143.root, FC#8, port C1

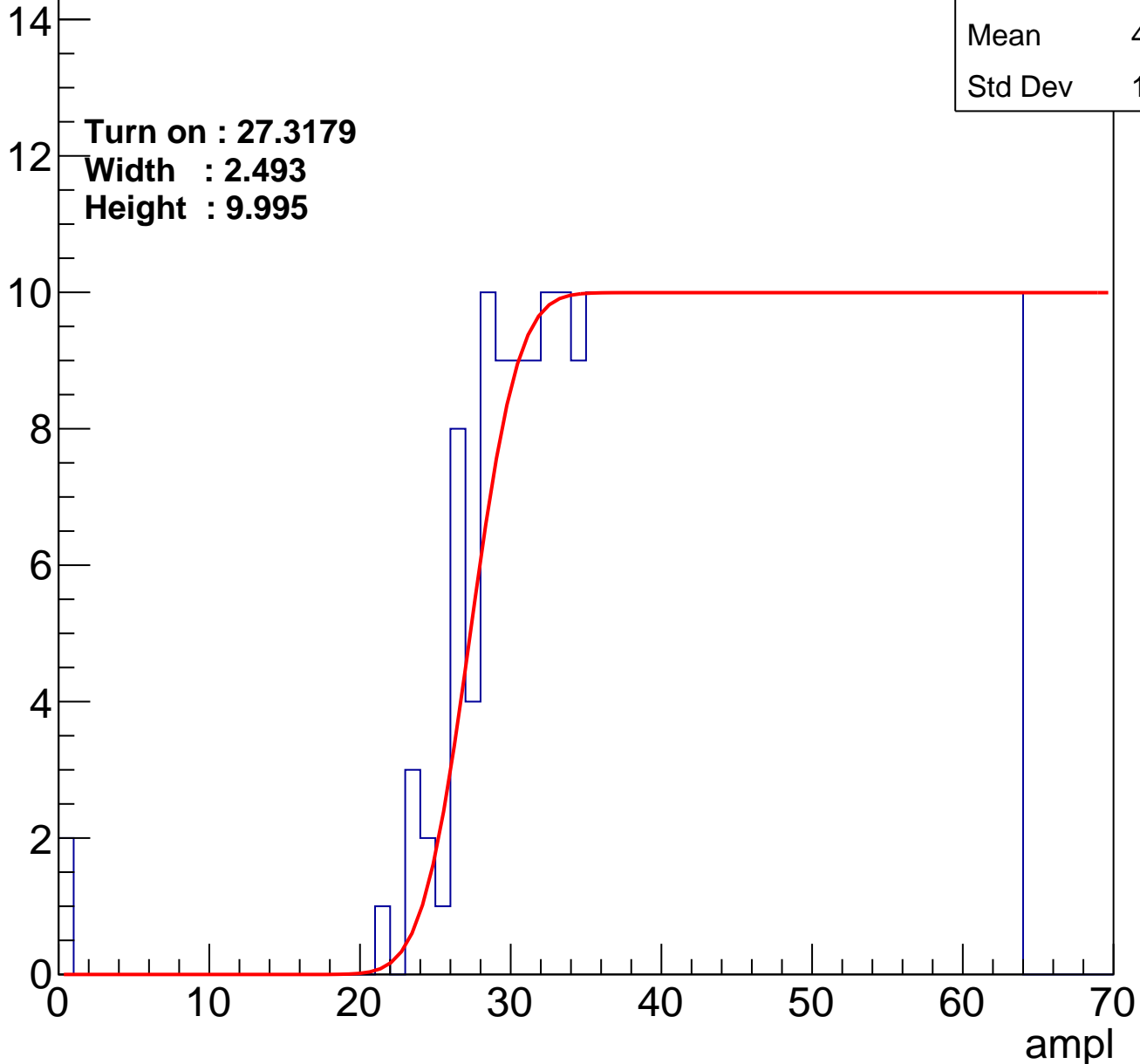
Entries	377
Mean	44.39
Std Dev	11.46

Turn on : 27.3179

Width : 2.493

Height : 9.995

Entry



B0L002S, U13-ch88

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.96
Std Dev	10.95

Turn on : 27.4638

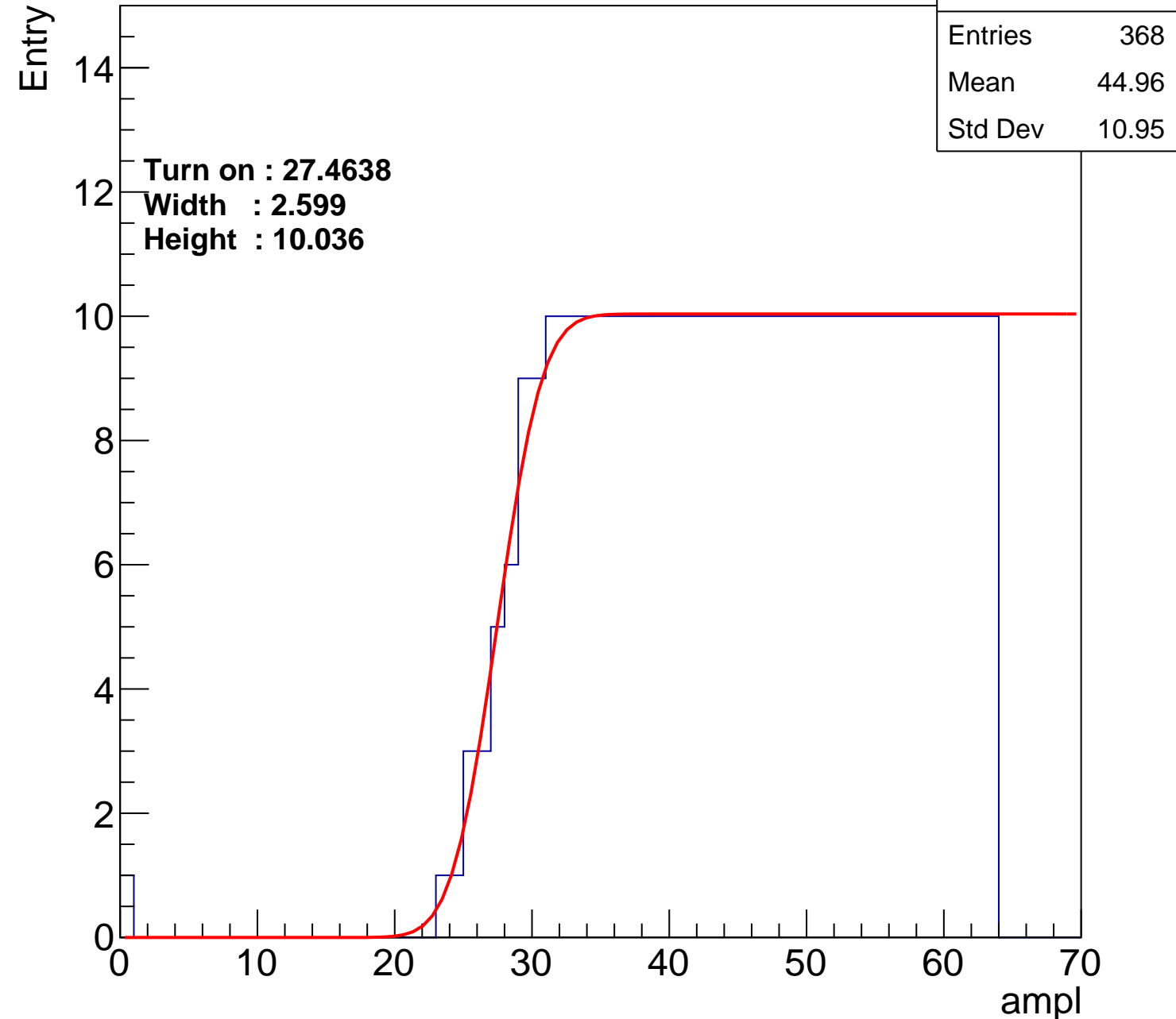
Width : 2.599

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.29
Std Dev	11.18

Turn on : 28.8787

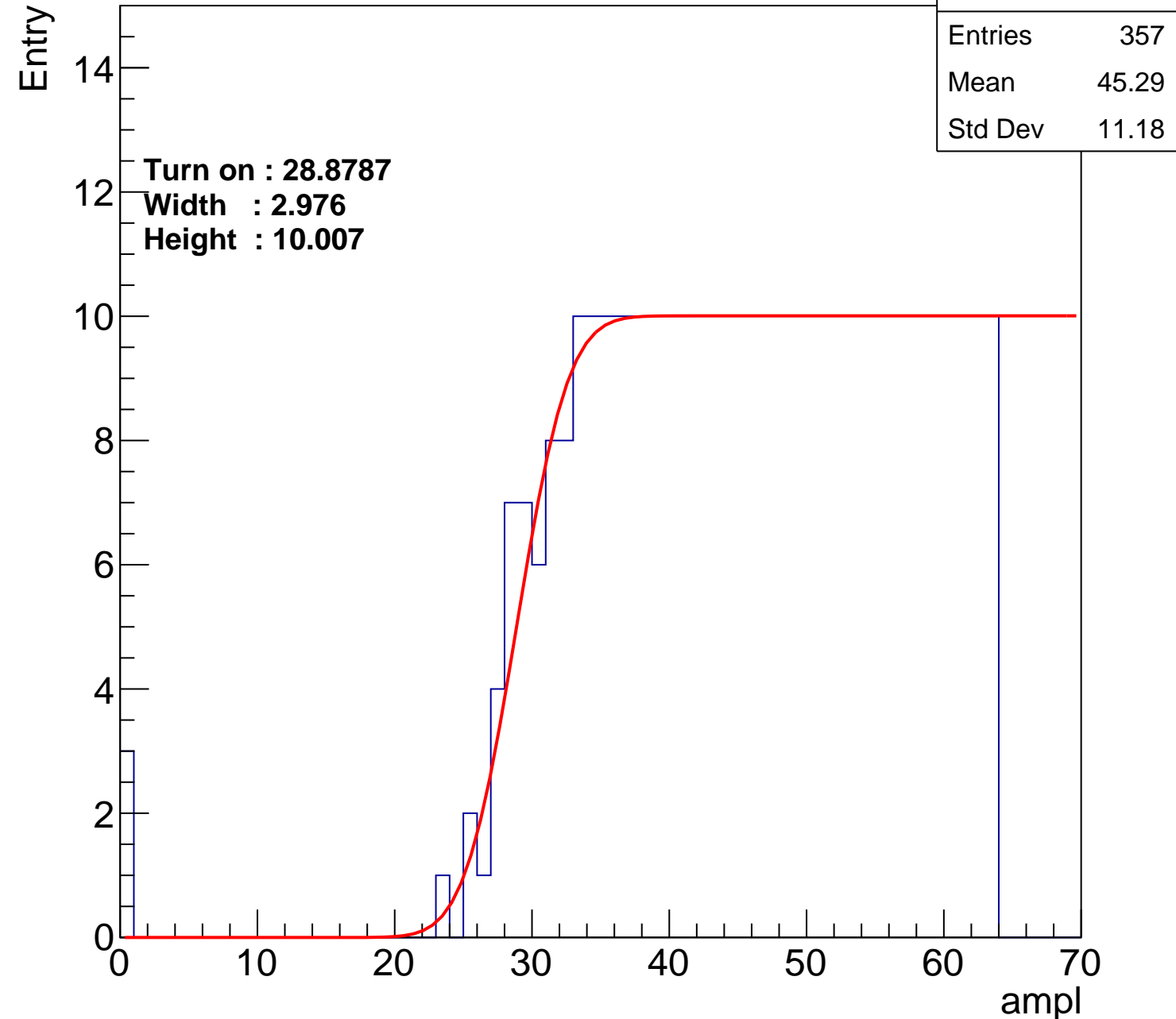
Width : 2.976

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.08
Std Dev	11.31

Turn on : 28.5347

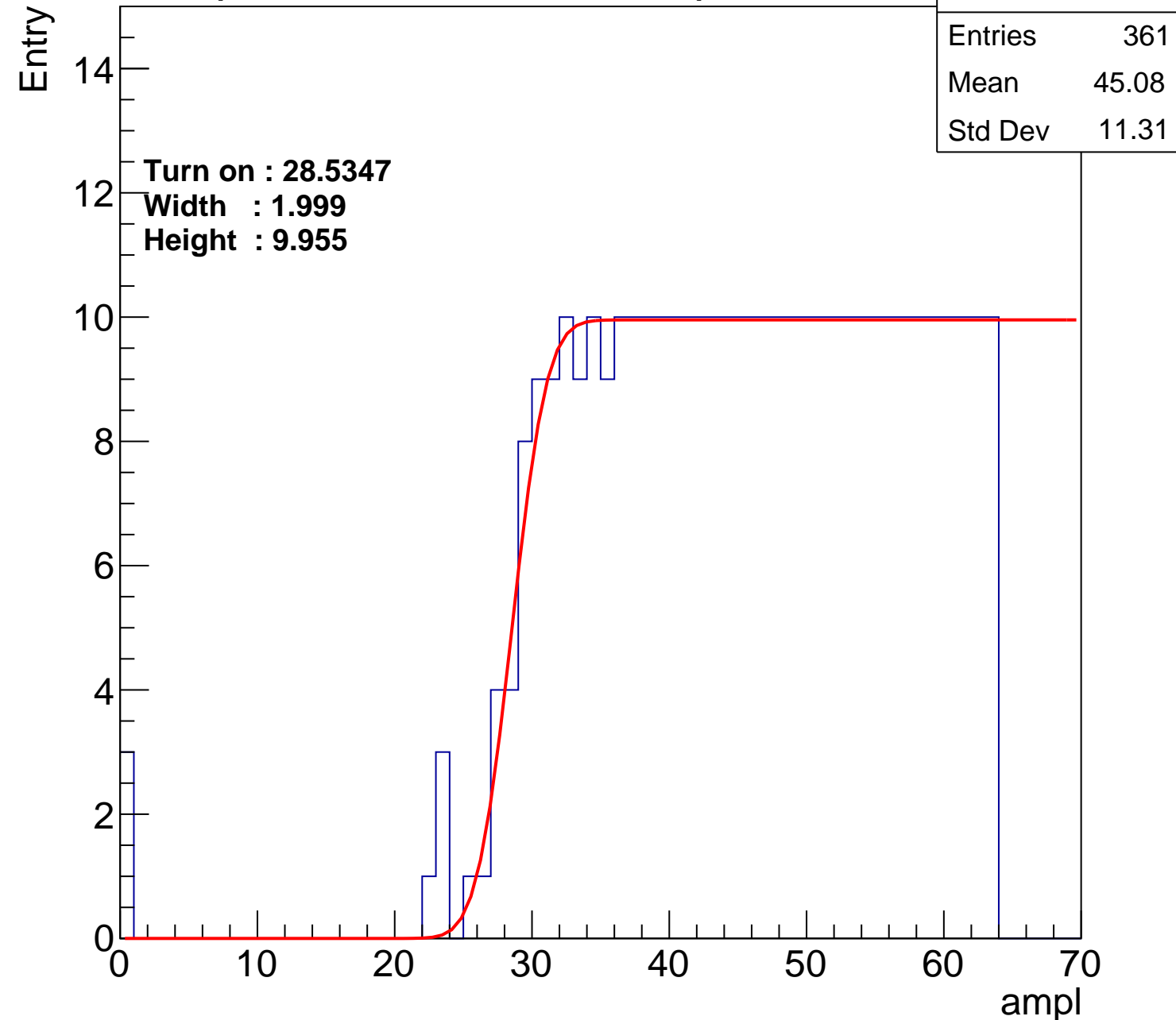
Width : 1.999

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.21
Std Dev	11.98

Turn on : 26.8712

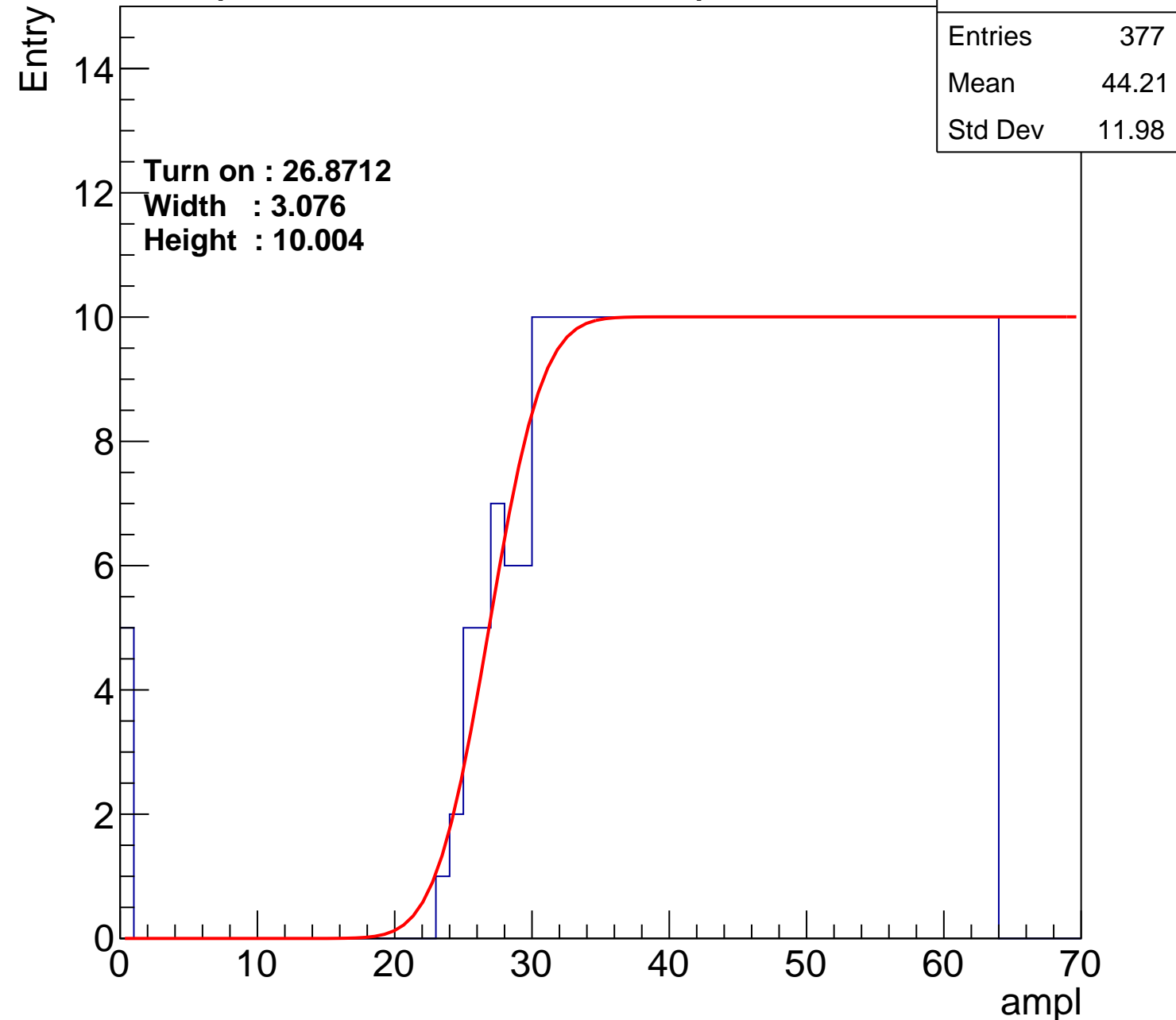
Width : 3.076

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.84
Std Dev	11.29

Turn on : 27.9193

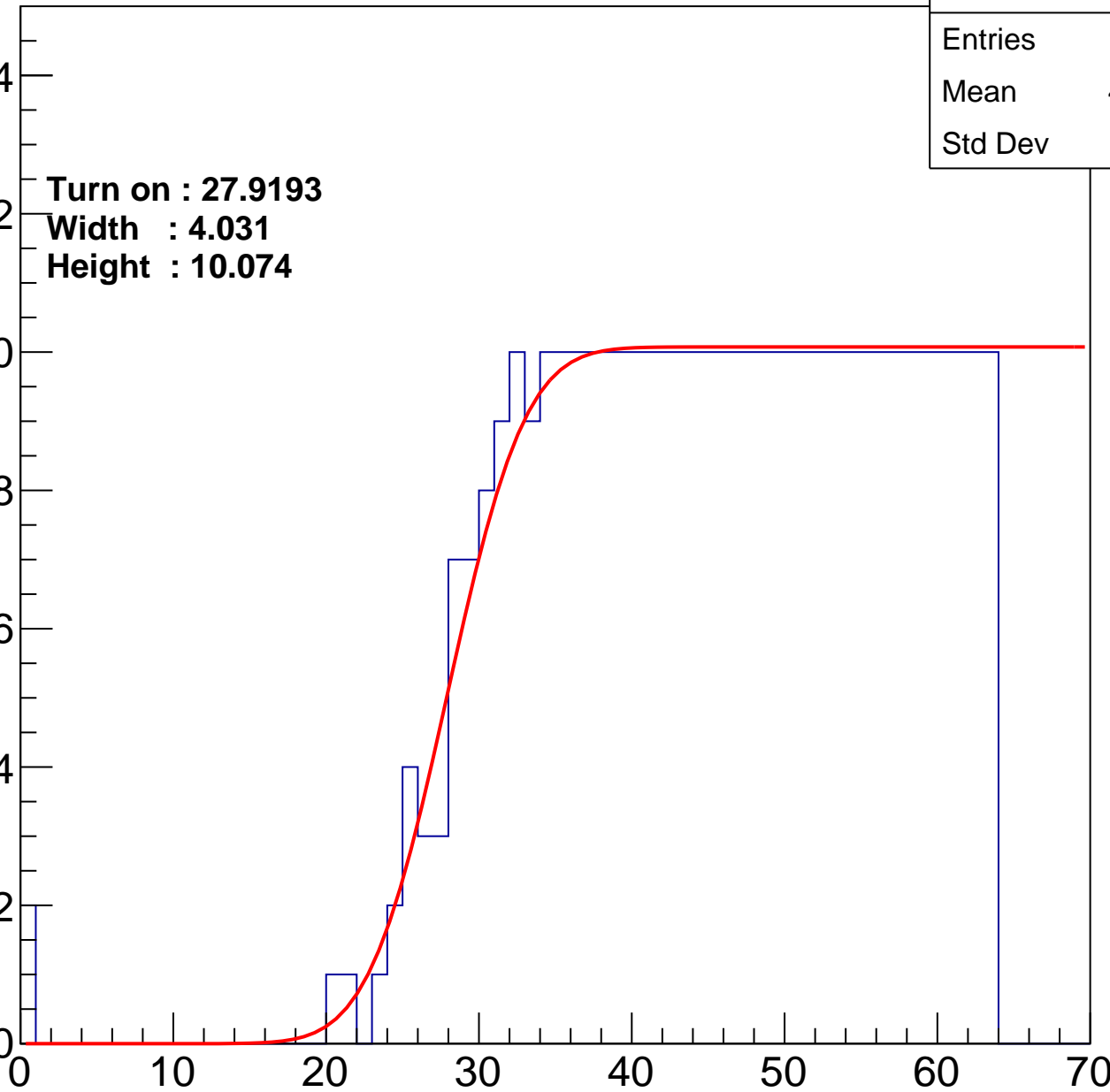
Width : 4.031

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch93

calib_packv5_042523_0143.root, FC#8, port C1

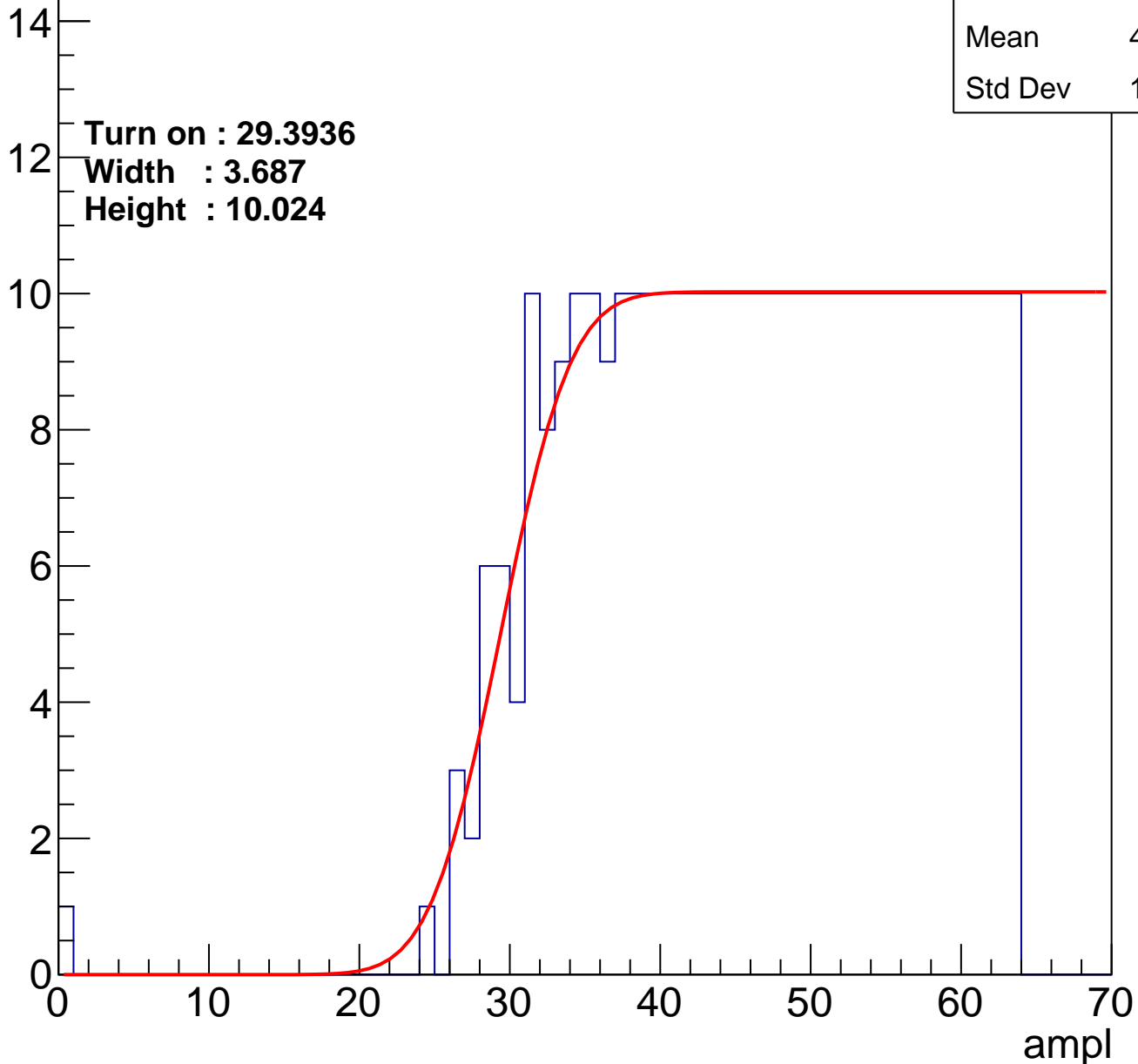
Entries	349
Mean	45.83
Std Dev	10.54

Turn on : 29.3936

Width : 3.687

Height : 10.024

Entry



B0L002S, U13-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.01
Std Dev	11.16

Turn on : 27.7847

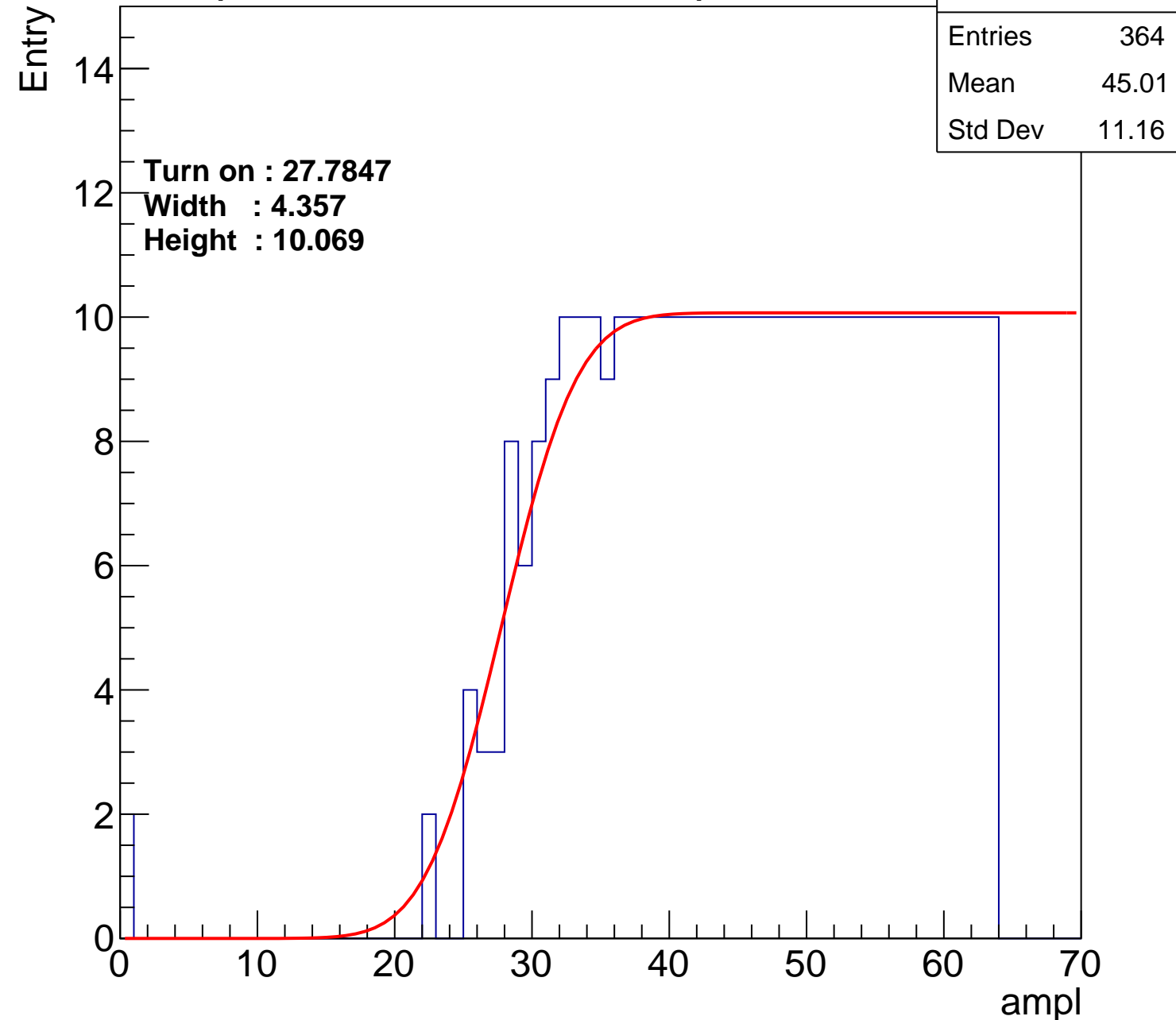
Width : 4.357

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch95

calib_packv5_042523_0143.root, FC#8, port C1

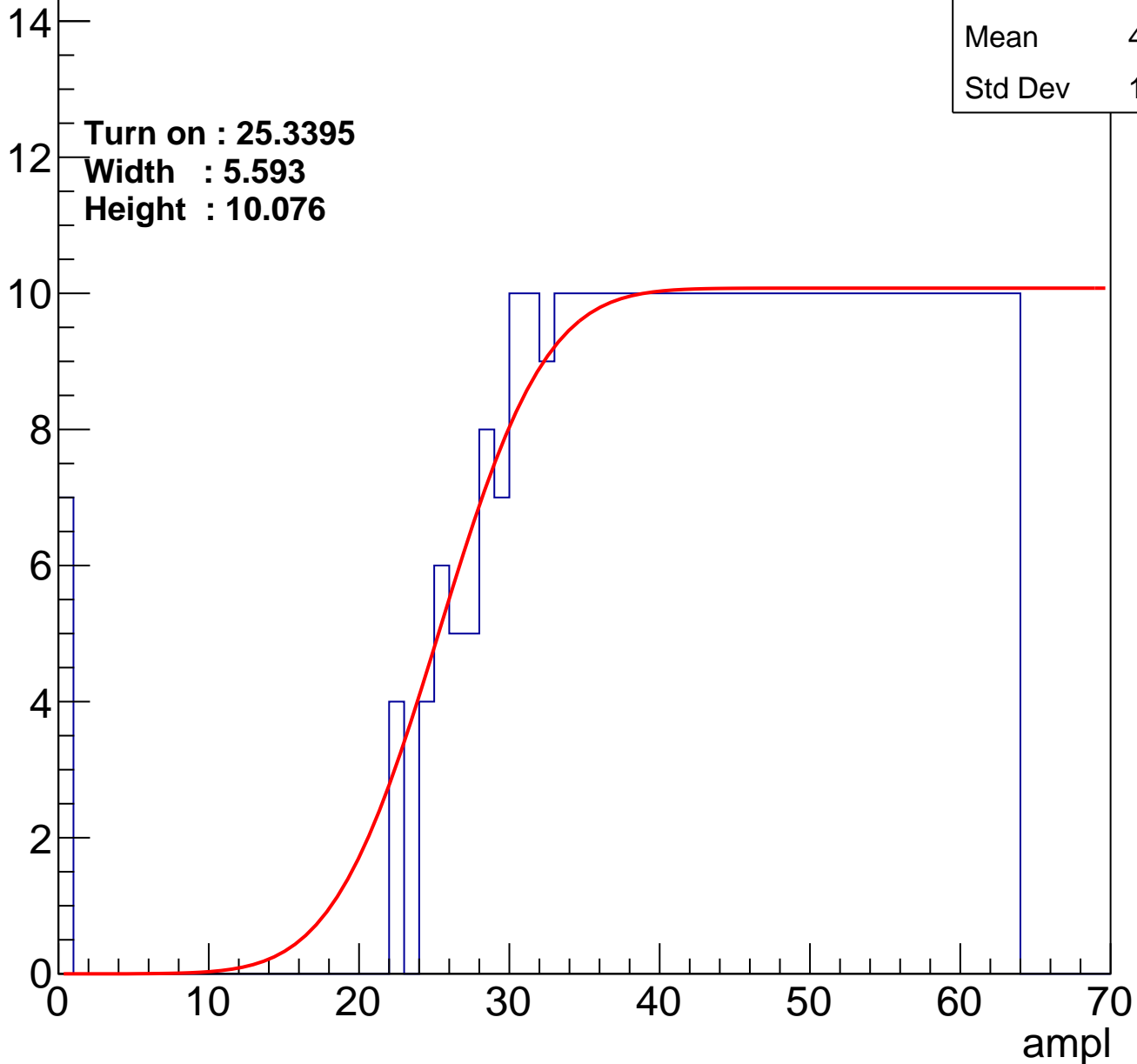
Entries	385
Mean	43.65
Std Dev	12.55

Turn on : 25.3395

Width : 5.593

Height : 10.076

Entry



B0L002S, U13-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.06
Std Dev	11.14

Turn on : 28.8232

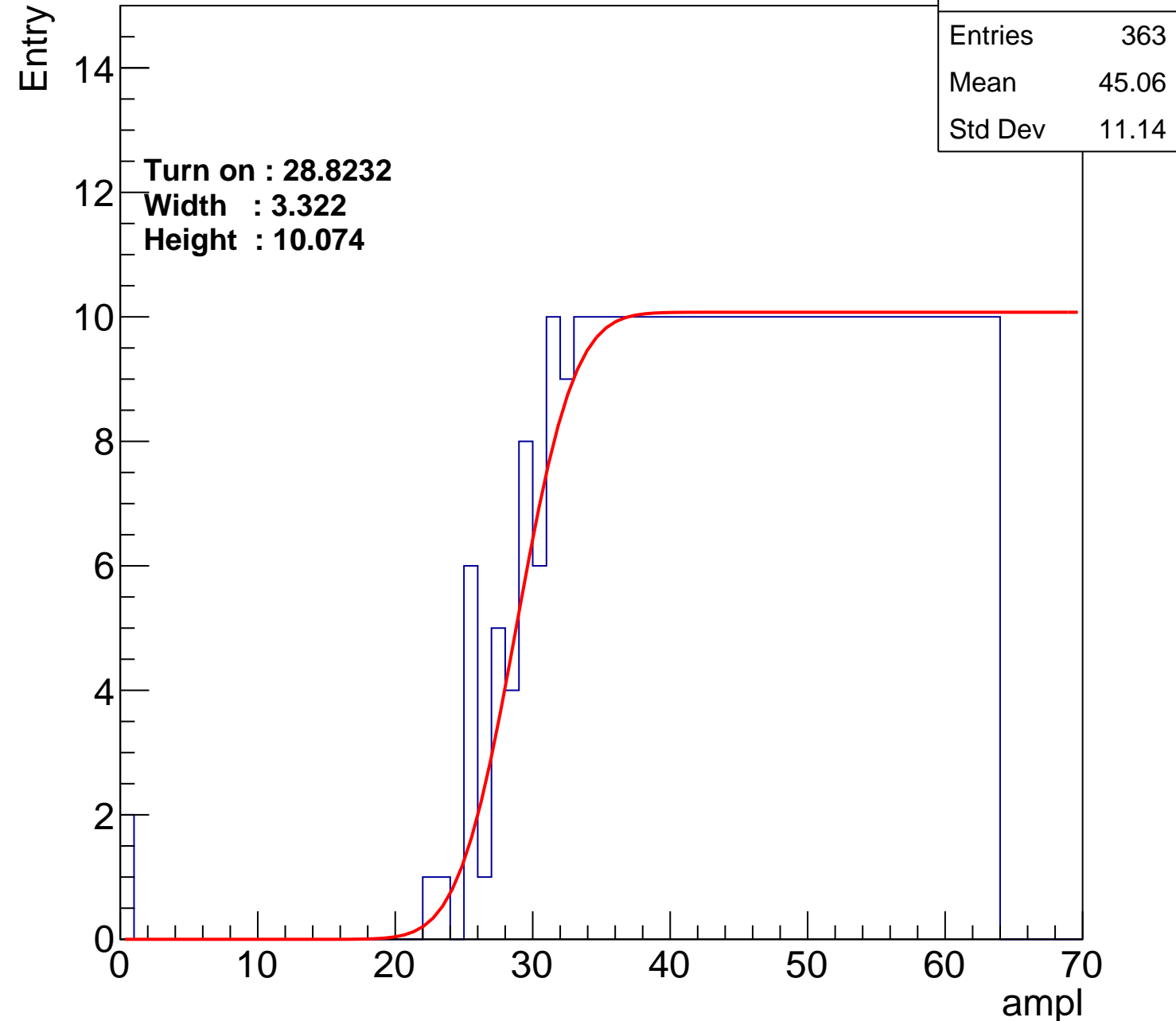
Width : 3.322

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.25
Std Dev	10.86

Turn on : 28.5206

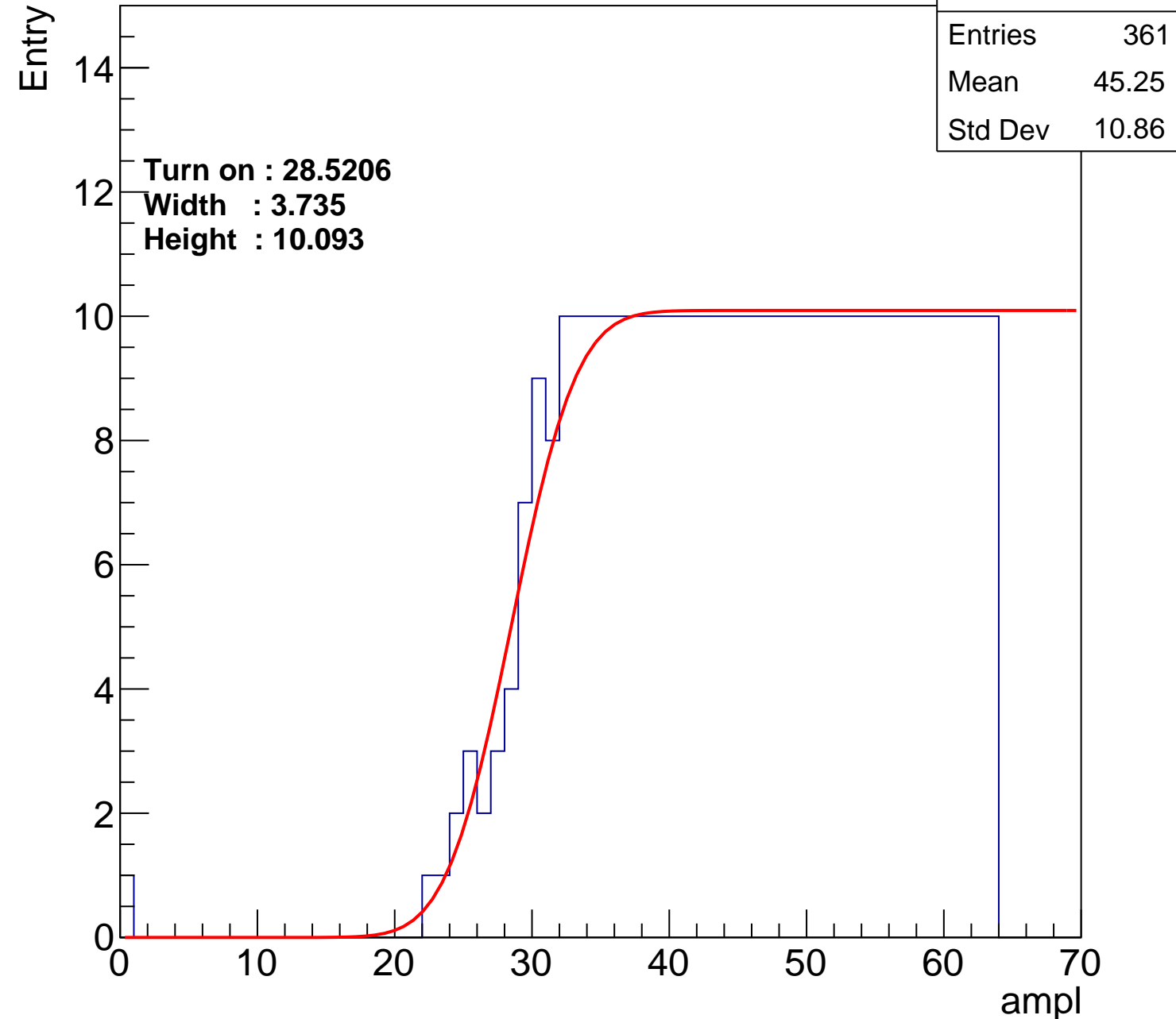
Width : 3.735

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.2
Std Dev	11.92

Turn on : 27.2006

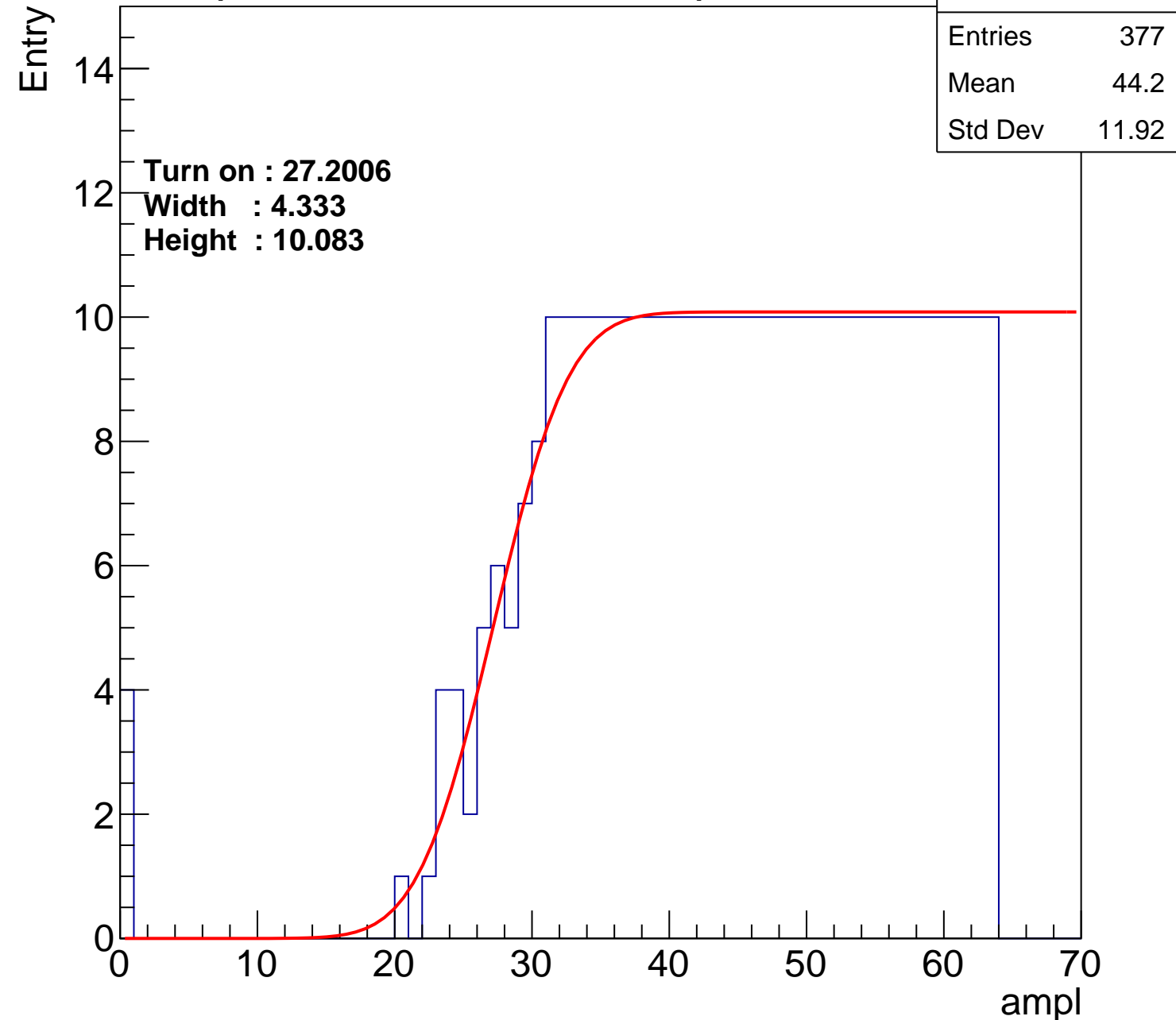
Width : 4.333

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch99

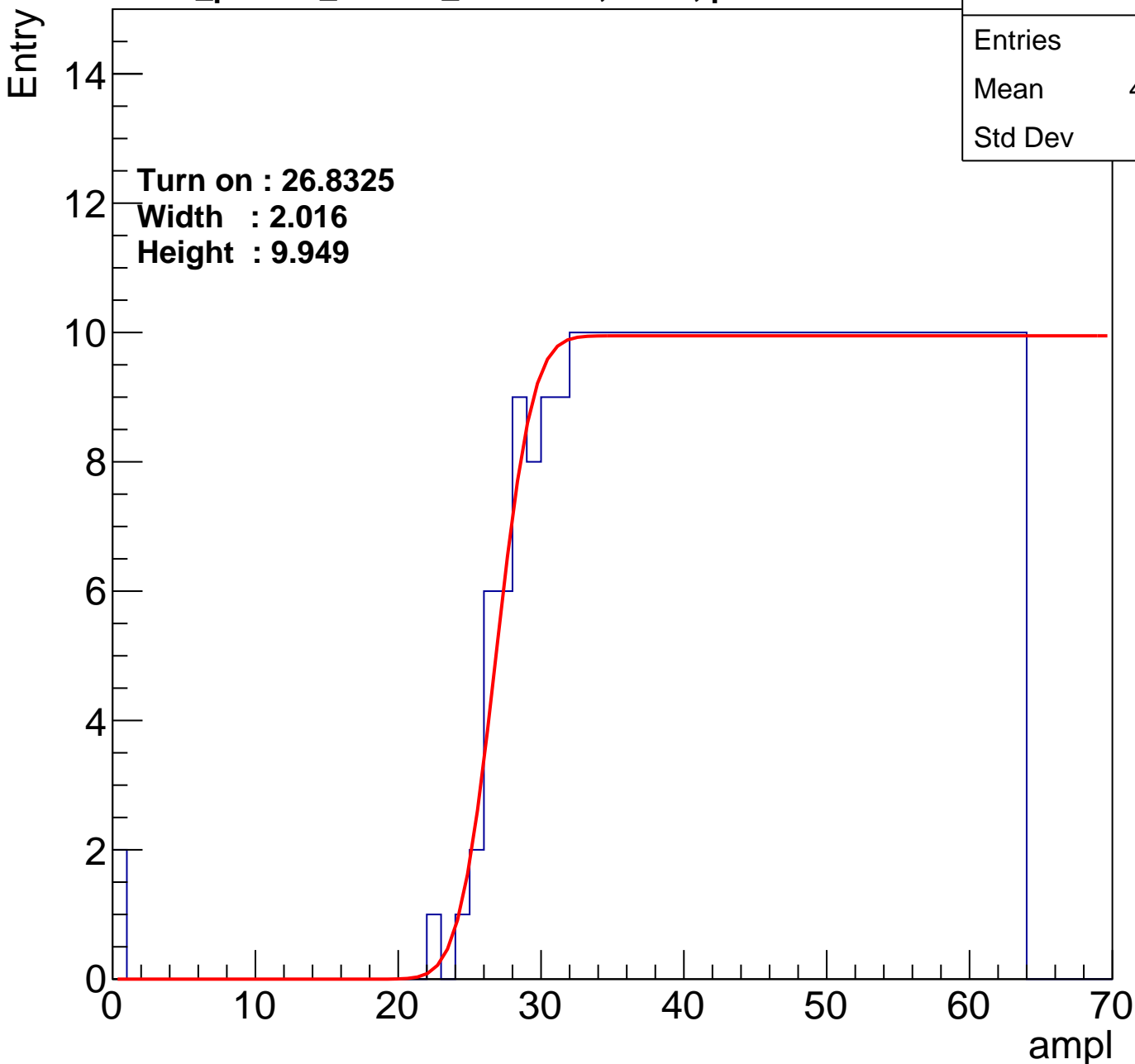
calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 26.8325

Width : 2.016

Height : 9.949



B0L002S, U13-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
Mean	46.15
Std Dev	10.35

Turn on : 29.2354

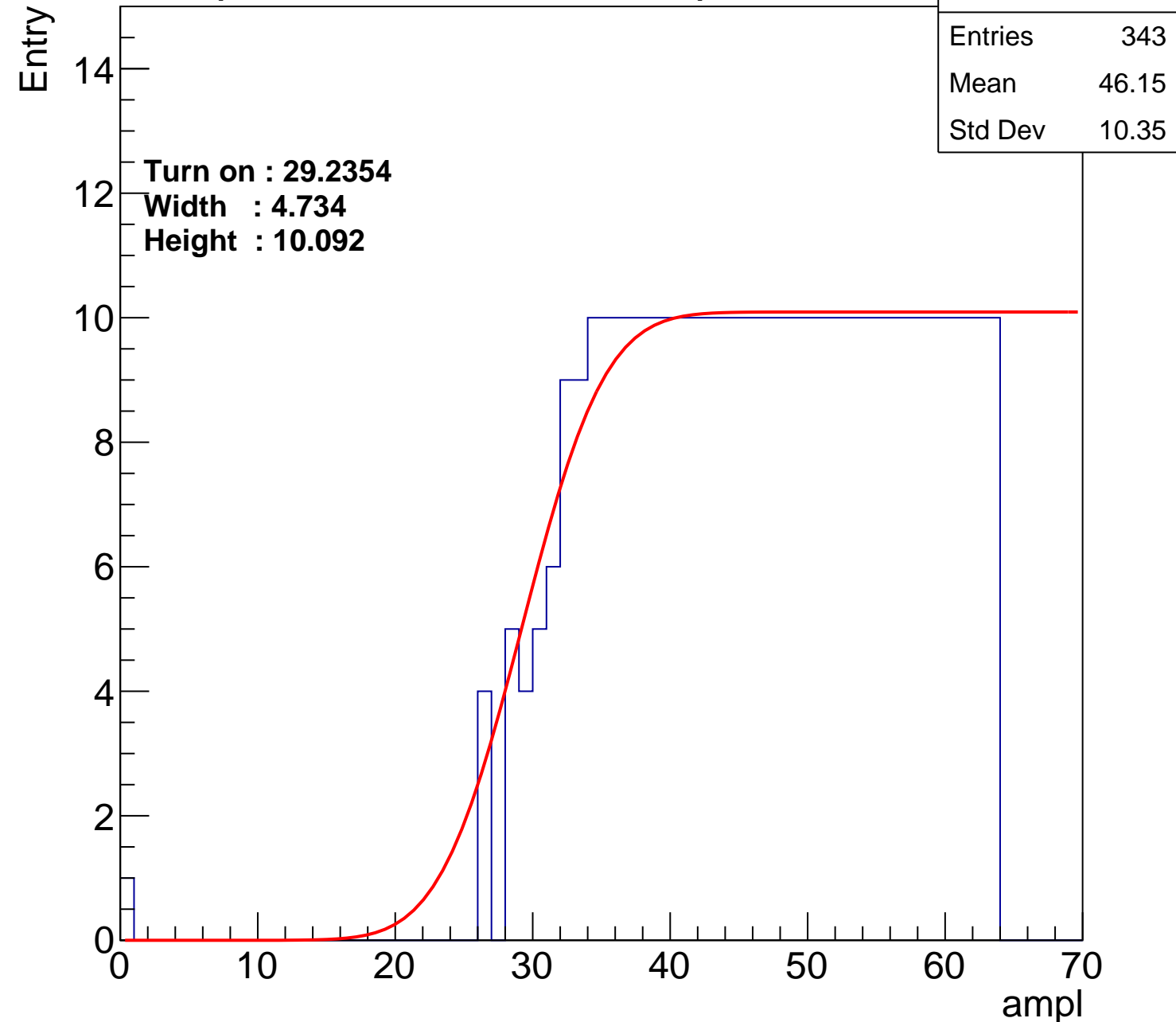
Width : 4.734

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.31
Std Dev	11.93

Turn on : 27.4012

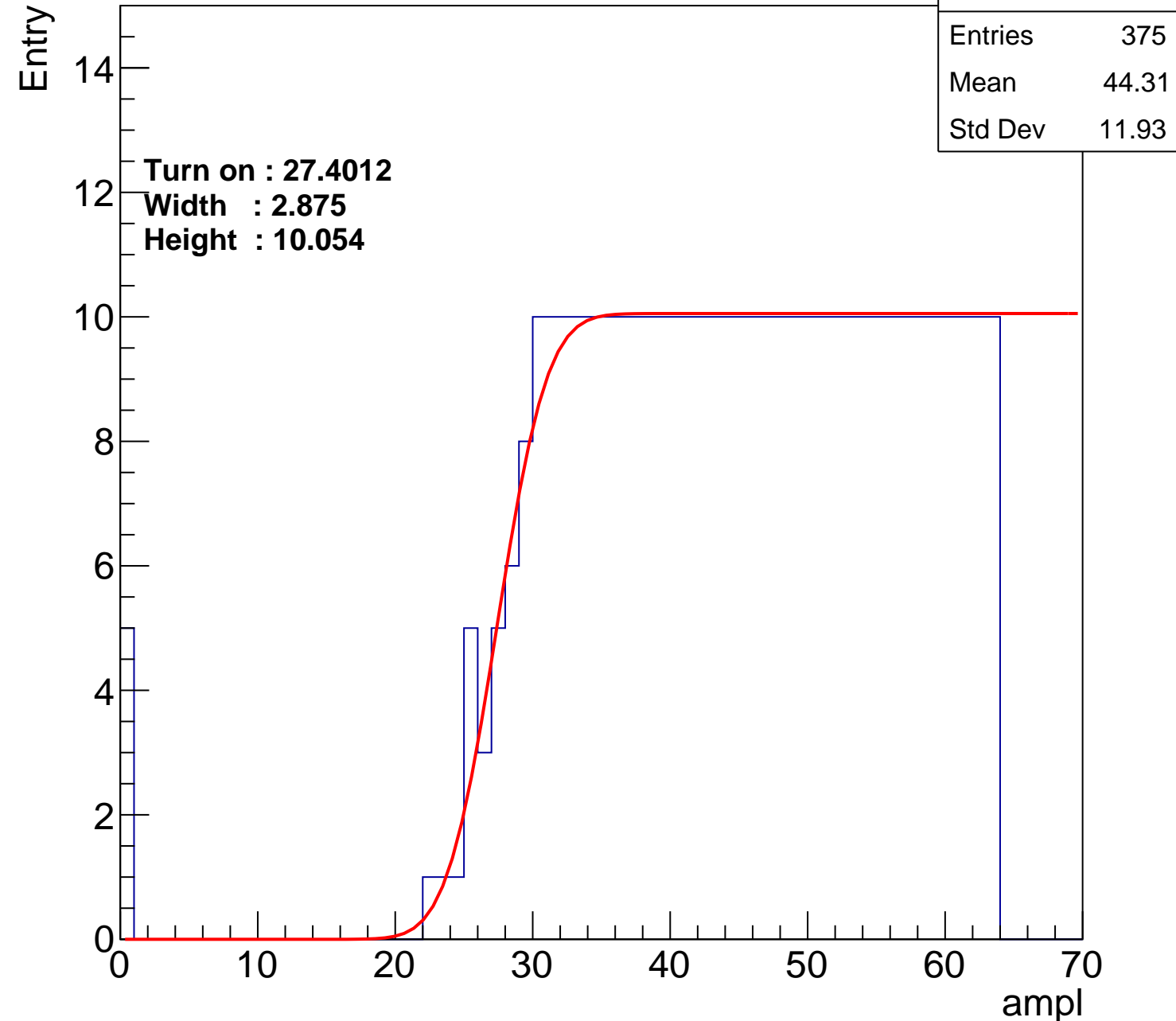
Width : 2.875

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch102

calib_packv5_042523_0143.root, FC#8, port C1

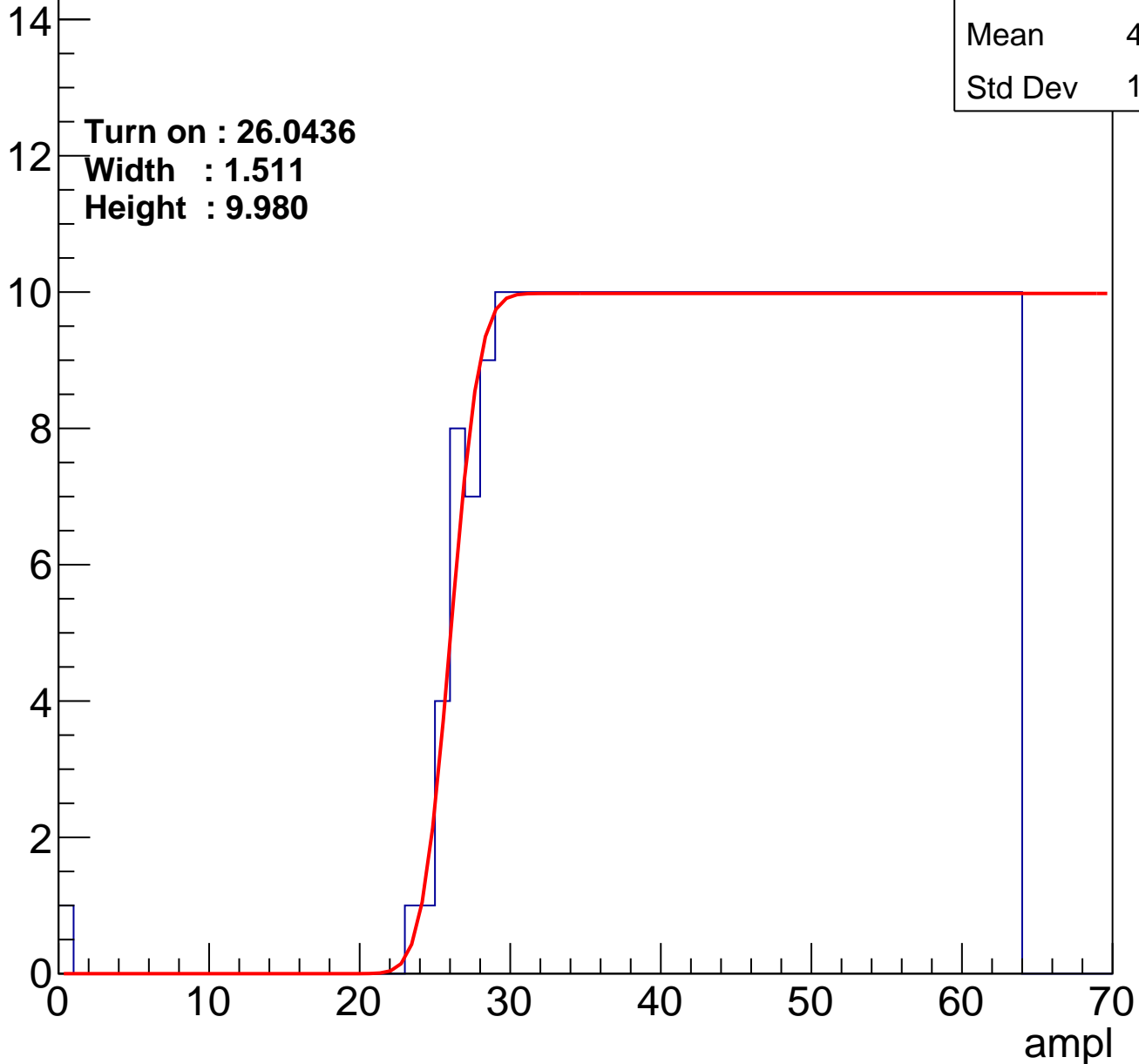
Entries	381
Mean	44.35
Std Dev	11.25

Turn on : 26.0436

Width : 1.511

Height : 9.980

Entry



B0L002S, U13-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.43
Std Dev	10.77

Turn on : 29.1293

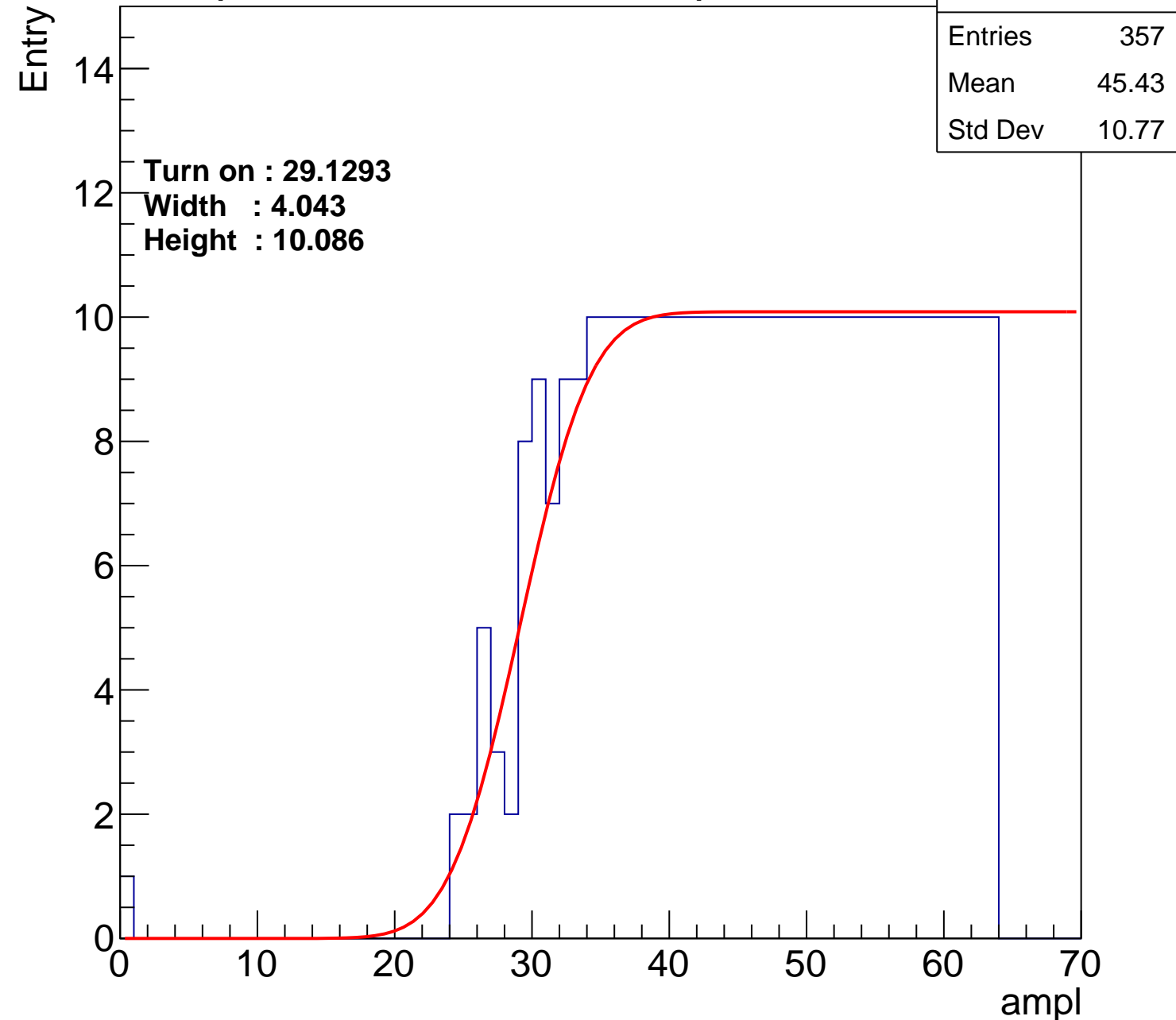
Width : 4.043

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.99
Std Dev	10.99

Turn on : 27.7080

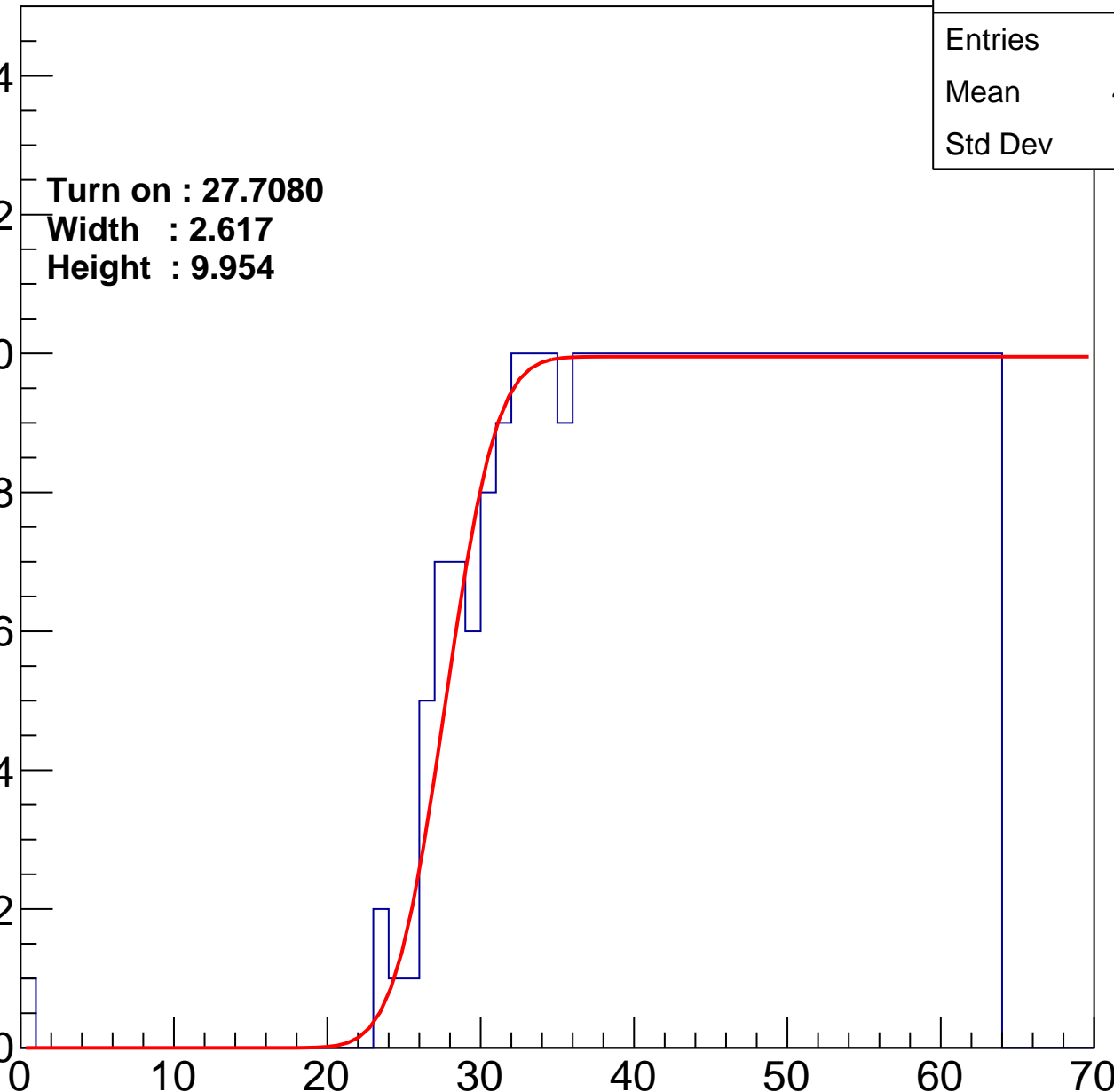
Width : 2.617

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.72
Std Dev	11.31

Turn on : 27.4515

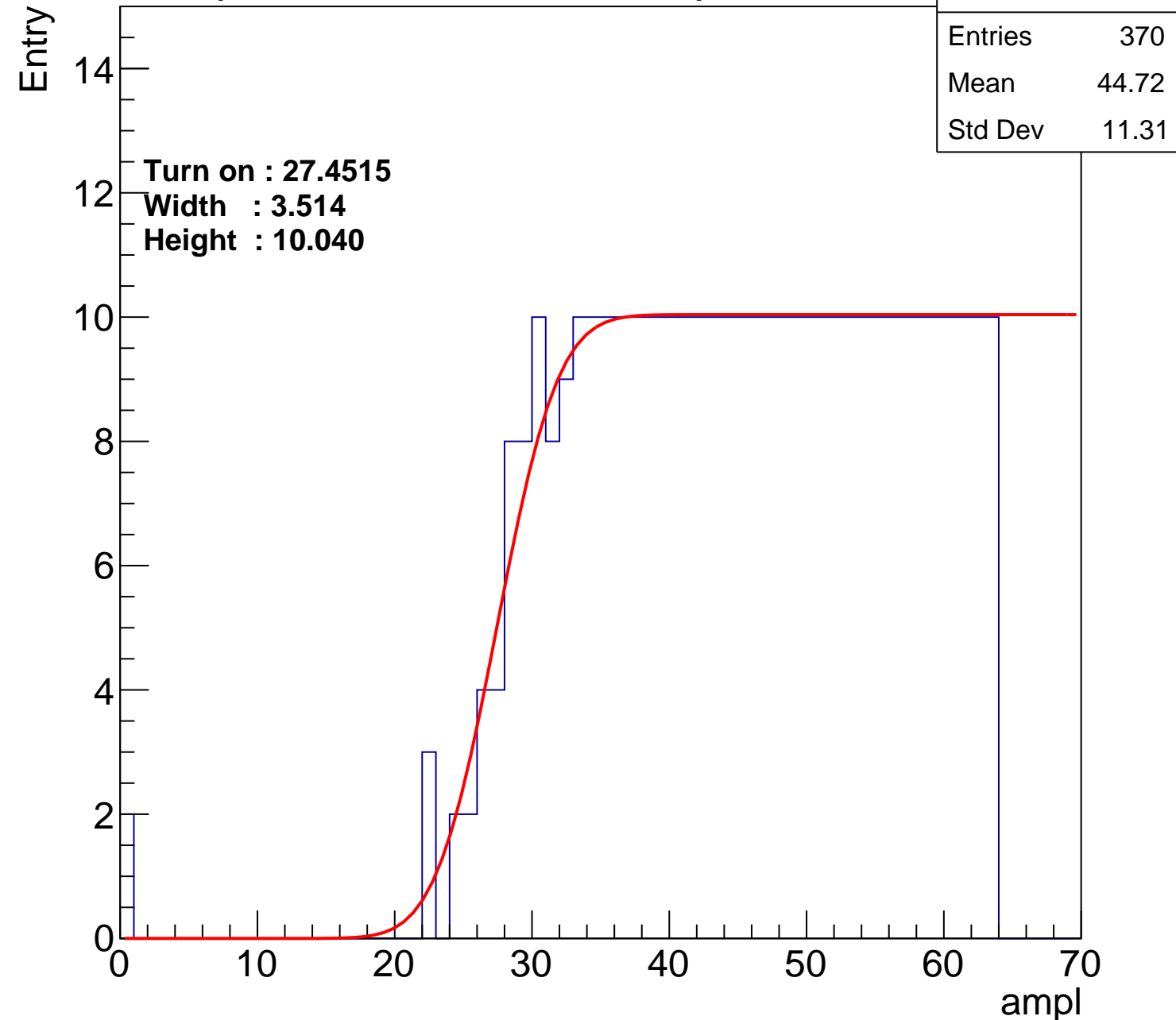
Width : 3.514

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.66
Std Dev	10.77

Turn on : 29.1544

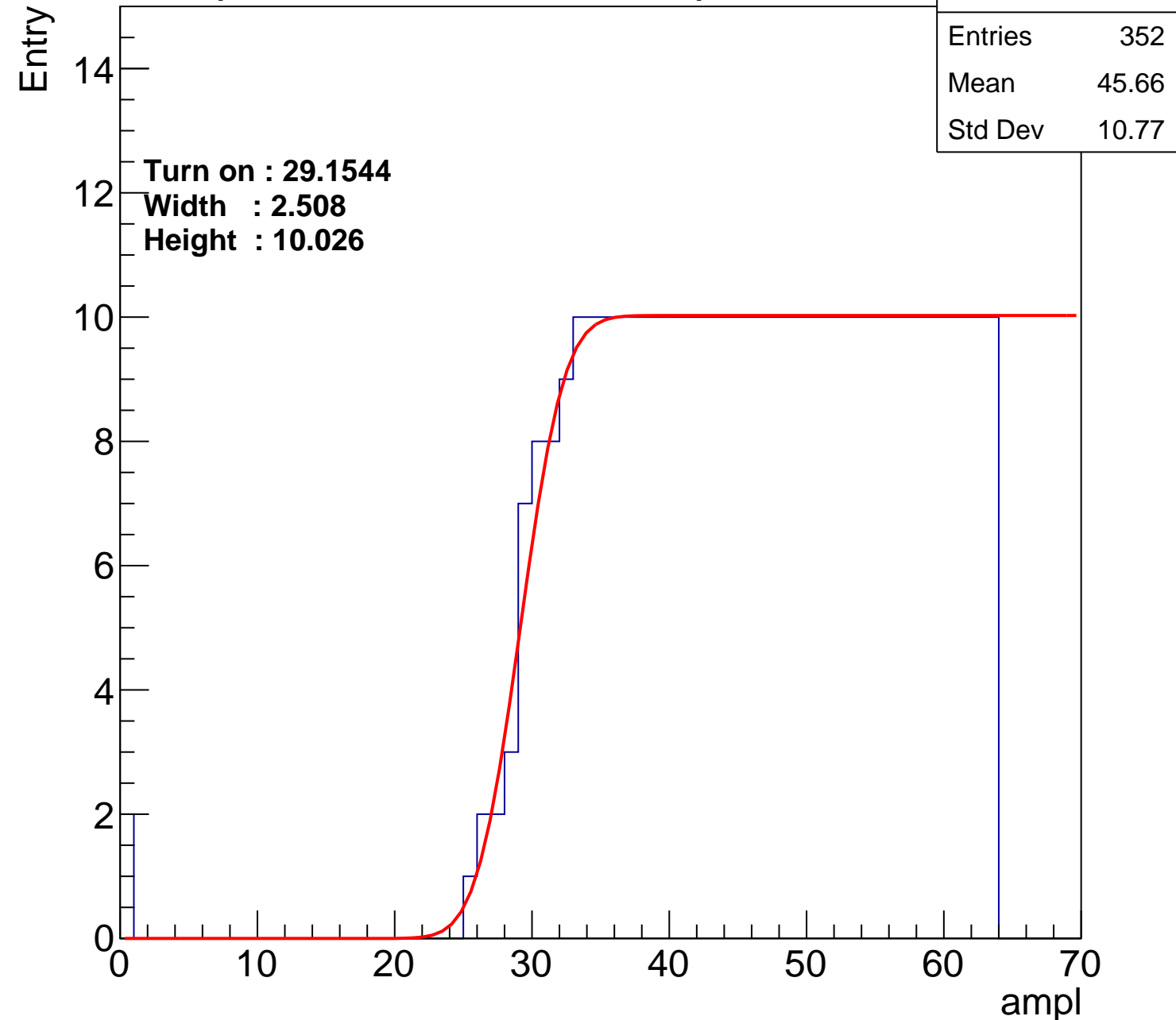
Width : 2.508

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.61
Std Dev	11.84

Turn on : 28.4679

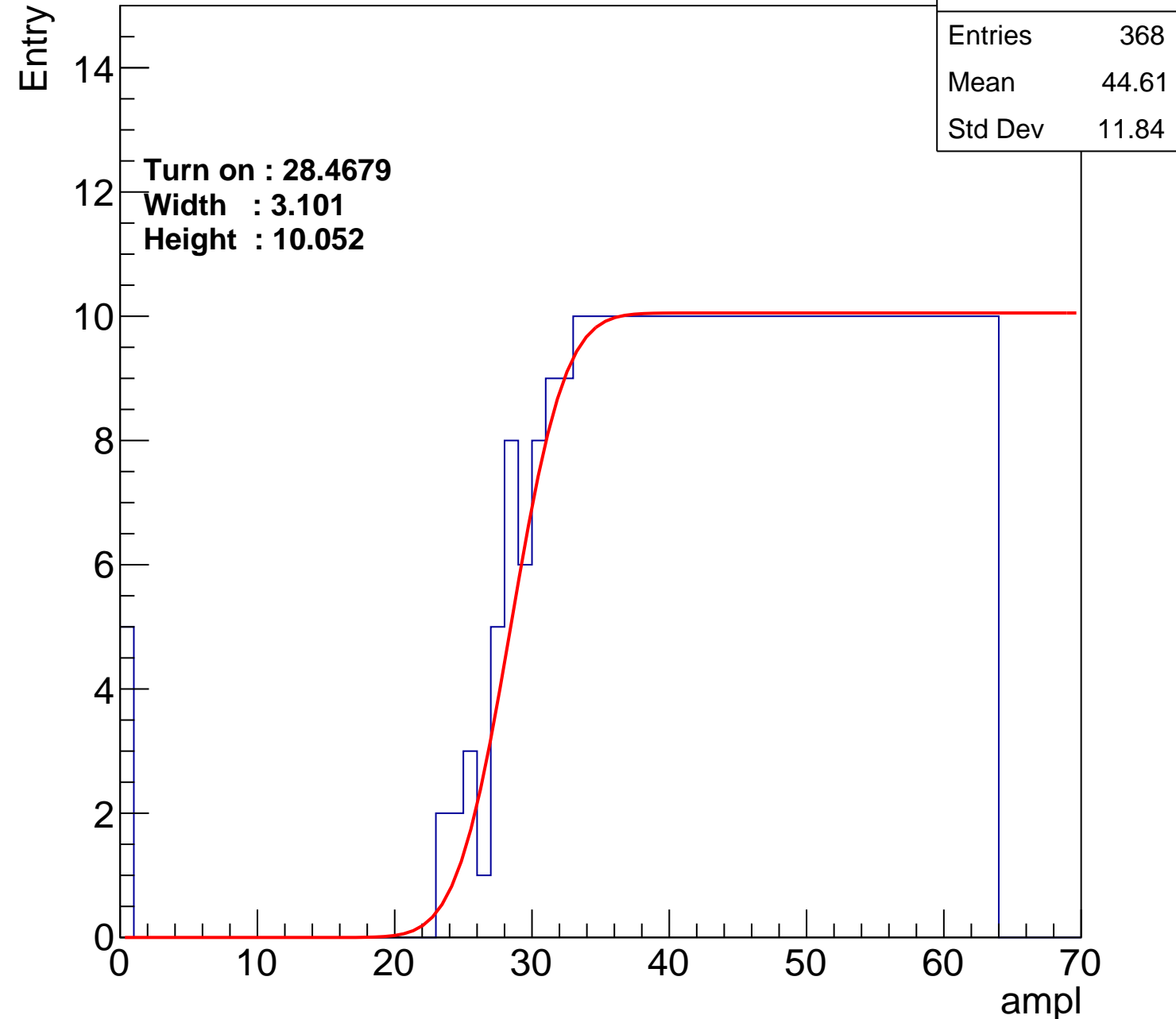
Width : 3.101

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.33
Std Dev	11.17

Turn on : 28.6319

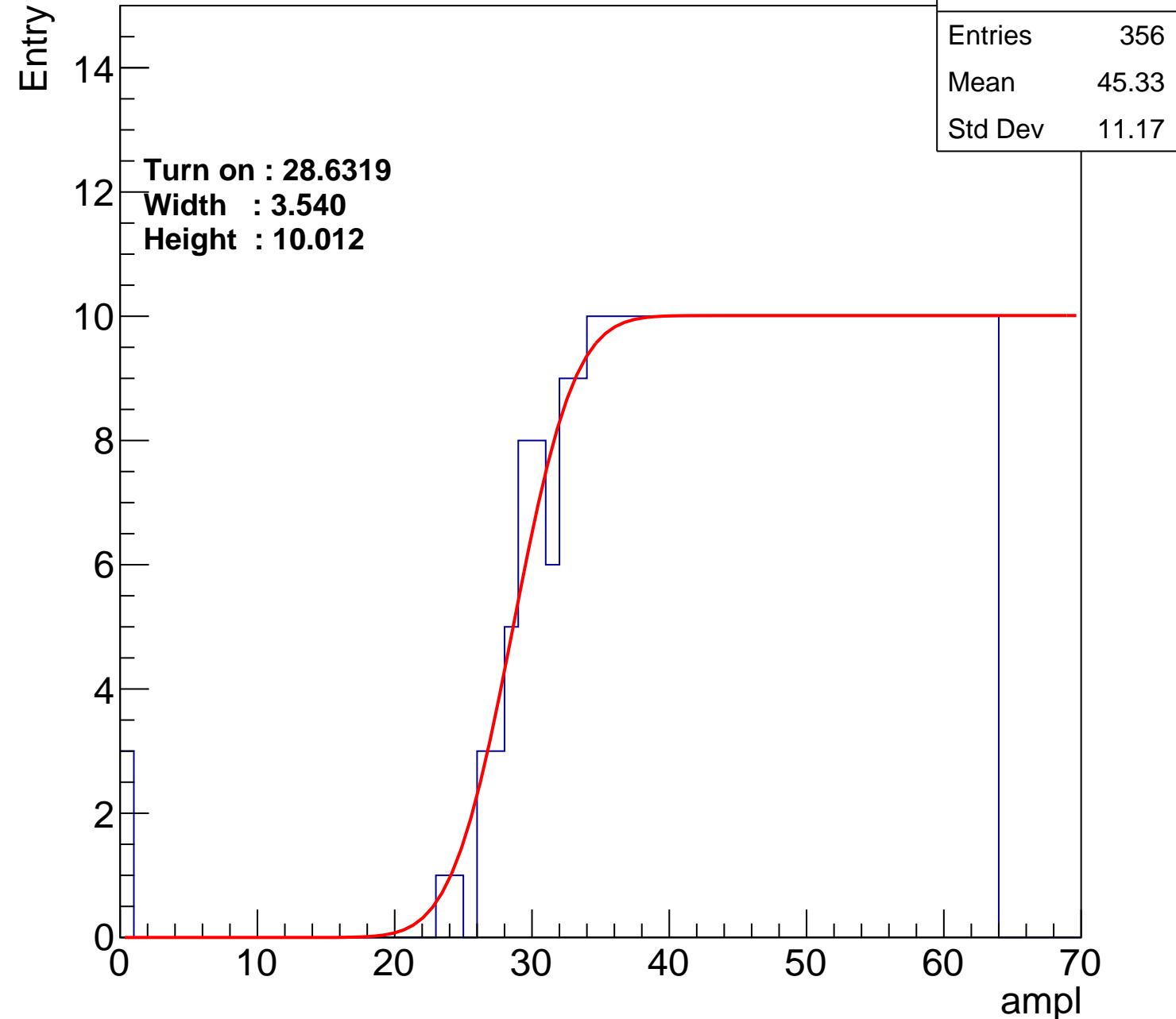
Width : 3.540

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch109

calib_packv5_042523_0143.root, FC#8, port C1

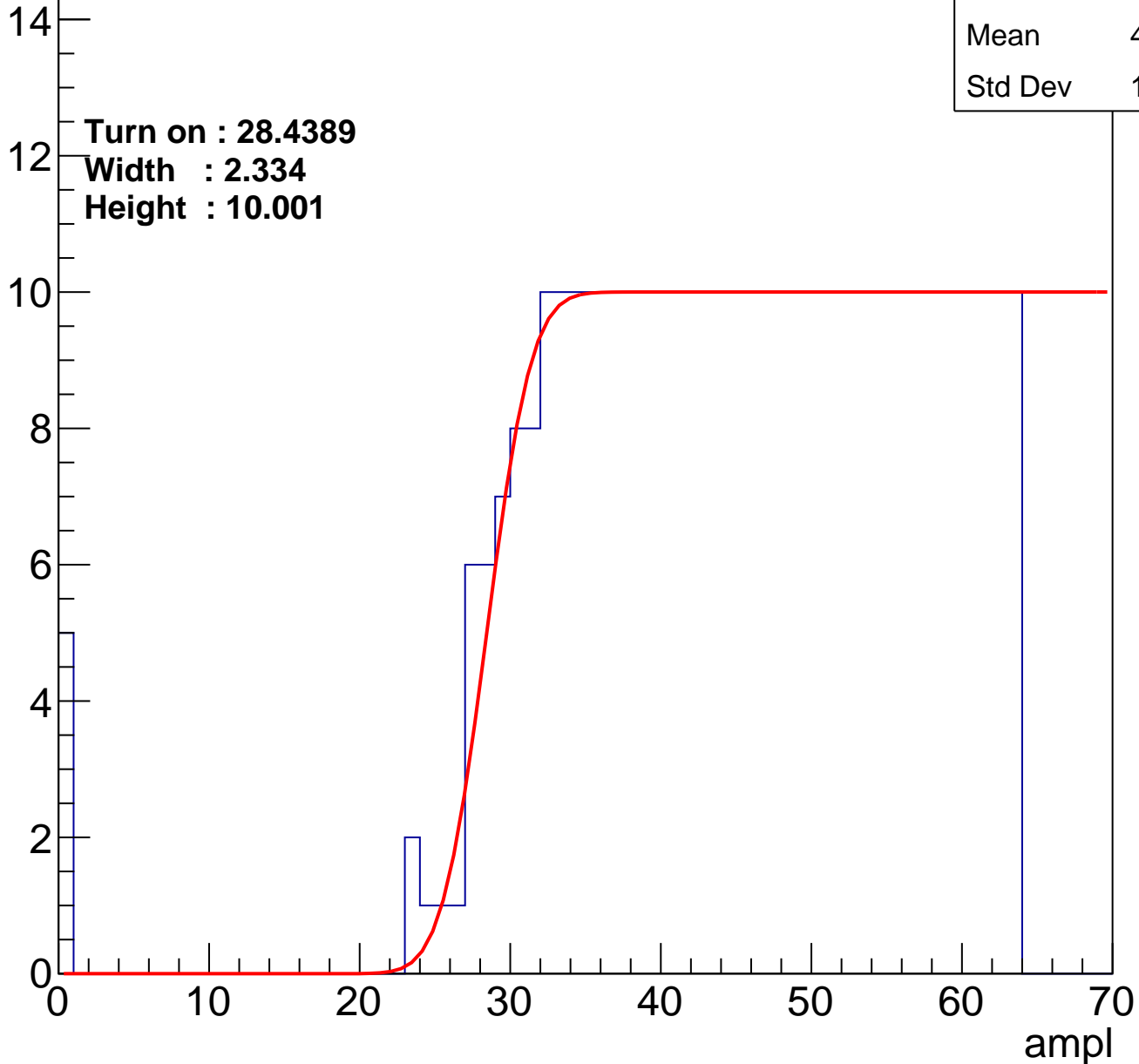
Entries	365
Mean	44.77
Std Dev	11.75

Turn on : 28.4389

Width : 2.334

Height : 10.001

Entry



B0L002S, U13-ch110

calib_packv5_042523_0143.root, FC#8, port C1

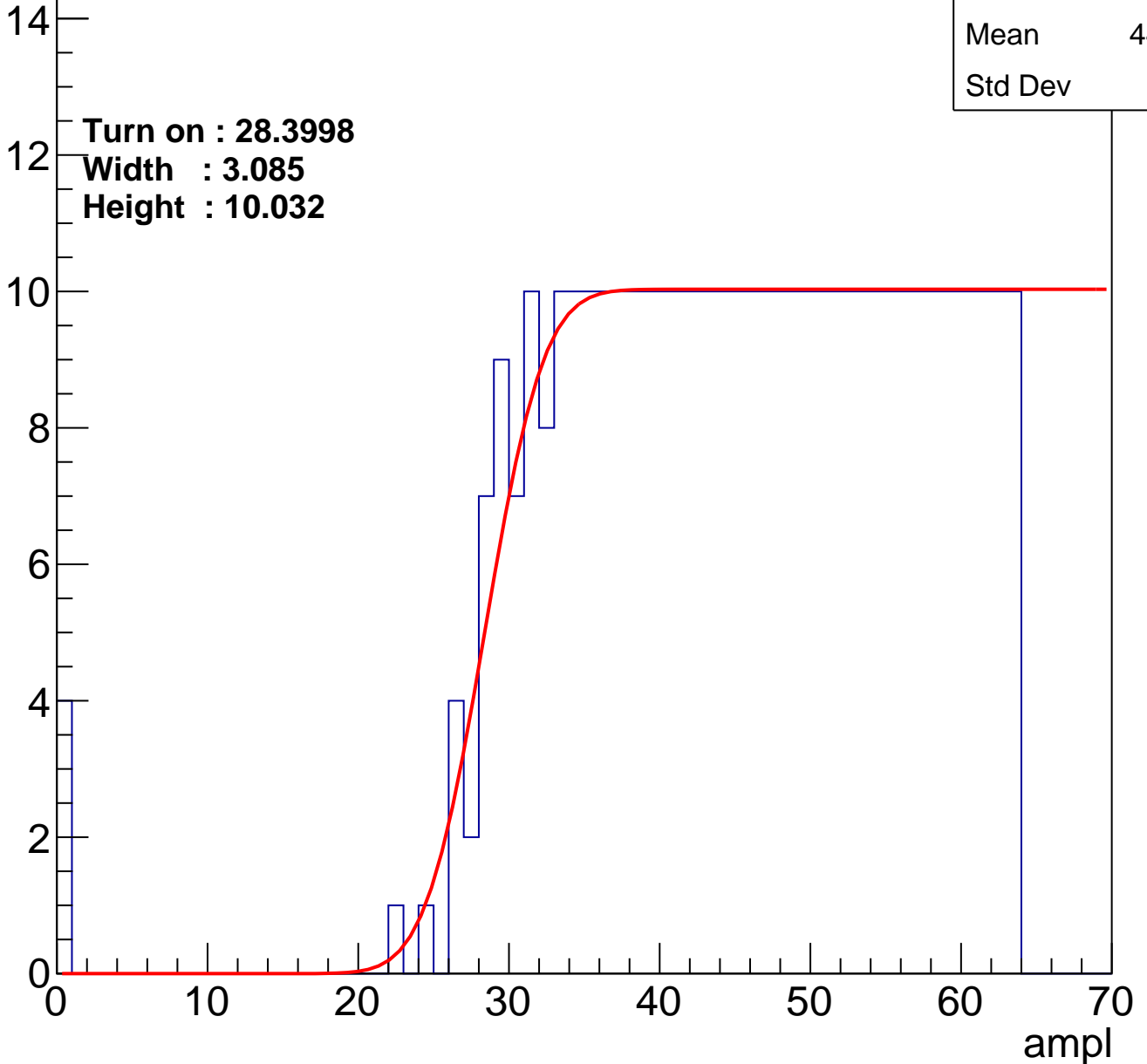
Entries	363
Mean	44.95
Std Dev	11.5

Turn on : 28.3998

Width : 3.085

Height : 10.032

Entry



B0L002S, U13-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.38
Std Dev	11.49

Turn on : 26.8317

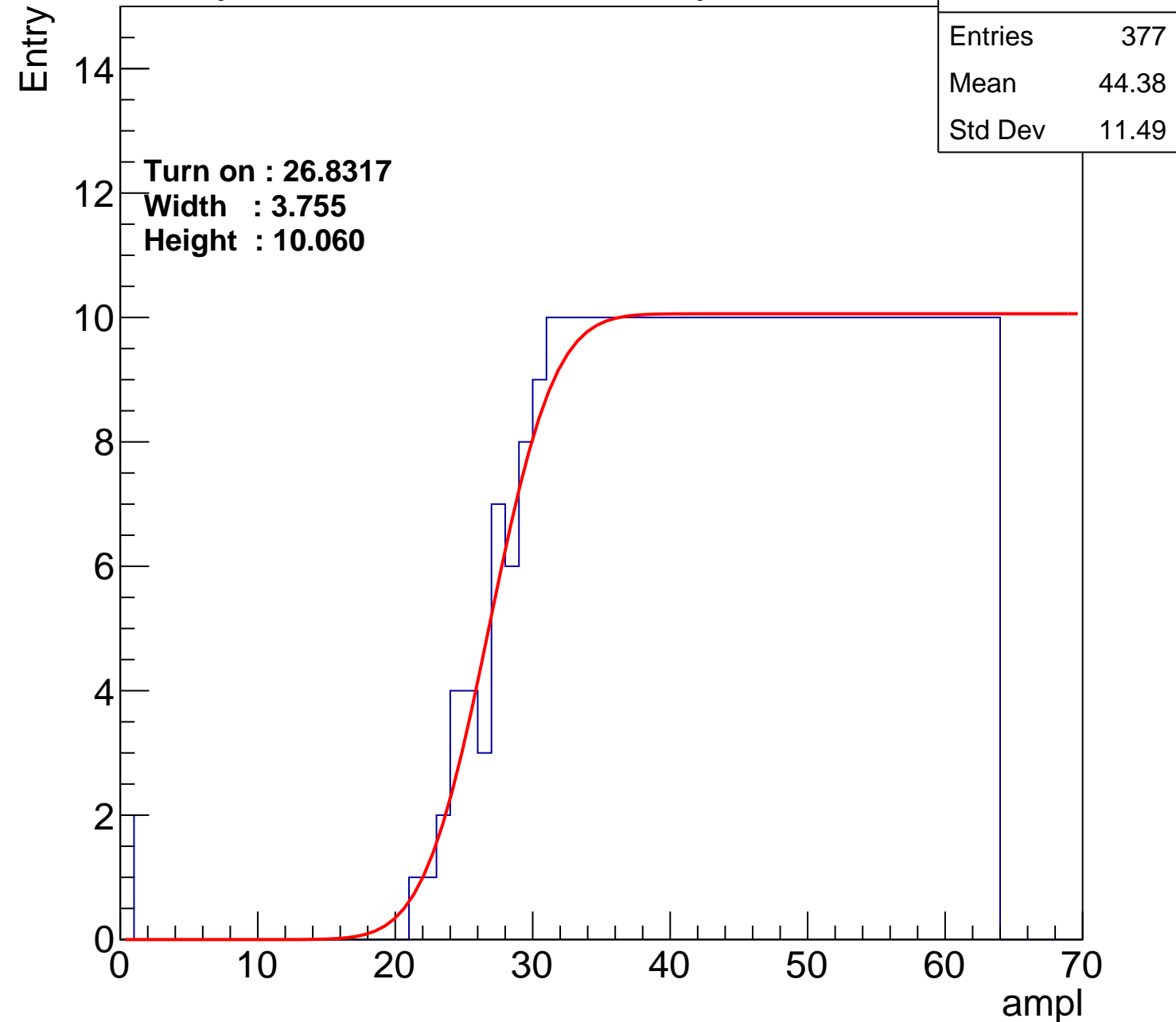
Width : 3.755

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.04
Std Dev	10.98

Turn on : 28.0881

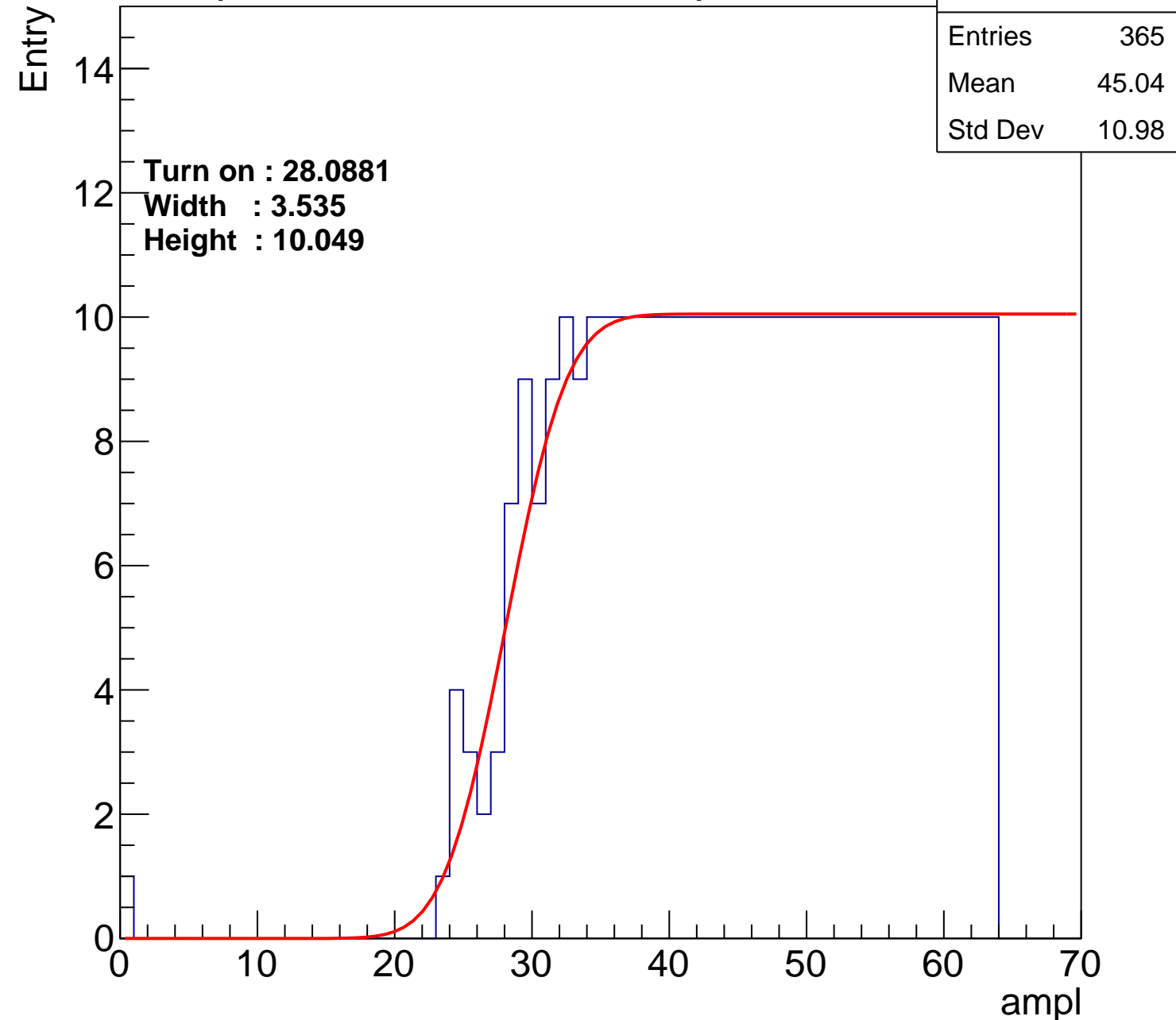
Width : 3.535

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.93
Std Dev	11.34

Turn on : 28.2333

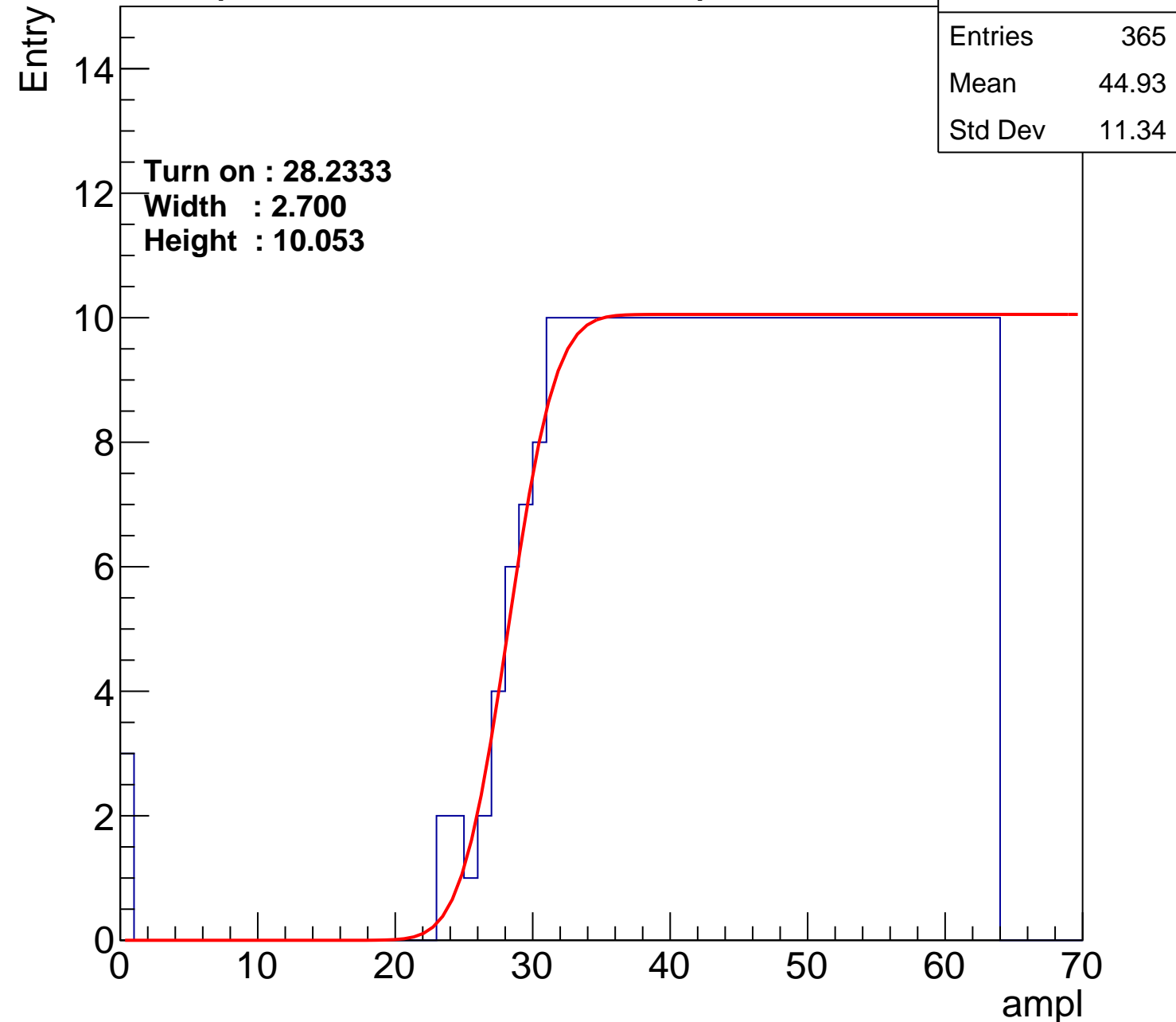
Width : 2.700

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.65
Std Dev	11.33

Turn on : 27.2206

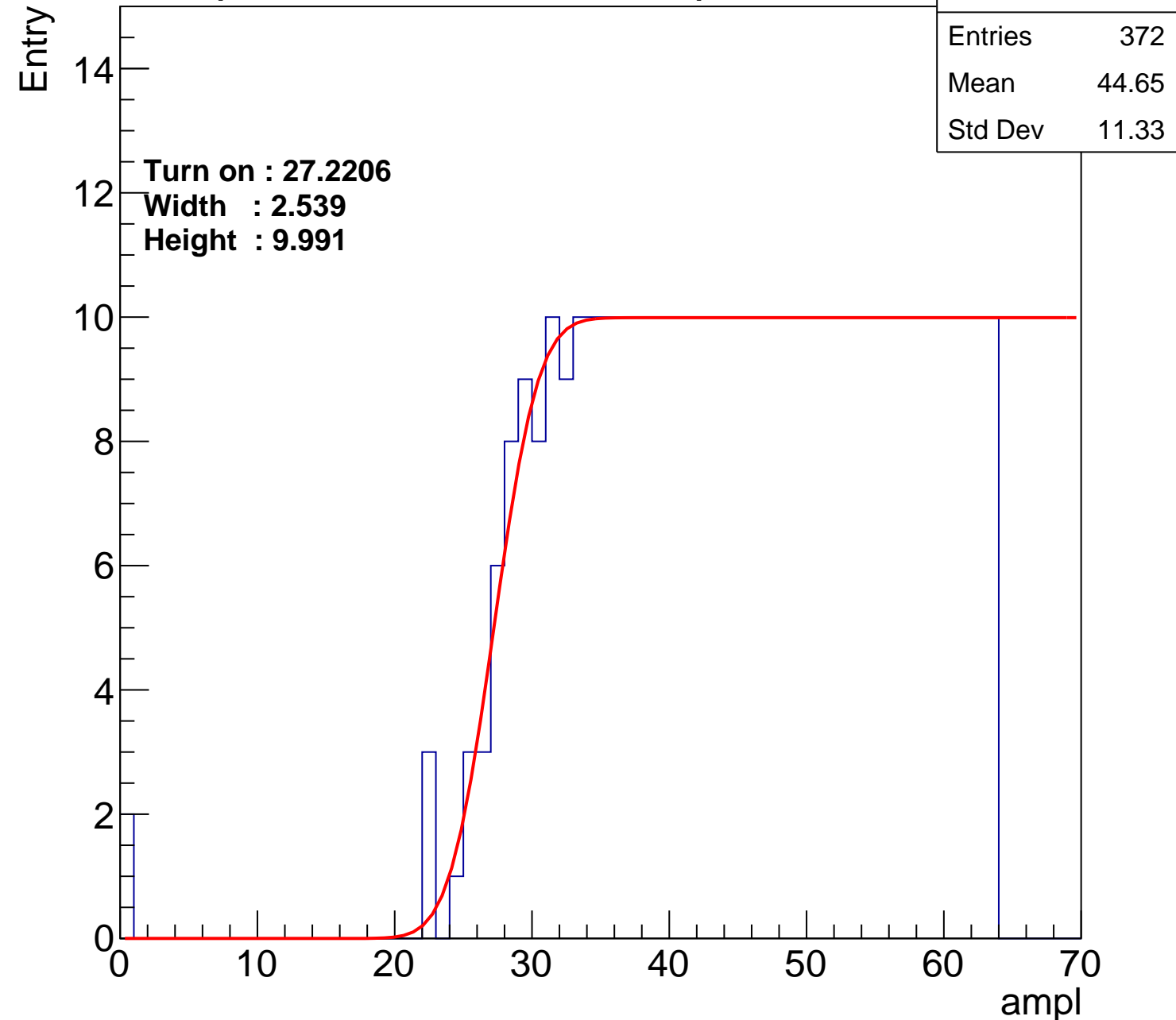
Width : 2.539

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.91
Std Dev	11.09

Turn on : 28.4811

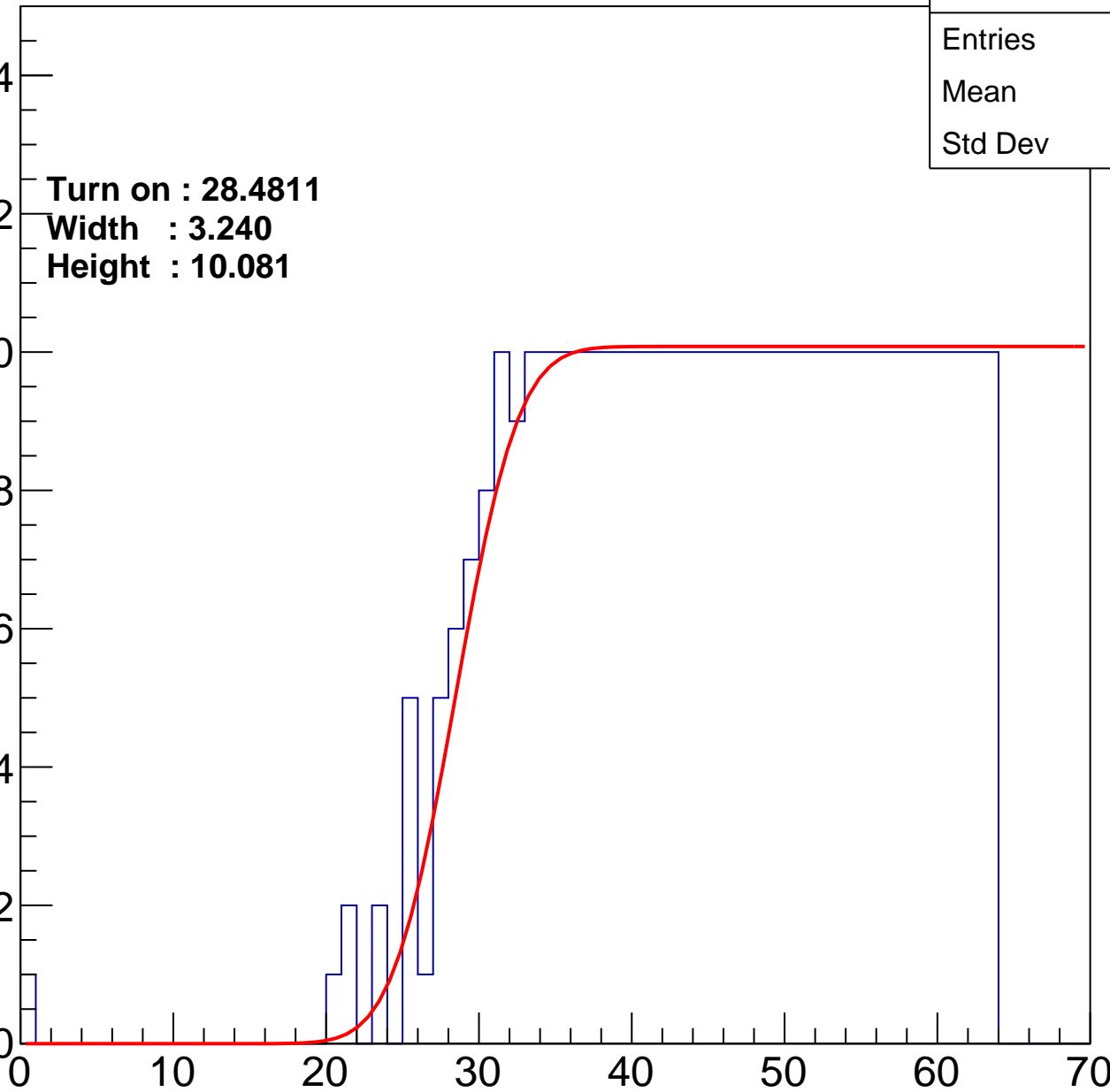
Width : 3.240

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.33
Std Dev	11.73

Turn on : 27.0369

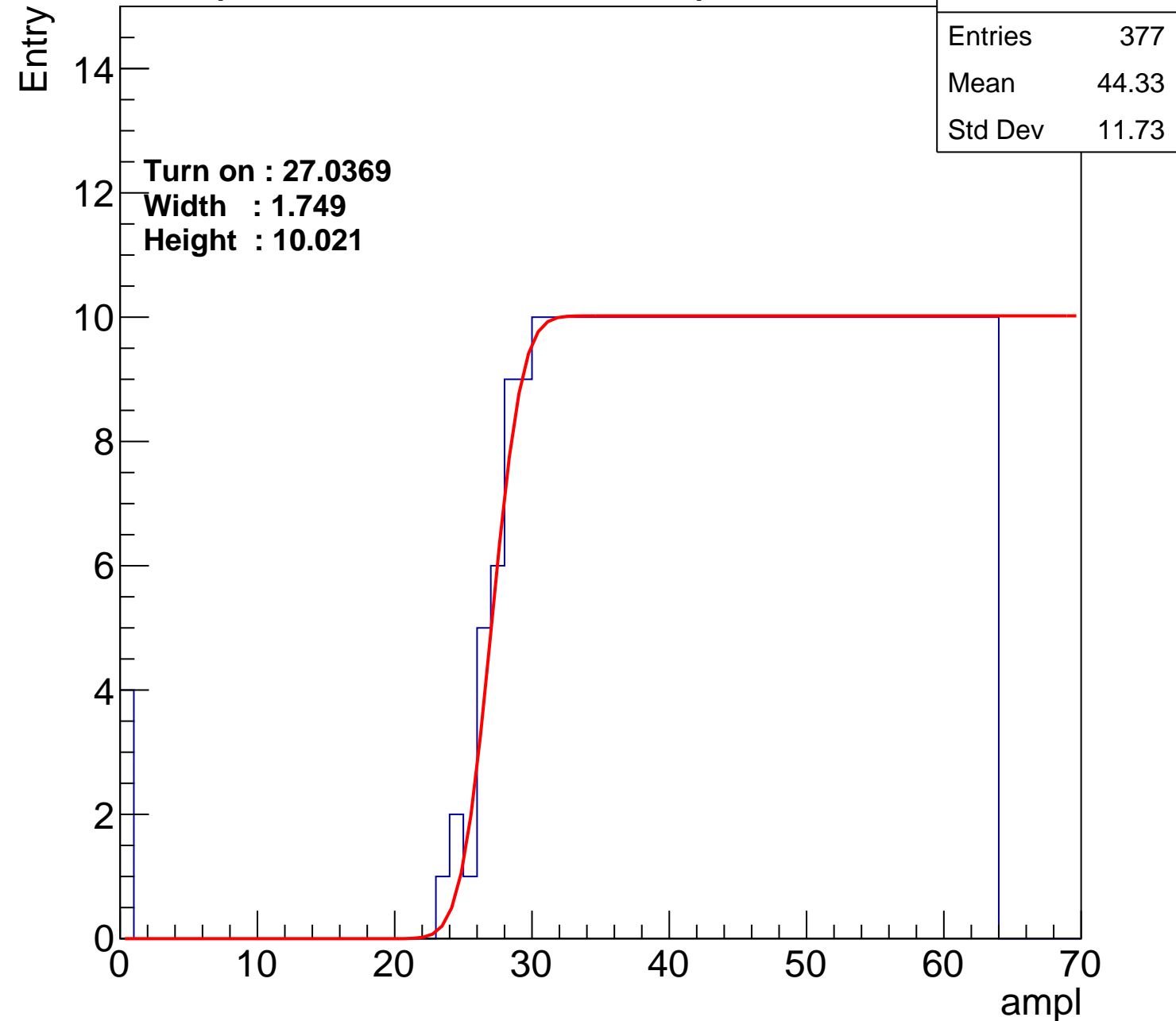
Width : 1.749

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.64
Std Dev	11.18

Turn on : 29.7741

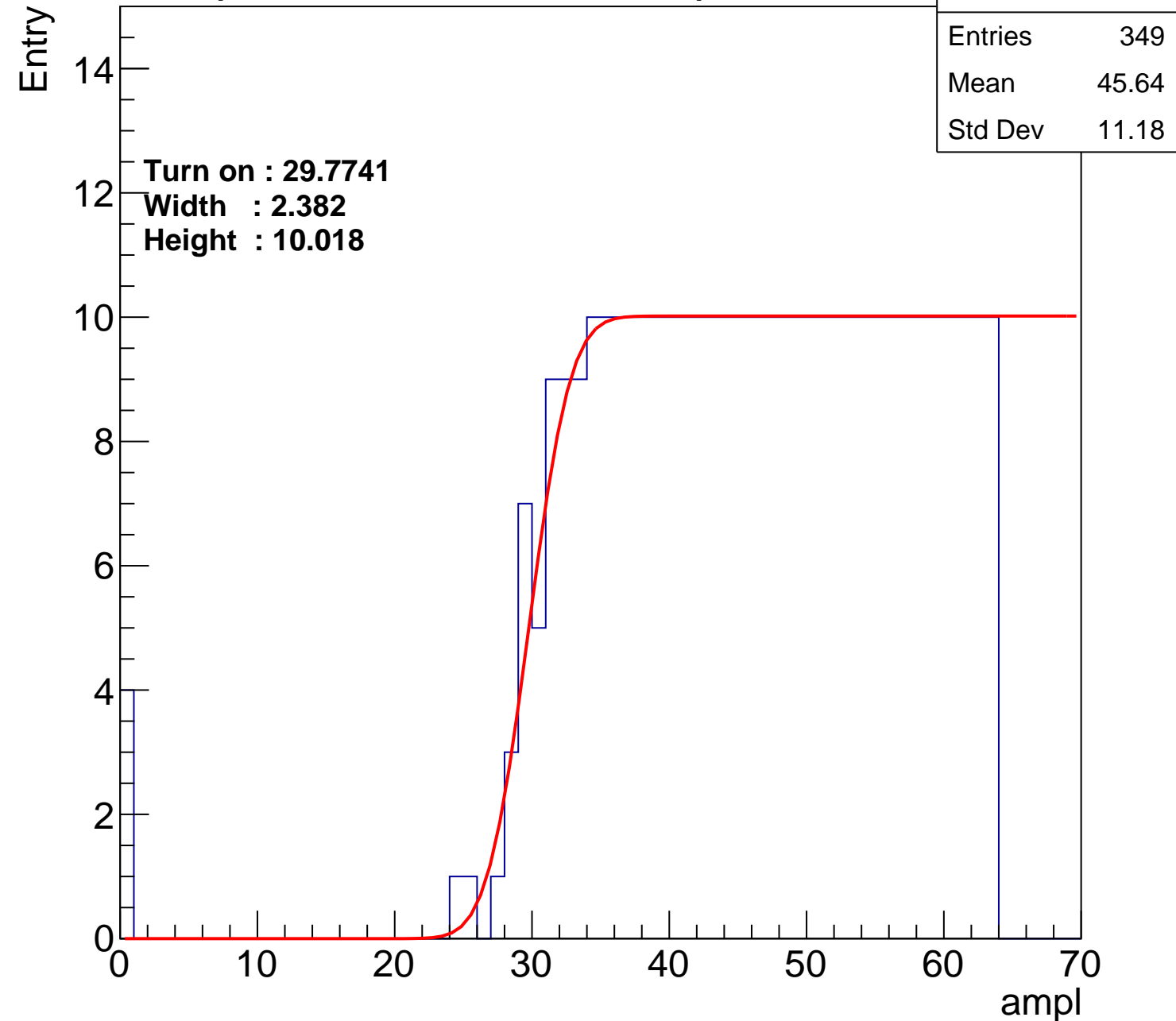
Width : 2.382

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.33
Std Dev	10.82

Turn on : 28.1514

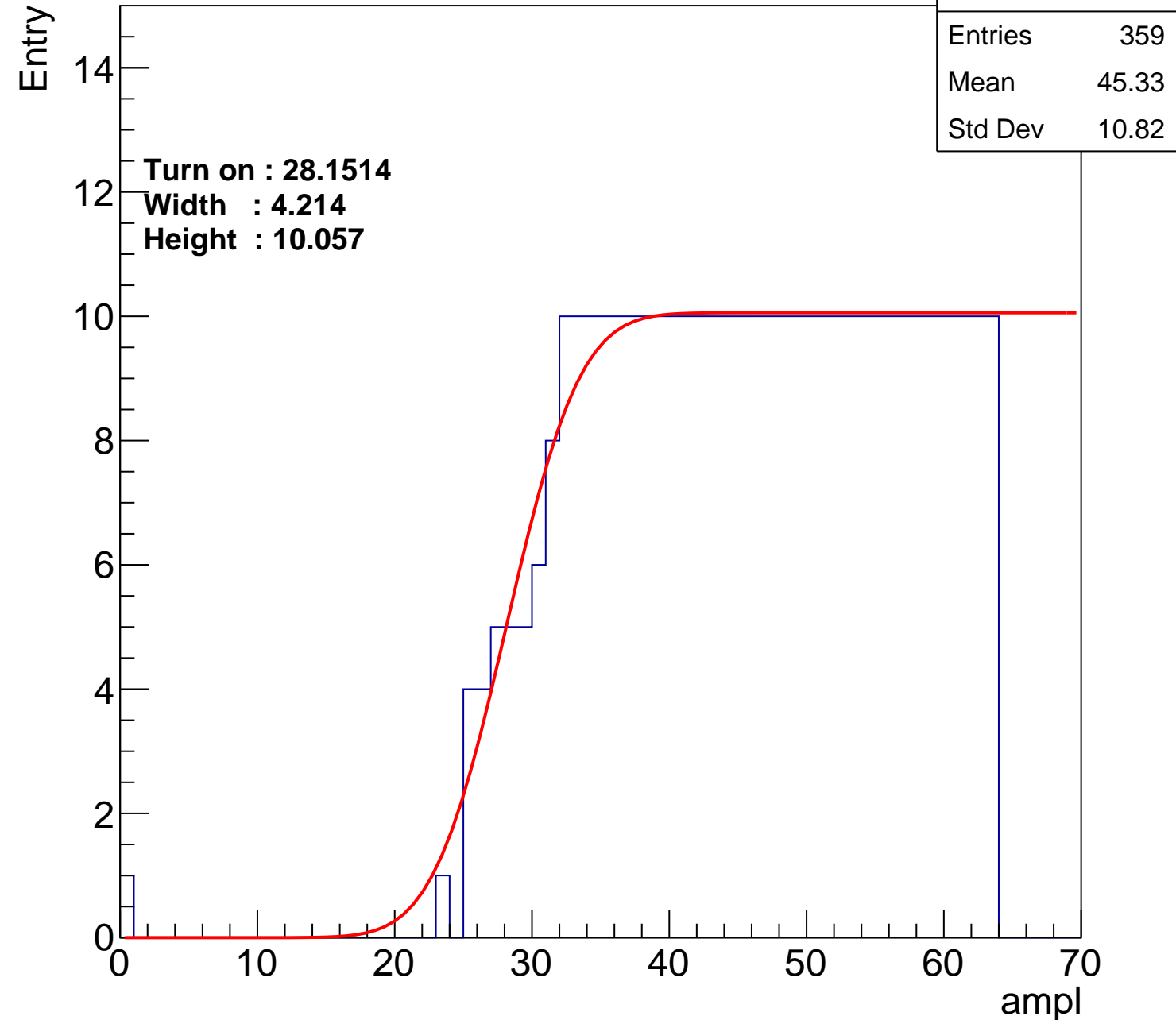
Width : 4.214

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.3
Std Dev	10.82

Turn on : 29.4611

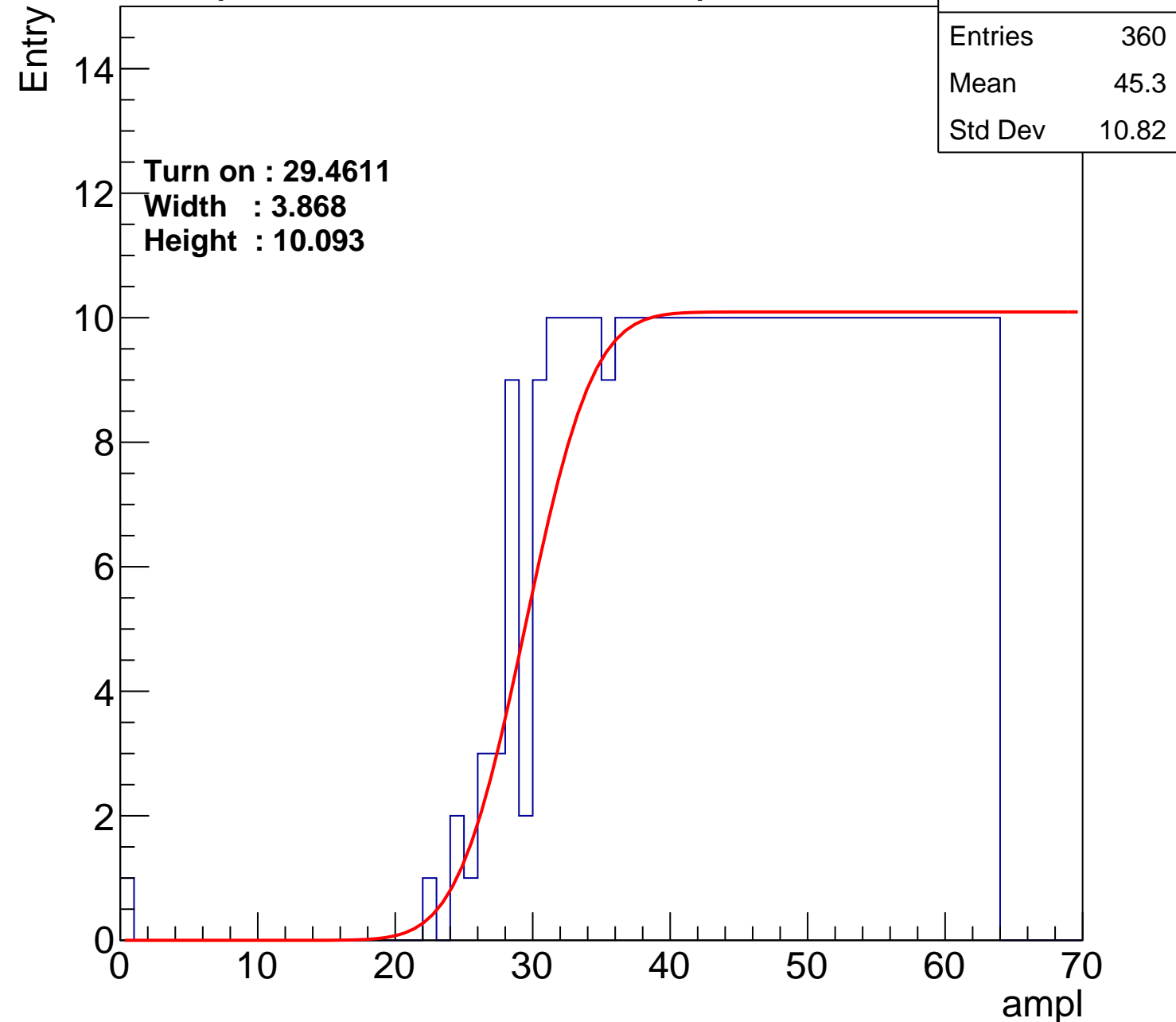
Width : 3.868

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch120

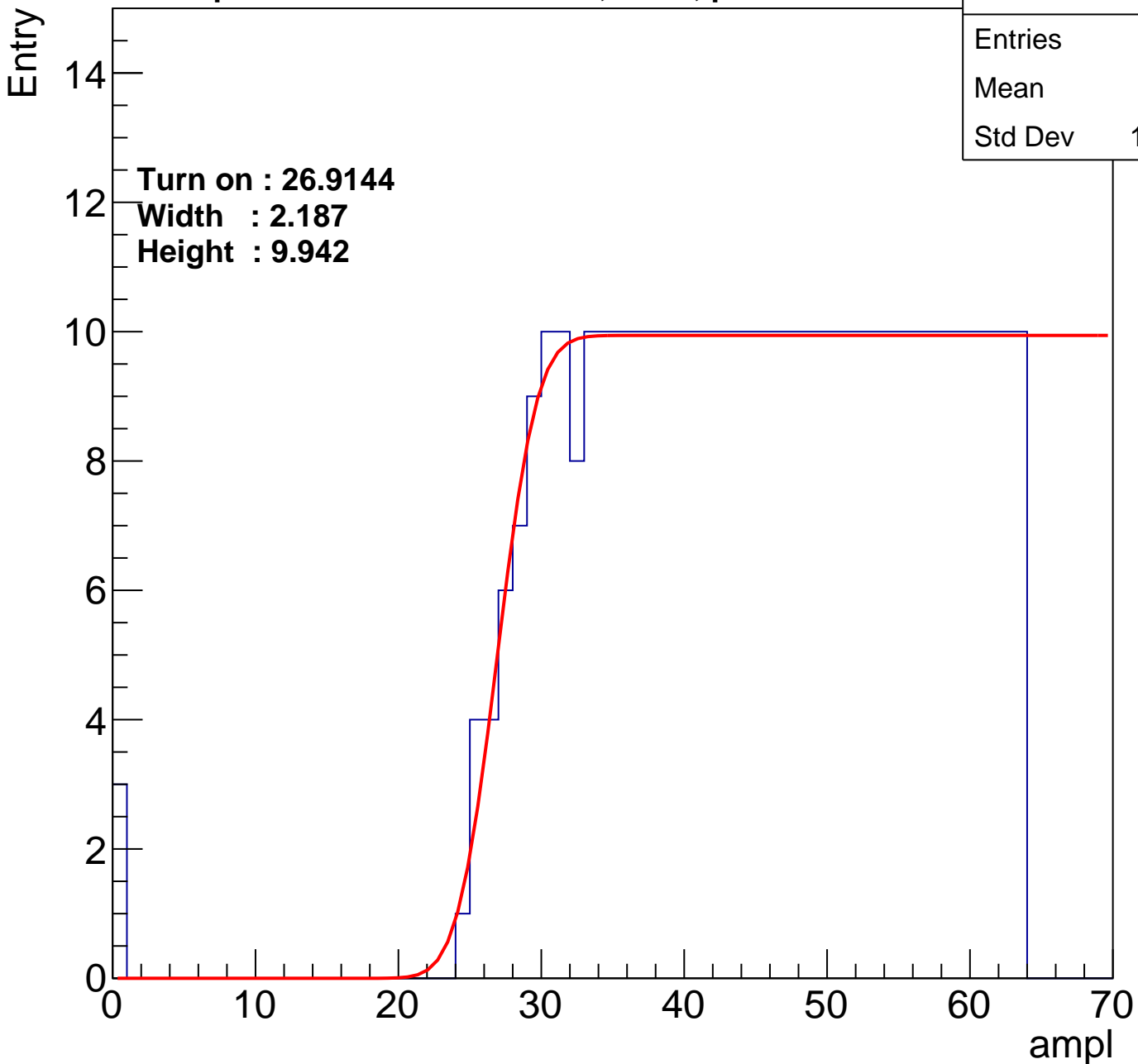
calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.6
Std Dev	11.47

Turn on : 26.9144

Width : 2.187

Height : 9.942



B0L002S, U13-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.34
Std Dev	10.82

Turn on : 28.4623

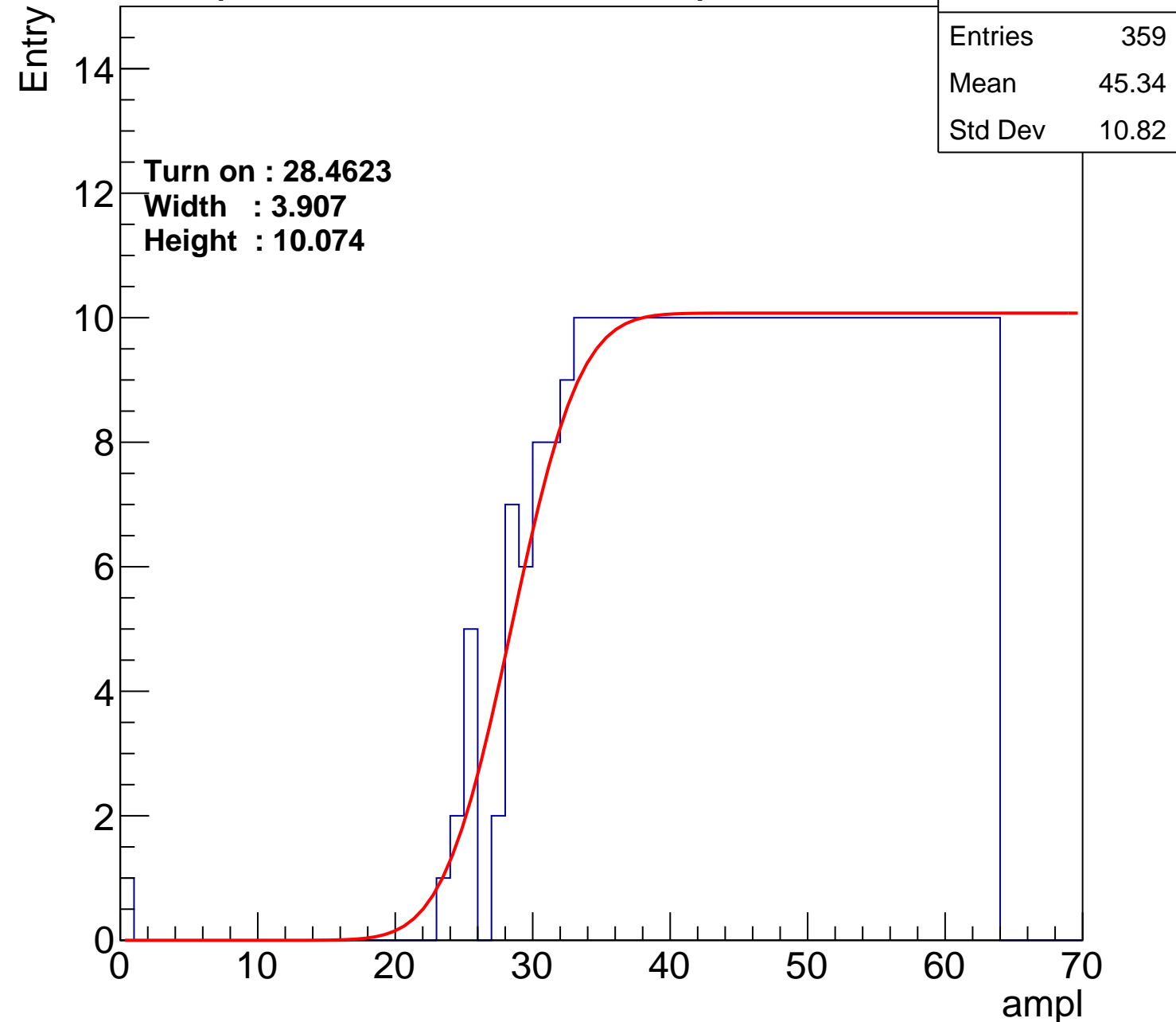
Width : 3.907

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch122

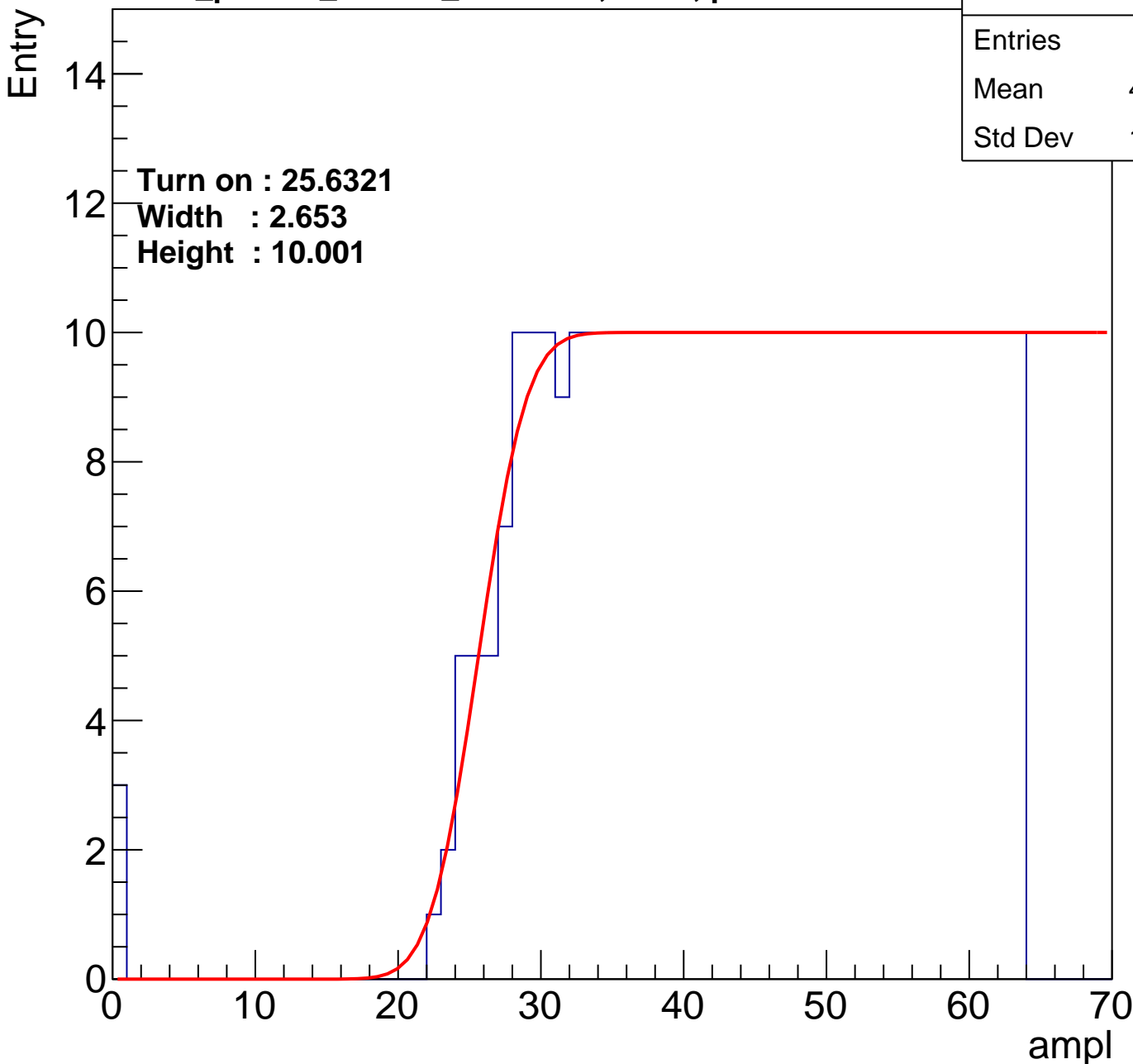
calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.88
Std Dev	11.82

Turn on : 25.6321

Width : 2.653

Height : 10.001



B0L002S, U13-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.9402

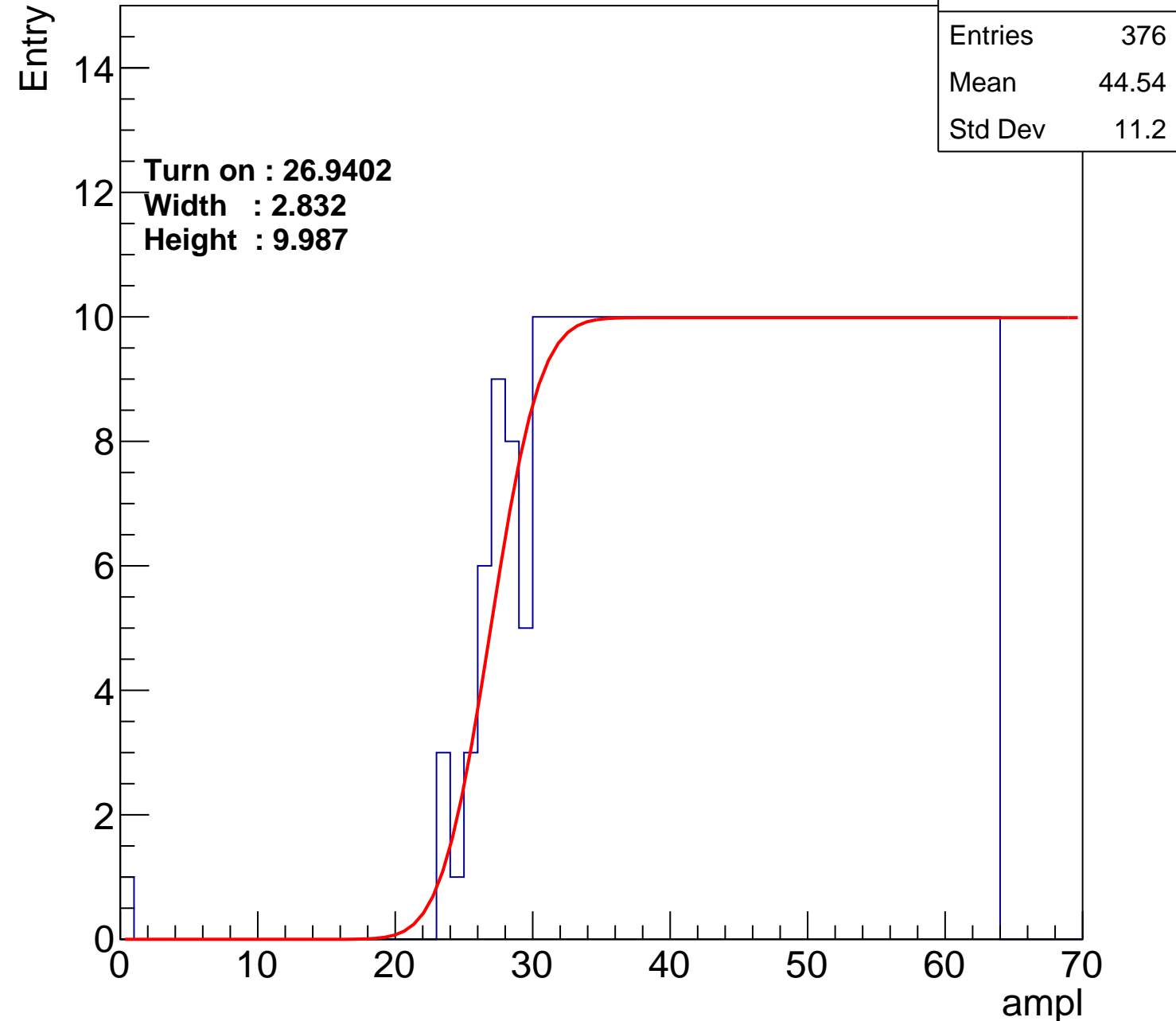
Width : 2.832

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.71
Std Dev	11.28

Turn on : 26.8482

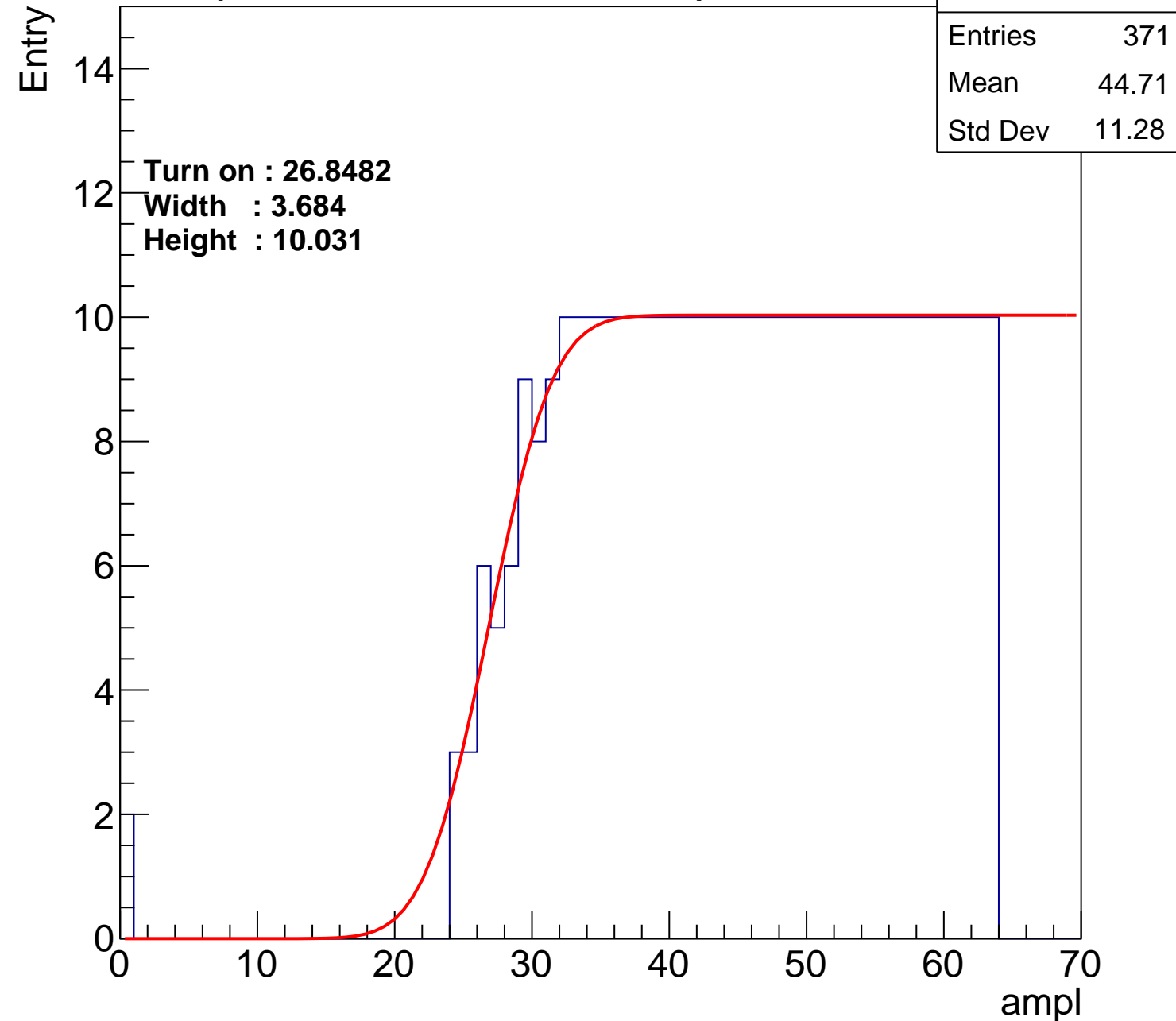
Width : 3.684

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.08
Std Dev	11.64

Turn on : 29.0362

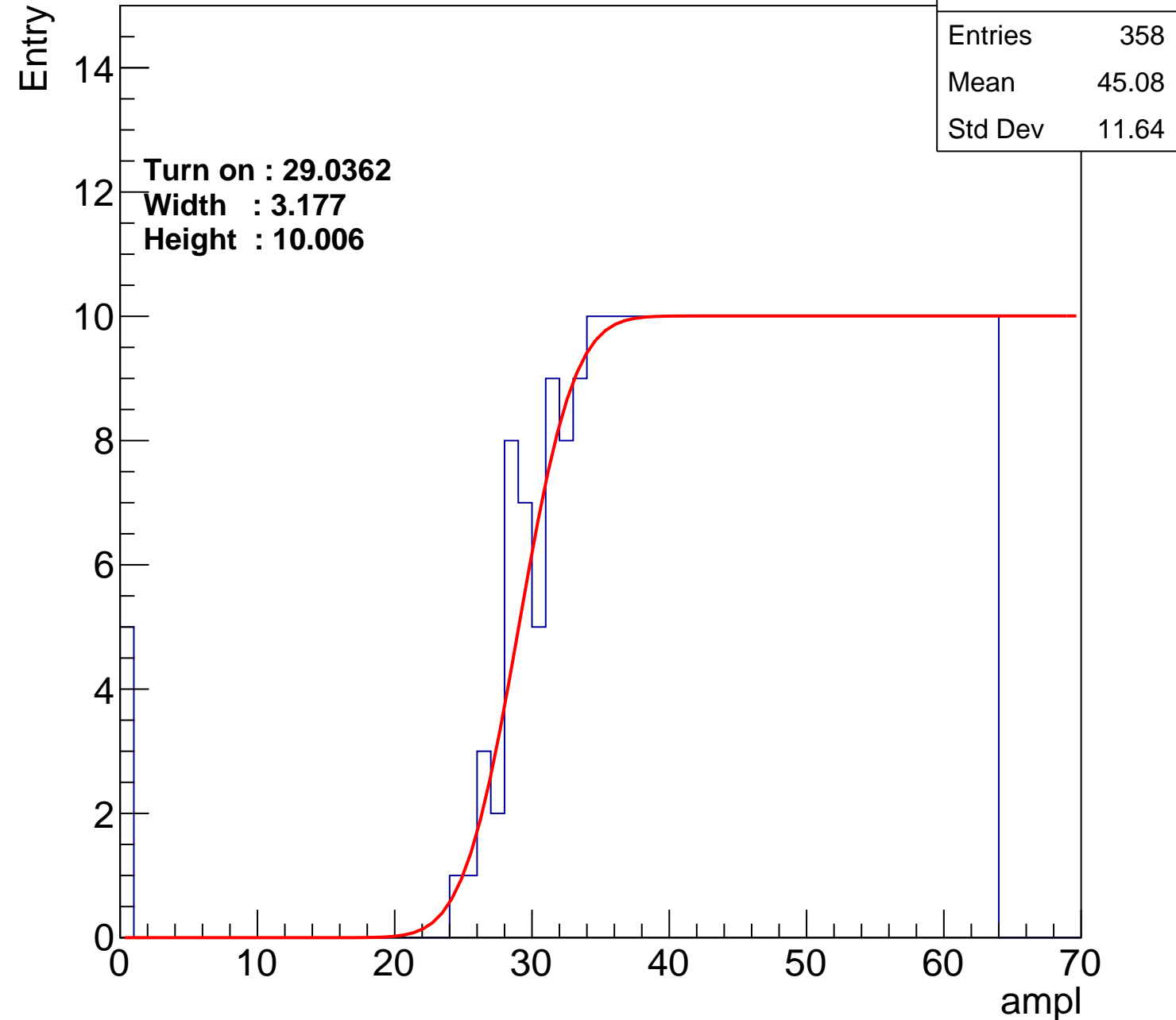
Width : 3.177

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.56
Std Dev	11.49

Turn on : 27.0027

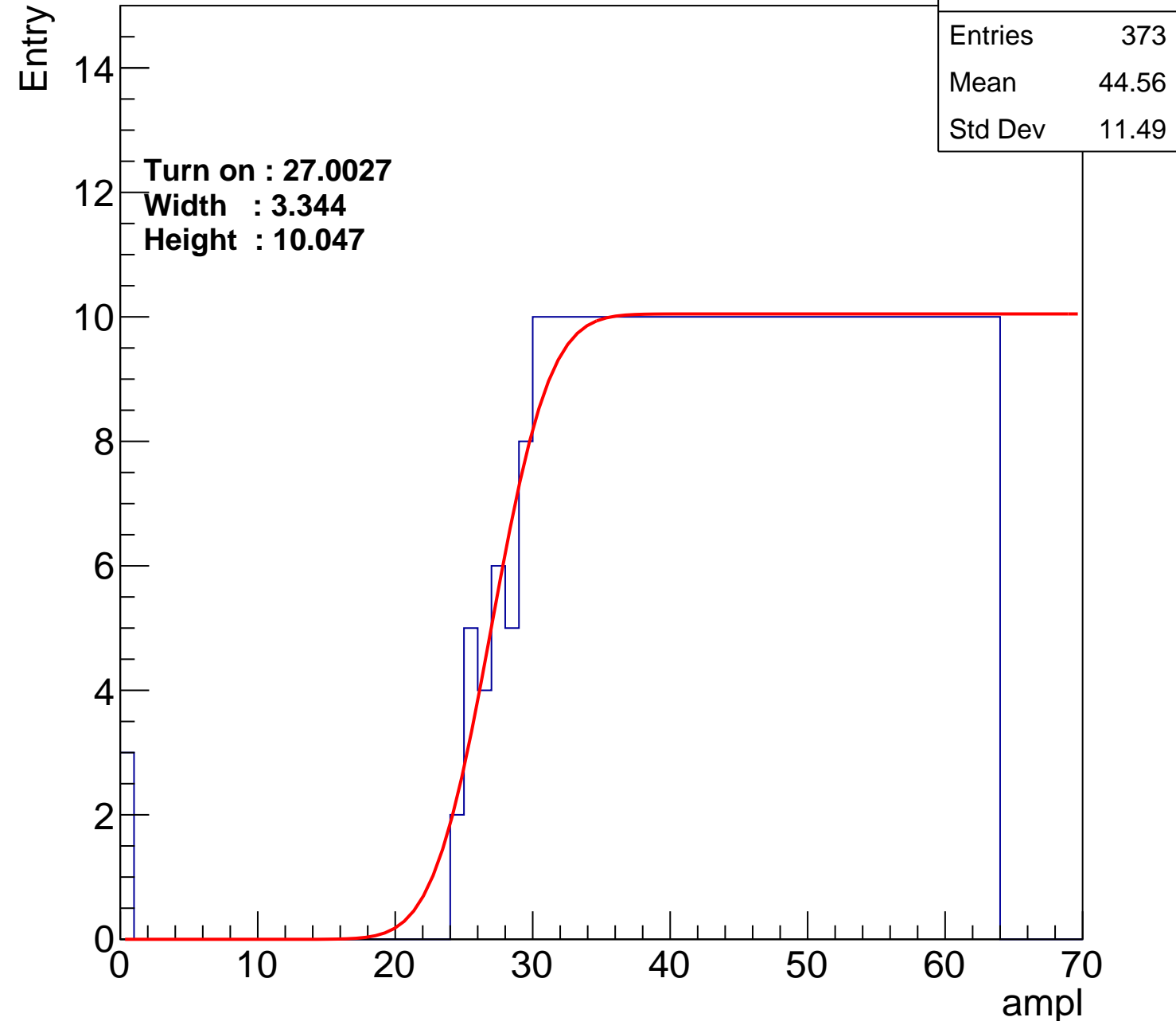
Width : 3.344

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.11
Std Dev	11.42

Turn on : 28.8386

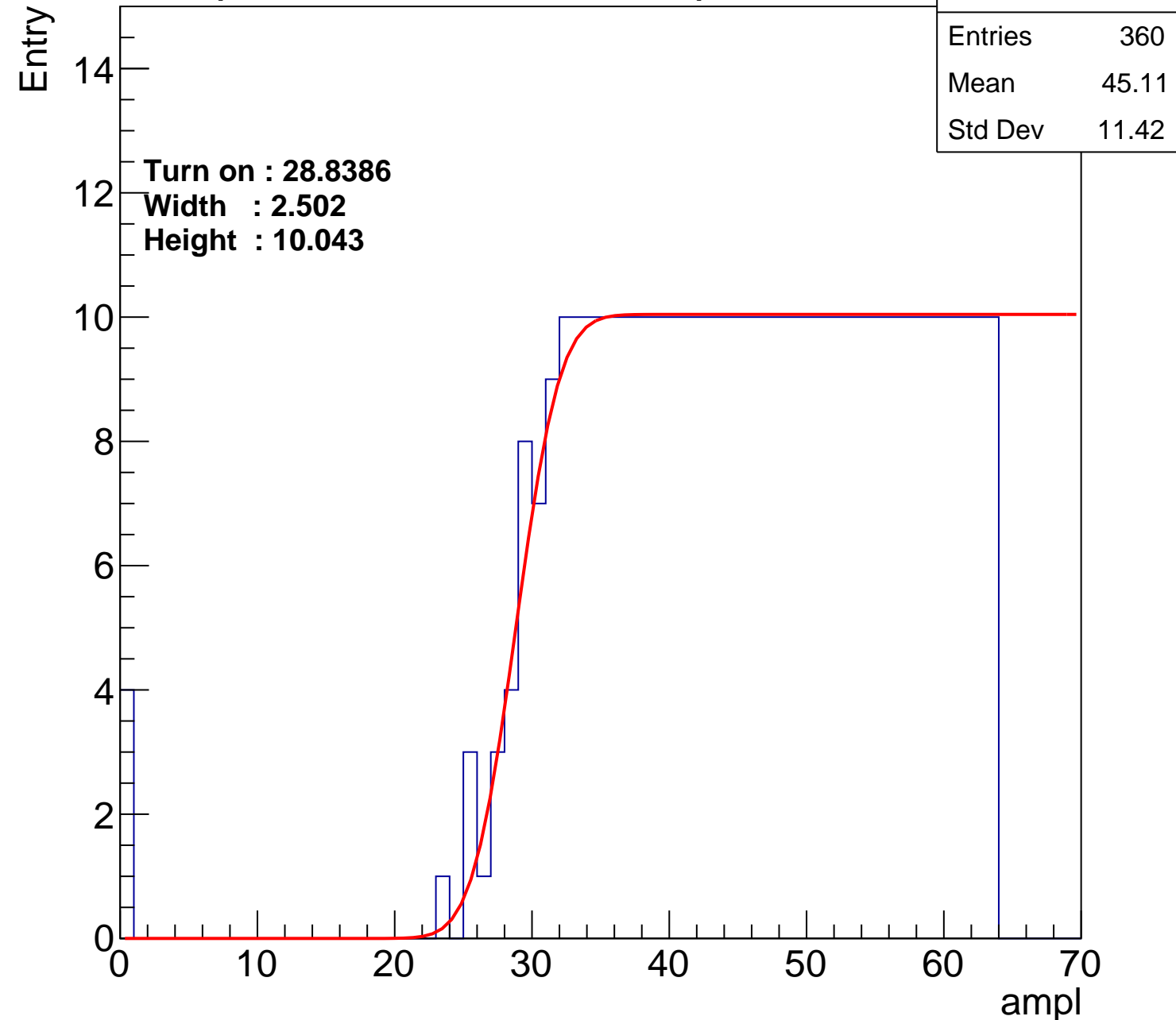
Width : 2.502

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U13-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.11
Std Dev	11.42

Turn on : 28.8386

Width : 2.502

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl

