



# B1L103S, U21-ch0

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	383
Mean	43.54
Std Dev	12.84

Turn on : 26.9118

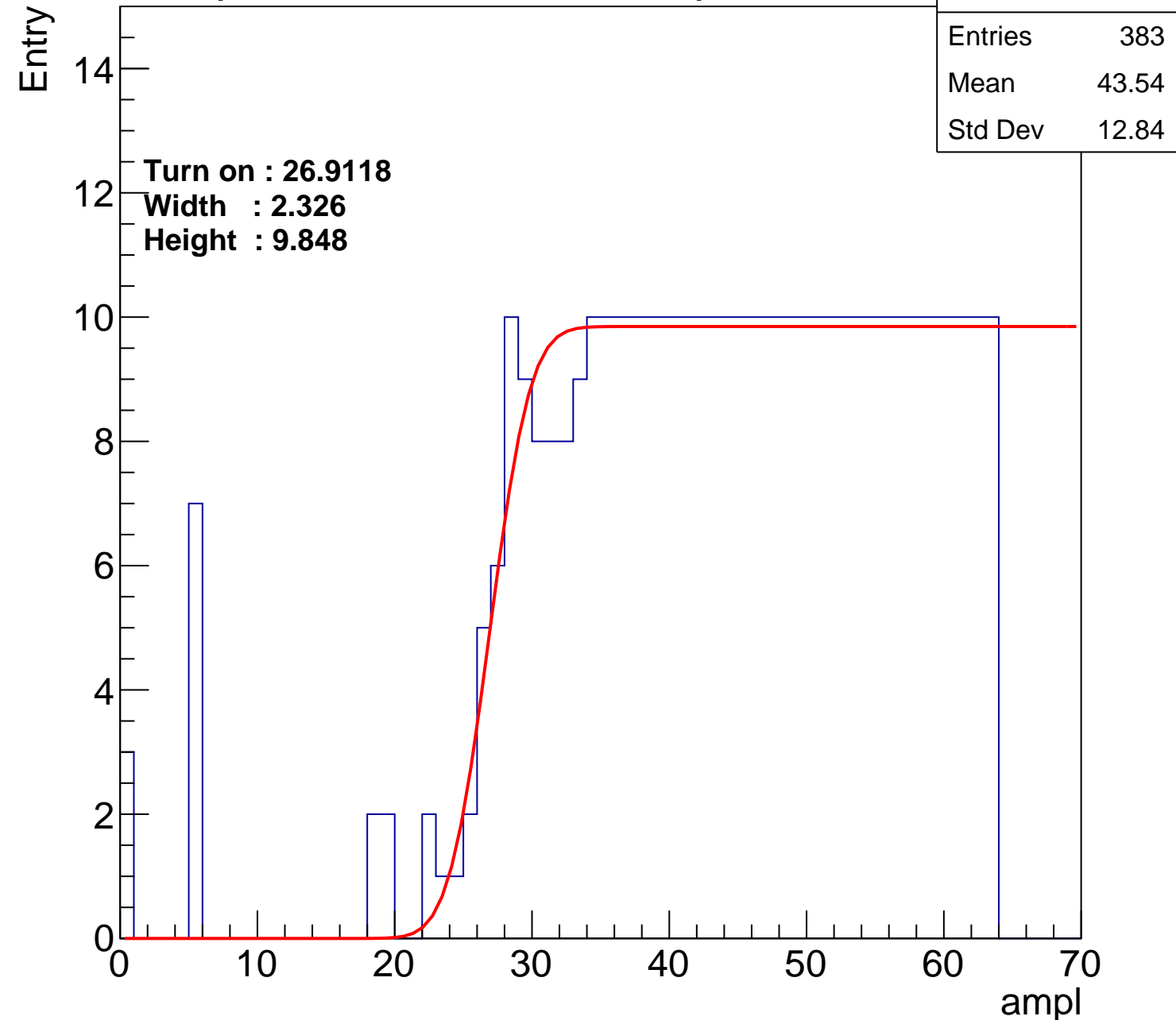
Width : 2.326

Height : 9.848

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch1

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	366
Mean	44.98
Std Dev	11.01

Turn on : 27.5930

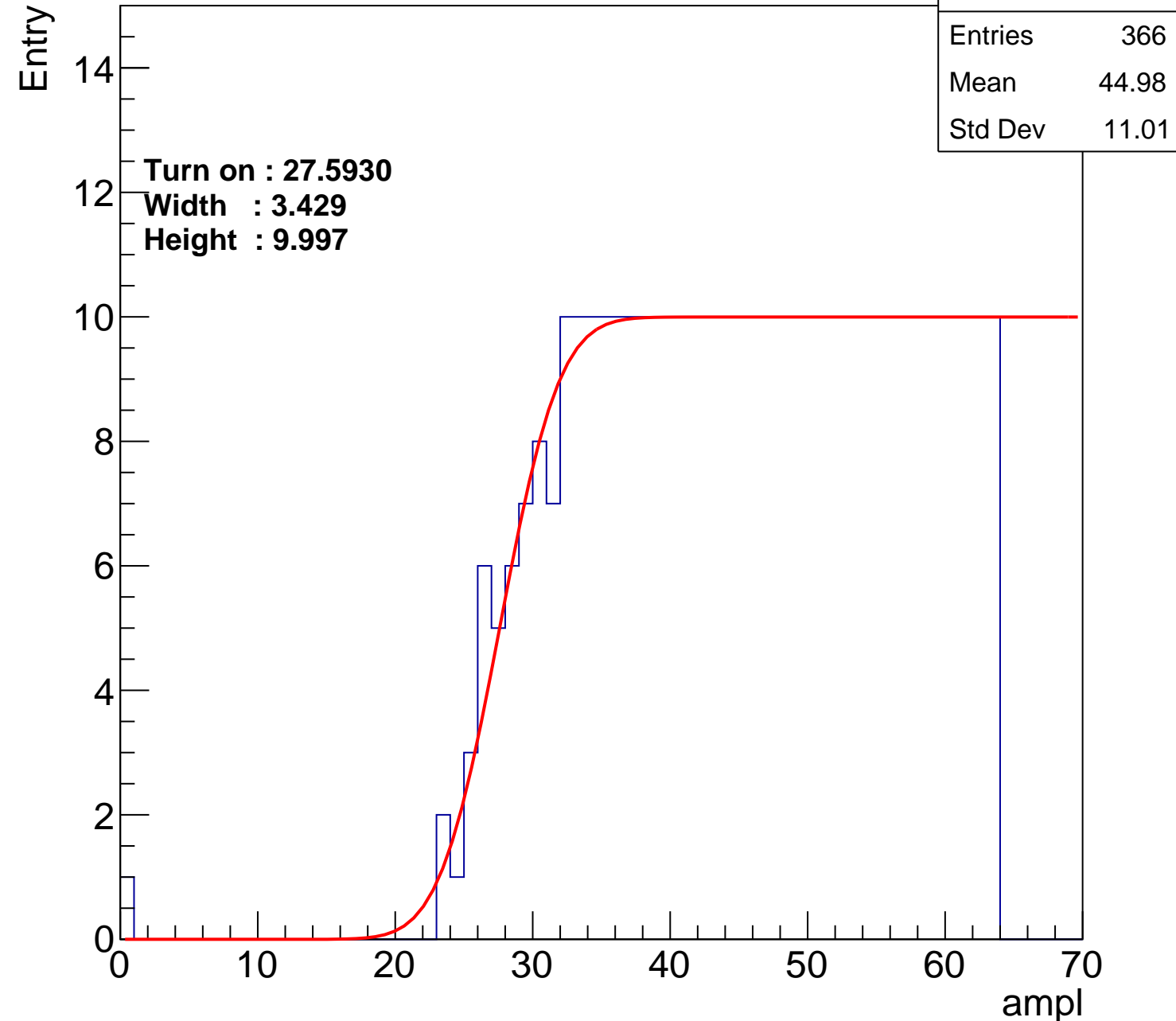
Width : 3.429

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch2

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	398
Mean	43.28
Std Dev	12.15

Turn on : 25.0605

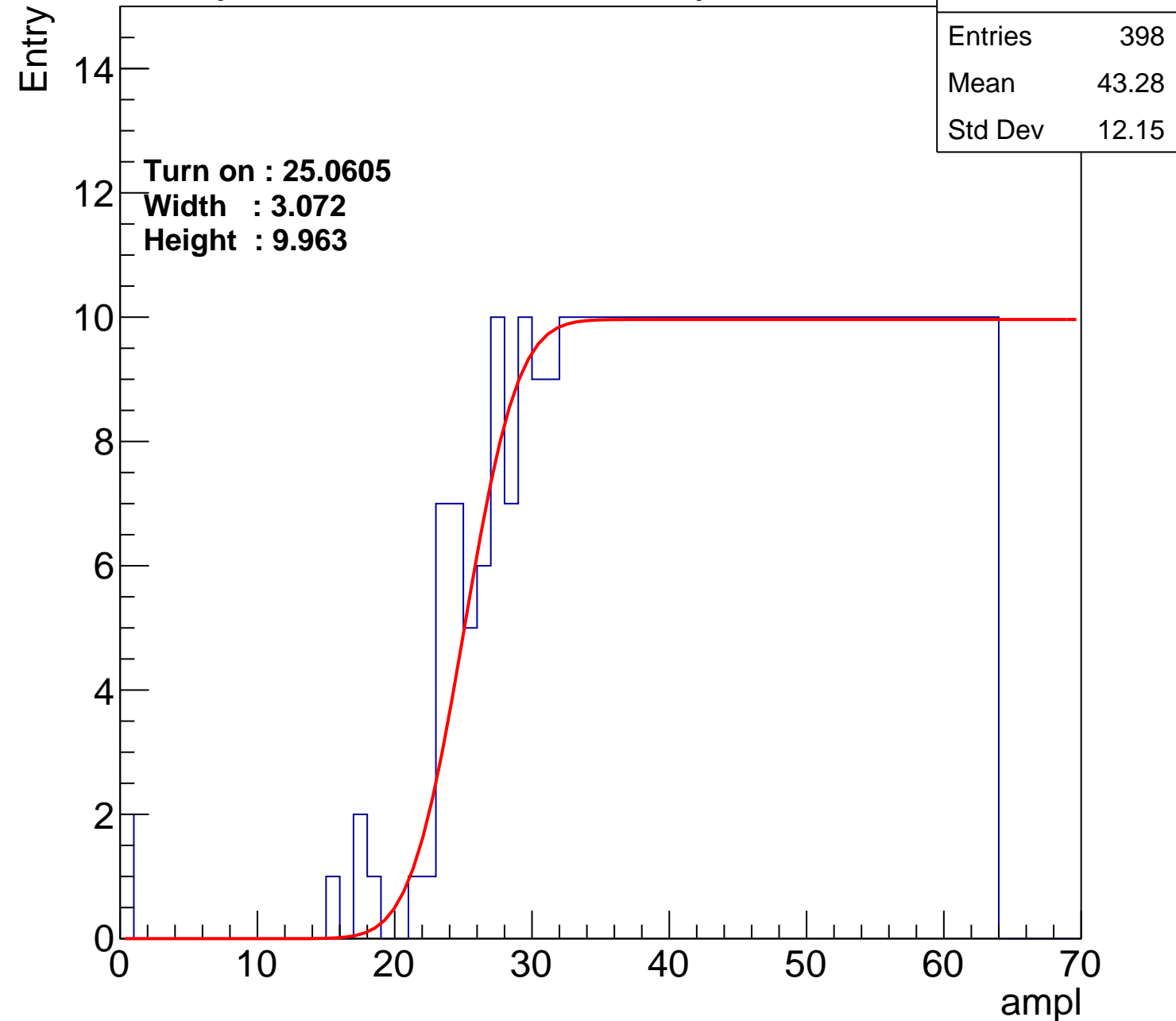
Width : 3.072

Height : 9.963

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch3

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	378
Mean	44.31
Std Dev	11.55

Turn on : 26.5555

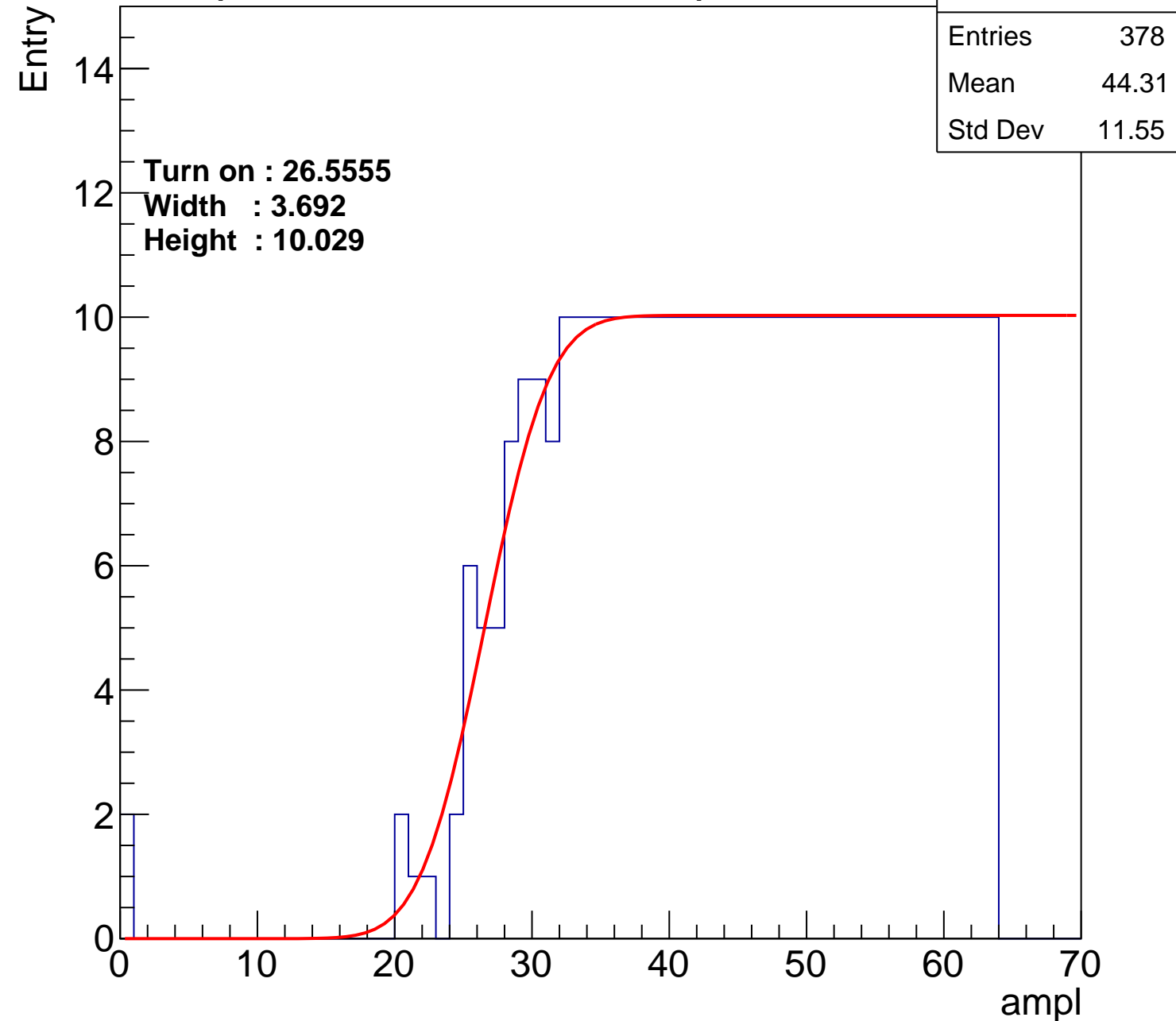
Width : 3.692

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch4

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 27.3668

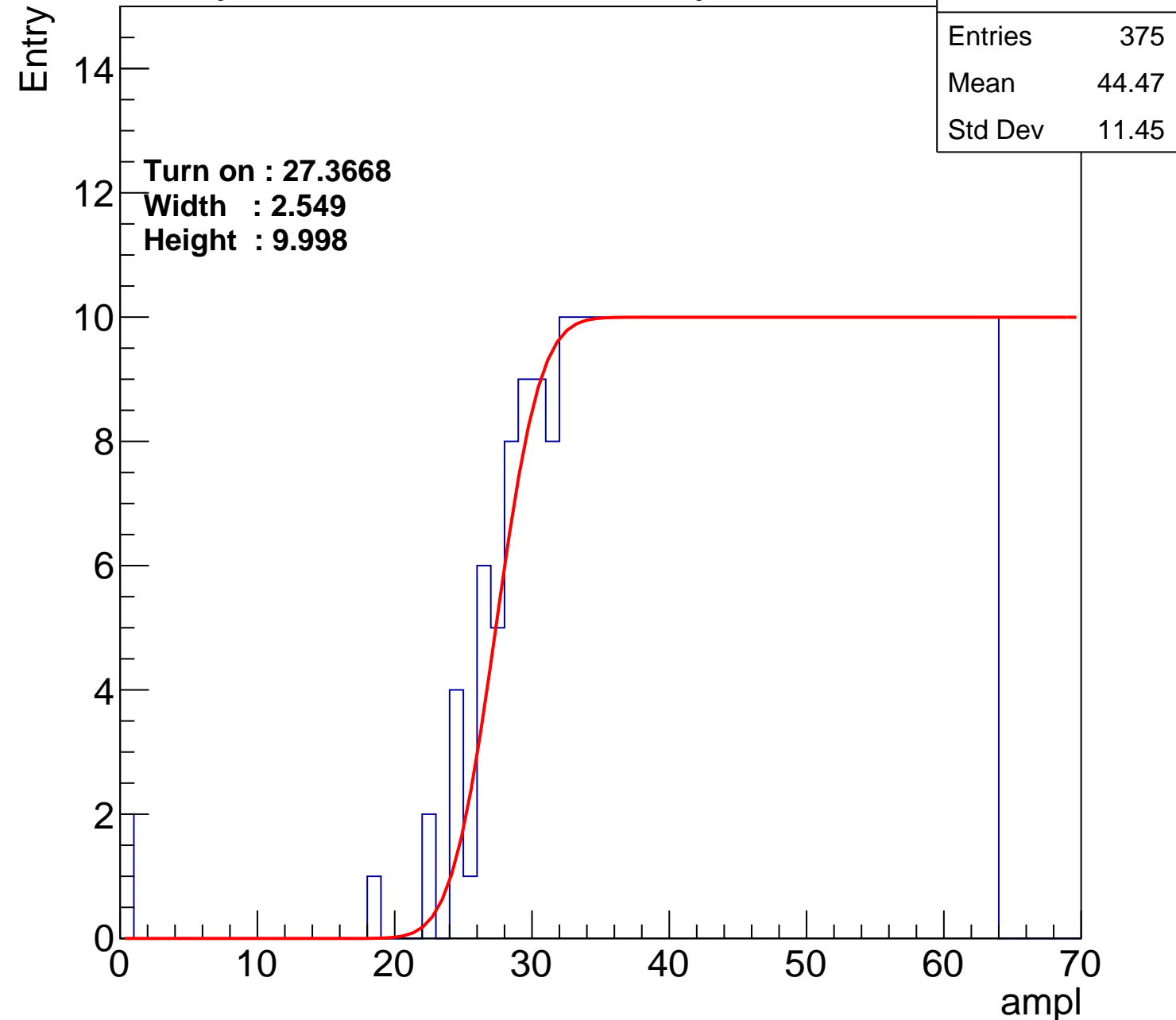
Width : 2.549

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch5

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 24.8614

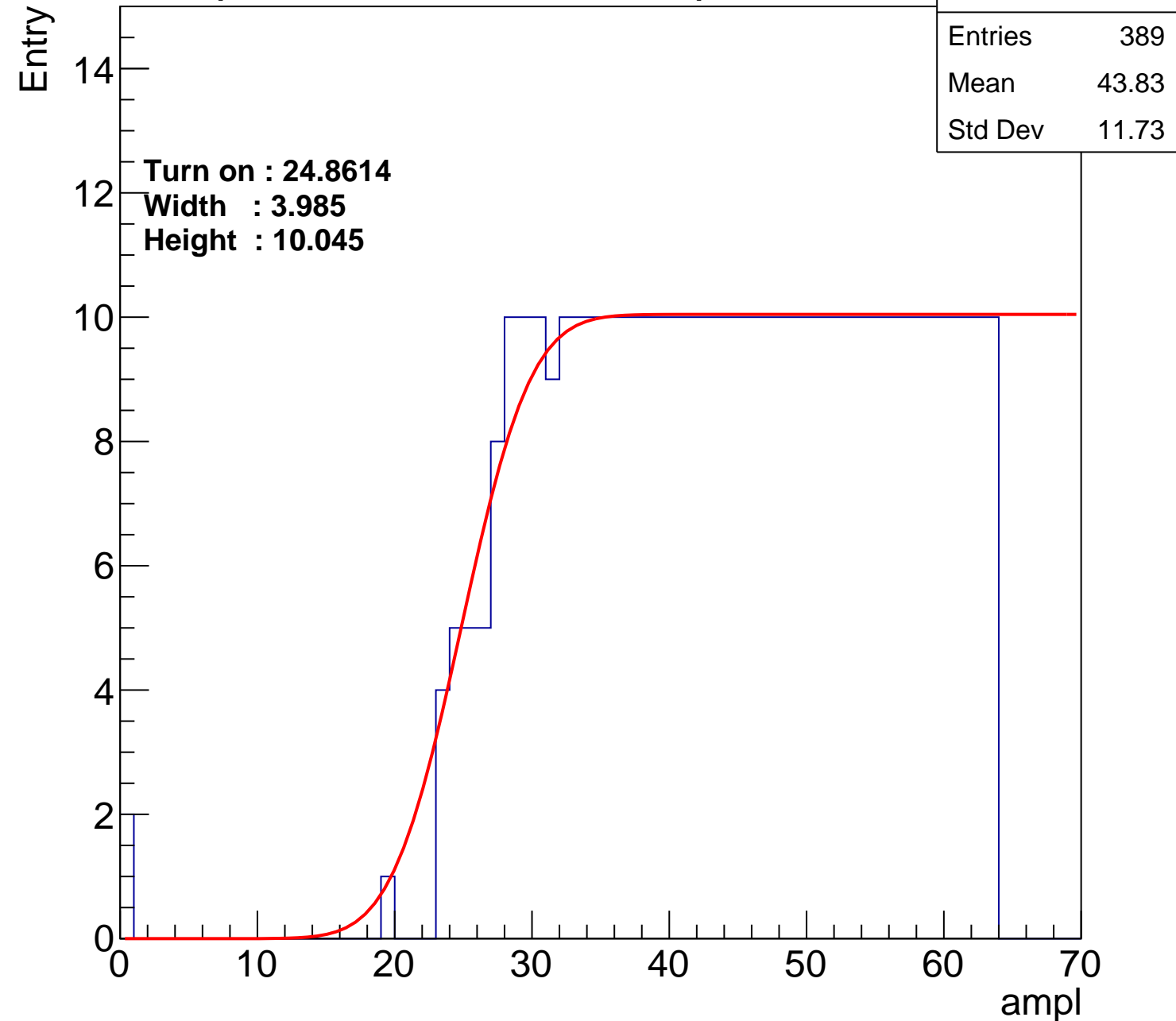
Width : 3.985

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch6

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	370
Mean	44.71
Std Dev	11.48

Turn on : 27.7170

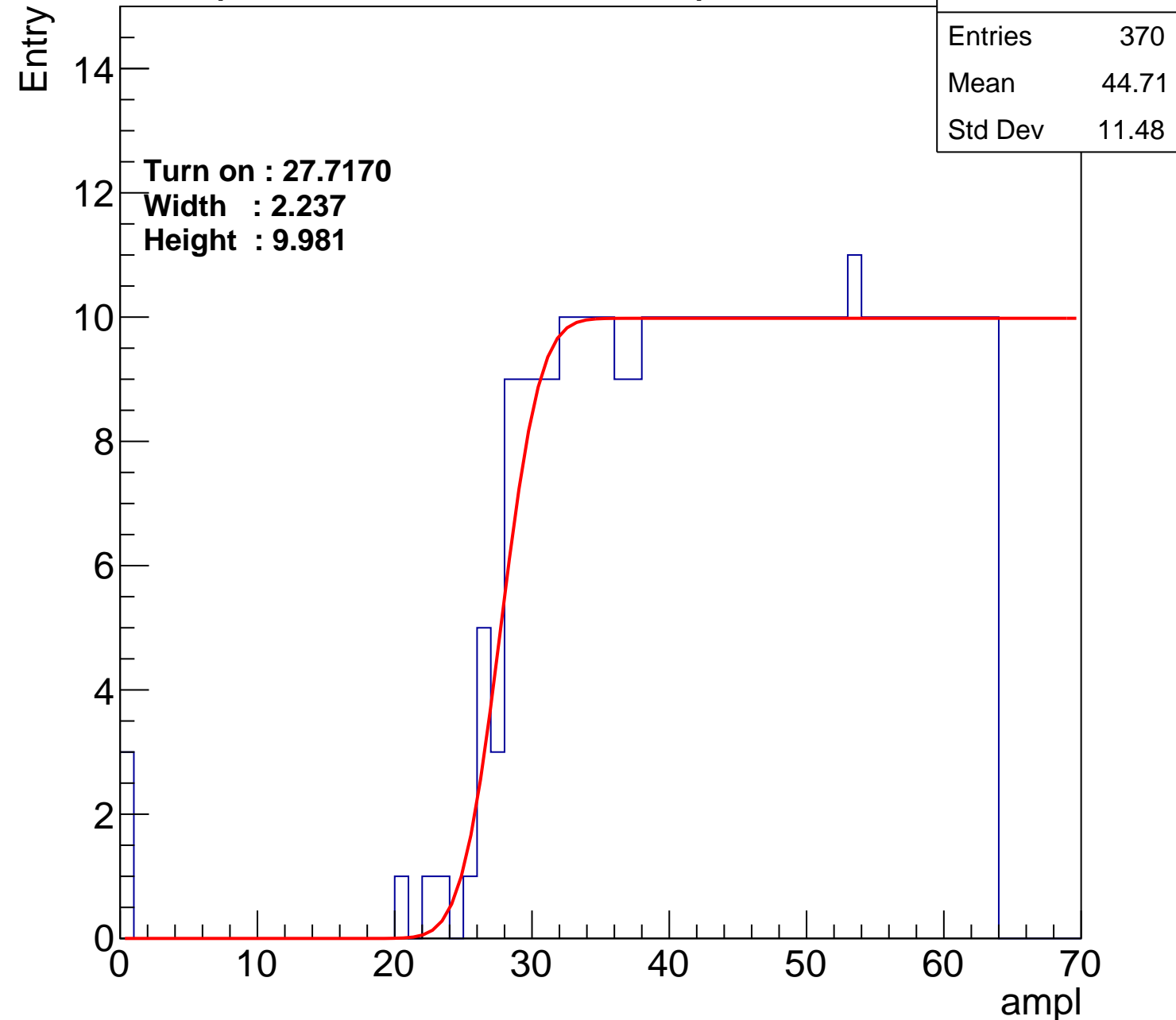
Width : 2.237

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch7

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	372
Mean	44.56
Std Dev	11.55

Turn on : 27.9723

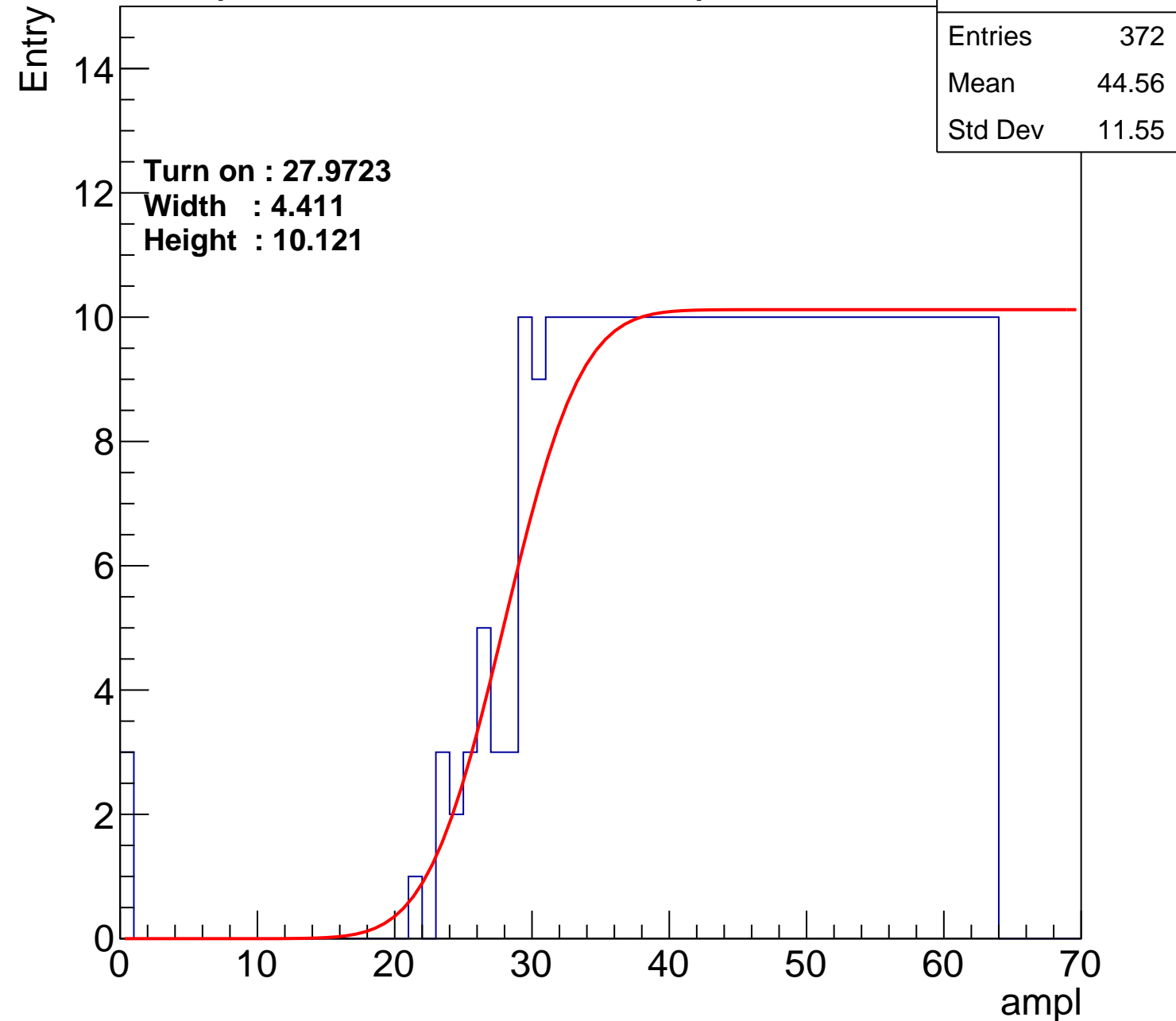
Width : 4.411

Height : 10.121

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch8

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	382
Mean	44.14
Std Dev	11.59

Turn on : 25.6999

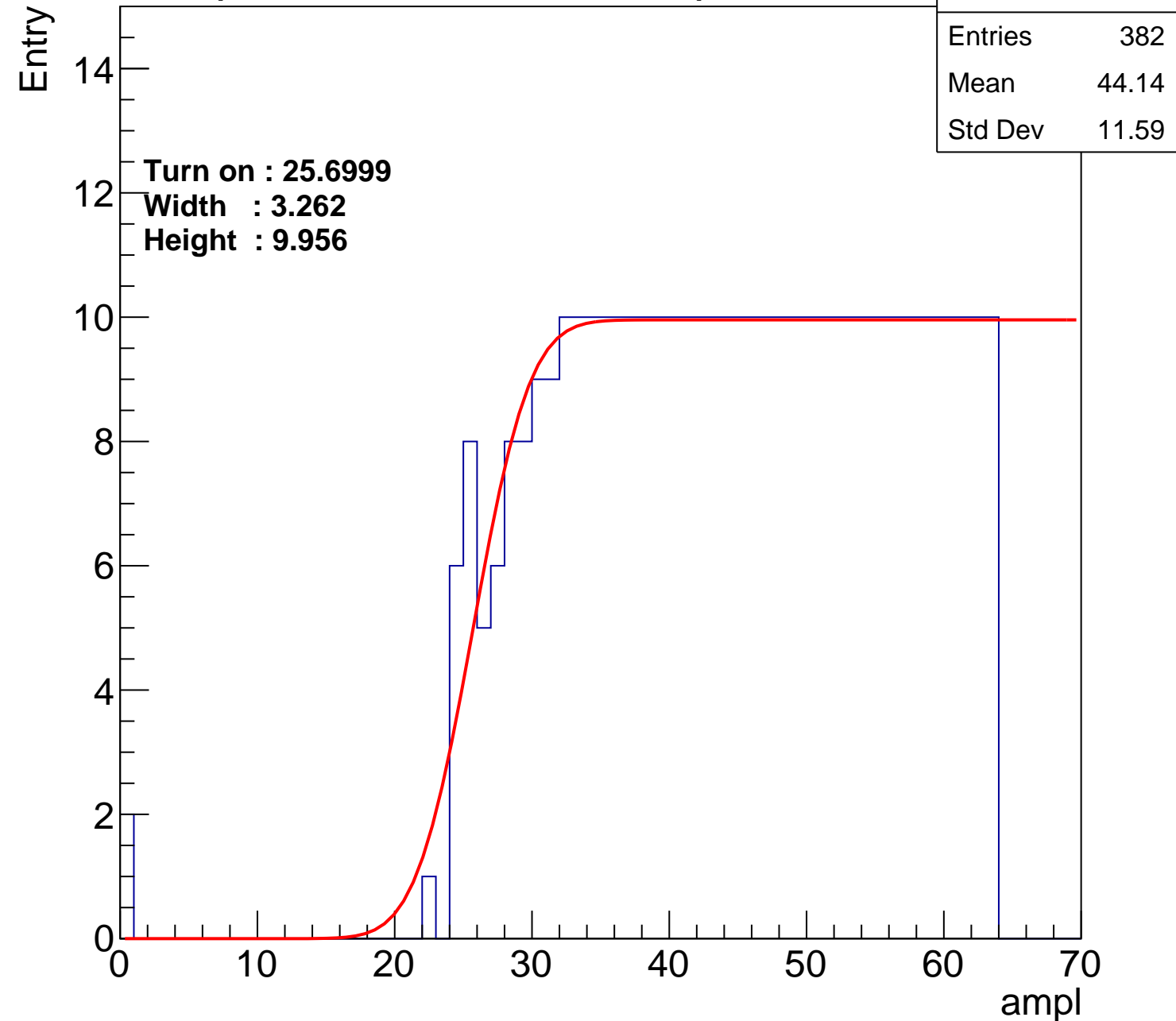
Width : 3.262

Height : 9.956

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch9

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	377
Mean	44.42
Std Dev	11.43

Turn on : 26.7495

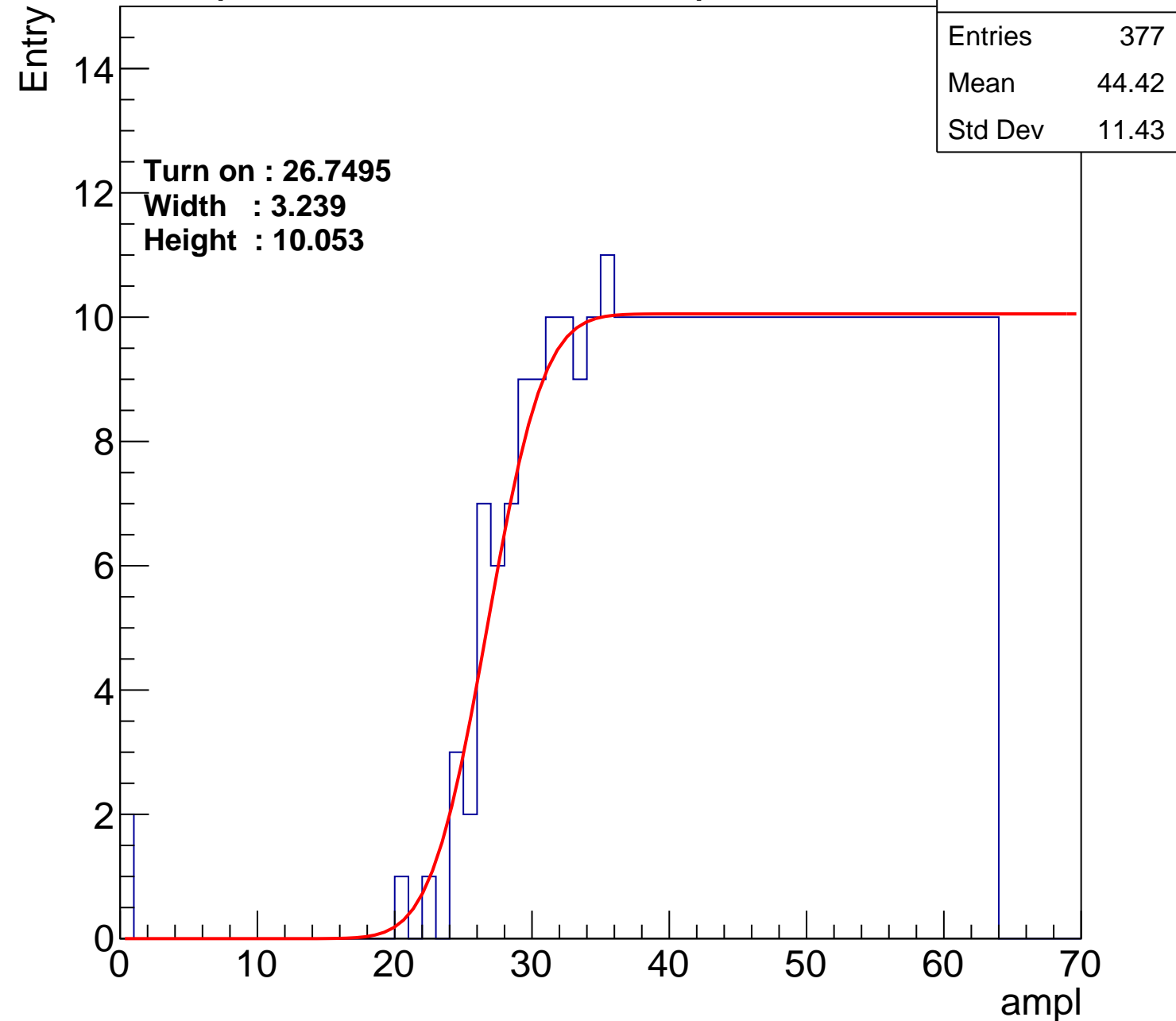
Width : 3.239

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch10

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	360
Mean	45.08
Std Dev	11.36

Turn on : 28.9330

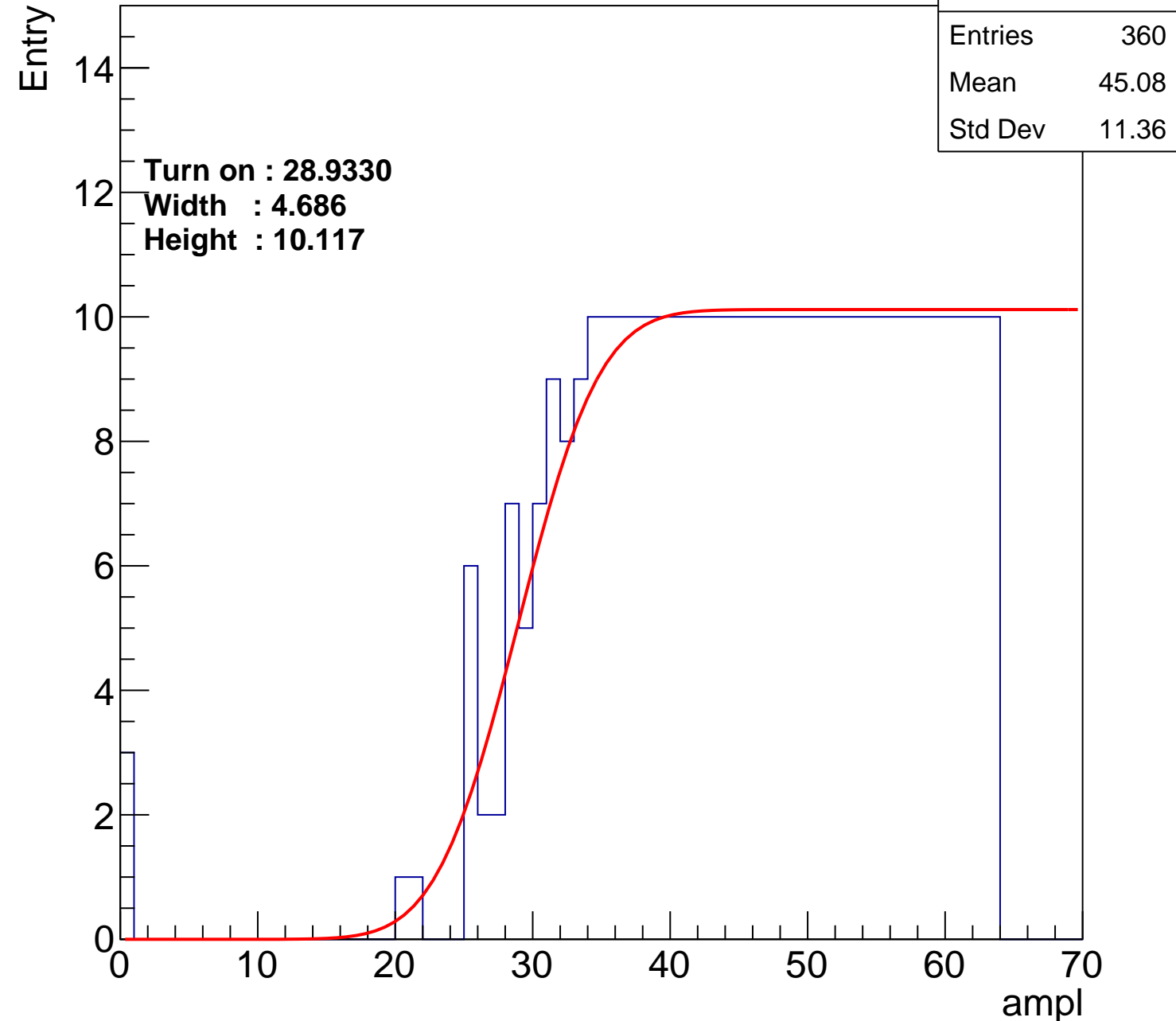
Width : 4.686

Height : 10.117

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch11

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	356
Mean	45.53
Std Dev	10.85

Turn on : 28.8968

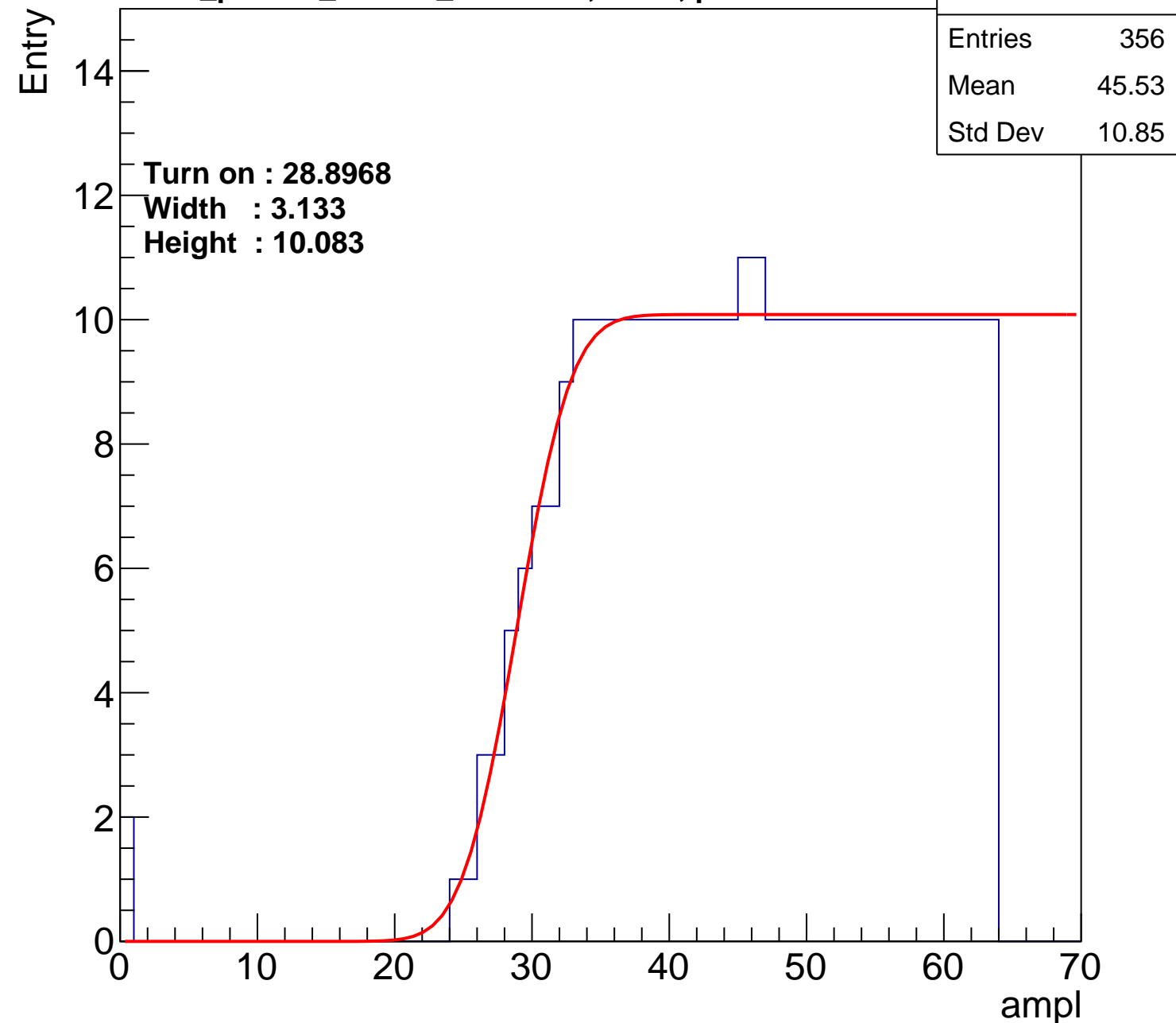
Width : 3.133

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch12

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	383
Mean	44.09
Std Dev	11.63

Turn on : 26.4275

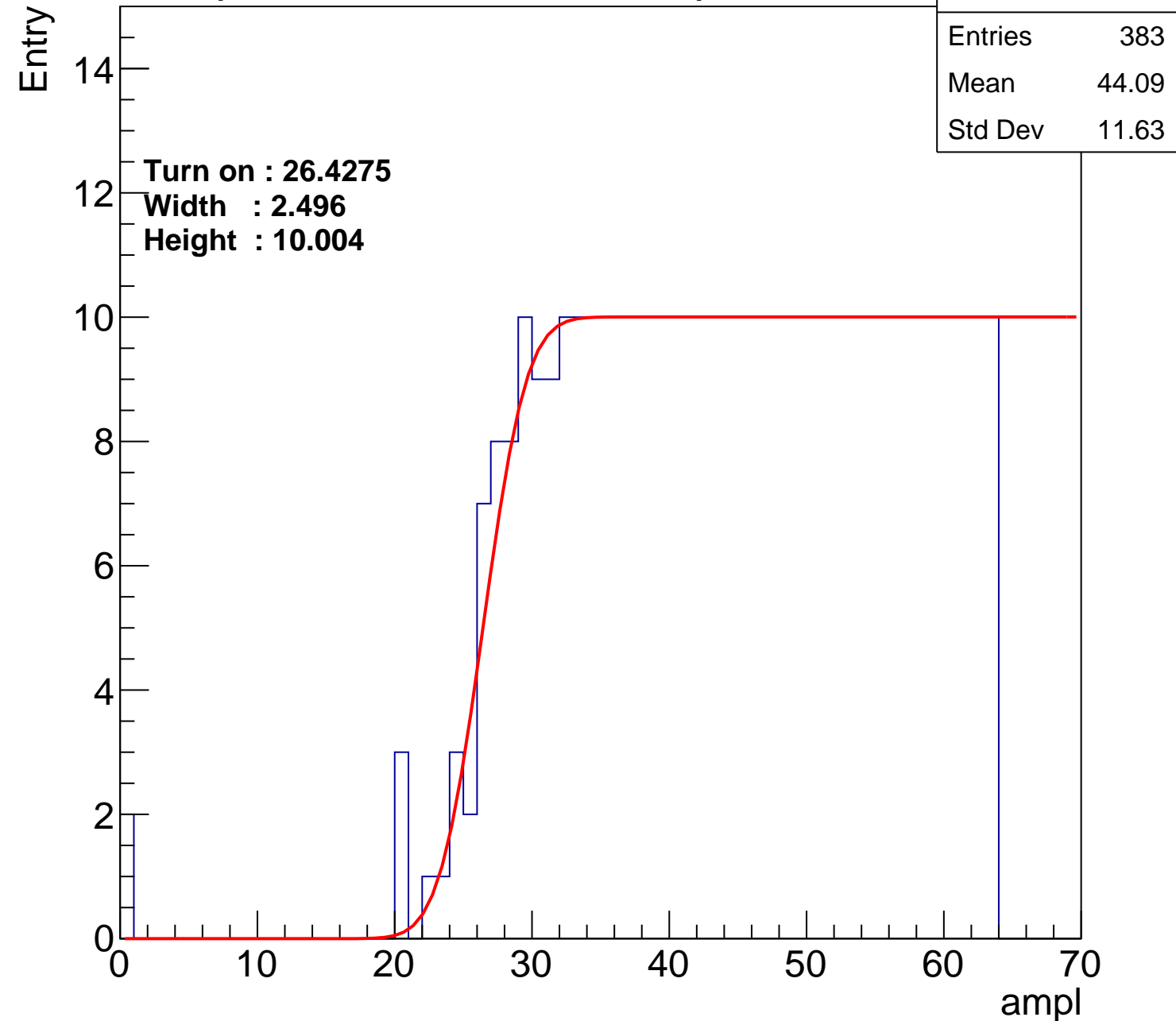
Width : 2.496

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch13

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	349
Mean	45.97
Std Dev	10.48

Turn on : 29.9552

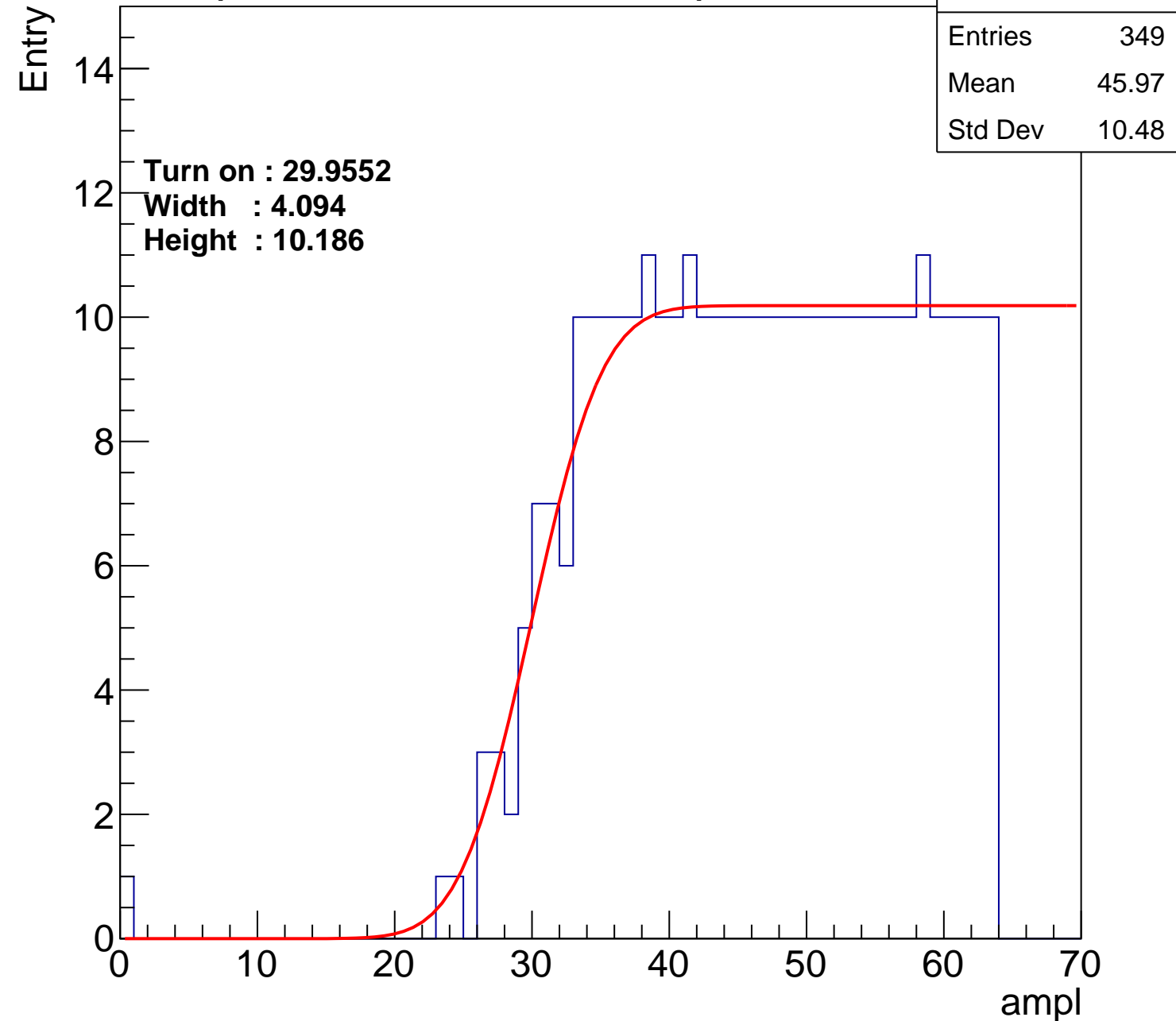
Width : 4.094

Height : 10.186

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch14

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	379
Mean	44.46
Std Dev	11.29

Turn on : 26.8284

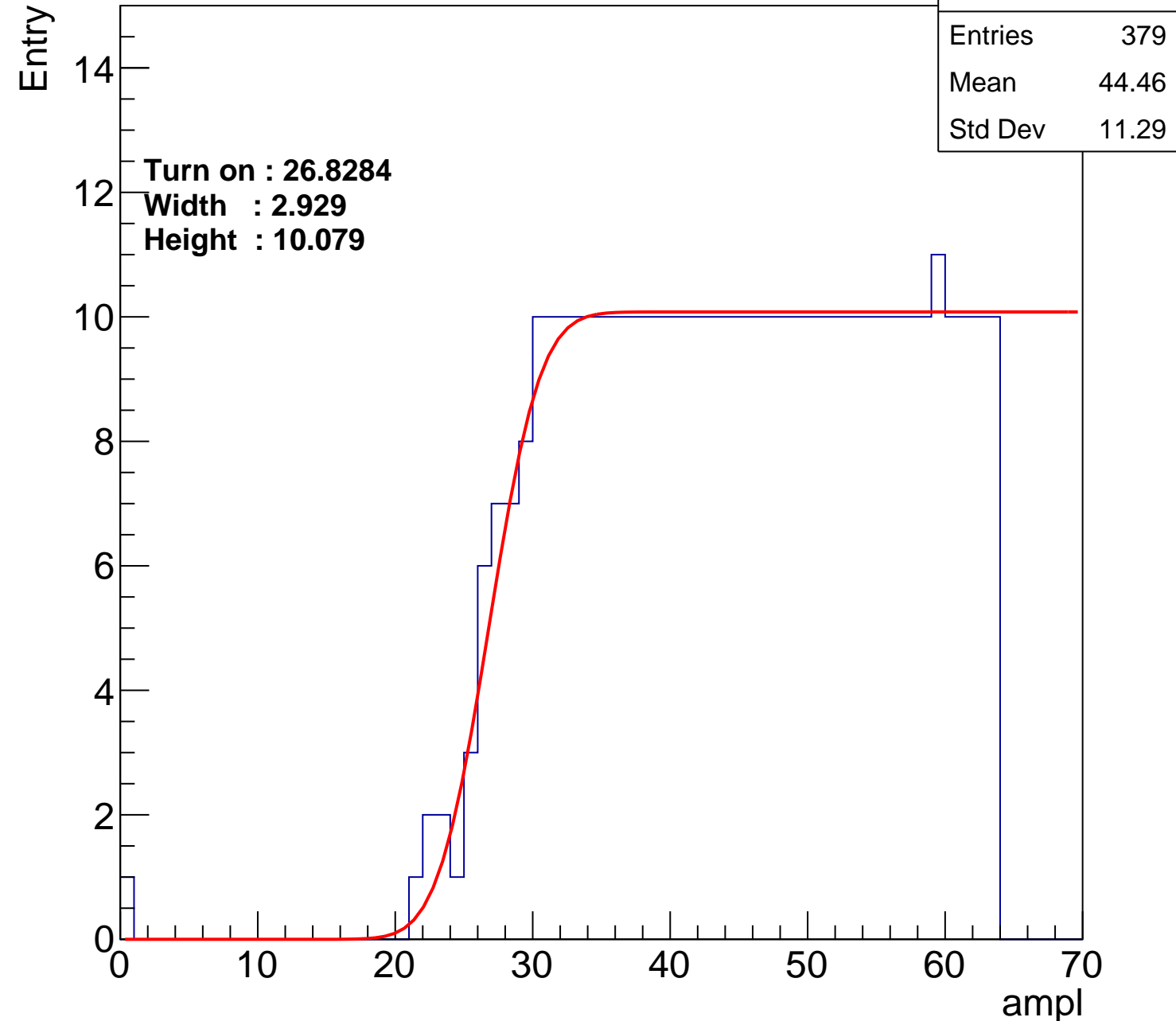
Width : 2.929

Height : 10.079

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch15

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.75
Std Dev	11.05

Turn on : 27.0704

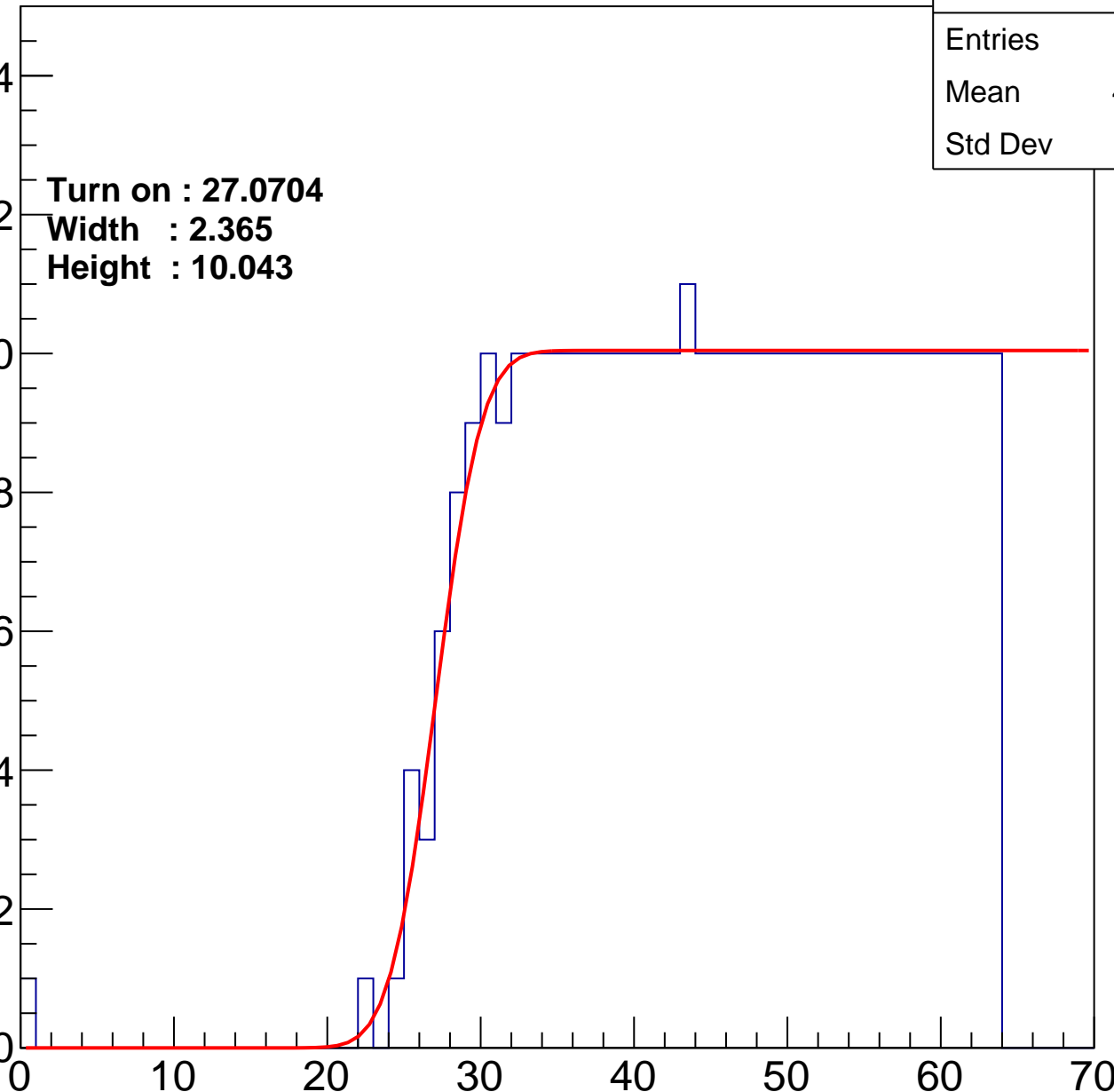
Width : 2.365

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch16

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	388
Mean	43.83
Std Dev	11.79

Turn on : 25.6721

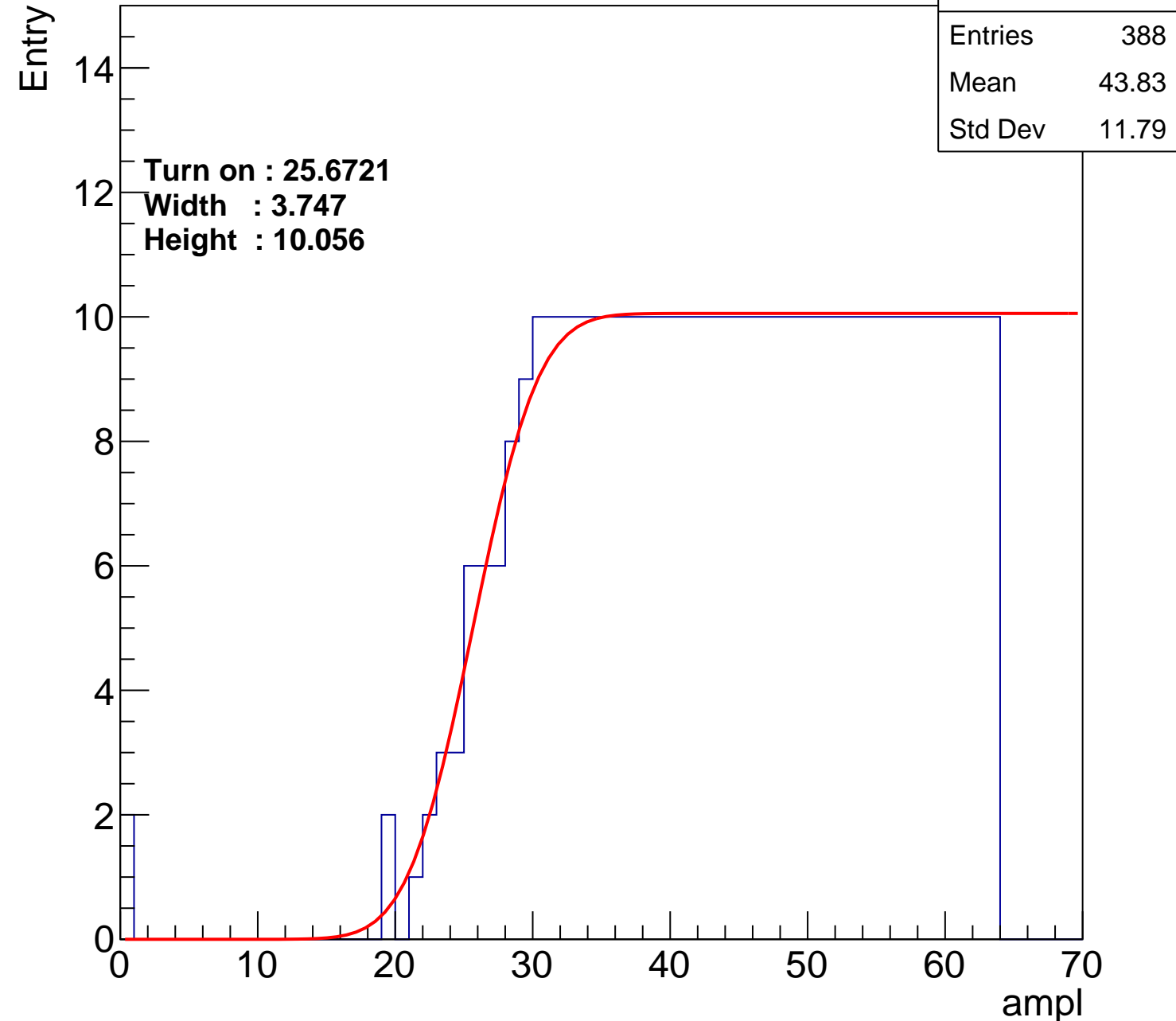
Width : 3.747

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch17

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	386
Mean	43.98
Std Dev	11.84

Turn on : 26.0291

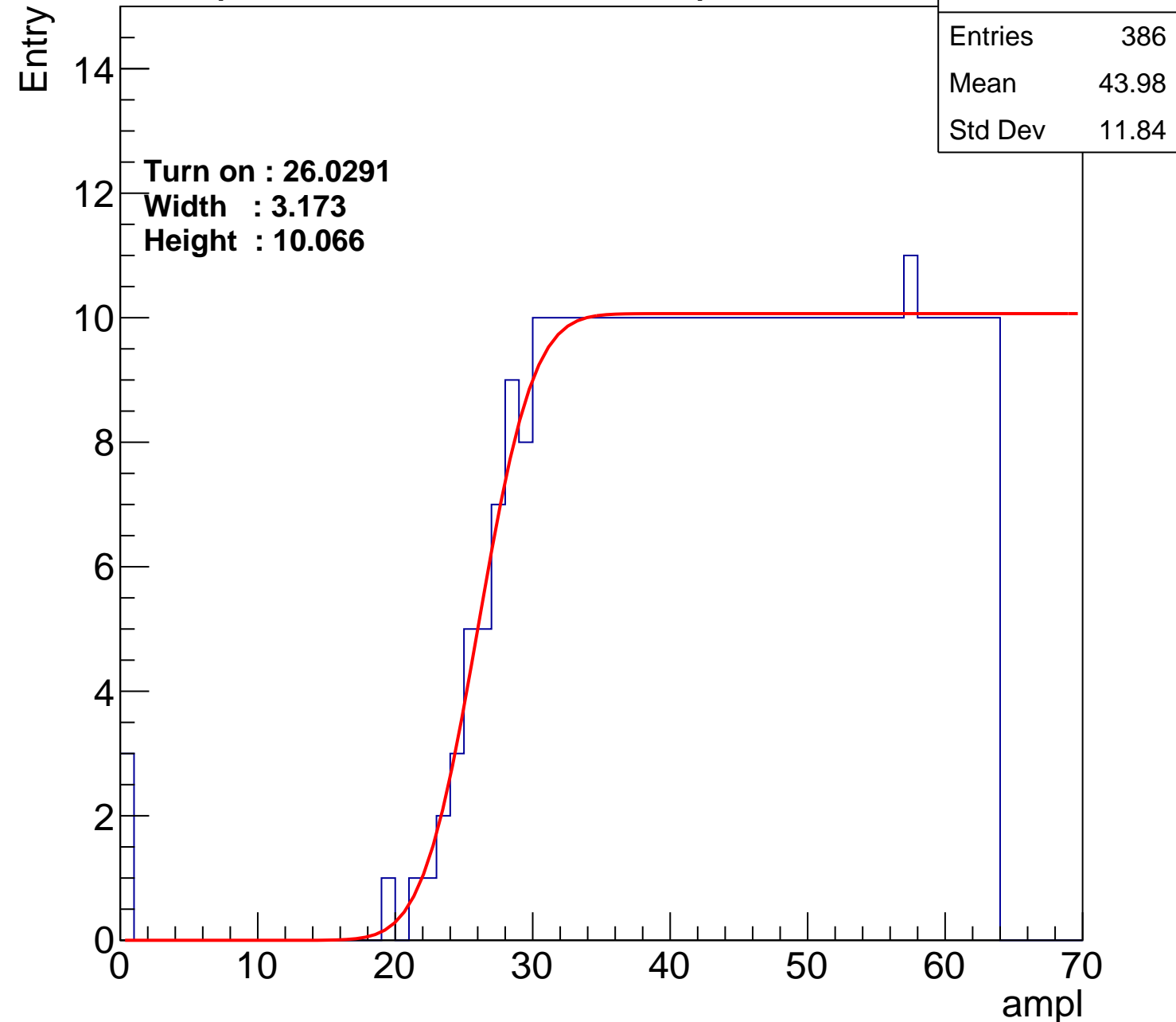
Width : 3.173

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch18

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	362
Mean	45.18
Std Dev	10.91

Turn on : 28.6468

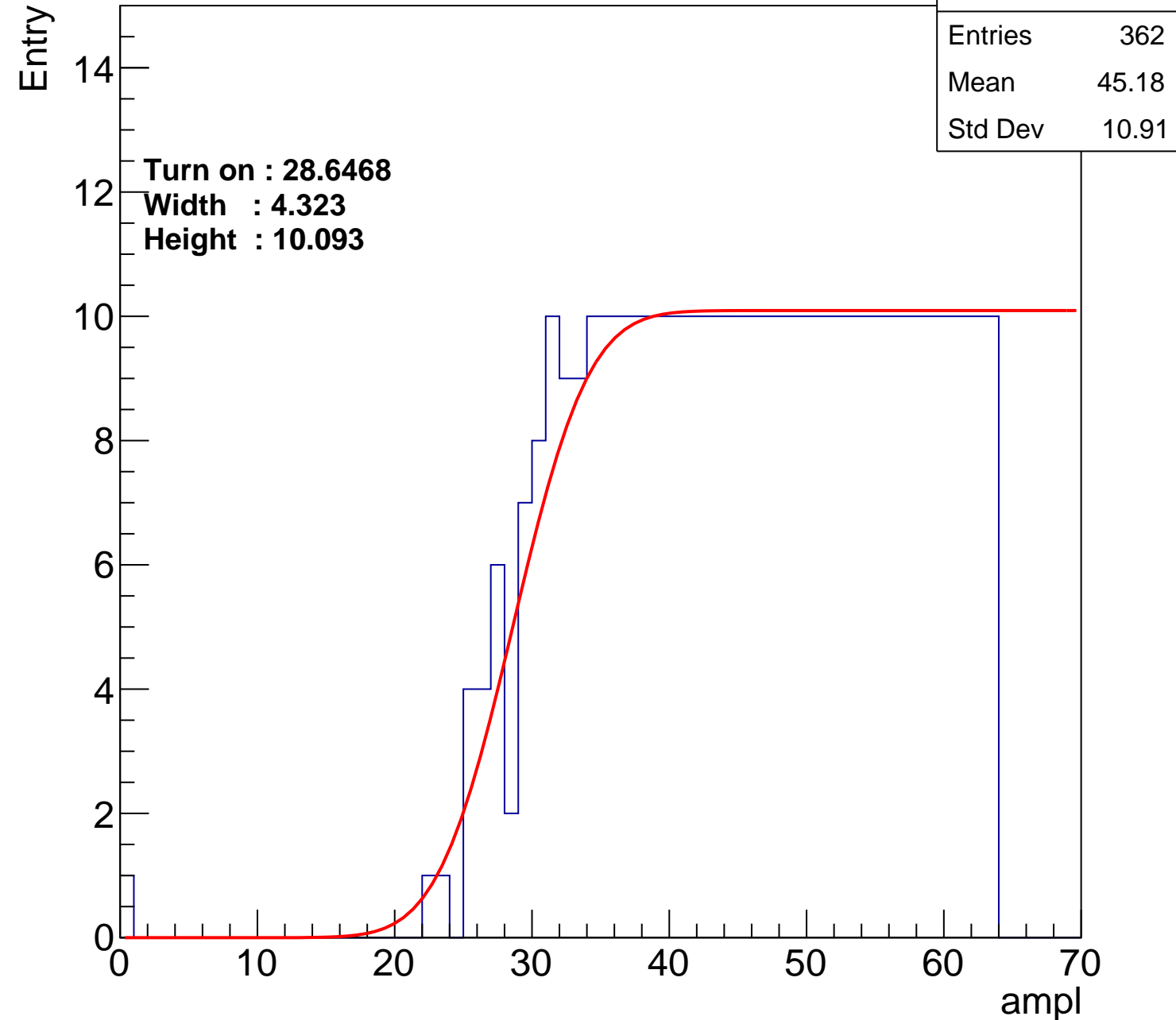
Width : 4.323

Height : 10.093

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch19

calib\_packv5\_042523\_0143.root, FC#7, port C2

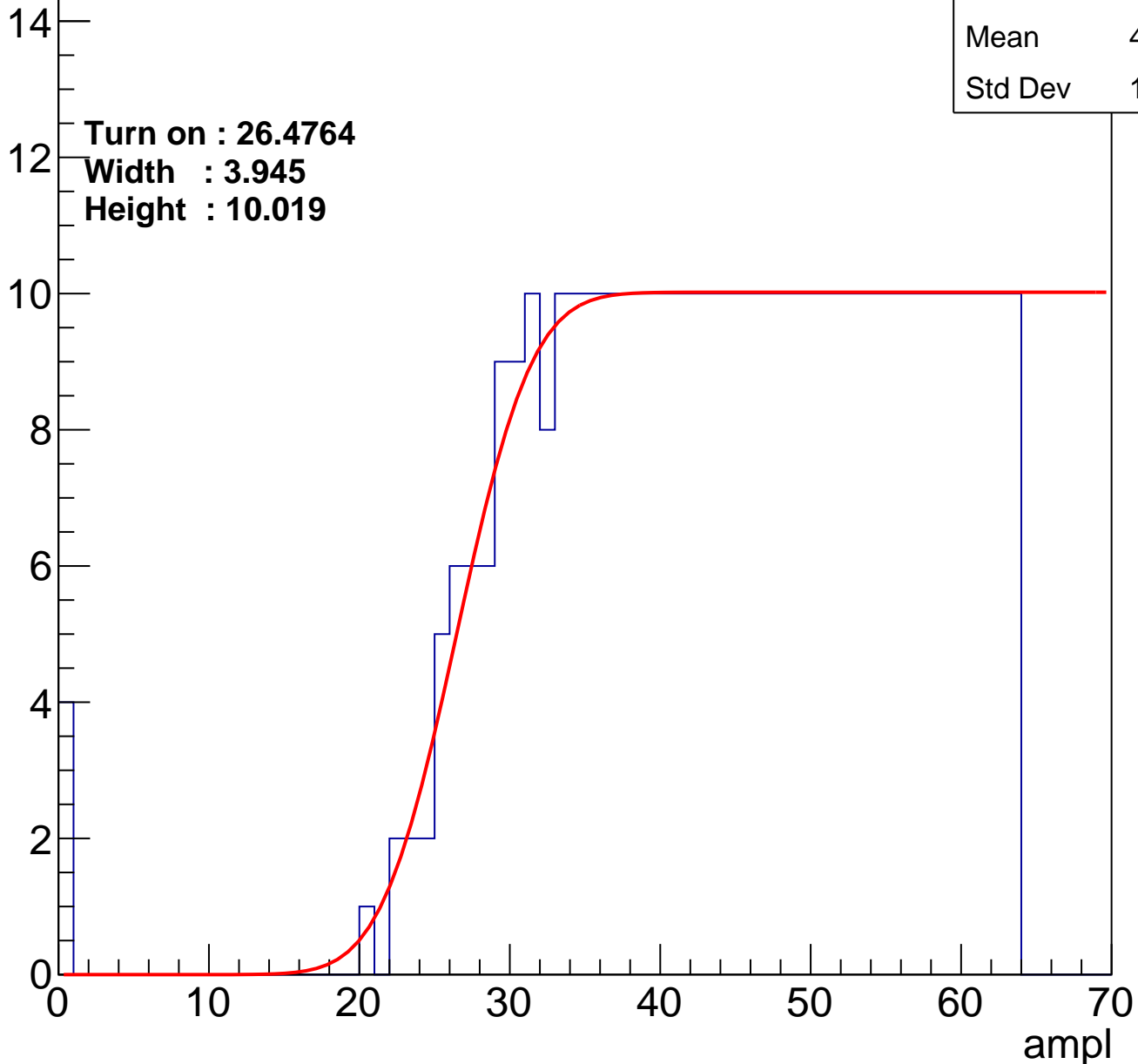
Entries	380
Mean	44.07
Std Dev	11.96

Turn on : 26.4764

Width : 3.945

Height : 10.019

Entry



# B1L103S, U21-ch20

calib\_packv5\_042523\_0143.root, FC#7, port C2

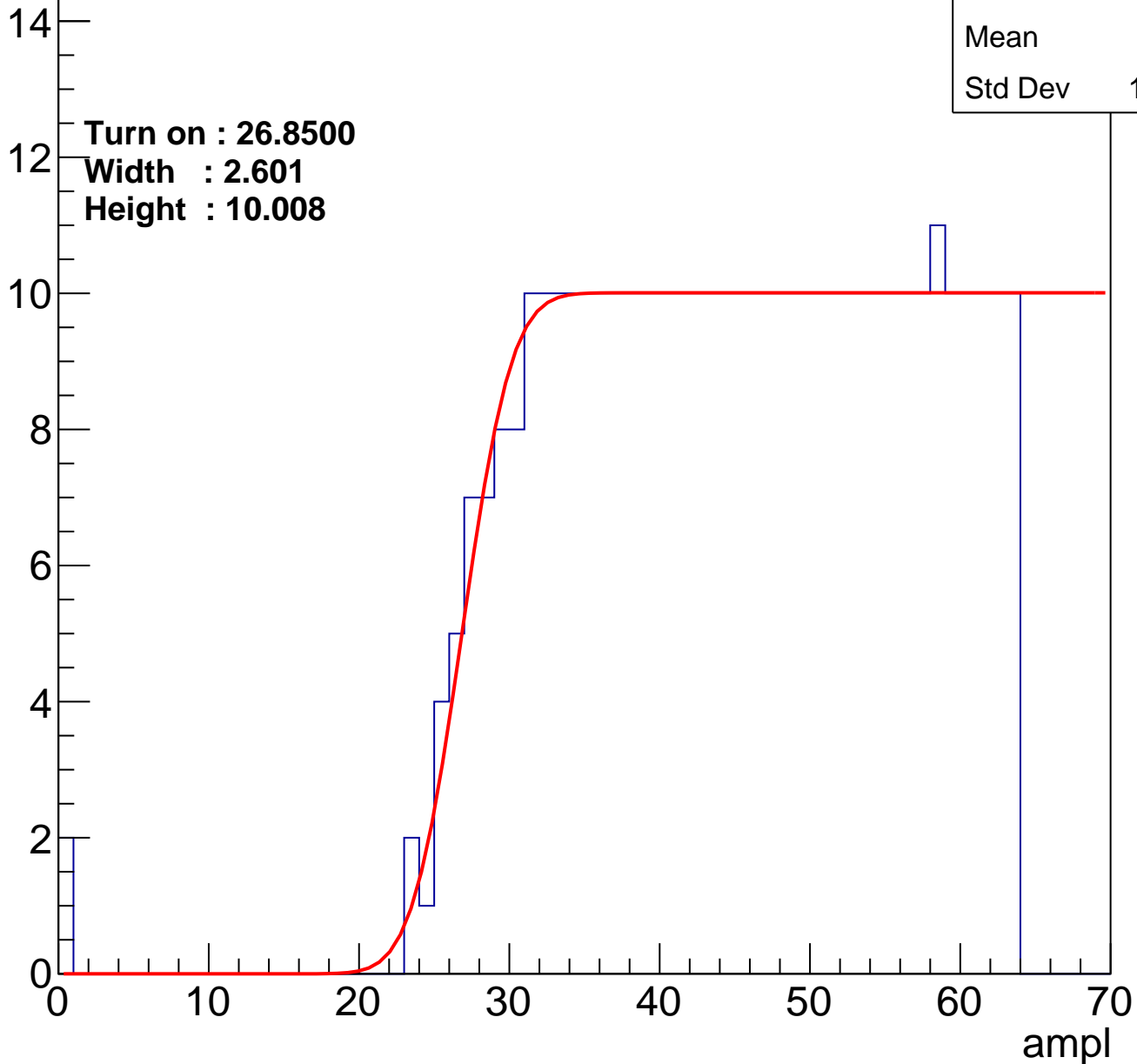
Entry

Entries	375
Mean	44.6
Std Dev	11.36

Turn on : 26.8500

Width : 2.601

Height : 10.008



# B1L103S, U21-ch21

calib\_packv5\_042523\_0143.root, FC#7, port C2

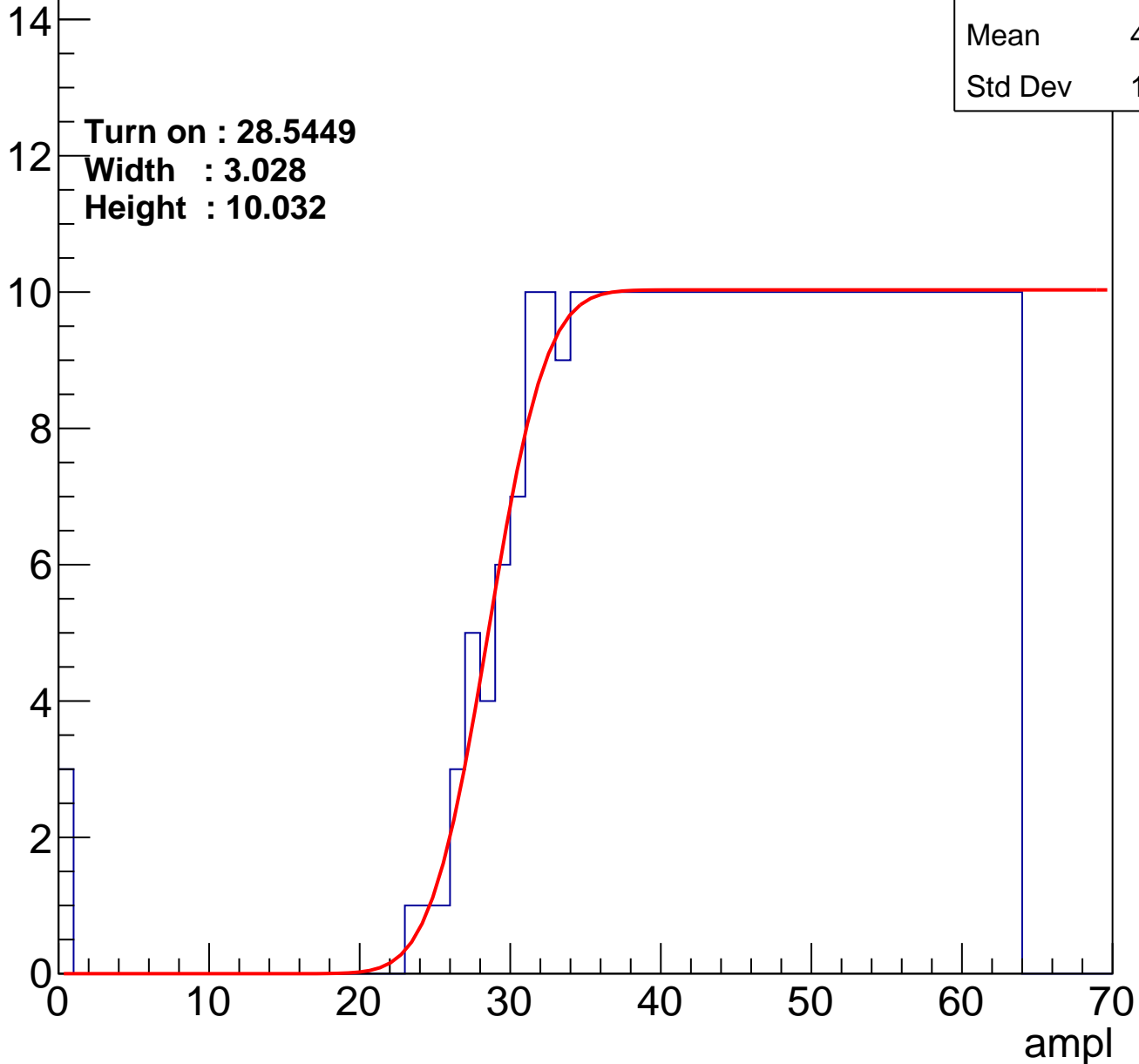
Entries	360
Mean	45.16
Std Dev	11.24

Turn on : 28.5449

Width : 3.028

Height : 10.032

Entry



# B1L103S, U21-ch22

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	380
Mean	44.31
Std Dev	11.37

Turn on : 26.6538

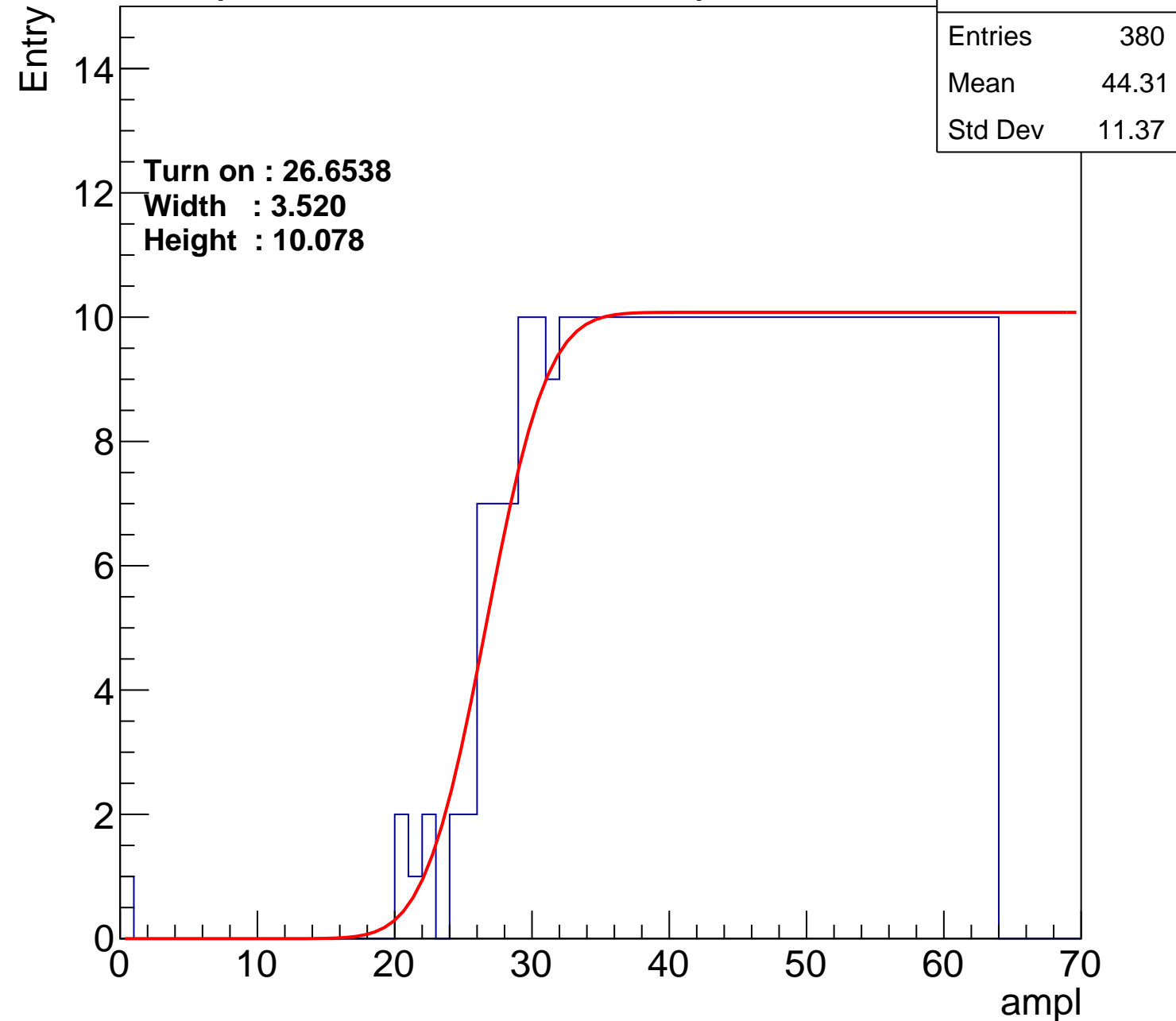
Width : 3.520

Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch23

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	384
Mean	44.21
Std Dev	11.64

Turn on : 26.0737

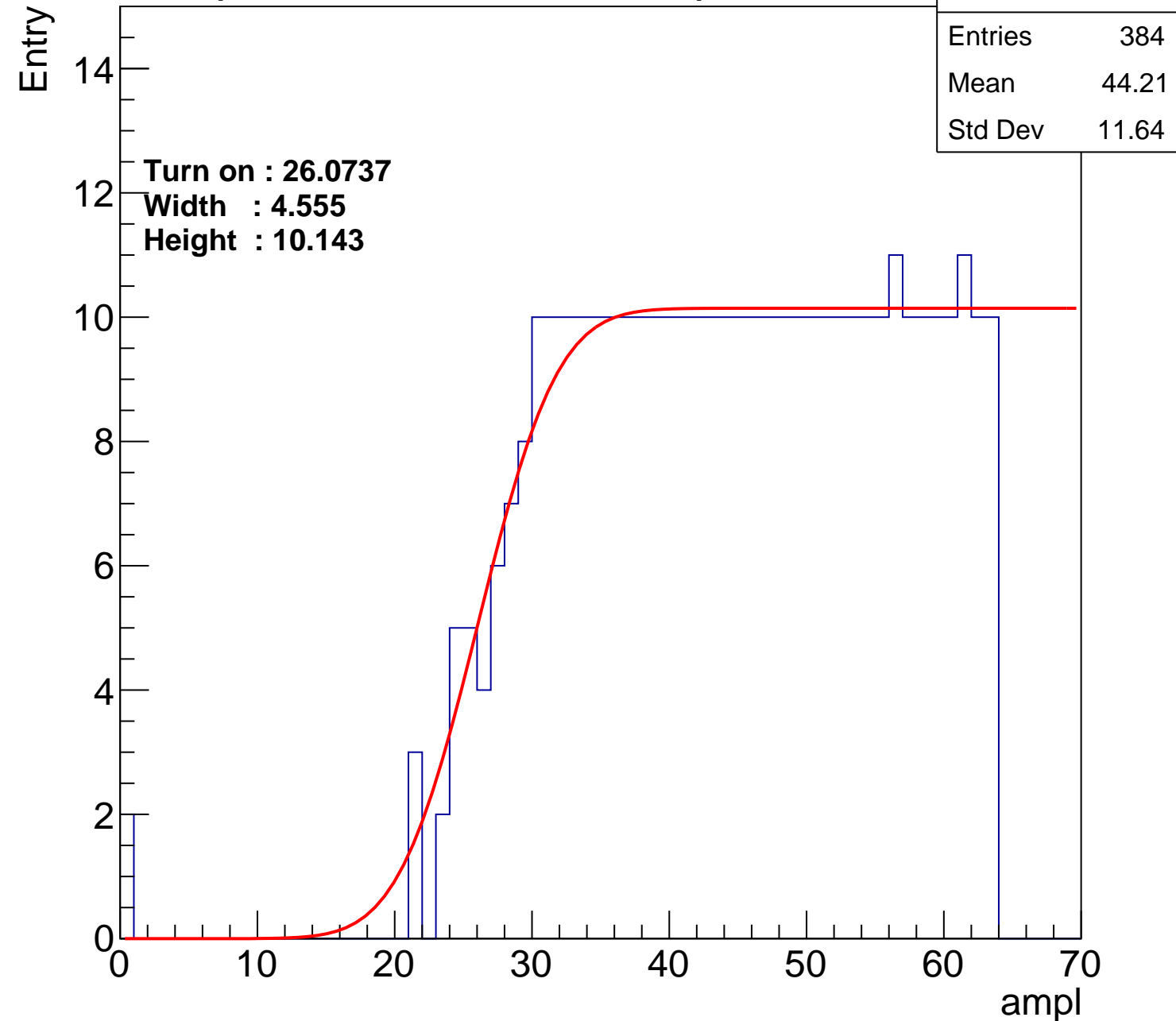
Width : 4.555

Height : 10.143

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch24

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	378
Mean	44.38
Std Dev	11.48

Turn on : 27.1561

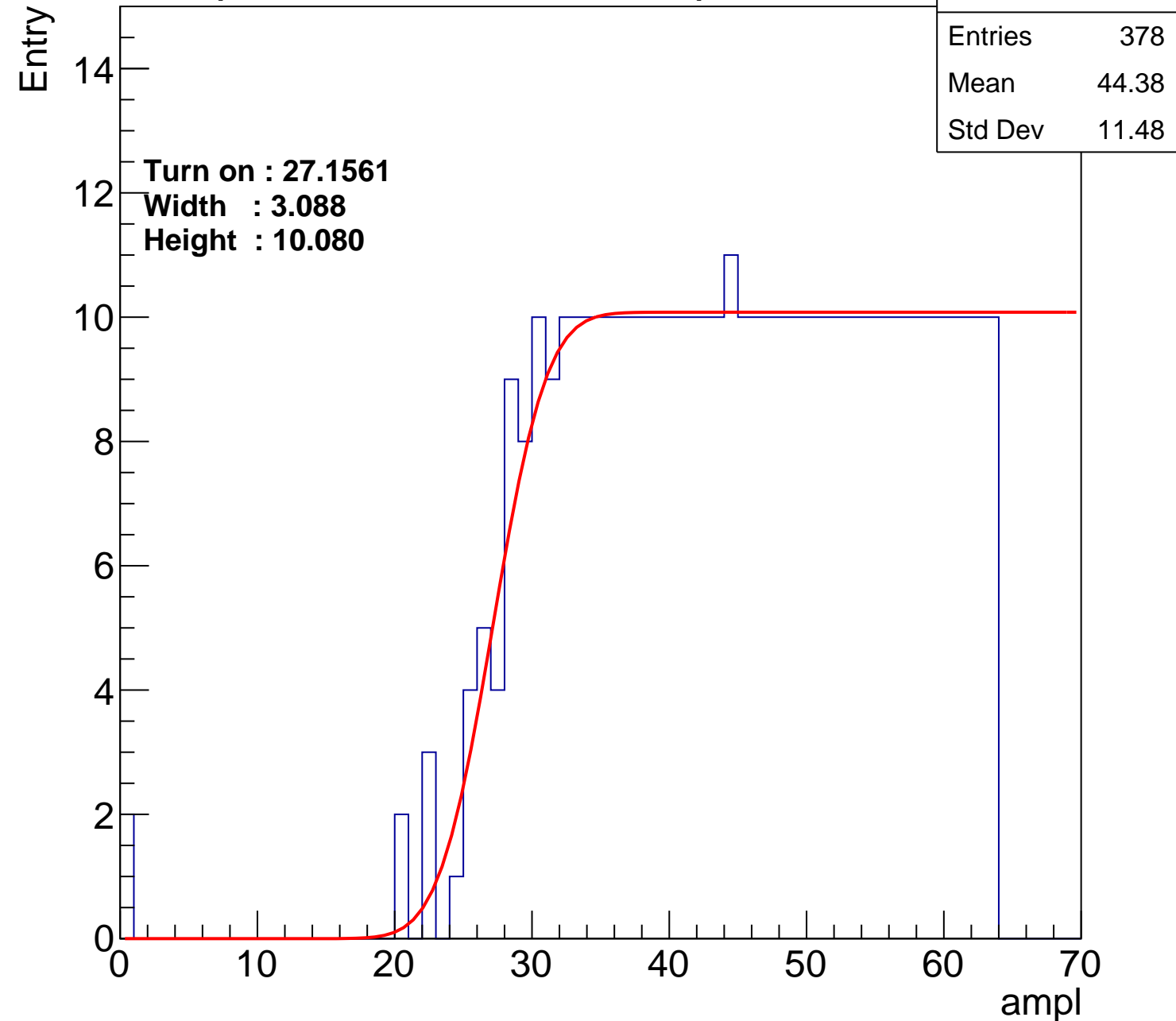
Width : 3.088

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch25

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	369
Mean	44.73
Std Dev	11.44

Turn on : 27.5279

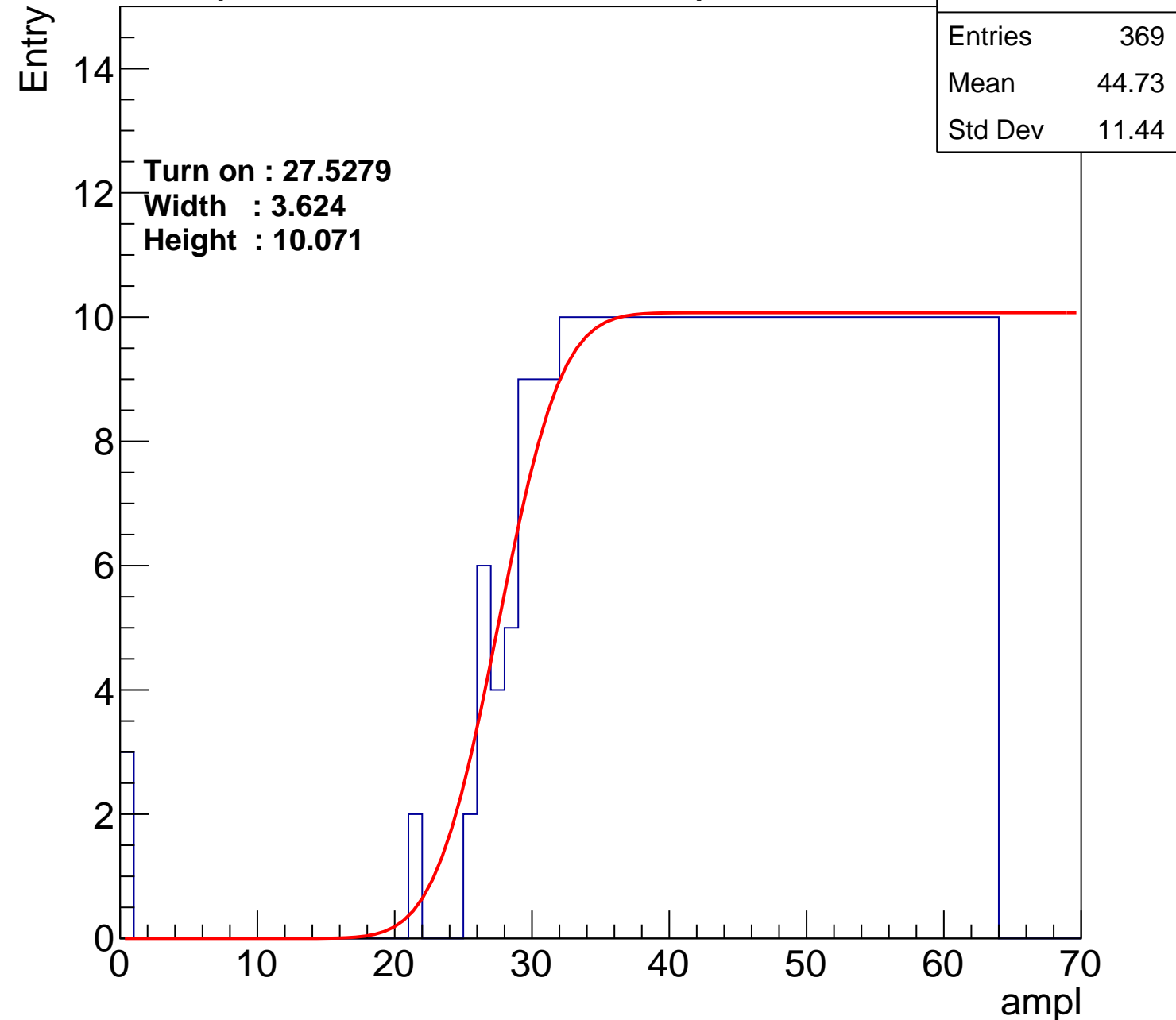
Width : 3.624

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch26

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	372
Mean	44.52
Std Dev	11.61

Turn on : 27.1783

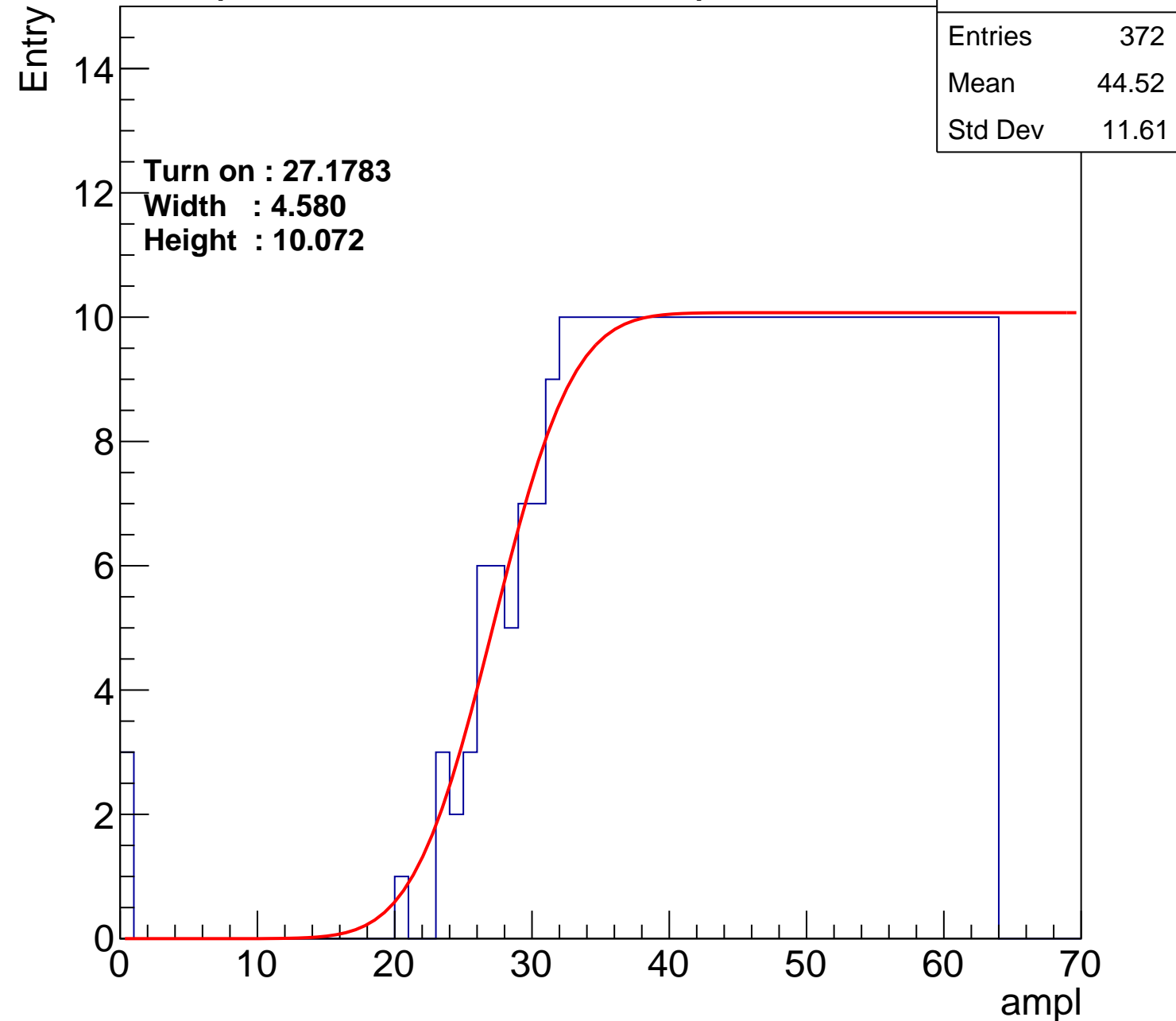
Width : 4.580

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch27

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	353
Mean	45.58
Std Dev	10.85

**Turn on : 28.8677**

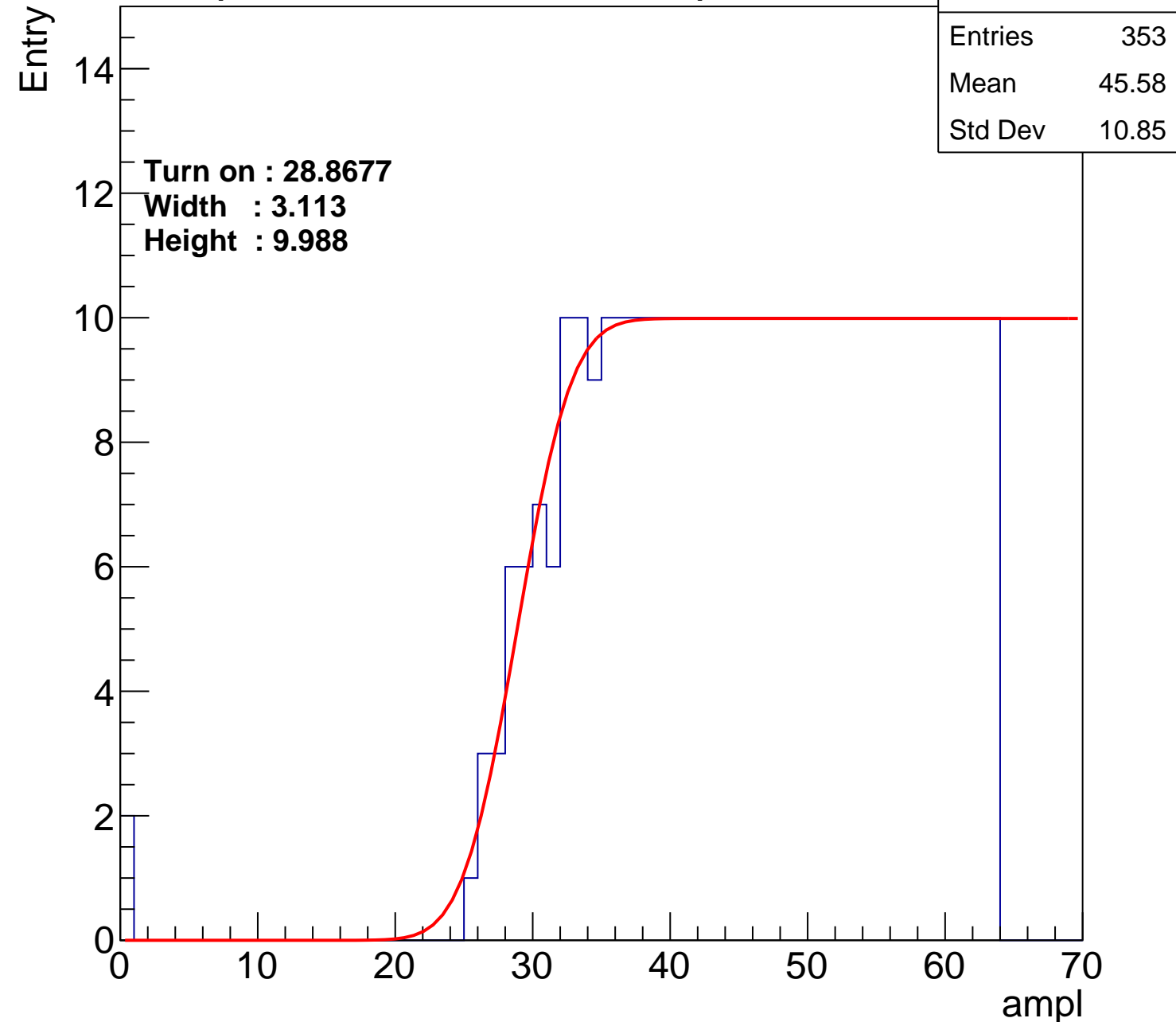
**Width : 3.113**

**Height : 9.988**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch28

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	376
Mean	44.46
Std Dev	11.34

Turn on : 27.1076

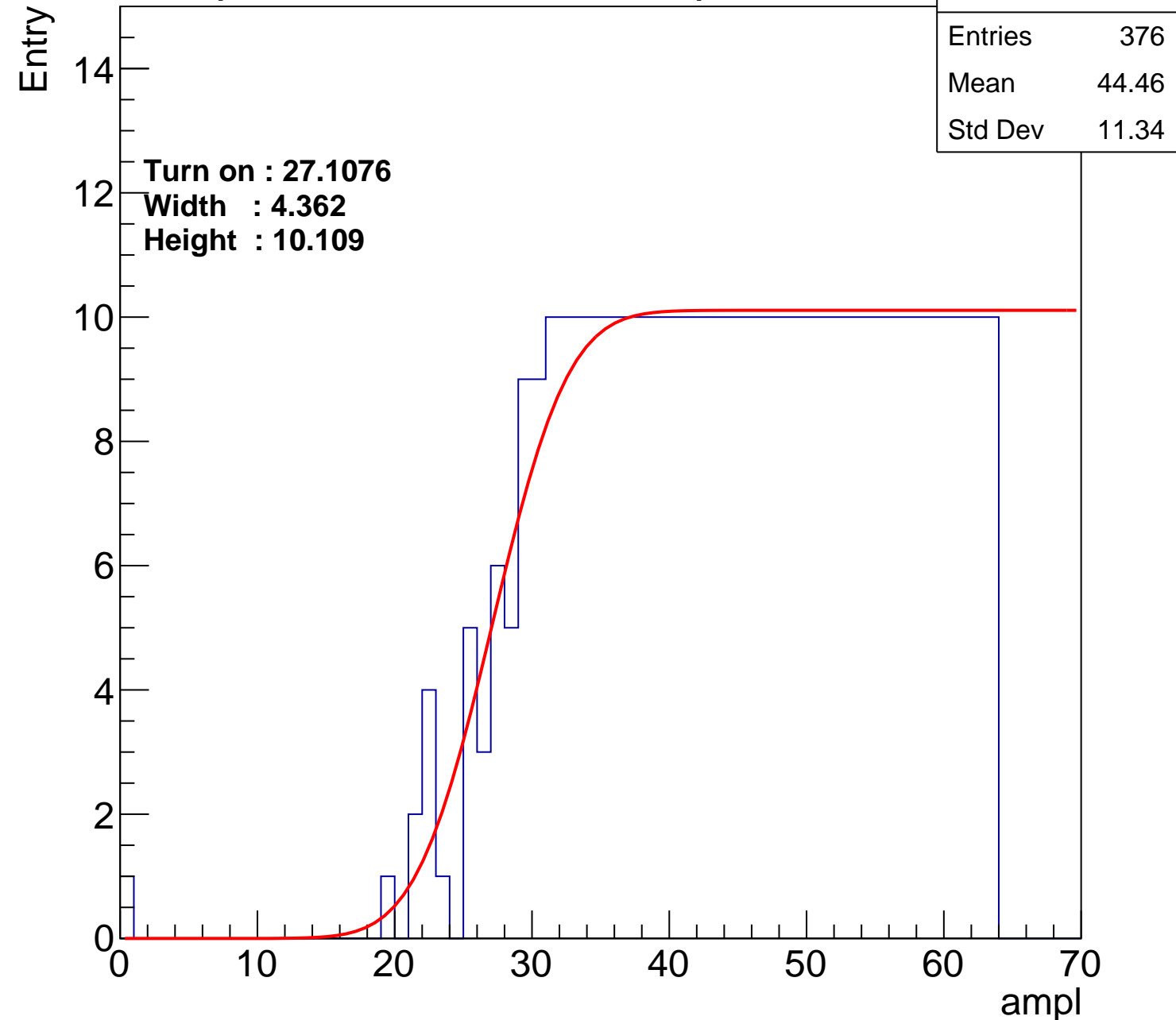
Width : 4.362

Height : 10.109

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch29

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	390
Mean	43.95
Std Dev	11.54

Turn on : 25.3140

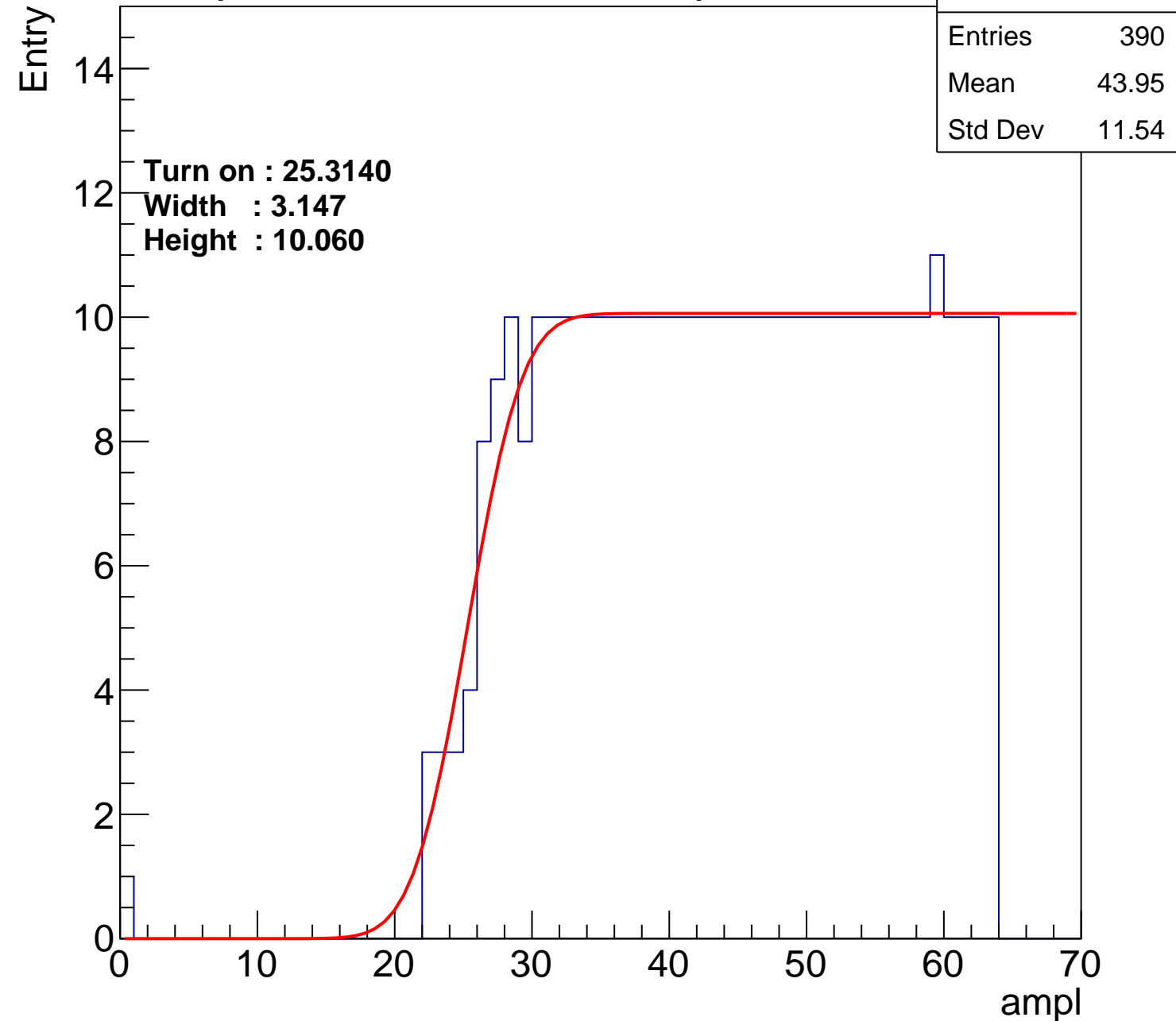
Width : 3.147

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch30

calib\_packv5\_042523\_0143.root, FC#7, port C2

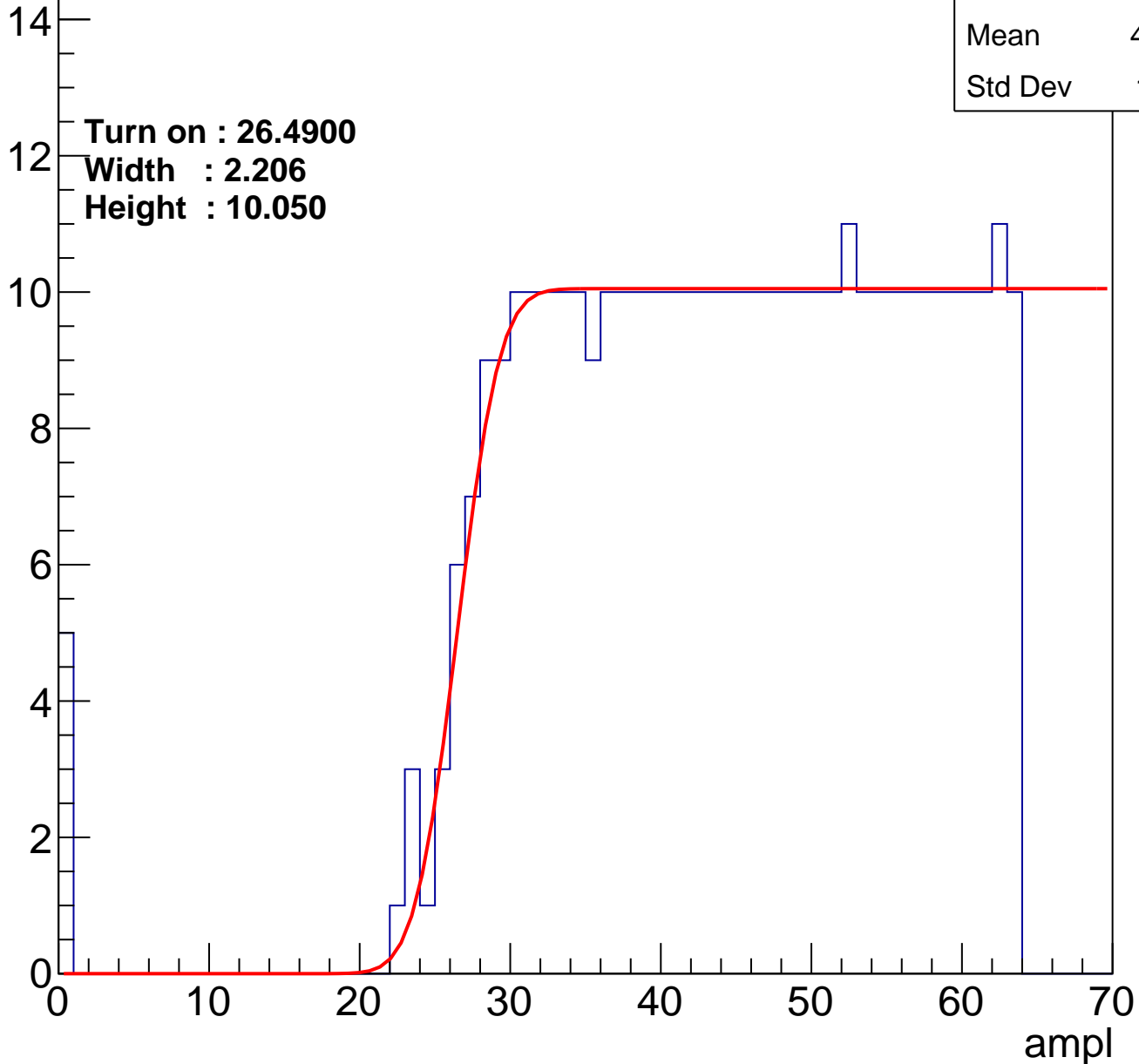
Entries	385
Mean	43.99
Std Dev	12.11

Turn on : 26.4900

Width : 2.206

Height : 10.050

Entry





# B1L103S, U21-ch31

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	356
Mean	45.42
Std Dev	10.94

Turn on : 29.1194

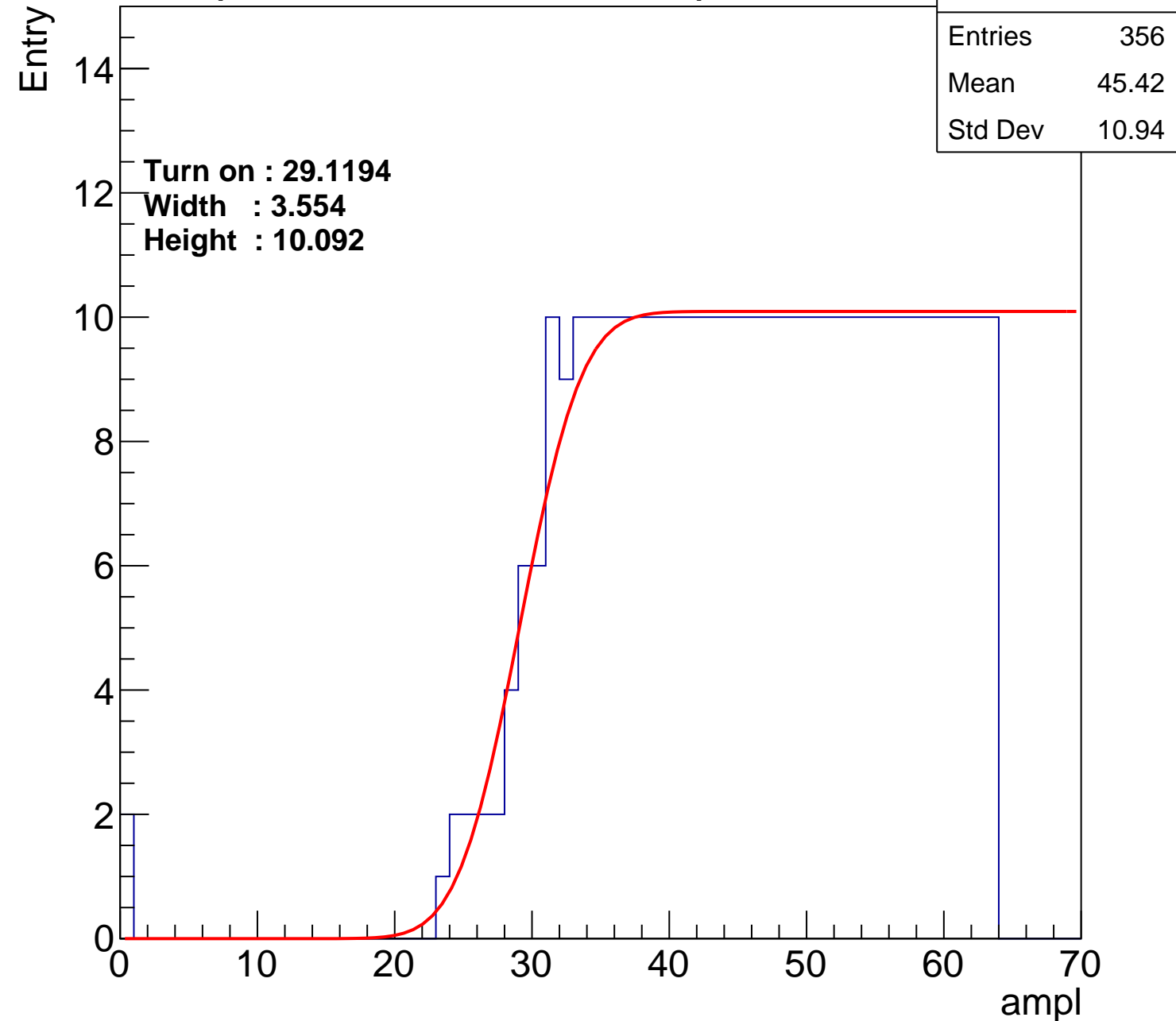
Width : 3.554

Height : 10.092

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch32

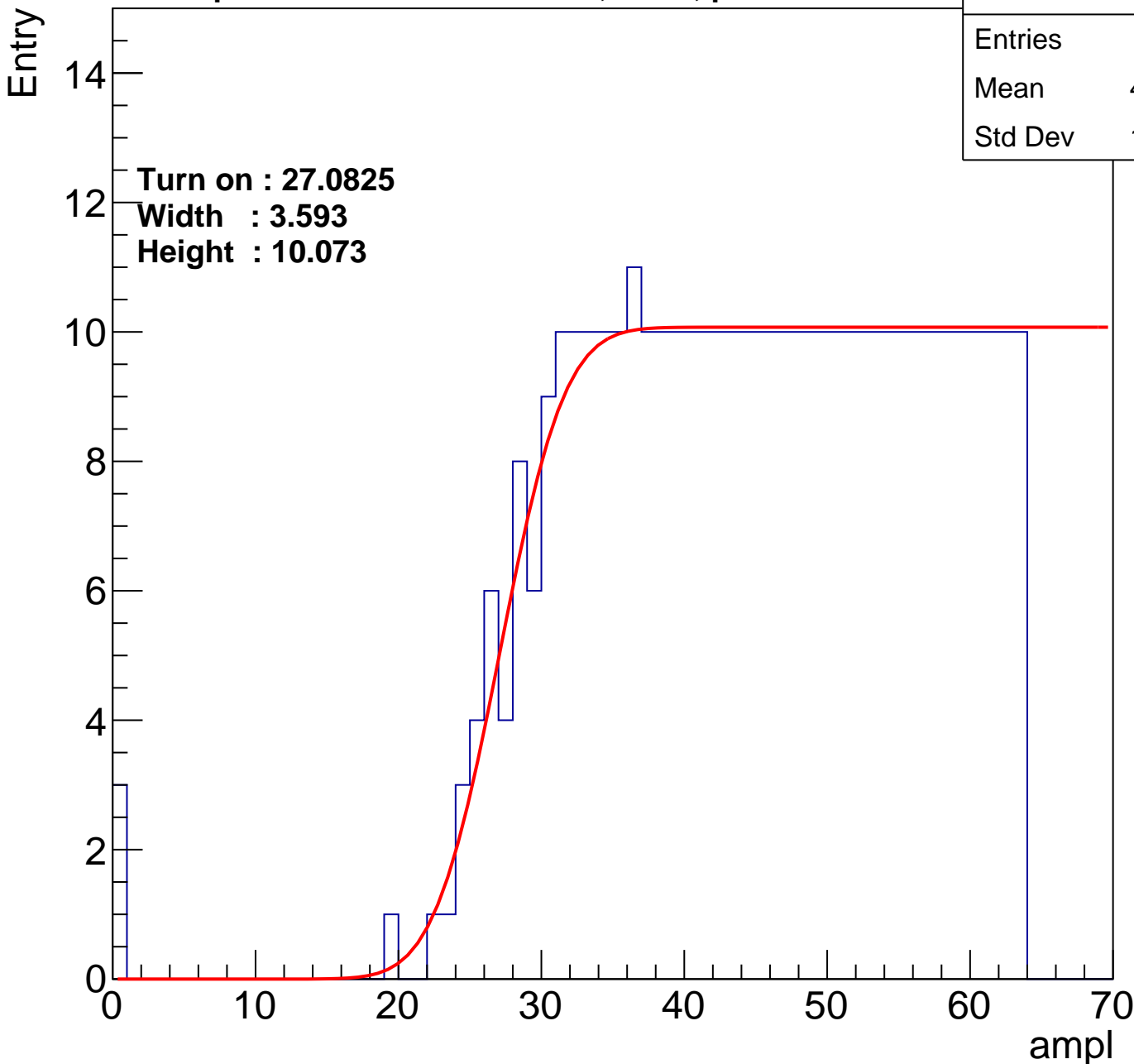
calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	377
Mean	44.33
Std Dev	11.65

Turn on : 27.0825

Width : 3.593

Height : 10.073



# B1L103S, U21-ch33

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	383
Mean	44.07
Std Dev	11.74

Turn on : 26.4169

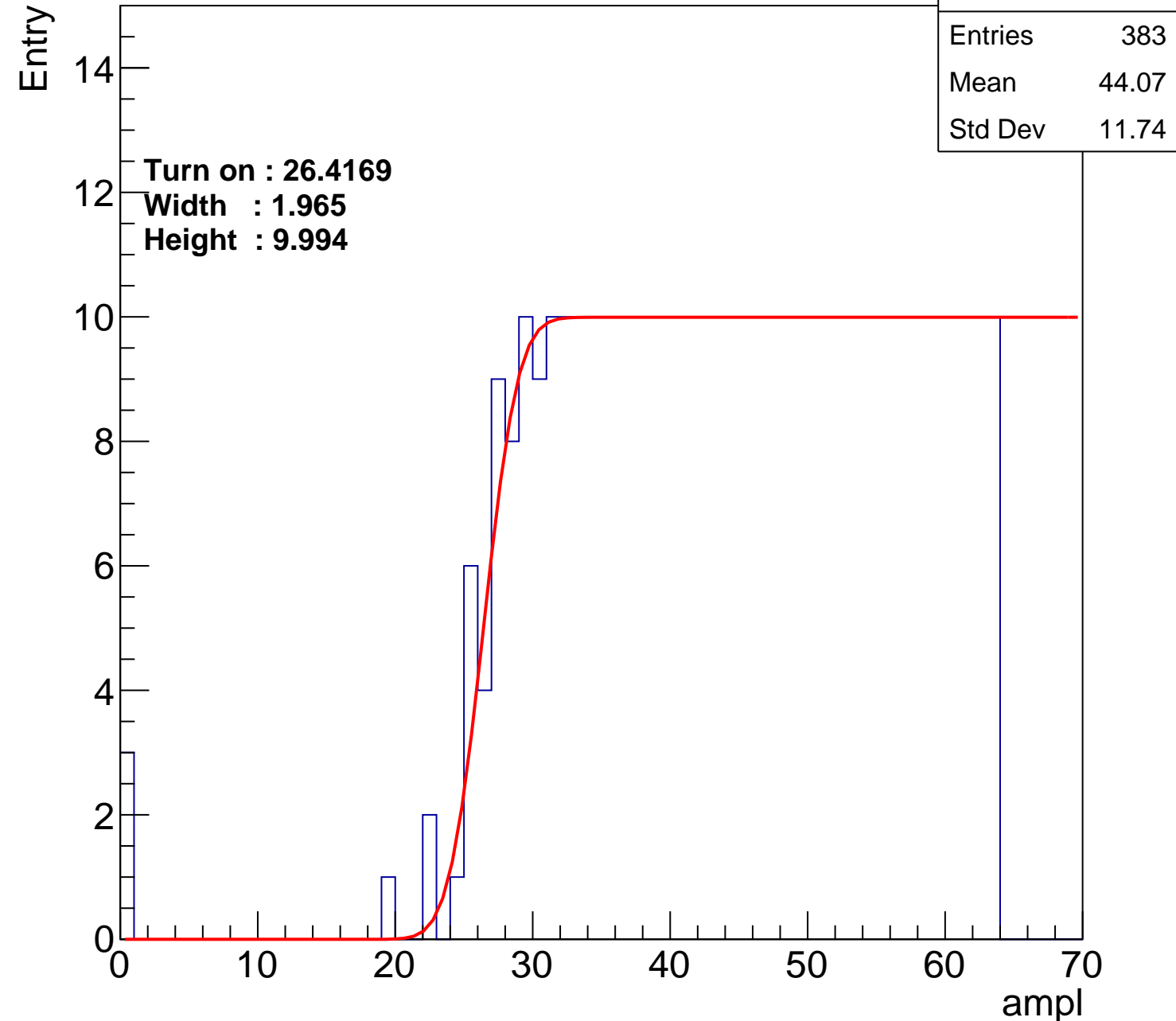
Width : 1.965

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch34

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	397
Mean	43.41
Std Dev	11.98

Turn on : 24.6265

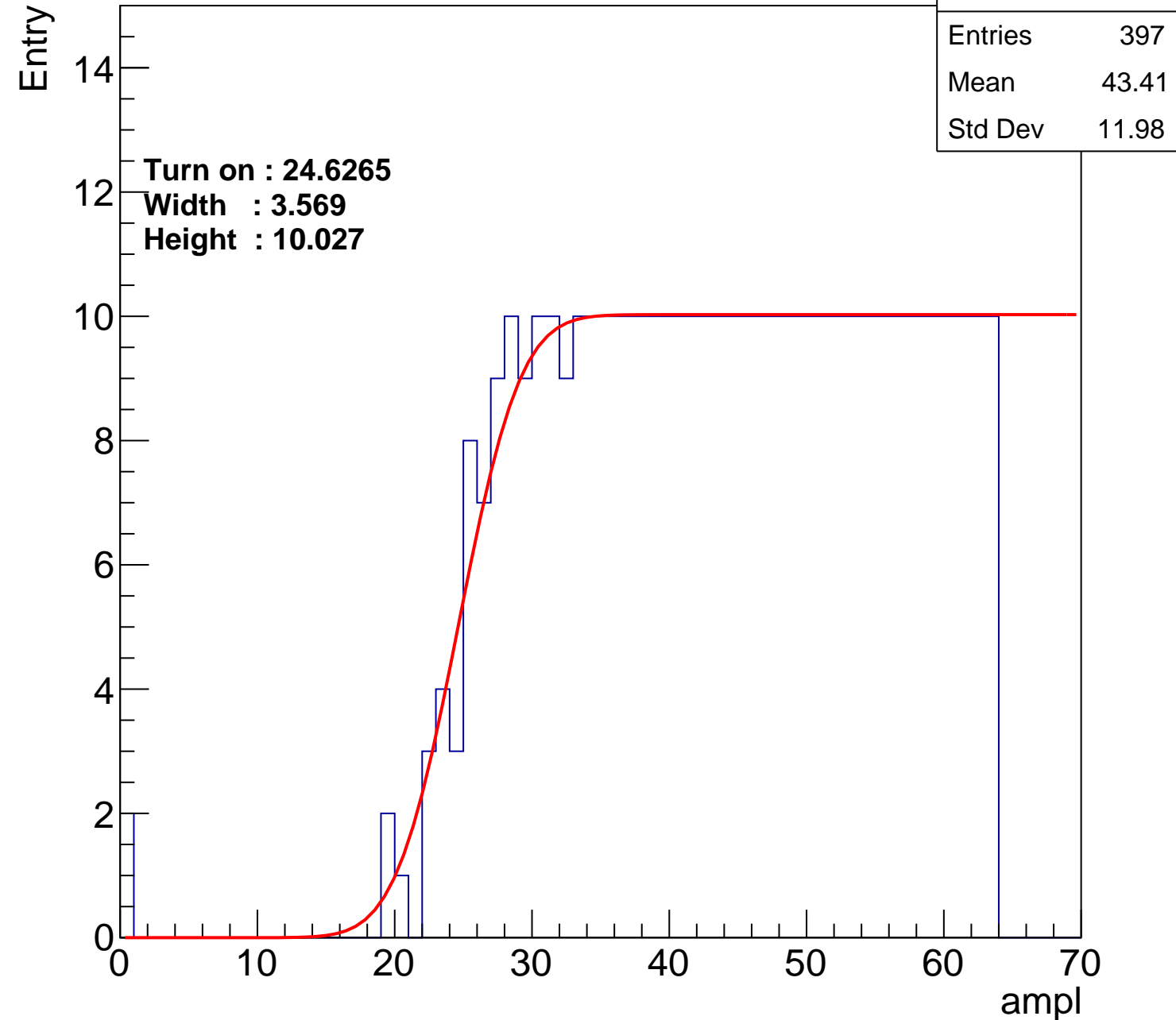
Width : 3.569

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch35

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	352
Mean	45.6
Std Dev	10.87

**Turn on : 28.9816**

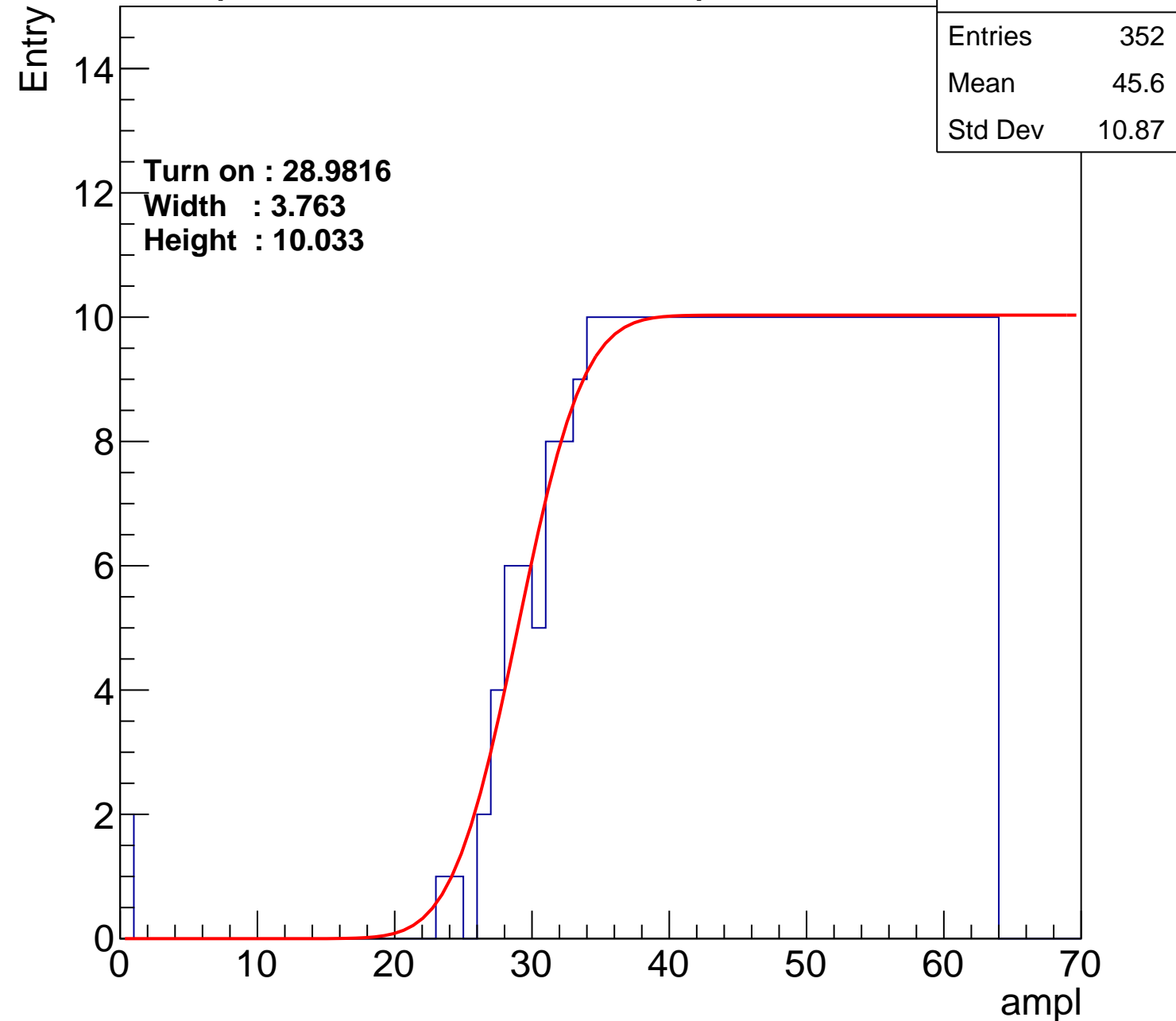
**Width : 3.763**

**Height : 10.033**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch36

calib\_packv5\_042523\_0143.root, FC#7, port C2

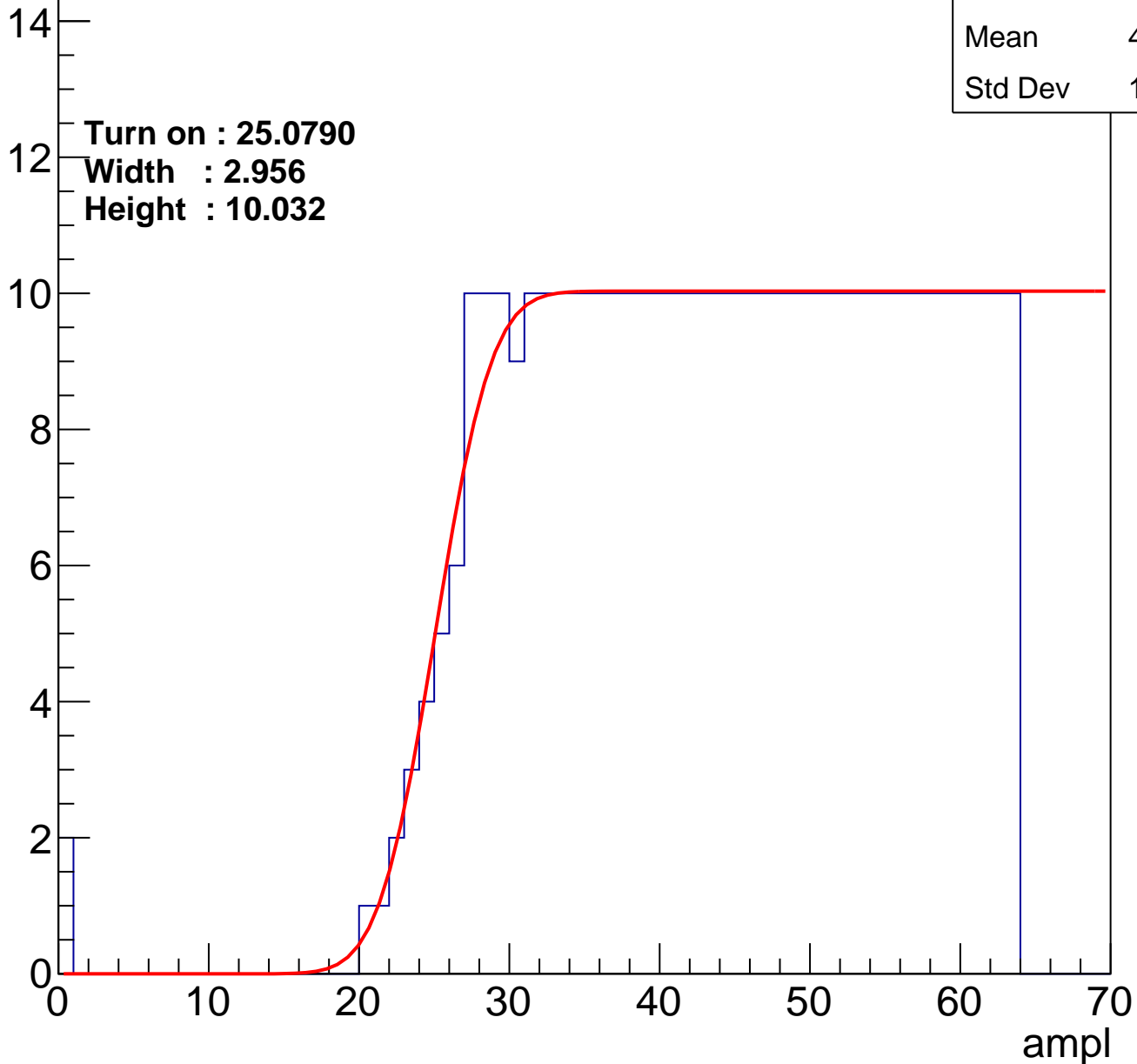
Entries	393
Mean	43.64
Std Dev	11.82

Turn on : 25.0790

Width : 2.956

Height : 10.032

Entry



# B1L103S, U21-ch37

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	357
Mean	45.37
Std Dev	11.19

Turn on : 29.4248

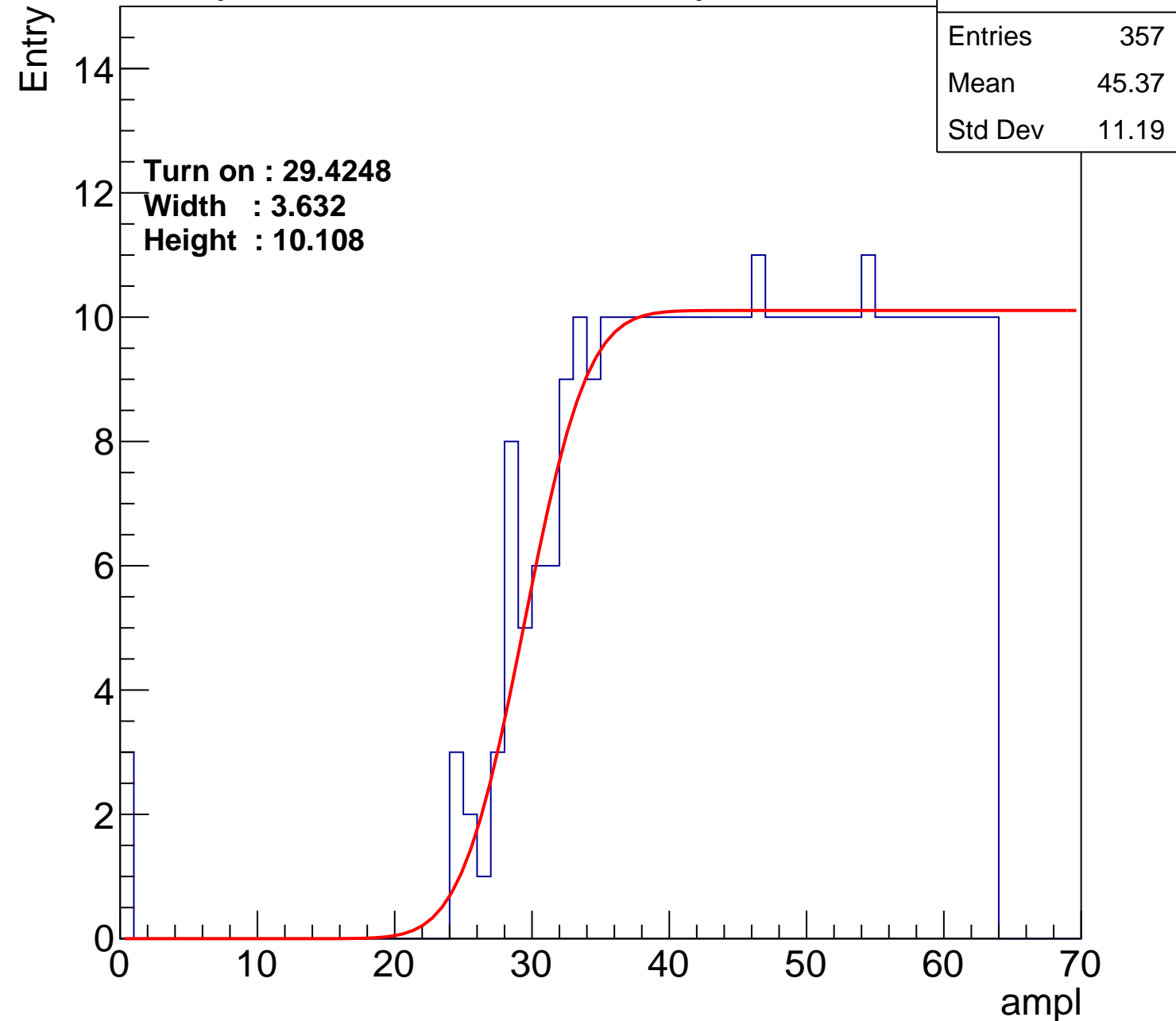
Width : 3.632

Height : 10.108

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch38

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	400
Mean	43.35
Std Dev	11.93

Turn on : 24.5517

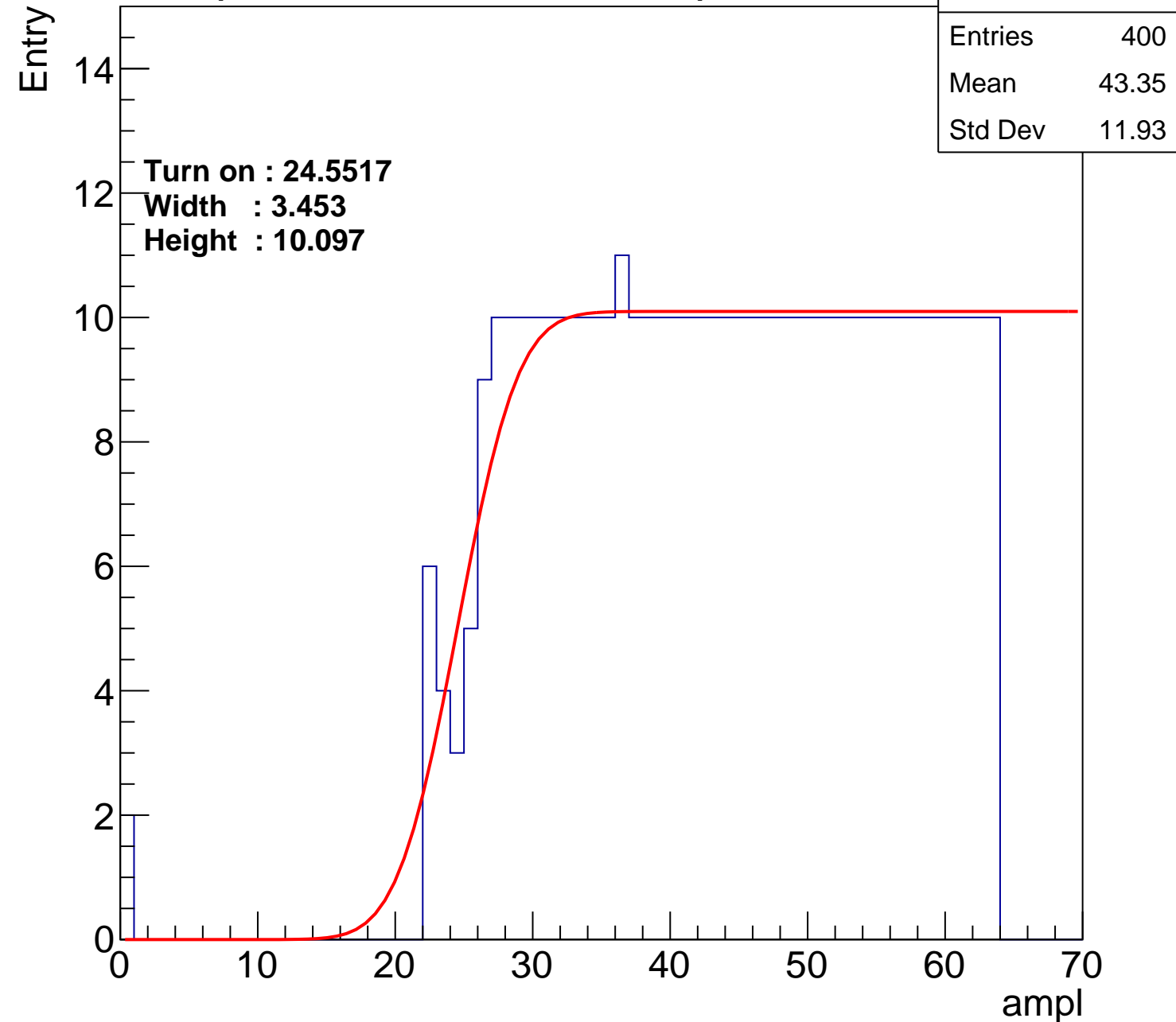
Width : 3.453

Height : 10.097

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch39

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	375
Mean	44.42
Std Dev	11.68

**Turn on : 26.8058**

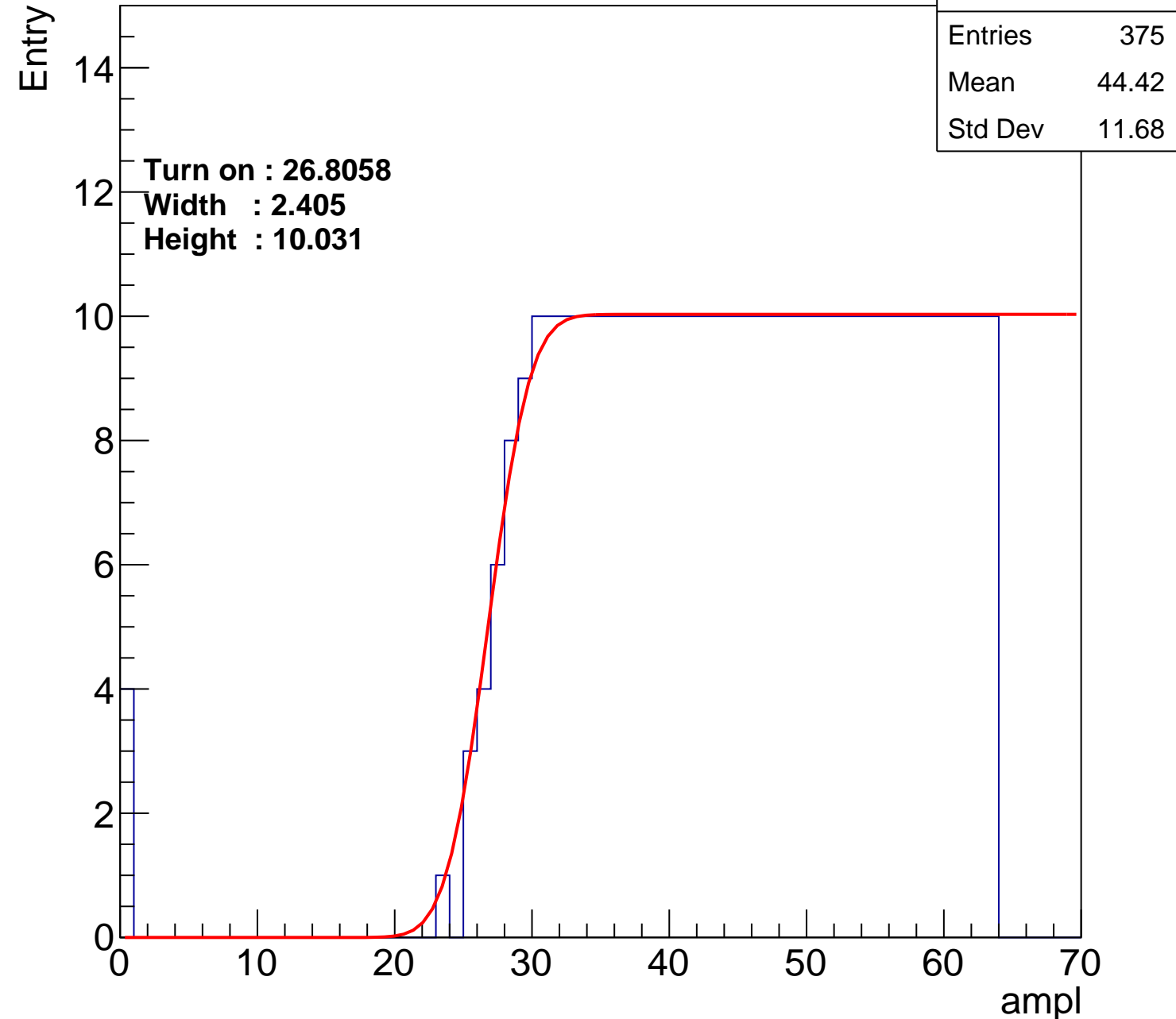
**Width : 2.405**

**Height : 10.031**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch40

calib\_packv5\_042523\_0143.root, FC#7, port C2

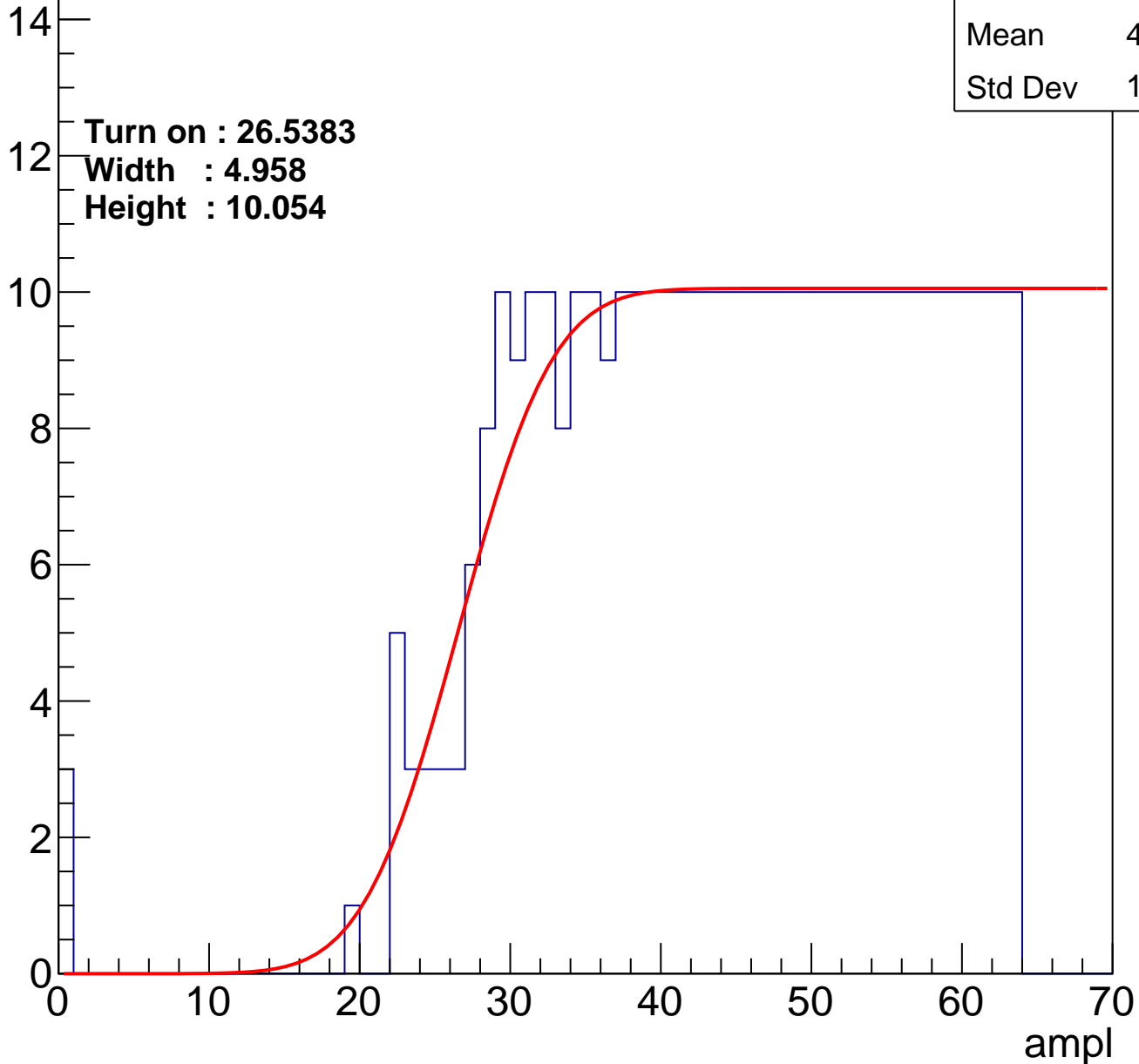
Entries	381
Mean	44.03
Std Dev	11.88

**Turn on : 26.5383**

**Width : 4.958**

**Height : 10.054**

Entry



# B1L103S, U21-ch41

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	359
Mean	45.31
Std Dev	10.86

Turn on : 28.6163

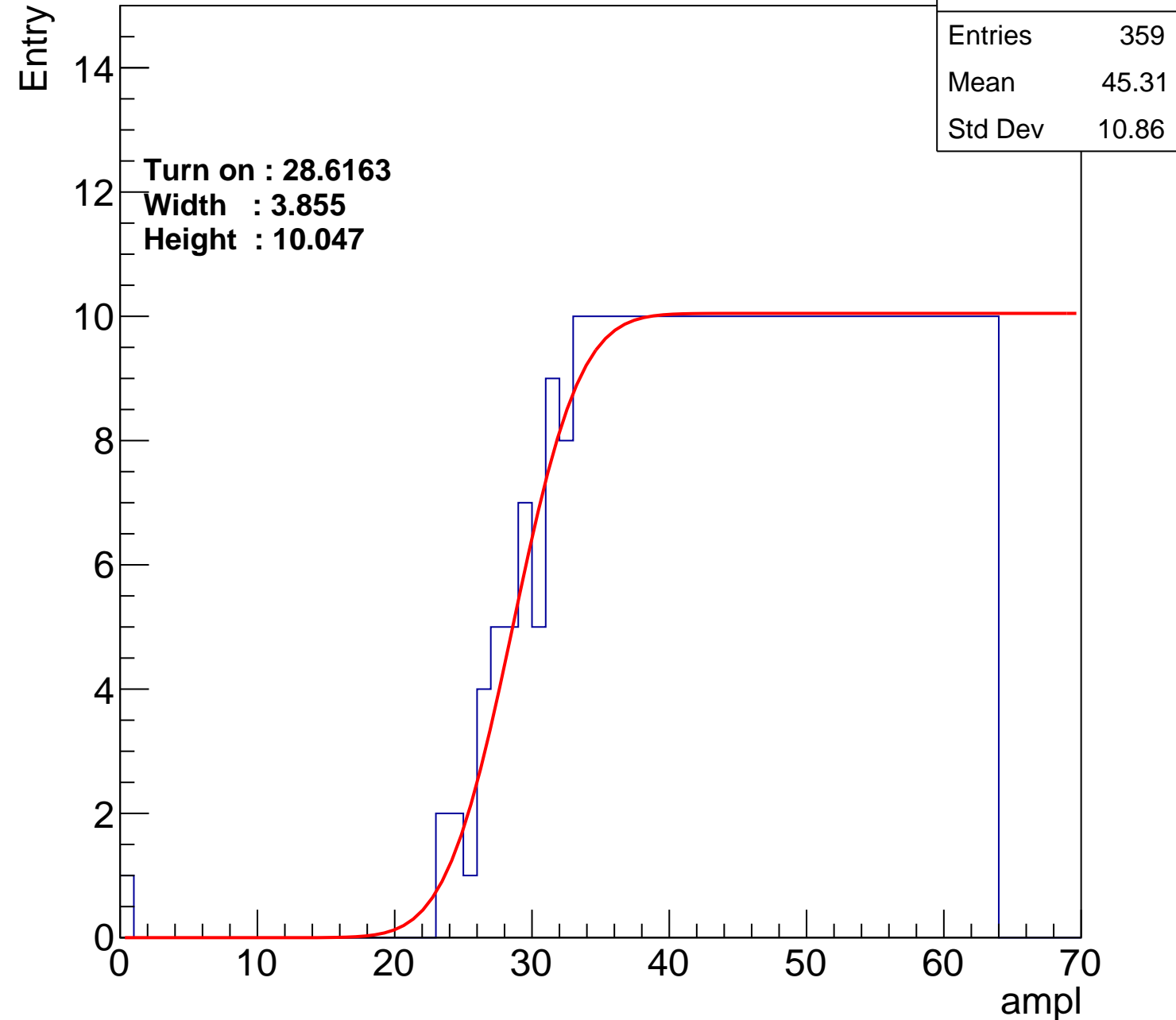
Width : 3.855

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch42

calib\_packv5\_042523\_0143.root, FC#7, port C2

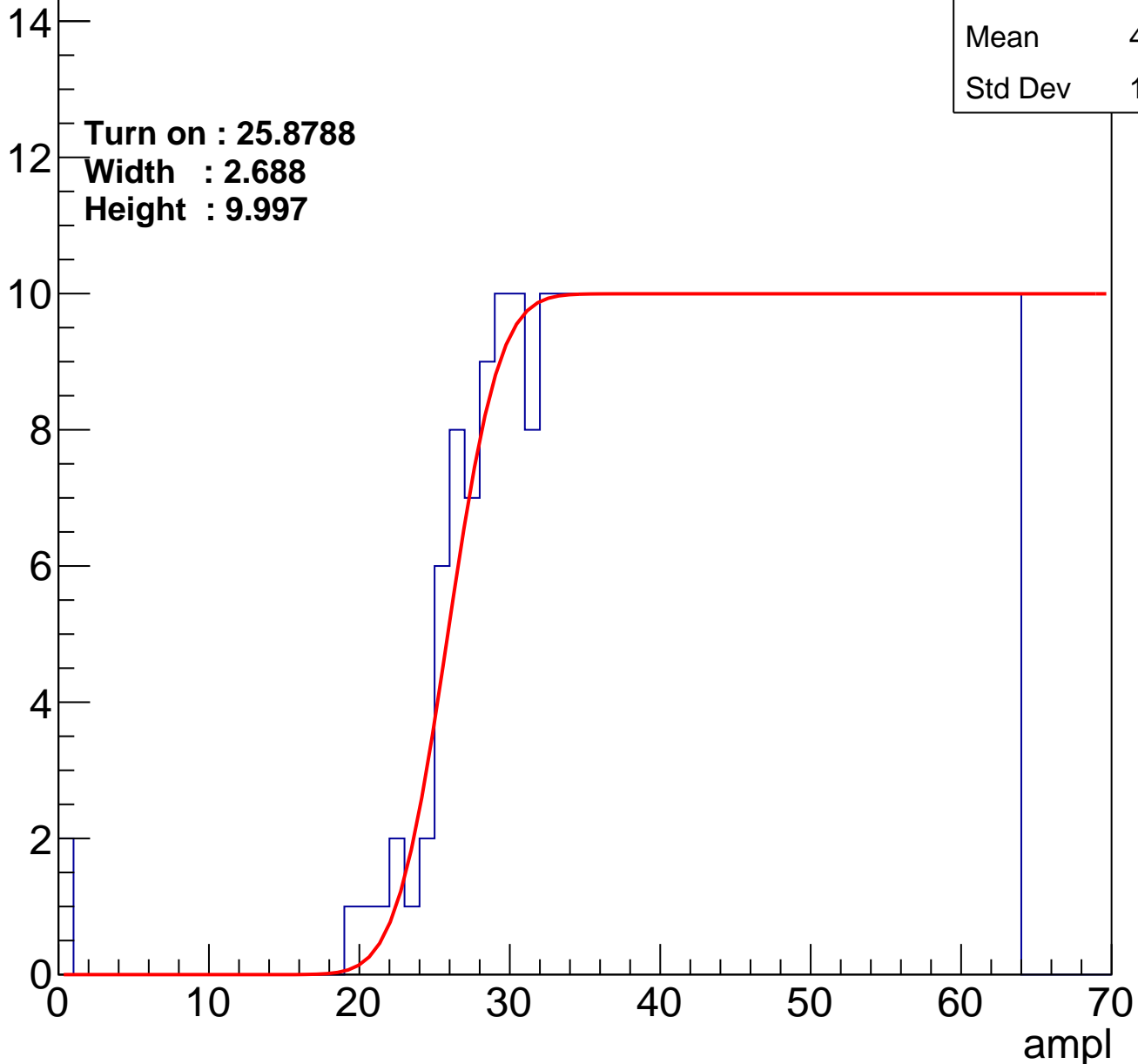
Entries	388
Mean	43.85
Std Dev	11.76

Turn on : 25.8788

Width : 2.688

Height : 9.997

Entry



# B1L103S, U21-ch43

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	375
Mean	44.39
Std Dev	11.72

**Turn on : 27.2793**

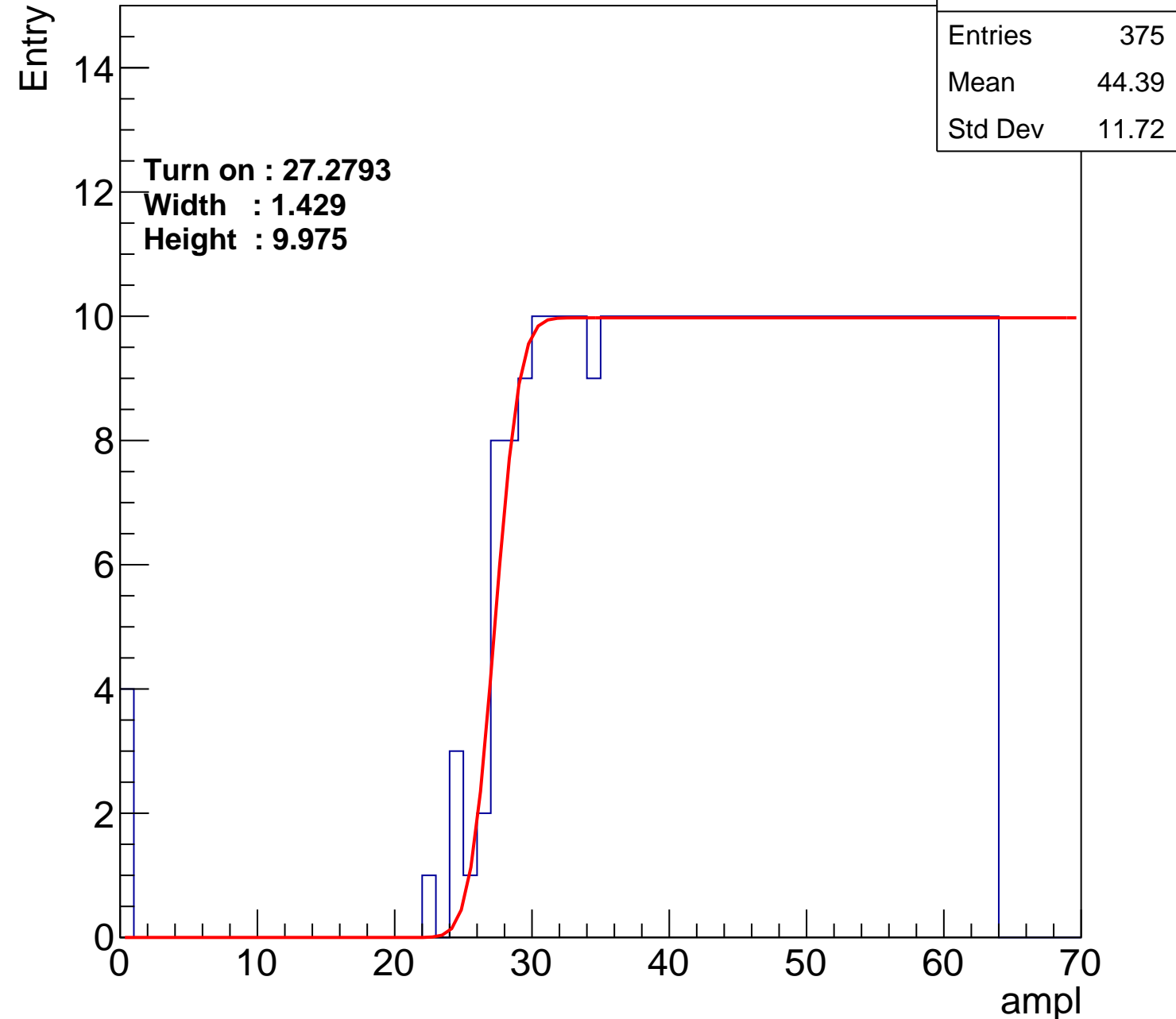
**Width : 1.429**

**Height : 9.975**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch44

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	399
Mean	43.24
Std Dev	12.27

Turn on : 24.2096

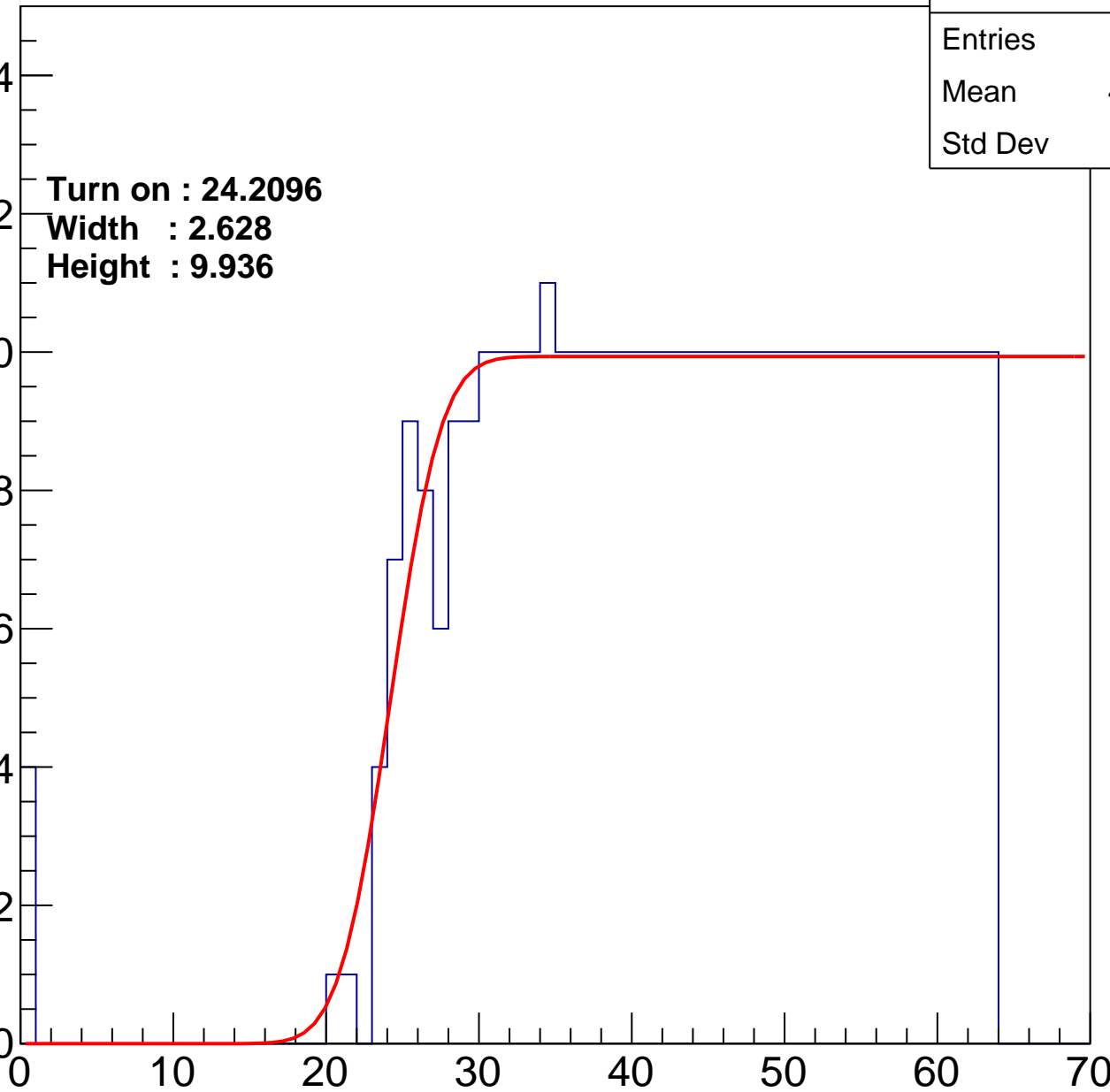
Width : 2.628

Height : 9.936

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch45

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	370
Mean	44.89
Std Dev	11.1

Turn on : 27.6622

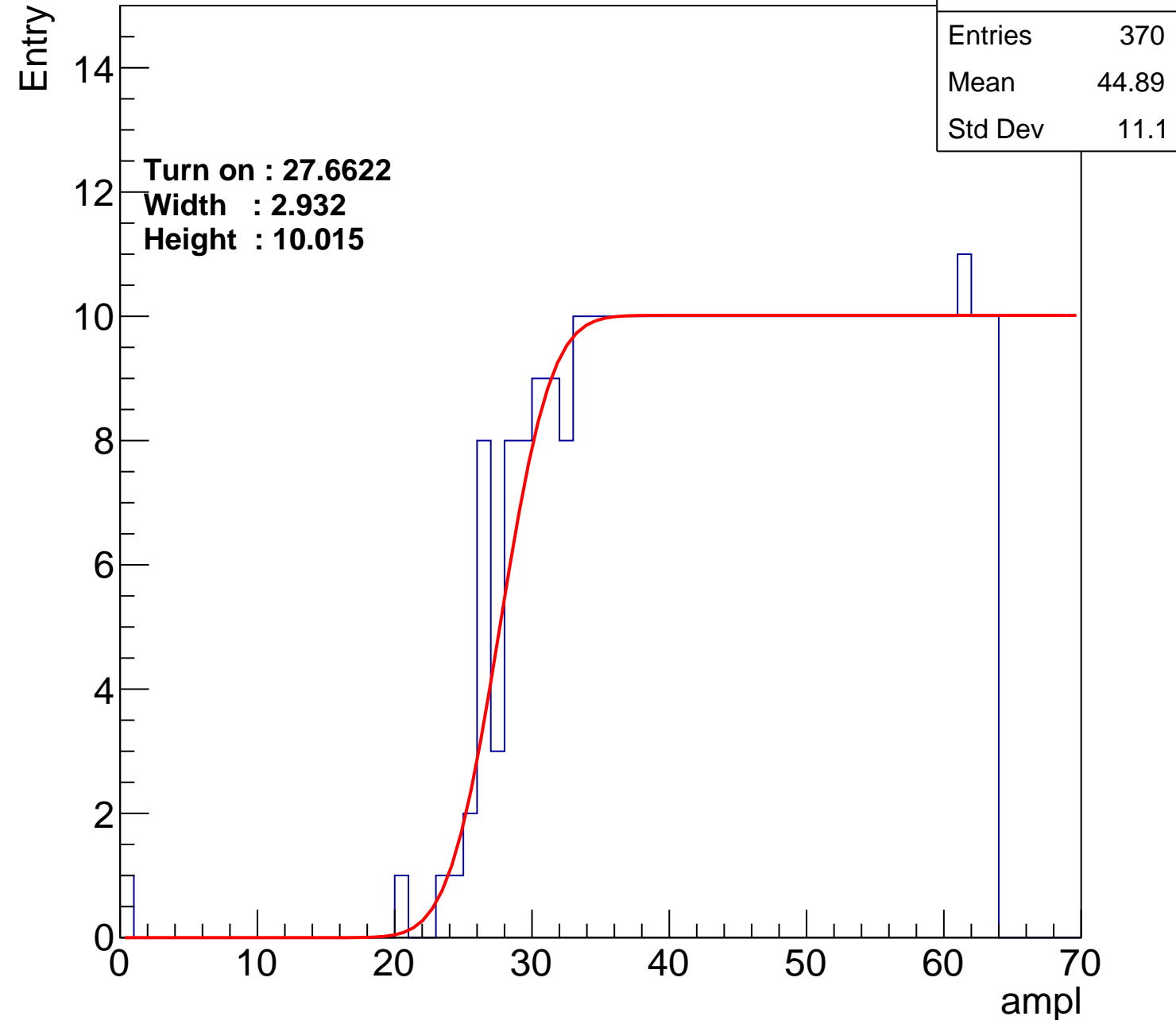
Width : 2.932

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch46

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	391
Mean	43.69
Std Dev	12.06

Turn on : 26.2246

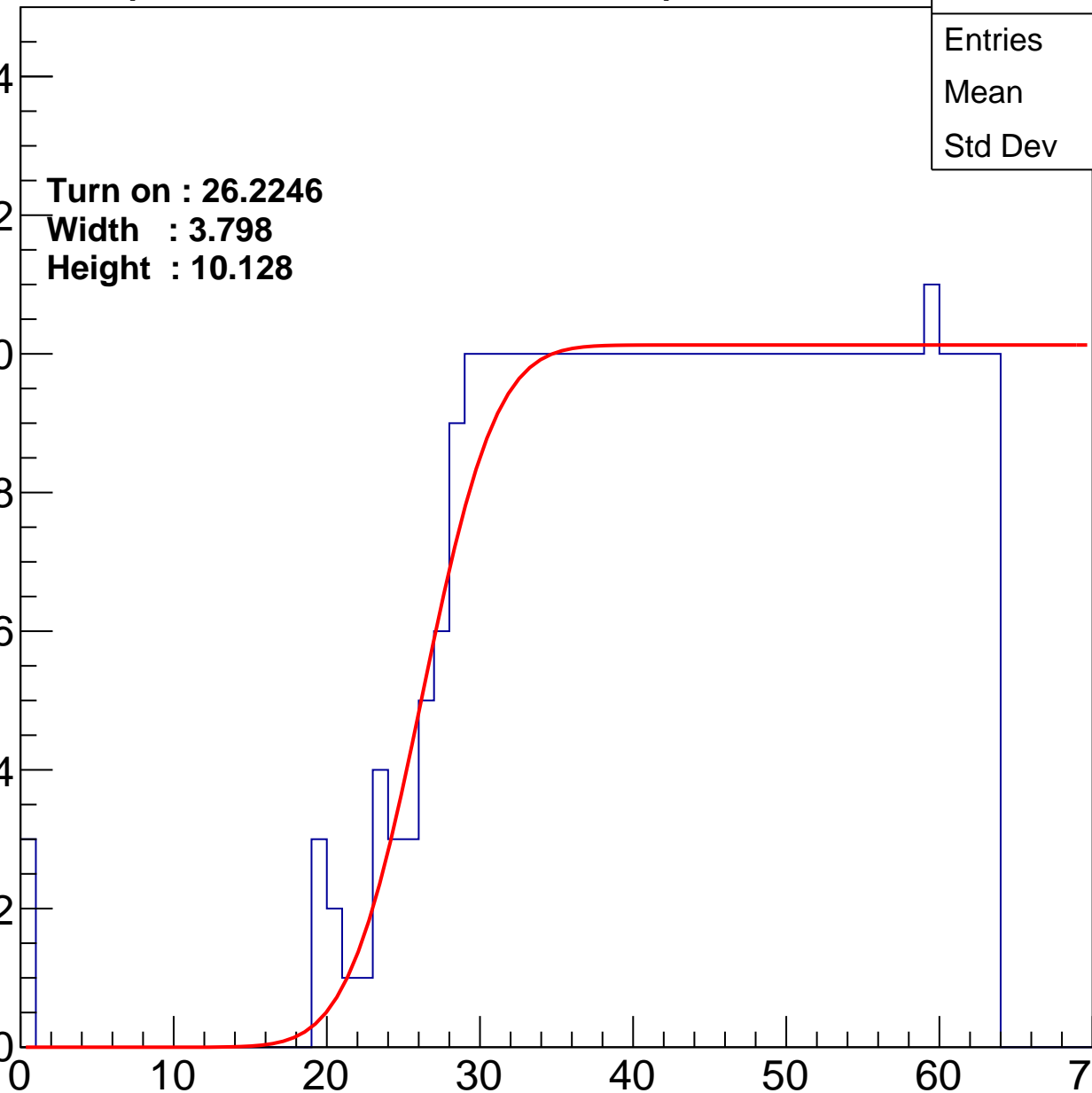
Width : 3.798

Height : 10.128

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch47

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	366
Mean	45.01
Std Dev	10.98

**Turn on : 28.3885**

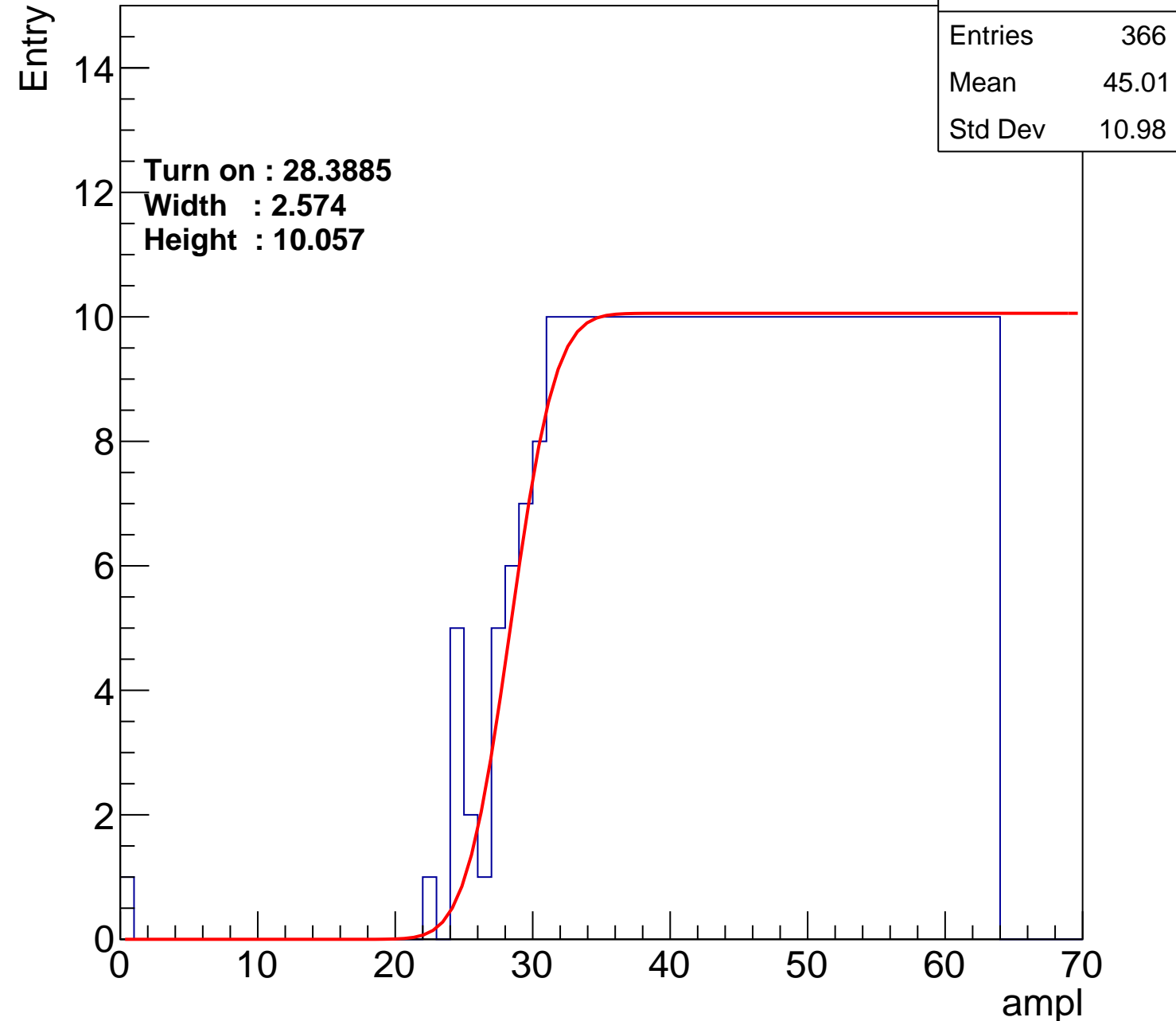
**Width : 2.574**

**Height : 10.057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch48

calib\_packv5\_042523\_0143.root, FC#7, port C2

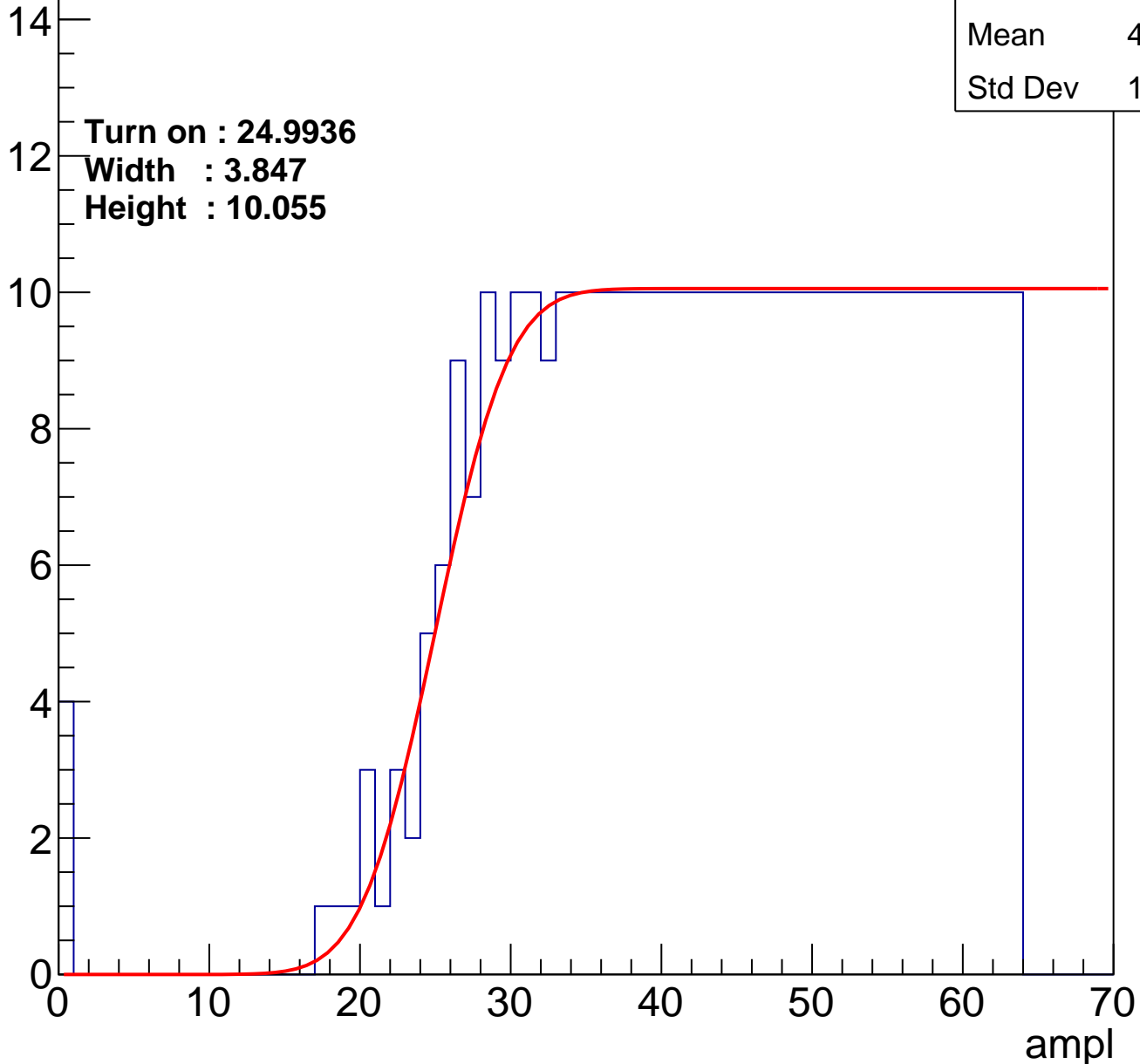
Entries	401
Mean	43.04
Std Dev	12.47

Turn on : 24.9936

Width : 3.847

Height : 10.055

Entry



# B1L103S, U21-ch49

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	380
Mean	43.93
Std Dev	12.3

Turn on : 26.8174

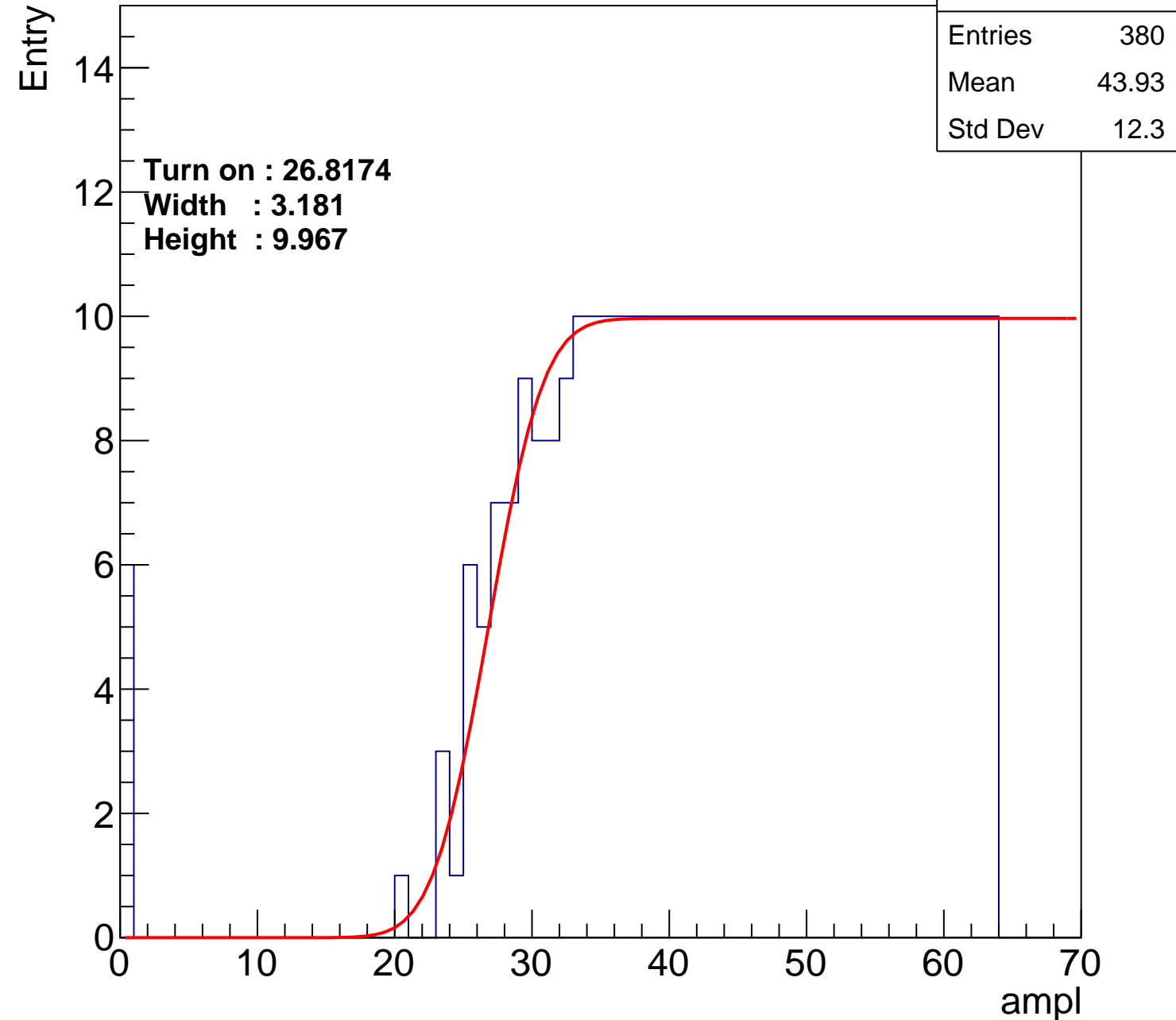
Width : 3.181

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch50

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	383
Mean	43.91
Std Dev	12.05

Turn on : 26.4844

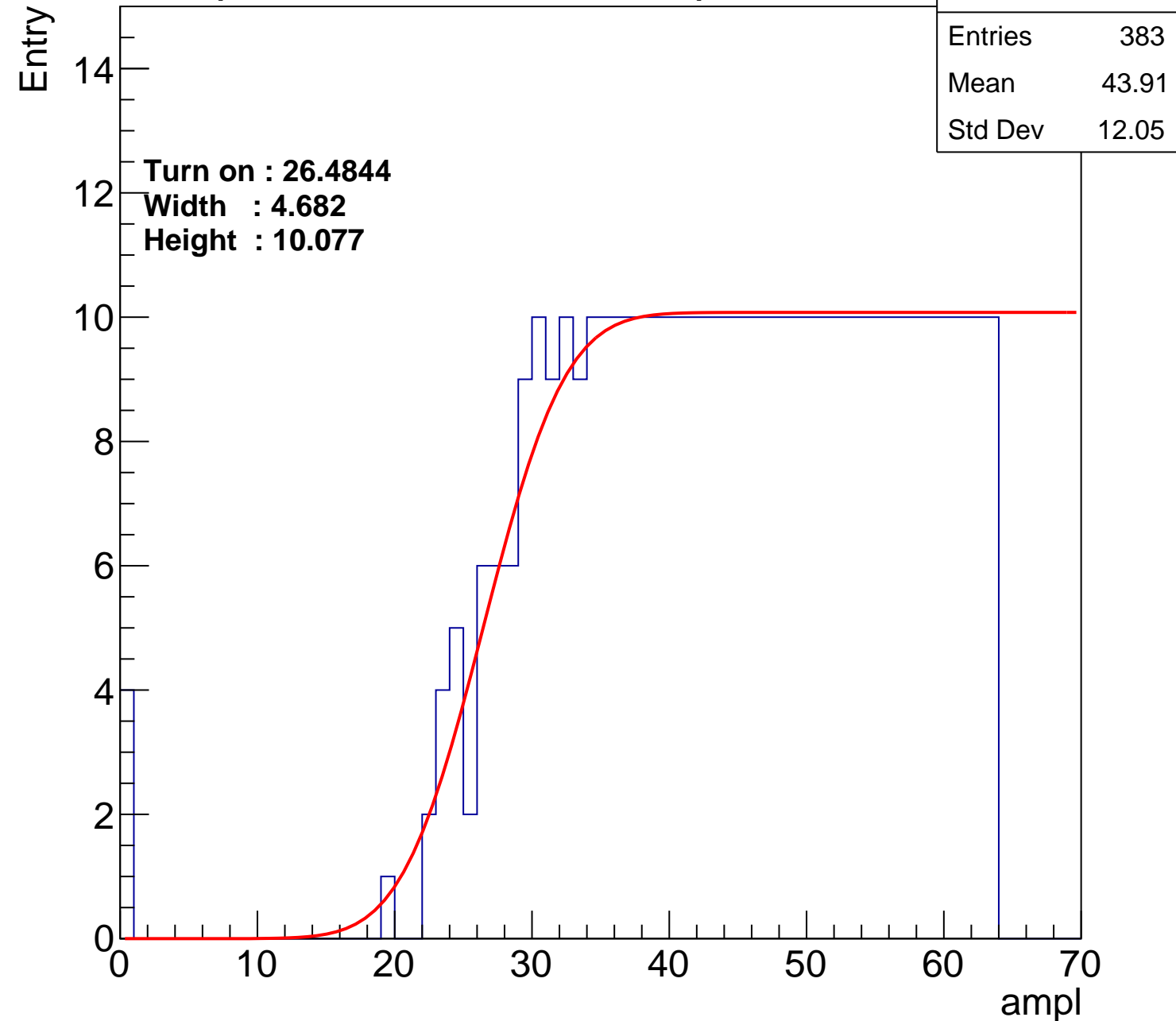
Width : 4.682

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch51

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	359
Mean	45.26
Std Dev	11.04

Turn on : 28.6959

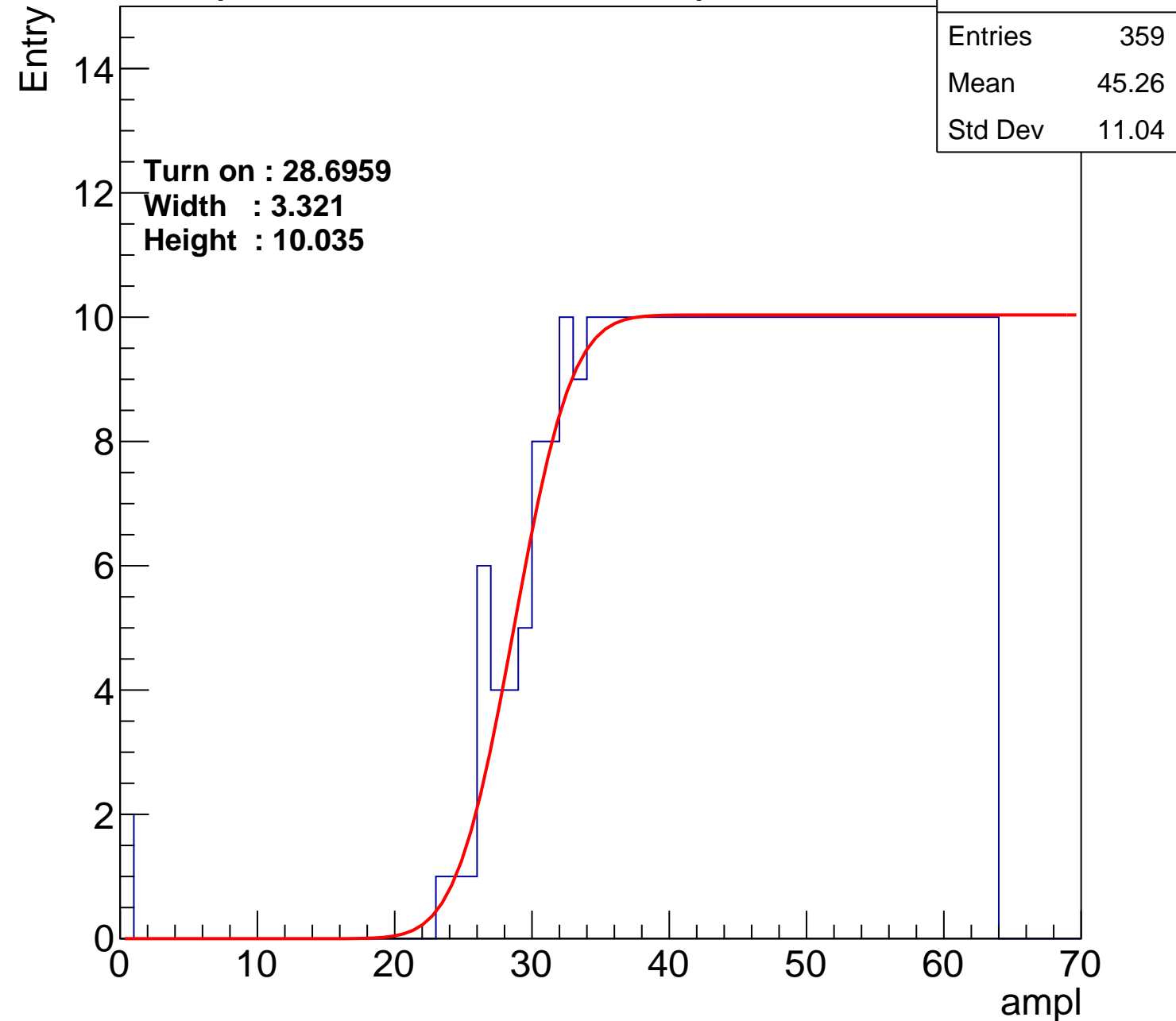
Width : 3.321

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch52

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	391
Mean	43.5
Std Dev	12.33

Turn on : 25.7236

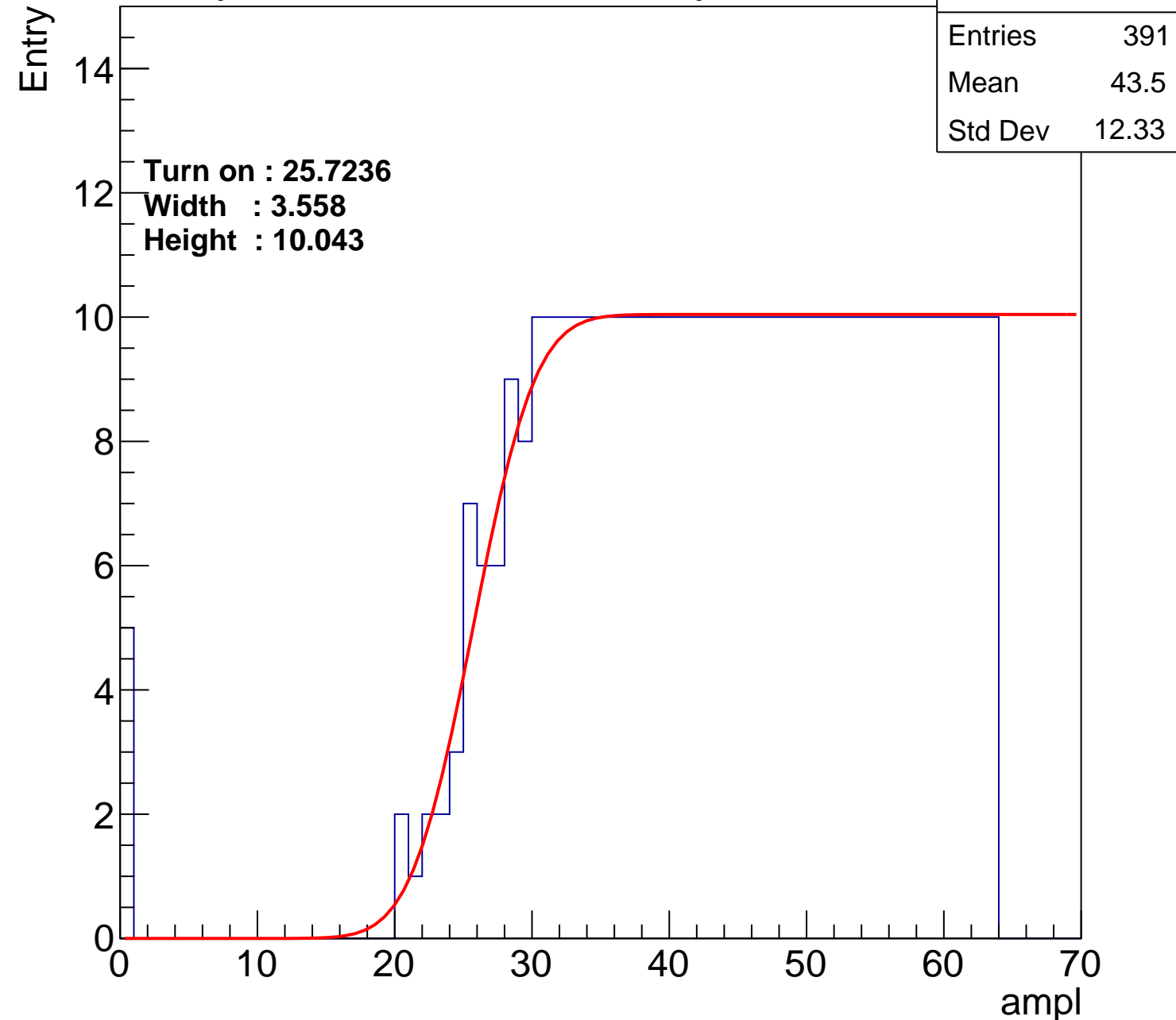
Width : 3.558

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch53

calib\_packv5\_042523\_0143.root, FC#7, port C2

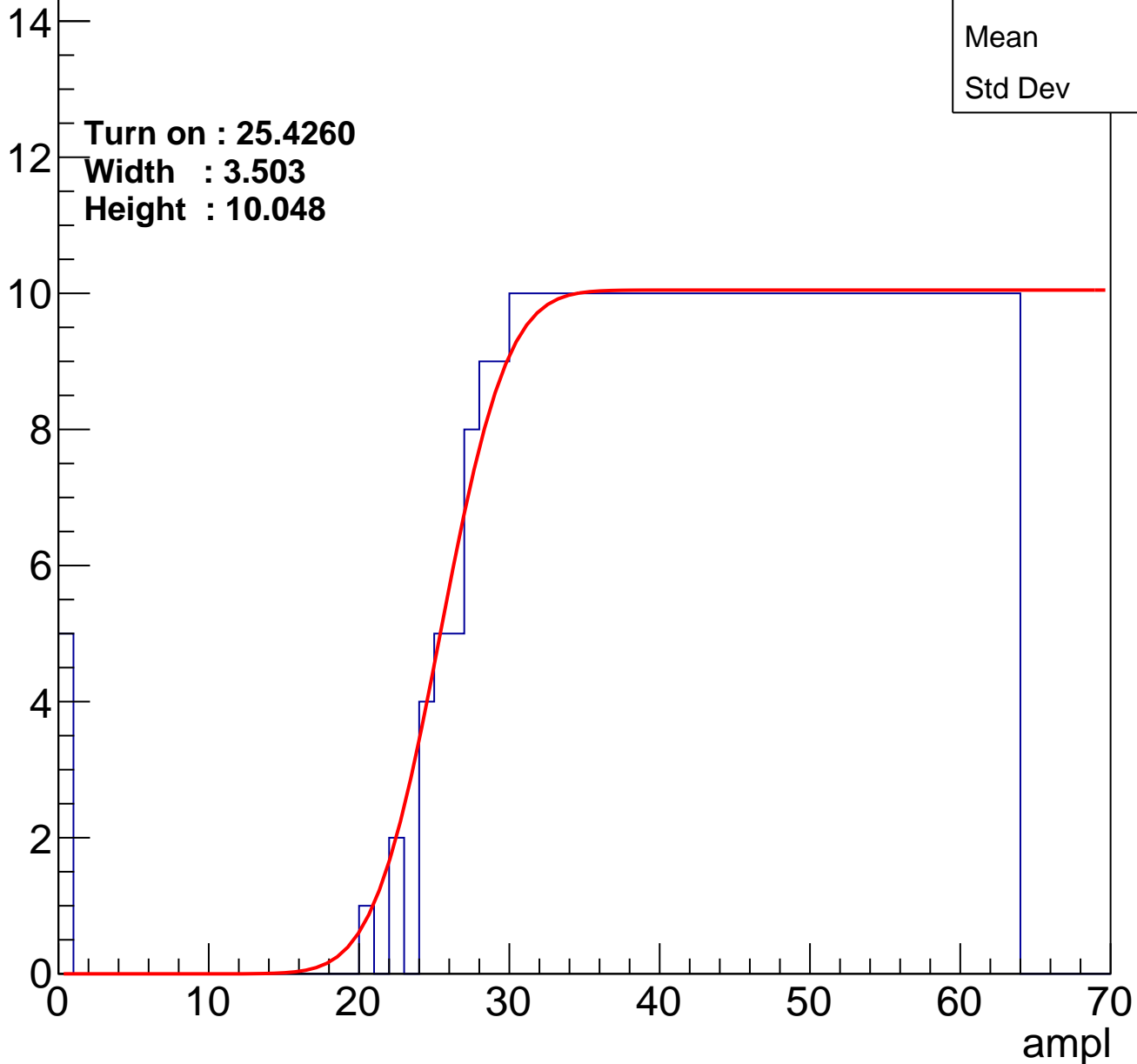
Entries	388
Mean	43.7
Std Dev	12.2

Turn on : 25.4260

Width : 3.503

Height : 10.048

Entry



# B1L103S, U21-ch54

calib\_packv5\_042523\_0143.root, FC#7, port C2

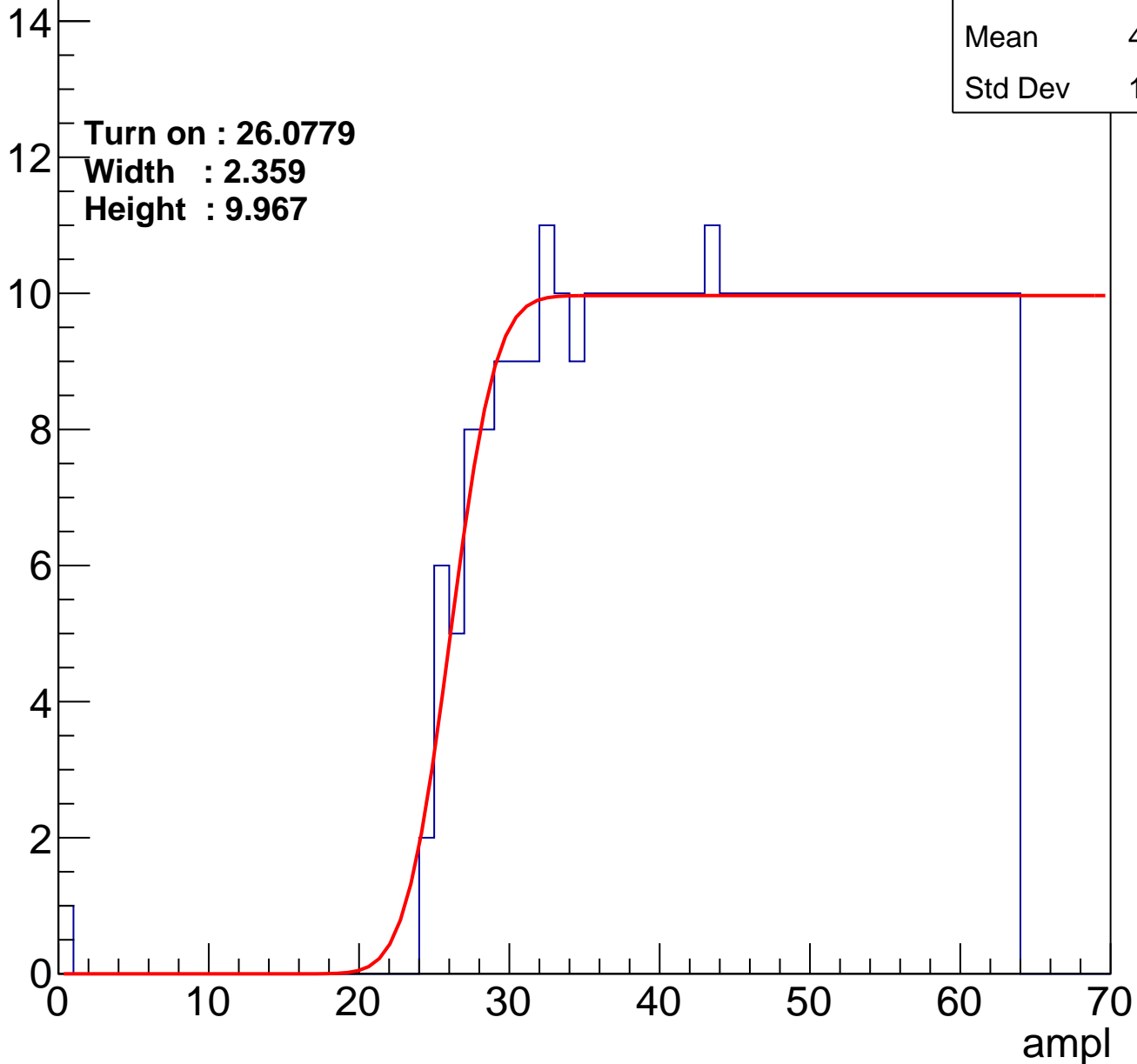
Entries	378
Mean	44.49
Std Dev	11.19

Turn on : 26.0779

Width : 2.359

Height : 9.967

Entry





# B1L103S, U21-ch55

calib\_packv5\_042523\_0143.root, FC#7, port C2

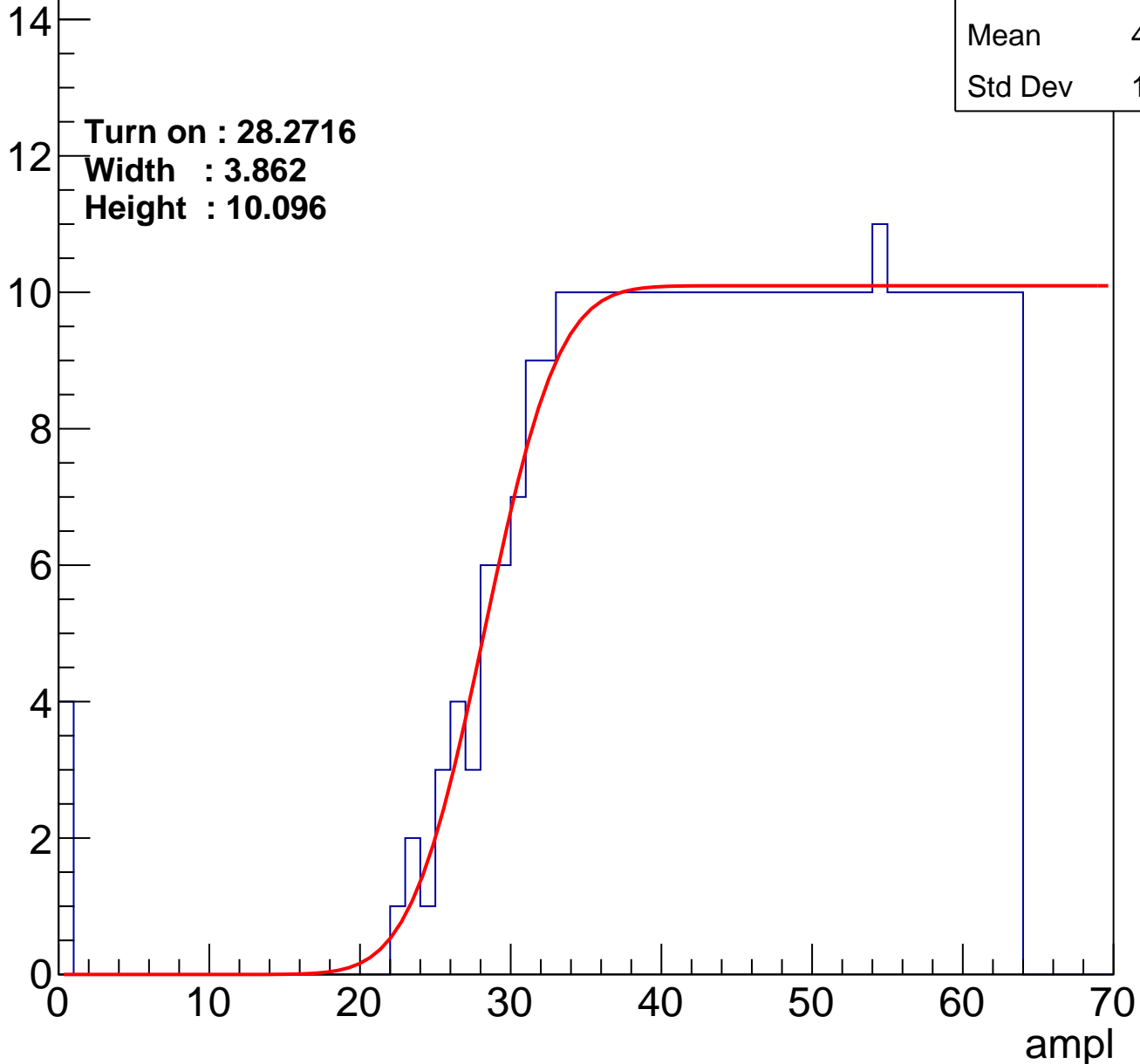
Entries	366
Mean	44.82
Std Dev	11.62

Turn on : 28.2716

Width : 3.862

Height : 10.096

Entry



# B1L103S, U21-ch56

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	403
Mean	43.04
Std Dev	12.35

Turn on : 24.1541

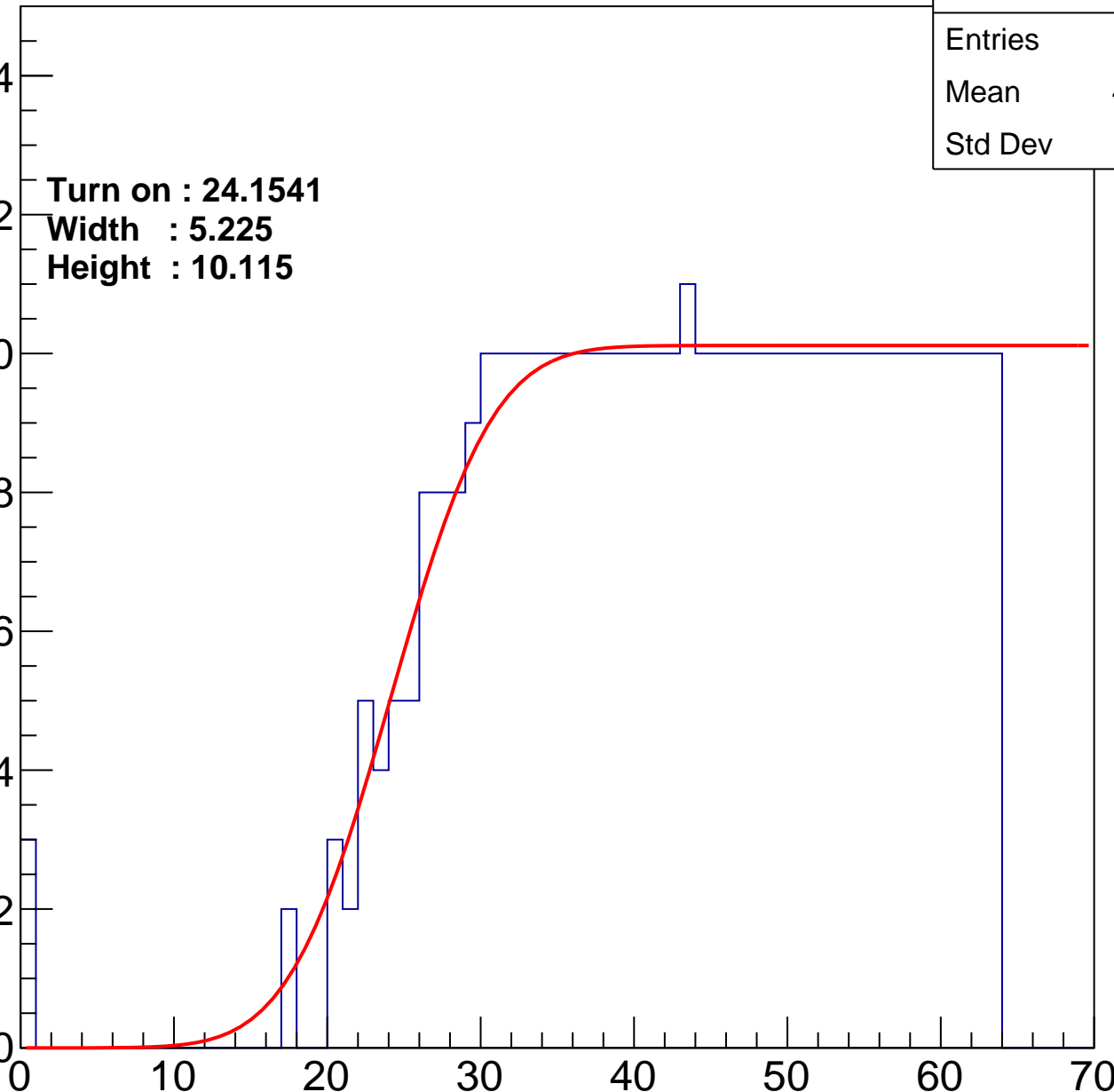
Width : 5.225

Height : 10.115

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch57

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	376
Mean	44.48
Std Dev	11.29

Turn on : 26.4836

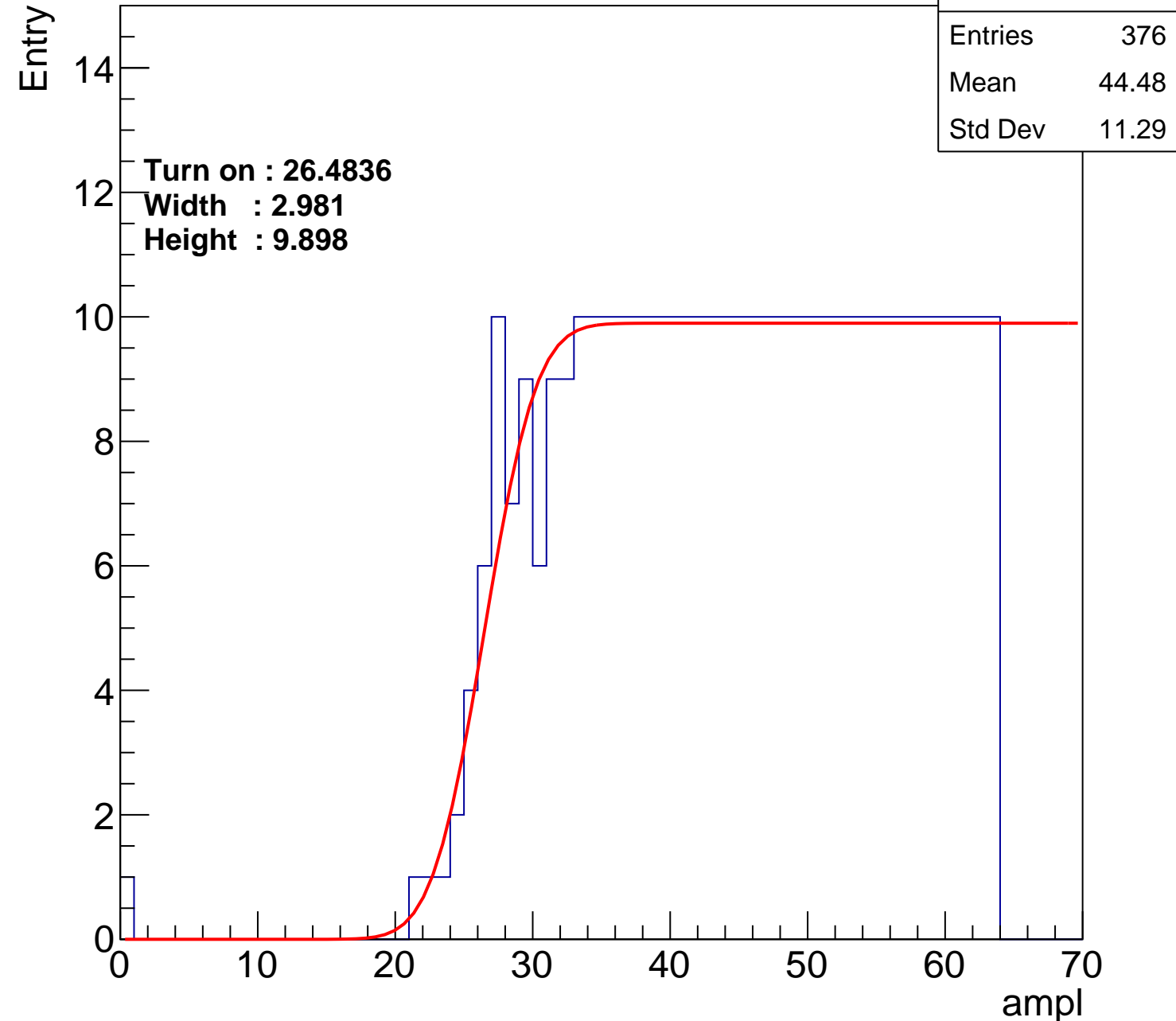
Width : 2.981

Height : 9.898

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch58

calib\_packv5\_042523\_0143.root, FC#7, port C2

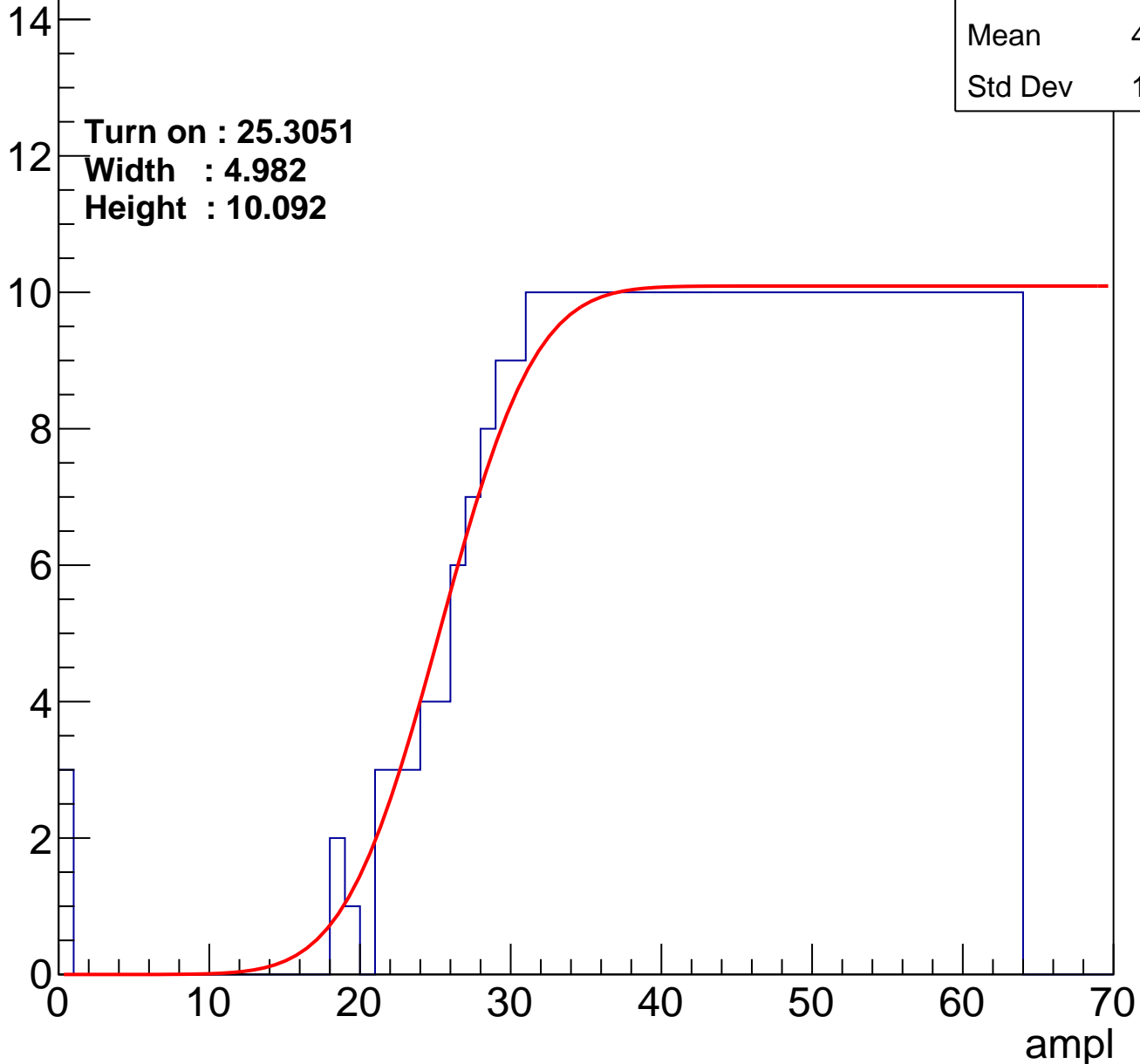
Entries	392
Mean	43.52
Std Dev	12.14

Turn on : 25.3051

Width : 4.982

Height : 10.092

Entry



# B1L103S, U21-ch59

calib\_packv5\_042523\_0143.root, FC#7, port C2

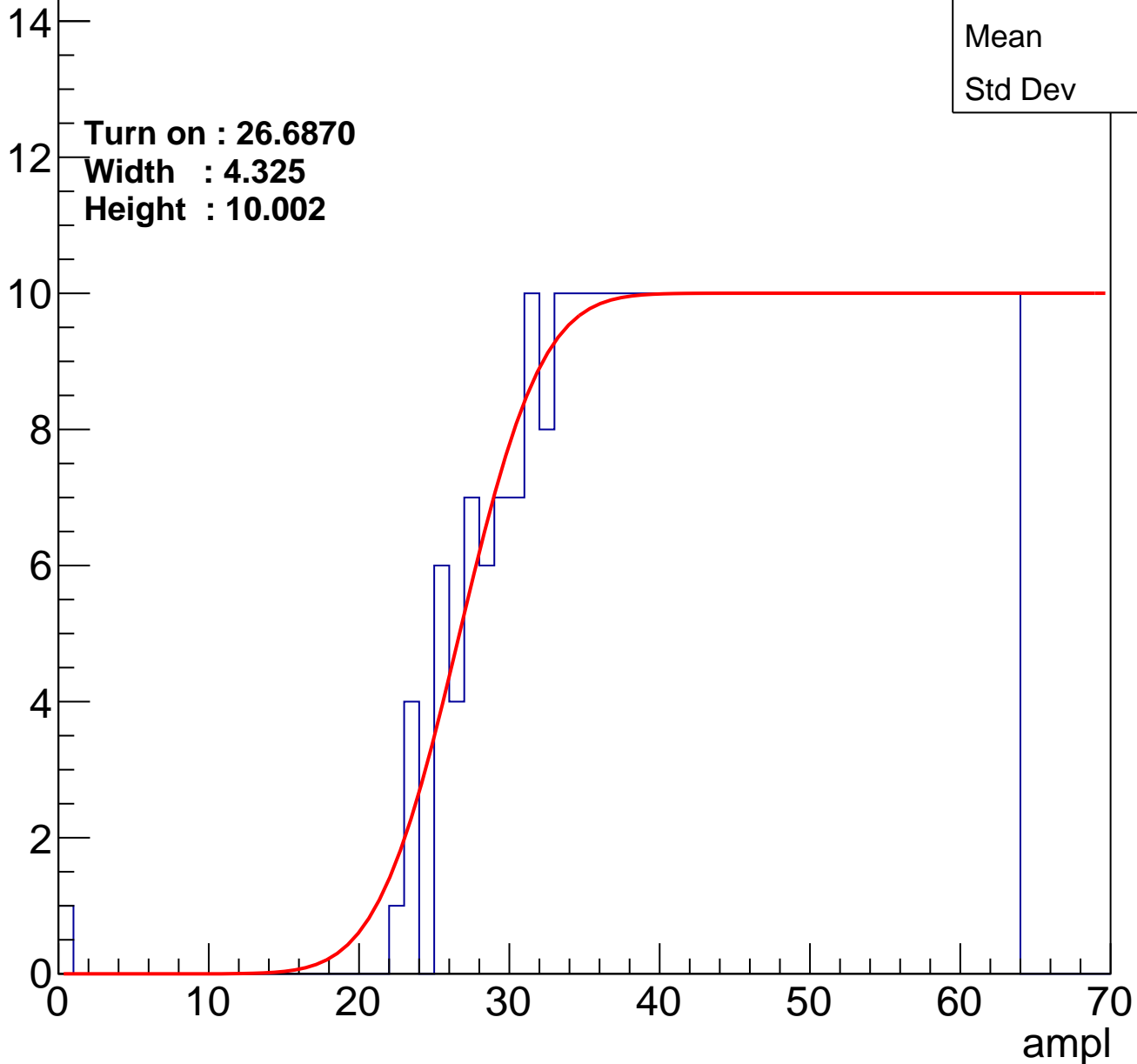
Entries	371
Mean	44.7
Std Dev	11.2

Turn on : 26.6870

Width : 4.325

Height : 10.002

Entry



# B1L103S, U21-ch60

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	393
Mean	43.62
Std Dev	11.87

Turn on : 25.5847

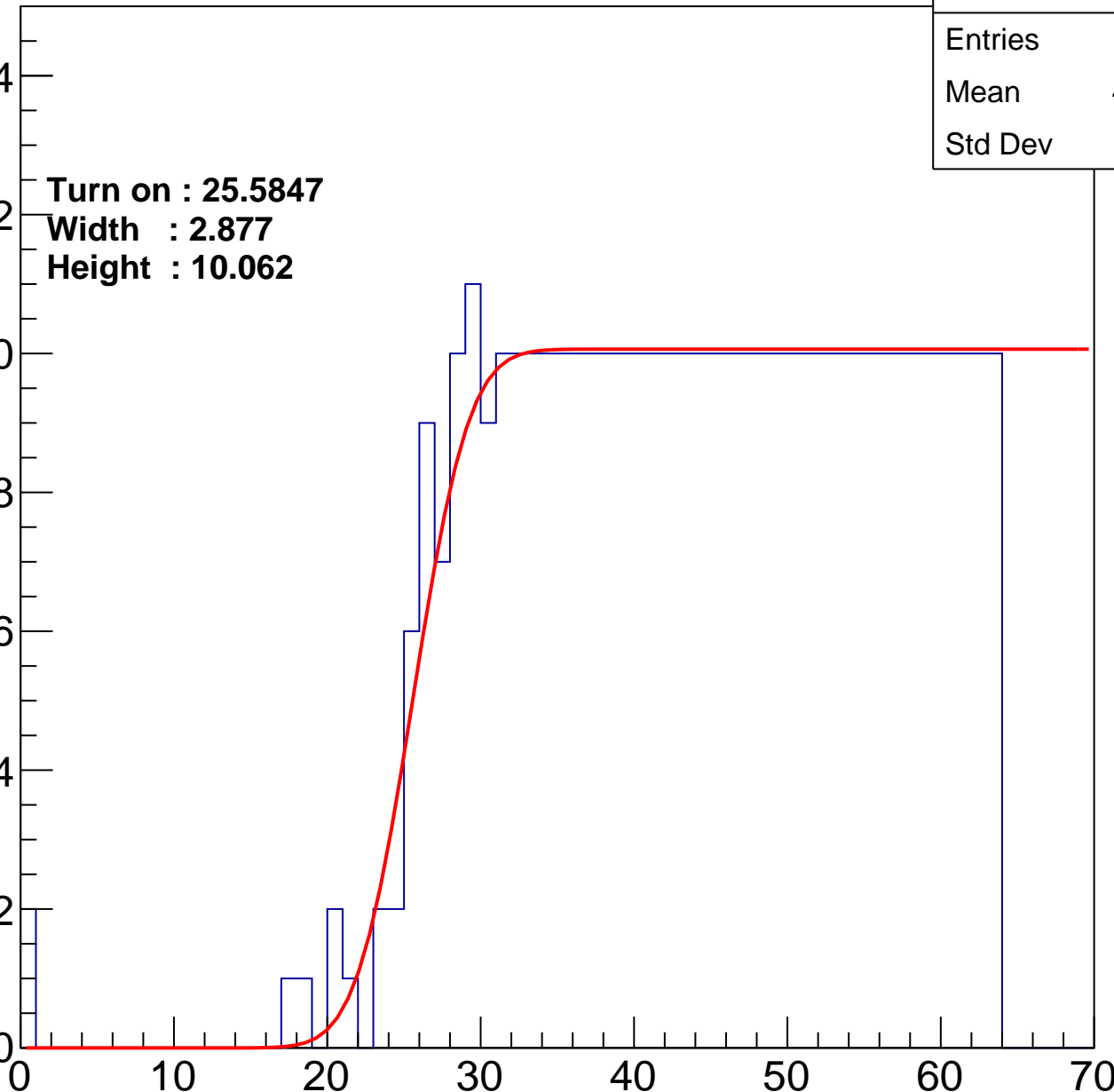
Width : 2.877

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch61

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	389
Mean	43.78
Std Dev	12.29

Turn on : 26.4677

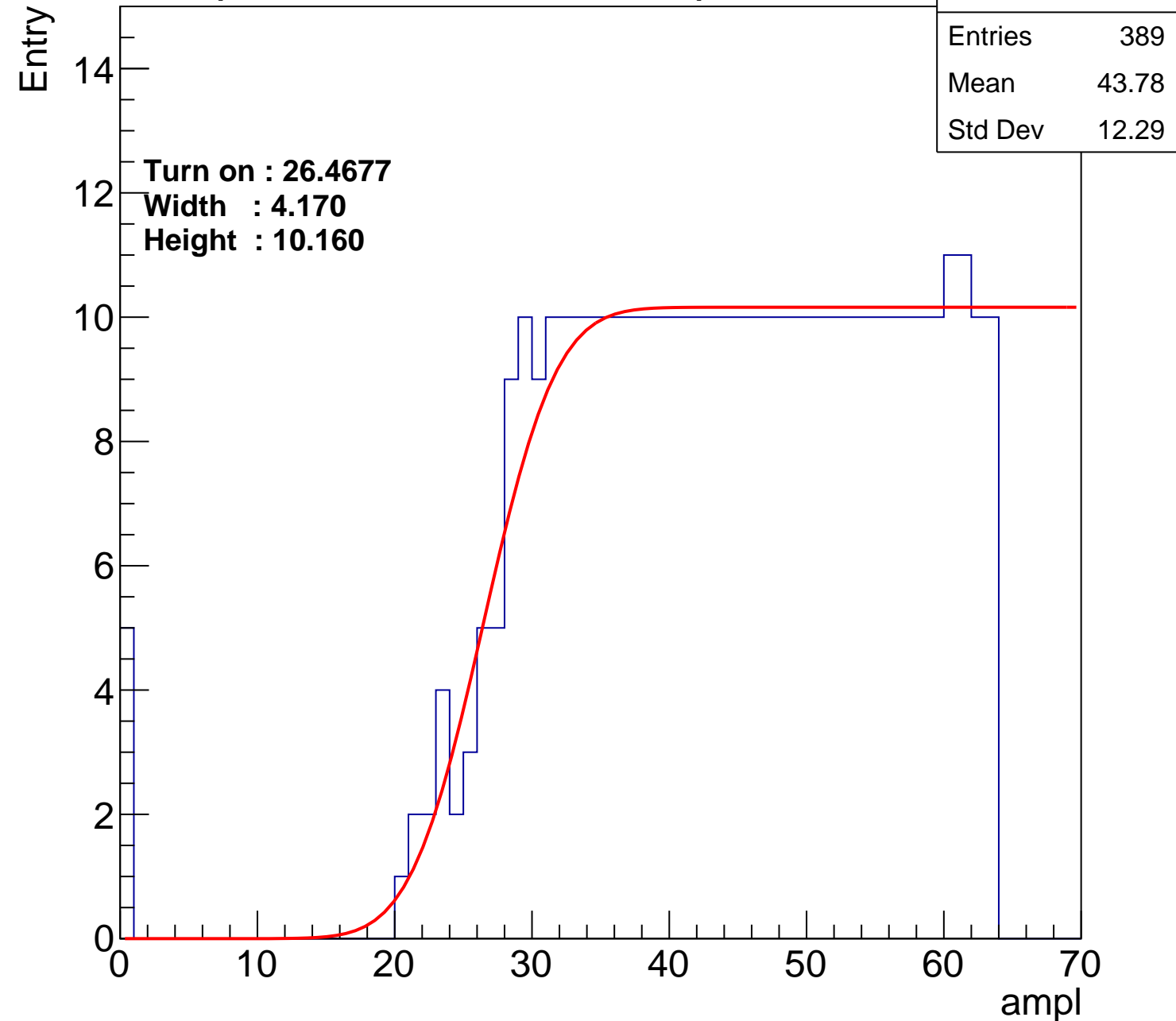
Width : 4.170

Height : 10.160

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch62

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	374
Mean	44.53
Std Dev	11.41

Turn on : 27.3603

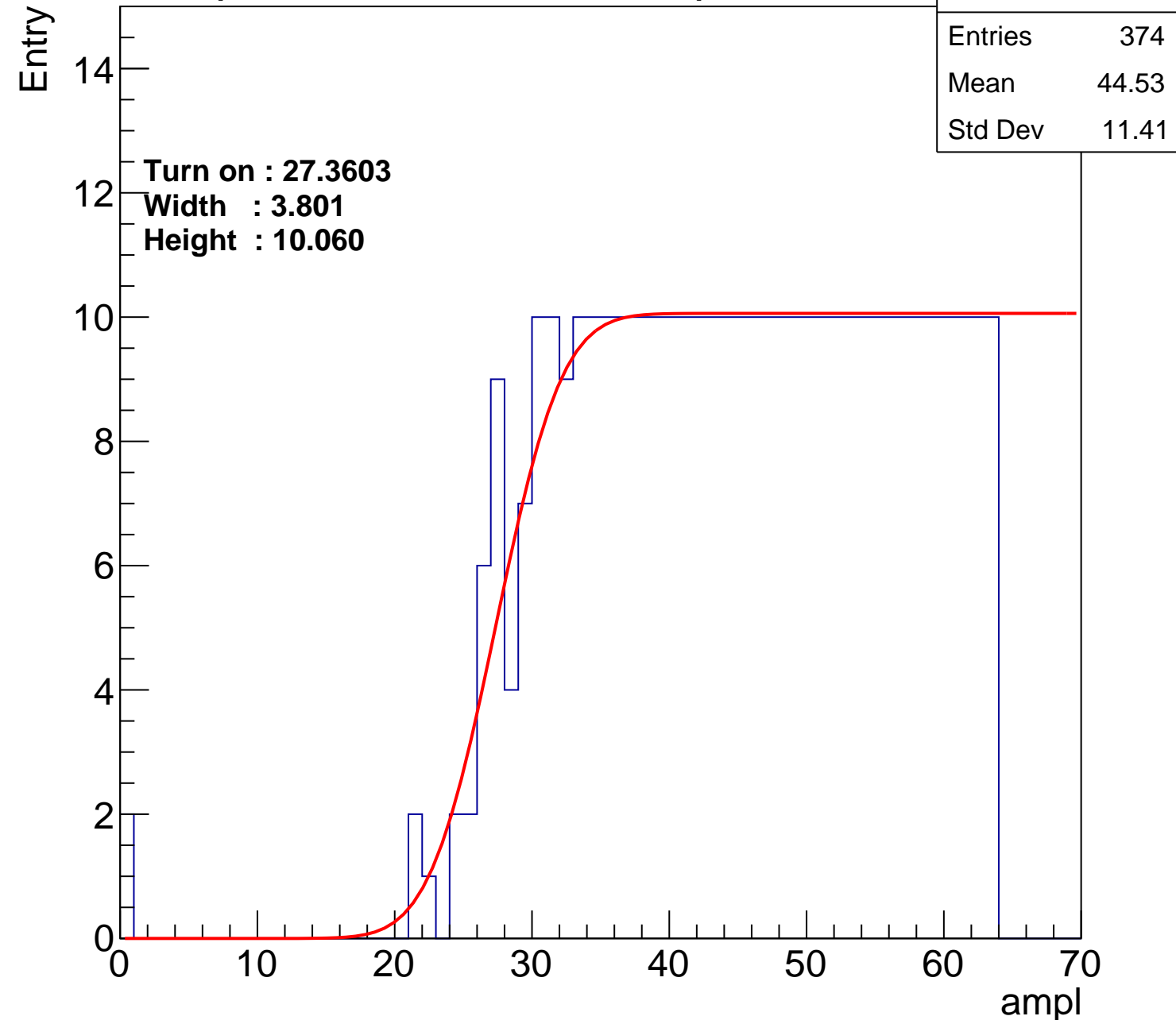
Width : 3.801

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch63

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	380
Mean	44.19
Std Dev	11.7

Turn on : 27.0145

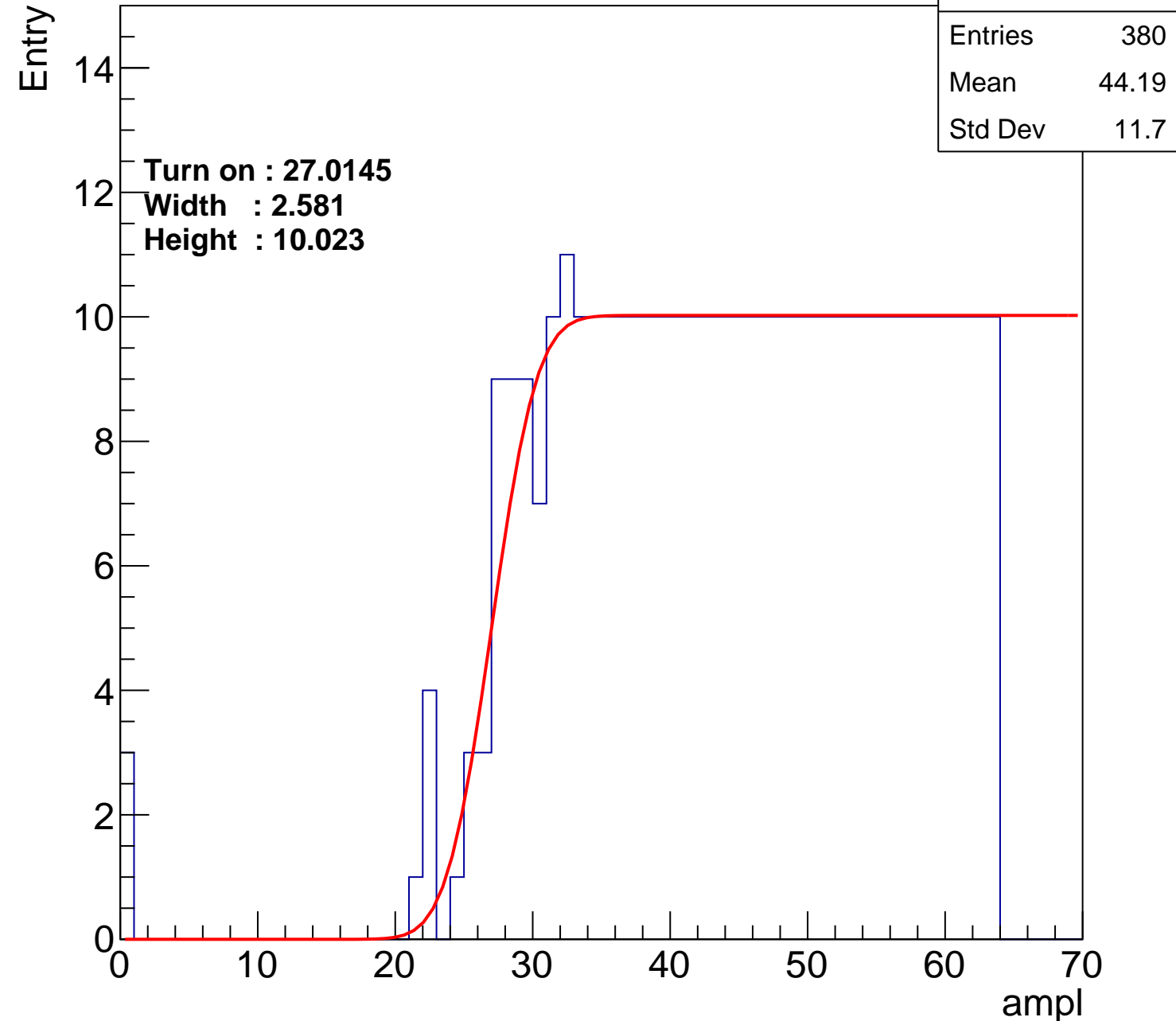
Width : 2.581

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch64

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	390
Mean	43.73
Std Dev	11.96

Turn on : 25.6657

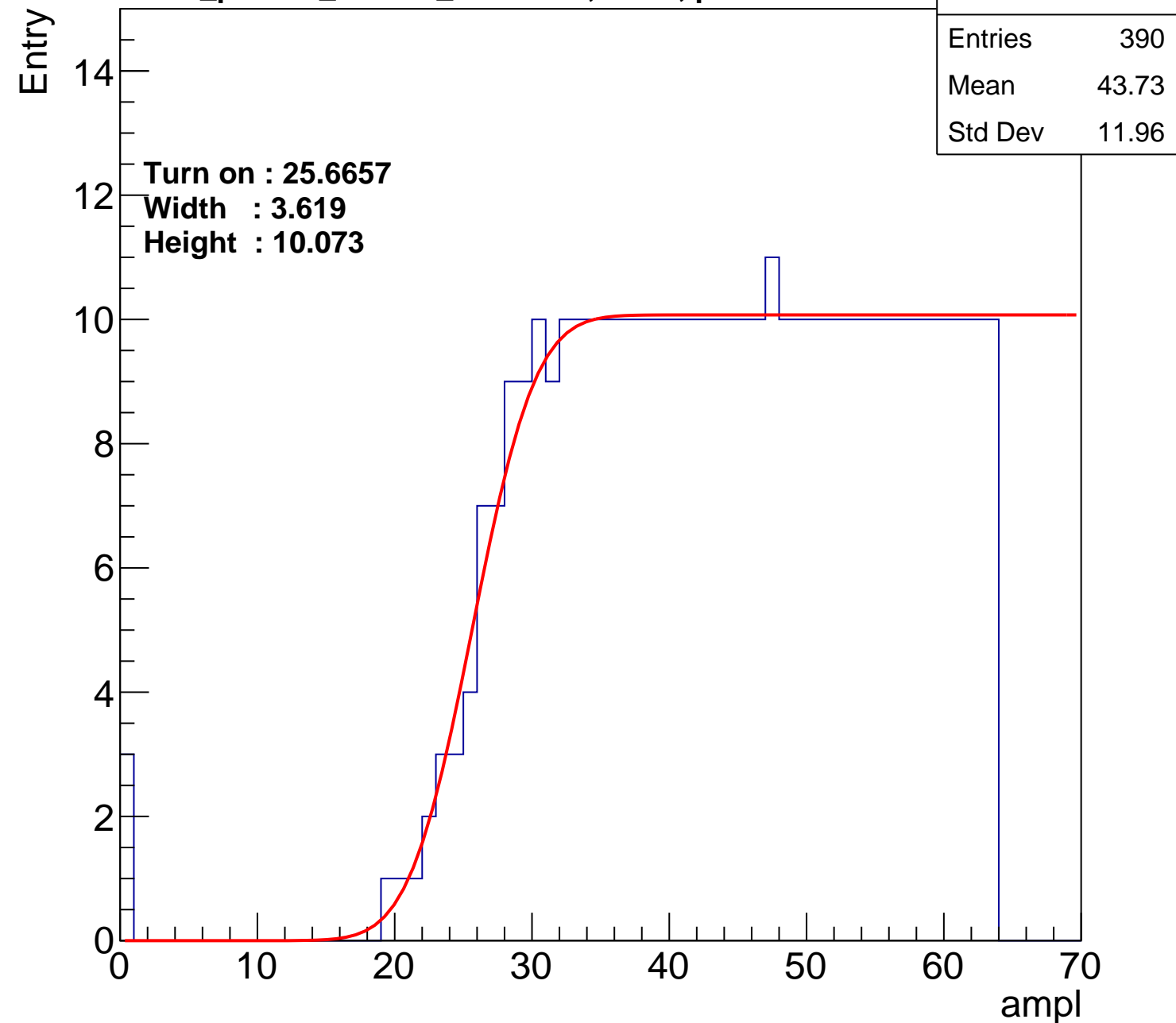
Width : 3.619

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch65

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.62
Std Dev	11.21

Turn on : 27.2057

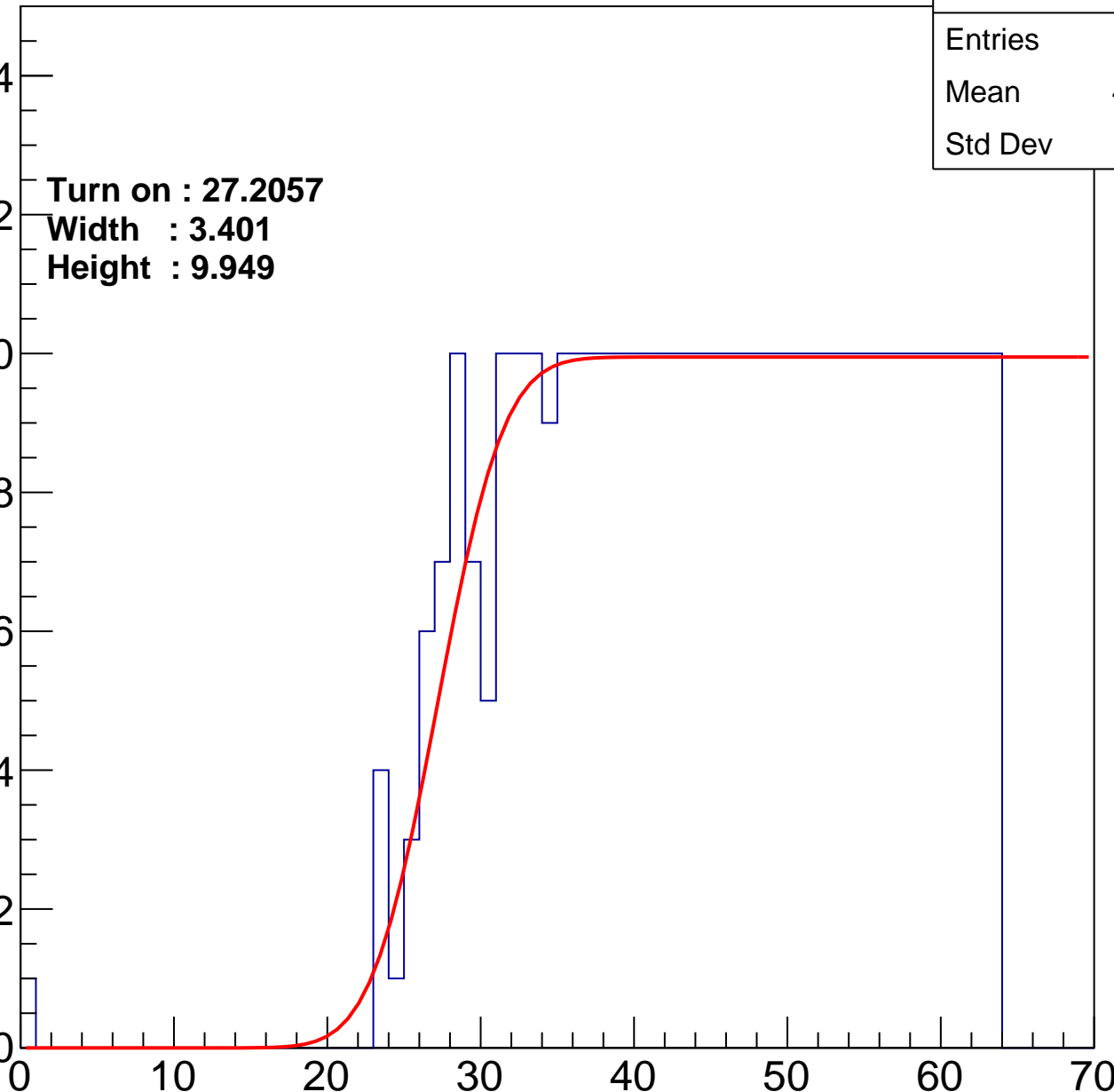
Width : 3.401

Height : 9.949

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch66

calib\_packv5\_042523\_0143.root, FC#7, port C2

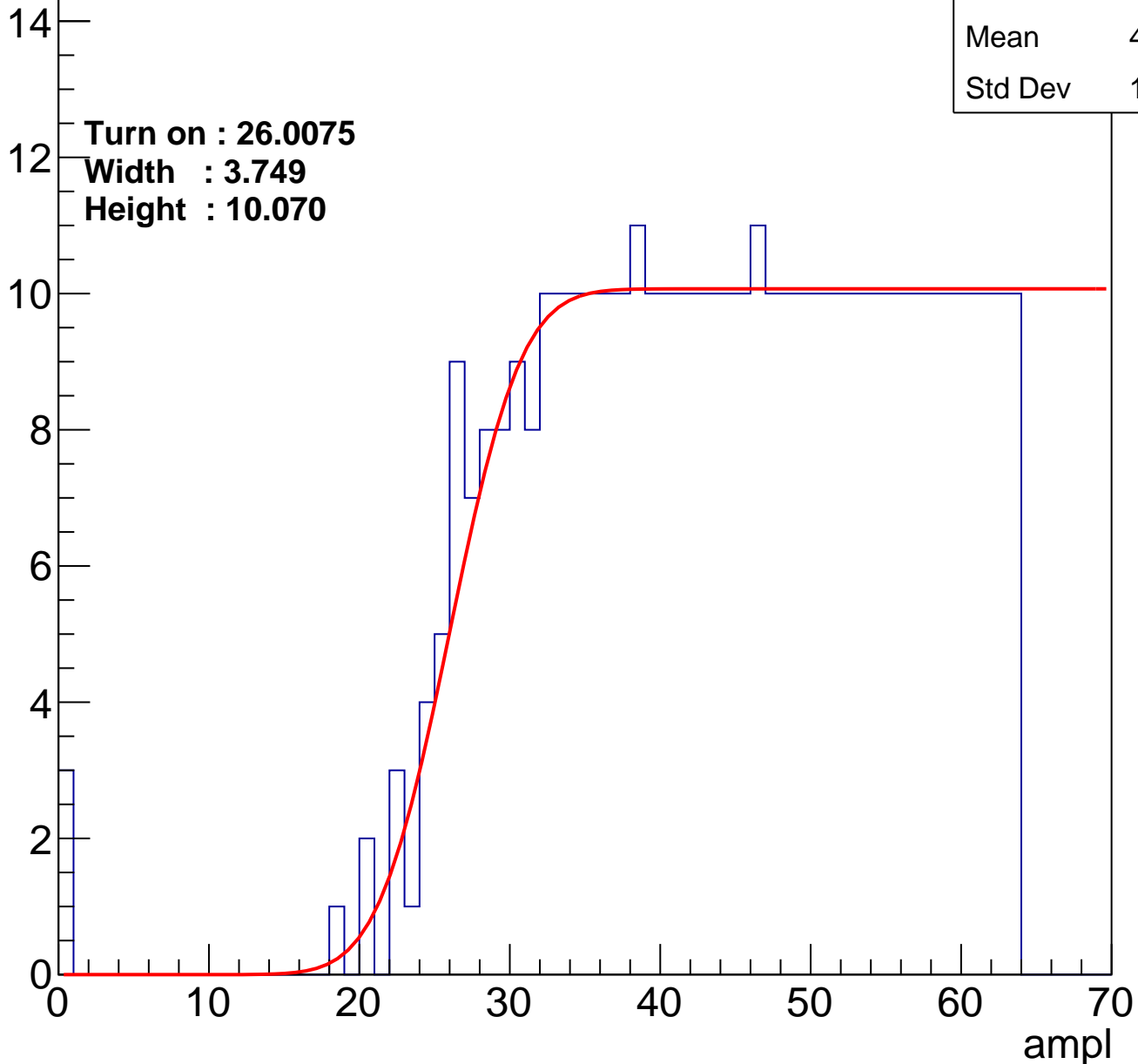
Entries	390
Mean	43.72
Std Dev	11.99

Turn on : 26.0075

Width : 3.749

Height : 10.070

Entry



# B1L103S, U21-ch67

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.53
Std Dev	11.59

Turn on : 28.0514

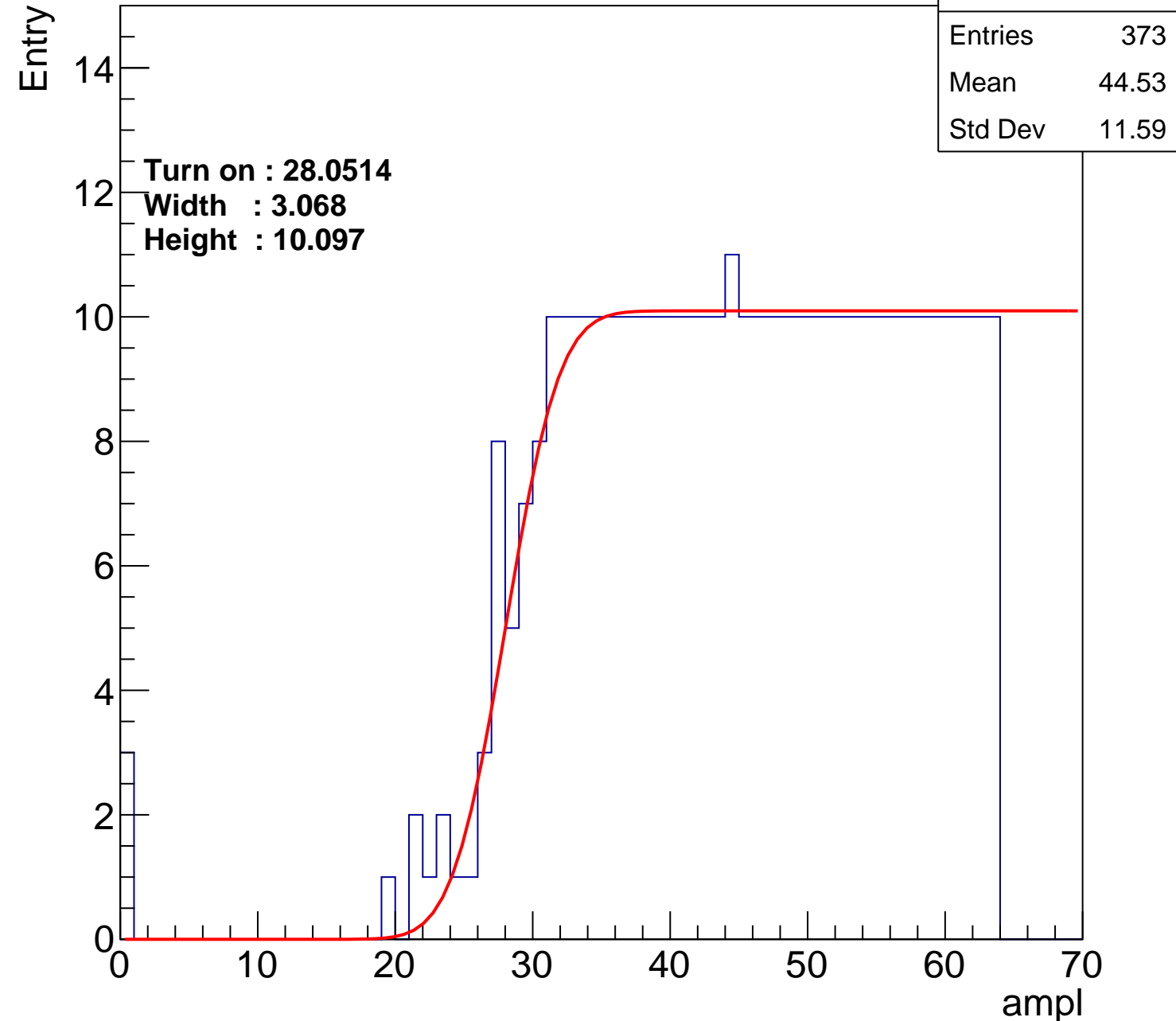
Width : 3.068

Height : 10.097

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

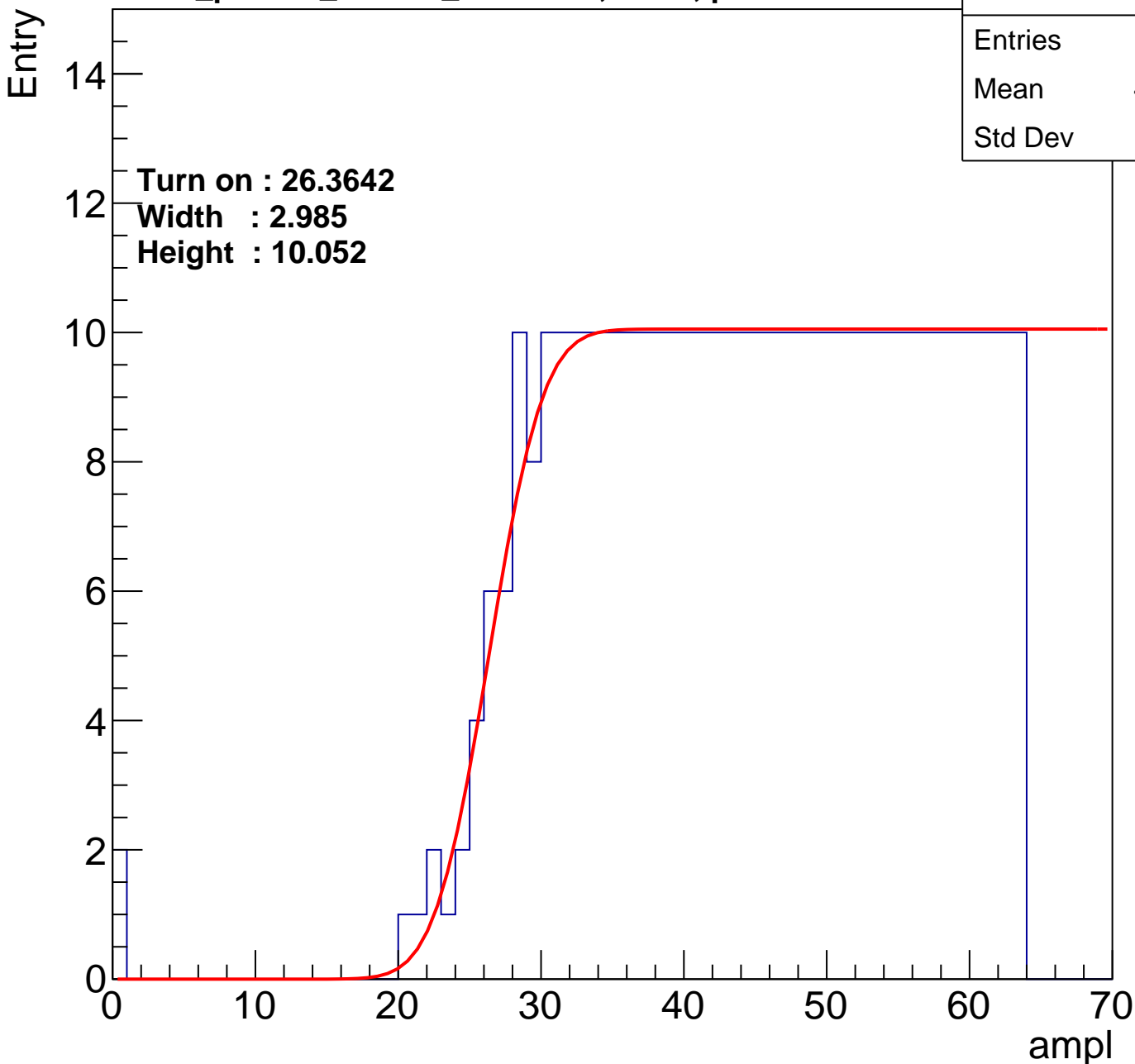


**calib\_packv5\_042523\_0143.root, FC#7, port C2**

**calib\_packv5\_042523\_0143.root, FC#7, port C2**

Entries	383
Mean	44.11
Std Dev	11.6

**Height : 10.052**



# B1L103S, U21-ch69

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	360
Mean	45.14
Std Dev	11.26

Turn on : 27.6835

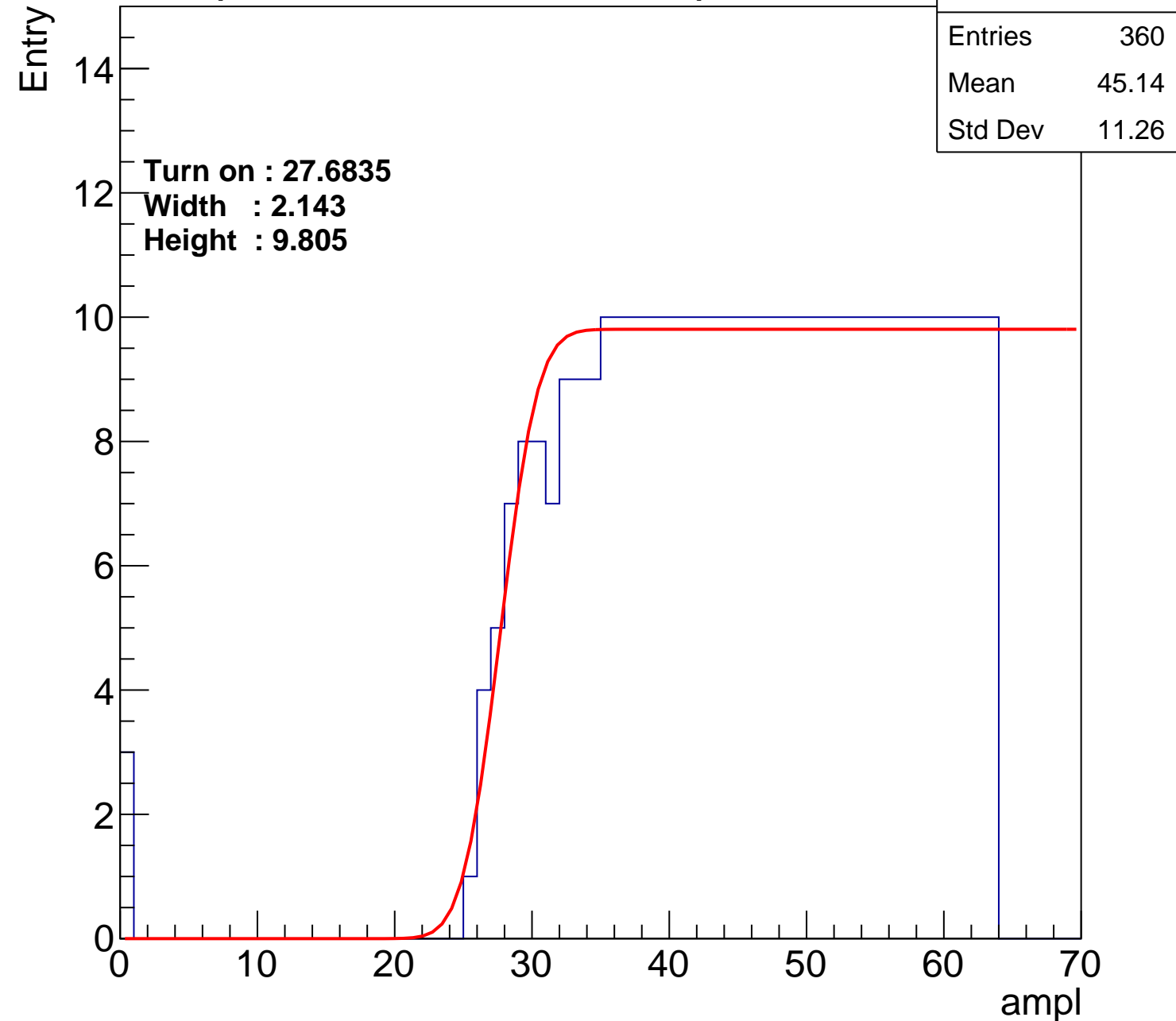
Width : 2.143

Height : 9.805

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch70

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	376
Mean	44.24
Std Dev	11.93

Turn on : 27.5133

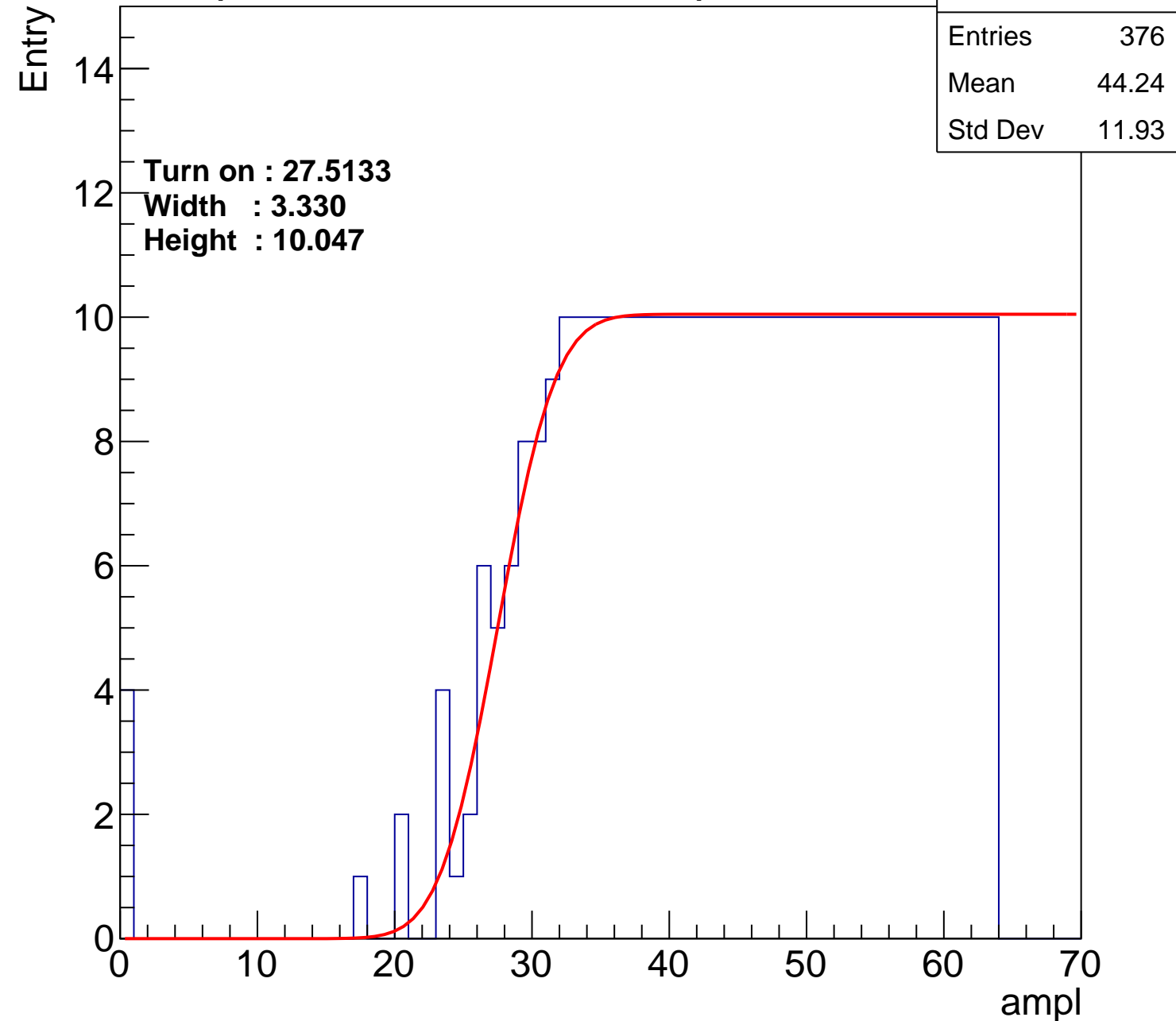
Width : 3.330

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch71

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	371
Mean	44.72
Std Dev	11.37

Turn on : 27.4414

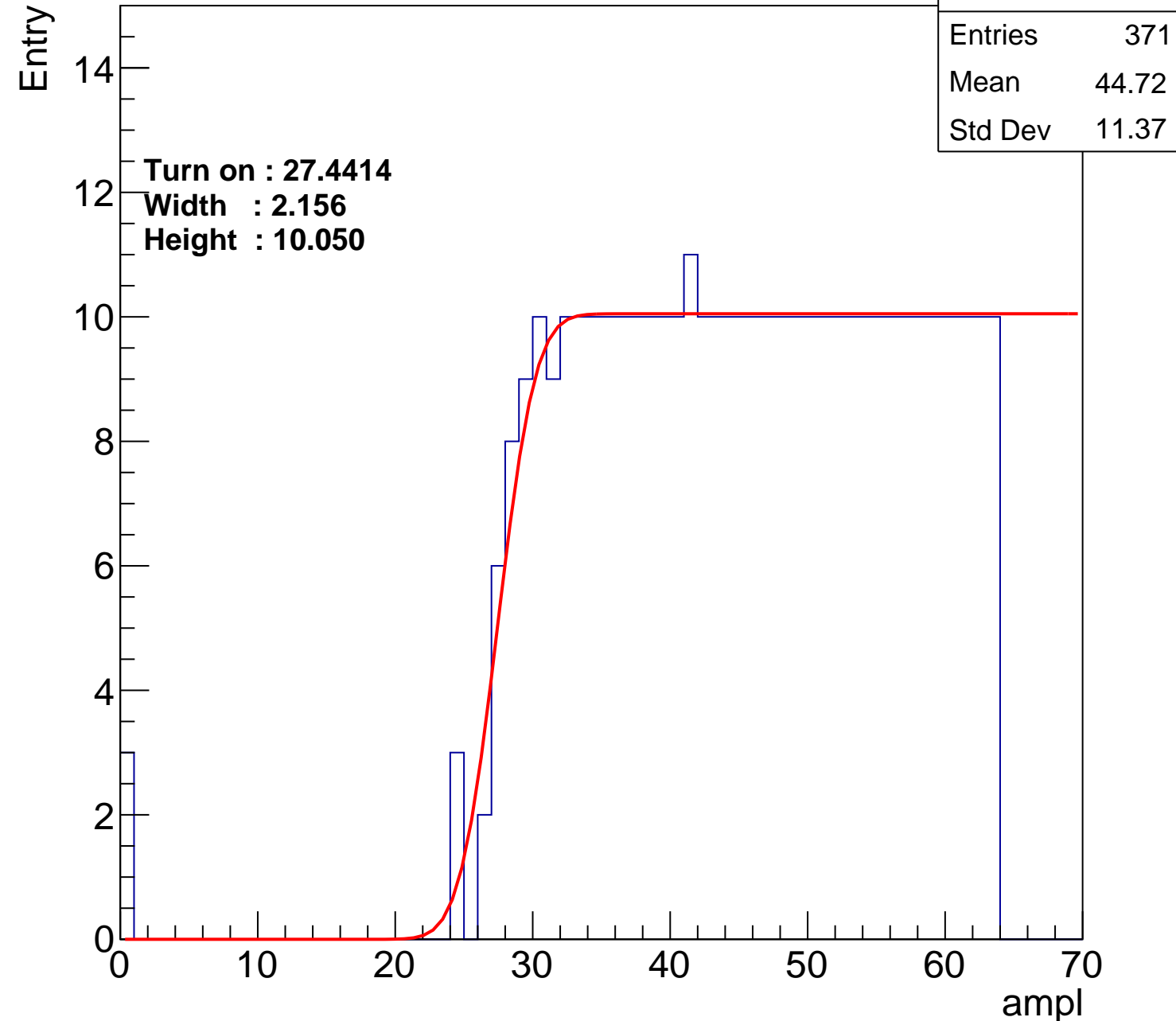
Width : 2.156

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch72

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	372
Mean	44.63
Std Dev	11.43

Turn on : 27.0268

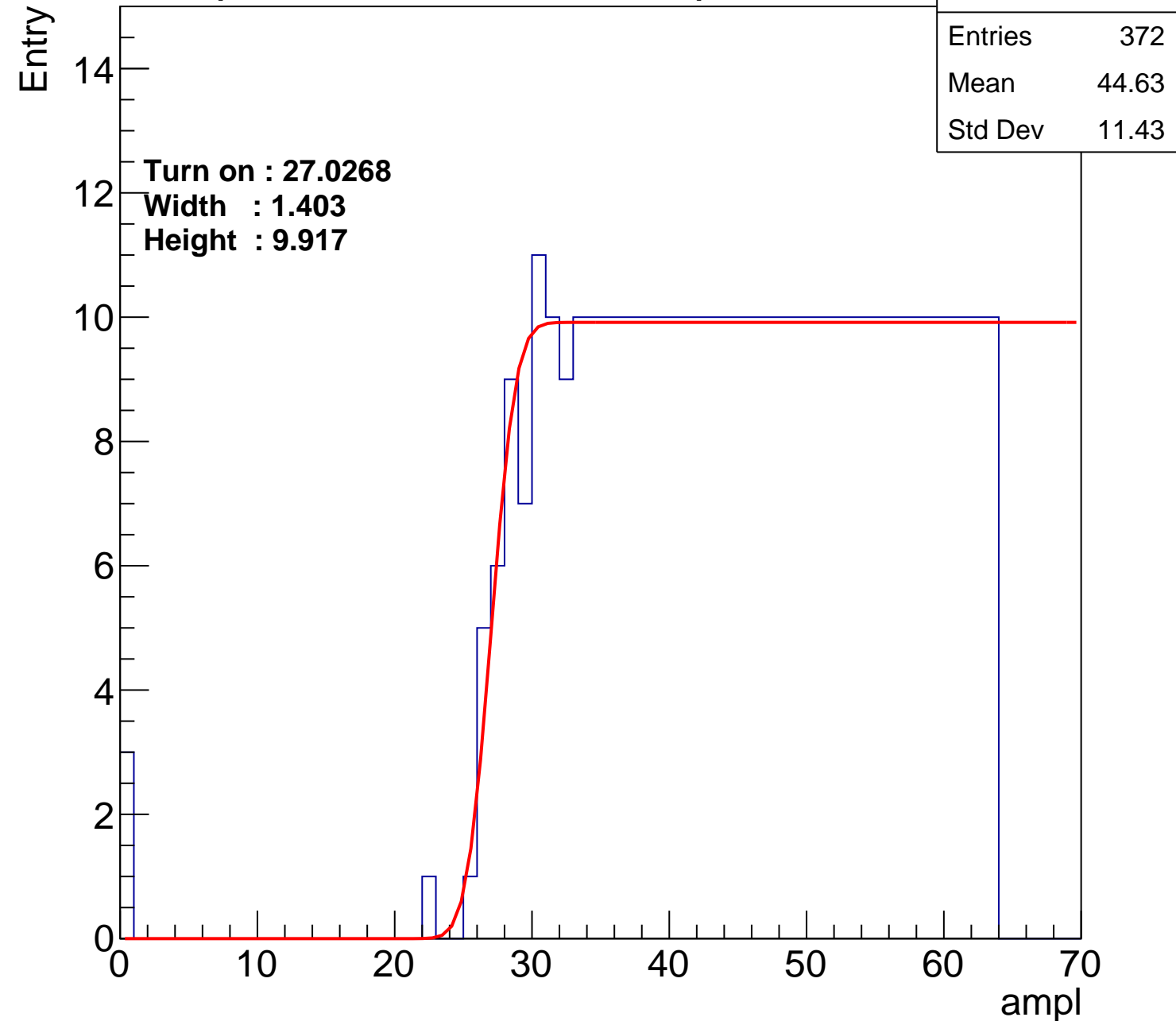
Width : 1.403

Height : 9.917

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch73

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	370
Mean	44.81
Std Dev	11.08

Turn on : 27.3455

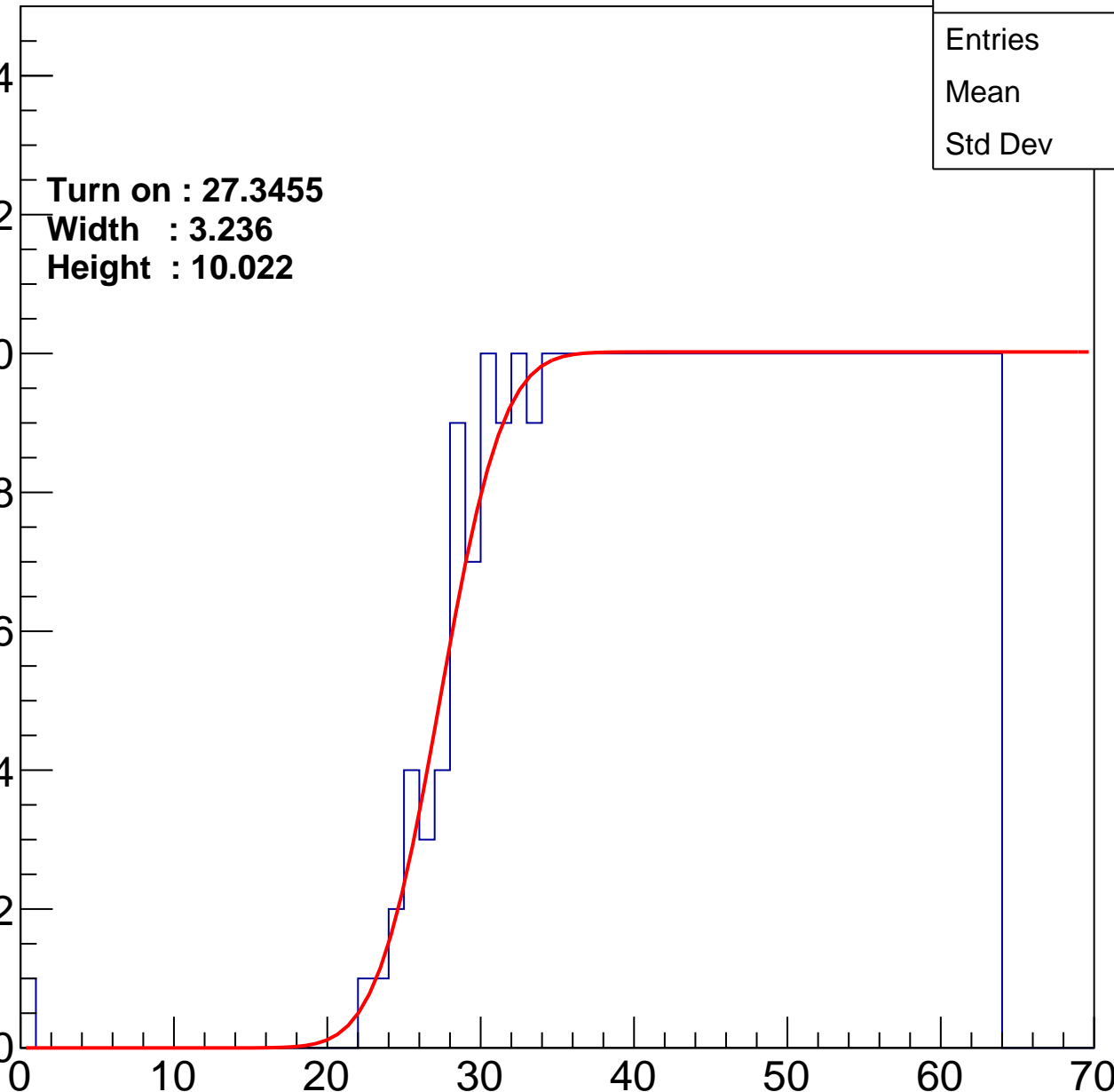
Width : 3.236

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch74

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.64
Std Dev	11.2

Turn on : 27.1876

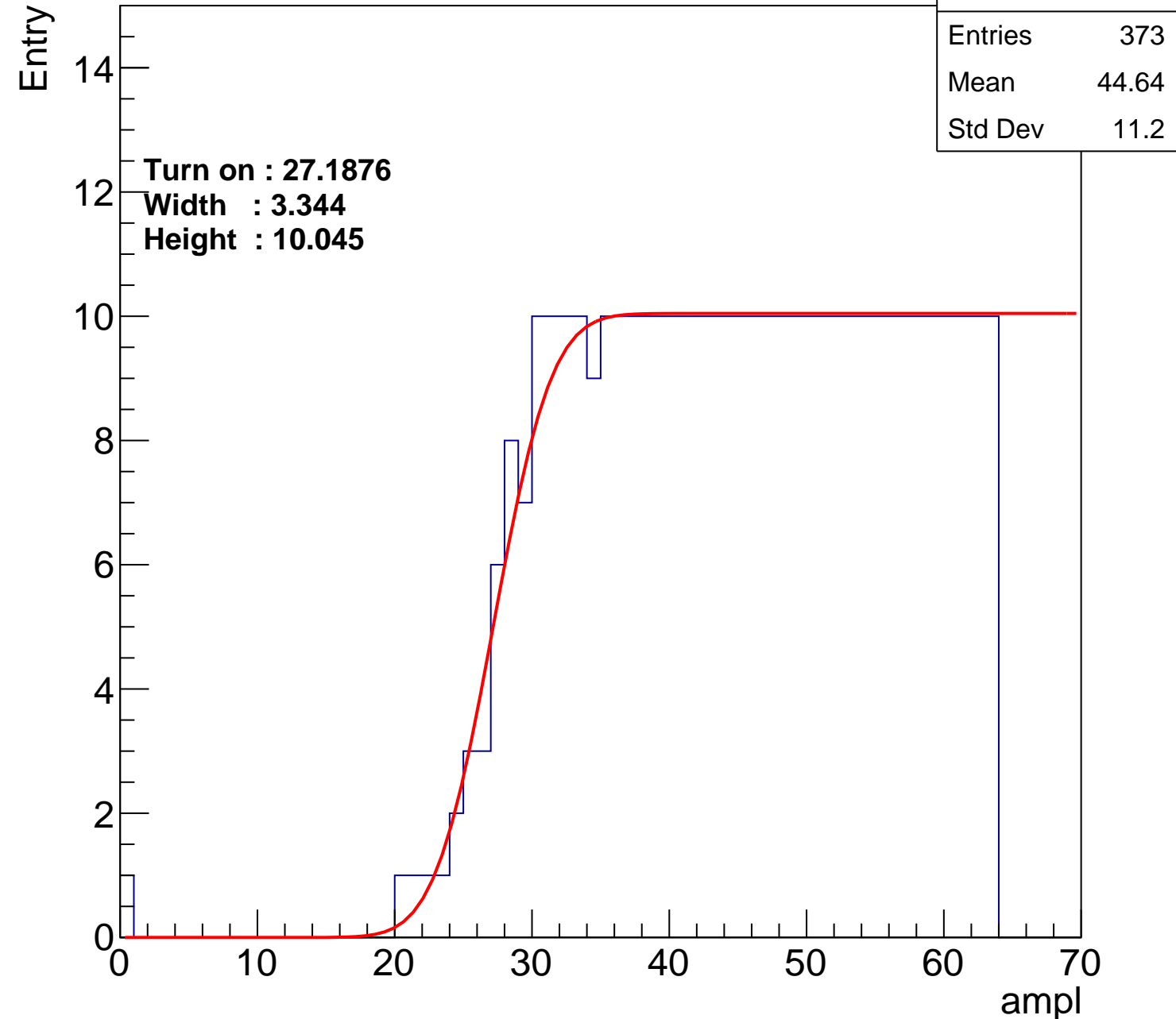
Width : 3.344

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch75

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.0679

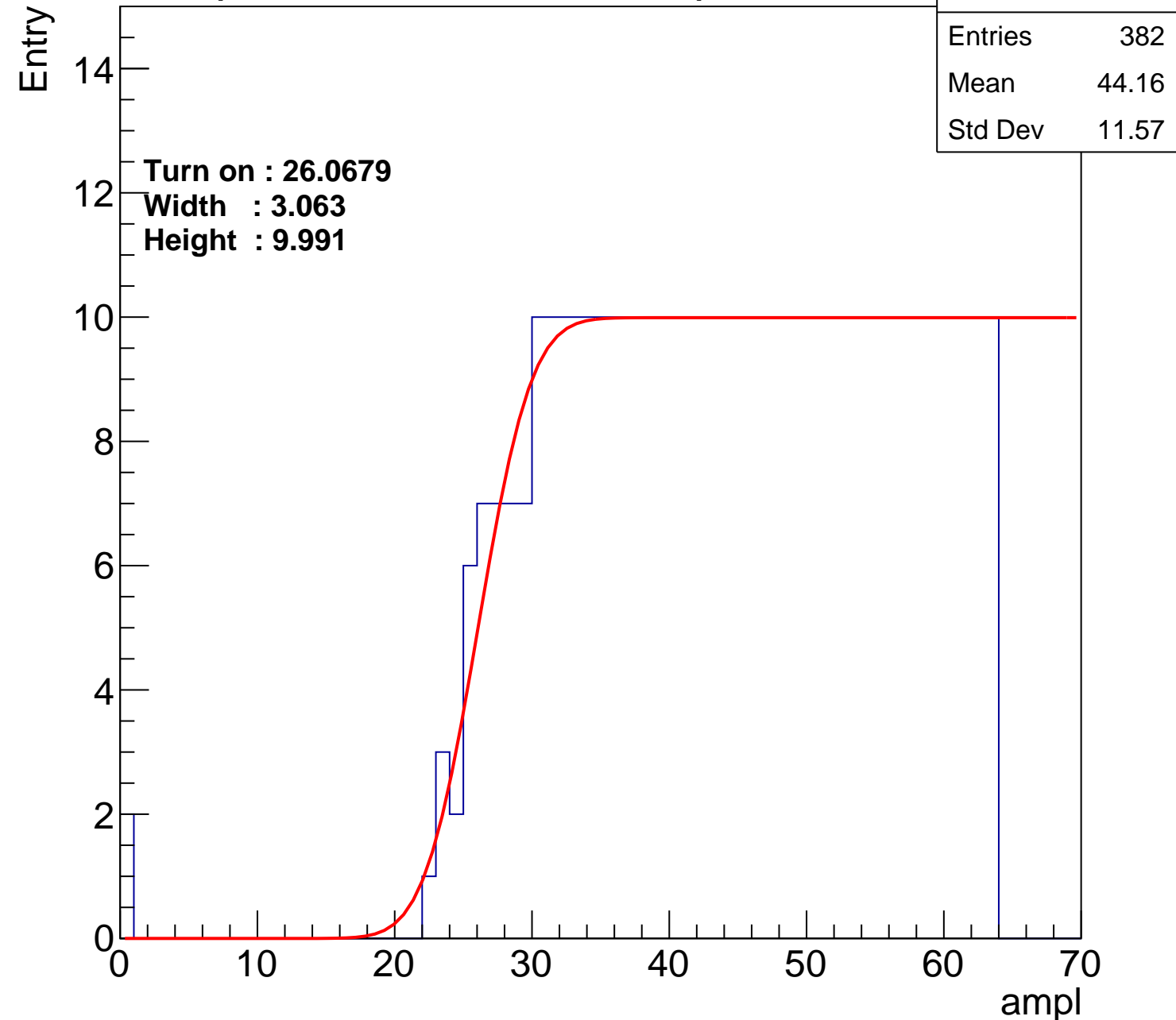
Width : 3.063

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch76

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	390
Mean	43.45
Std Dev	12.53

Turn on : 26.0122

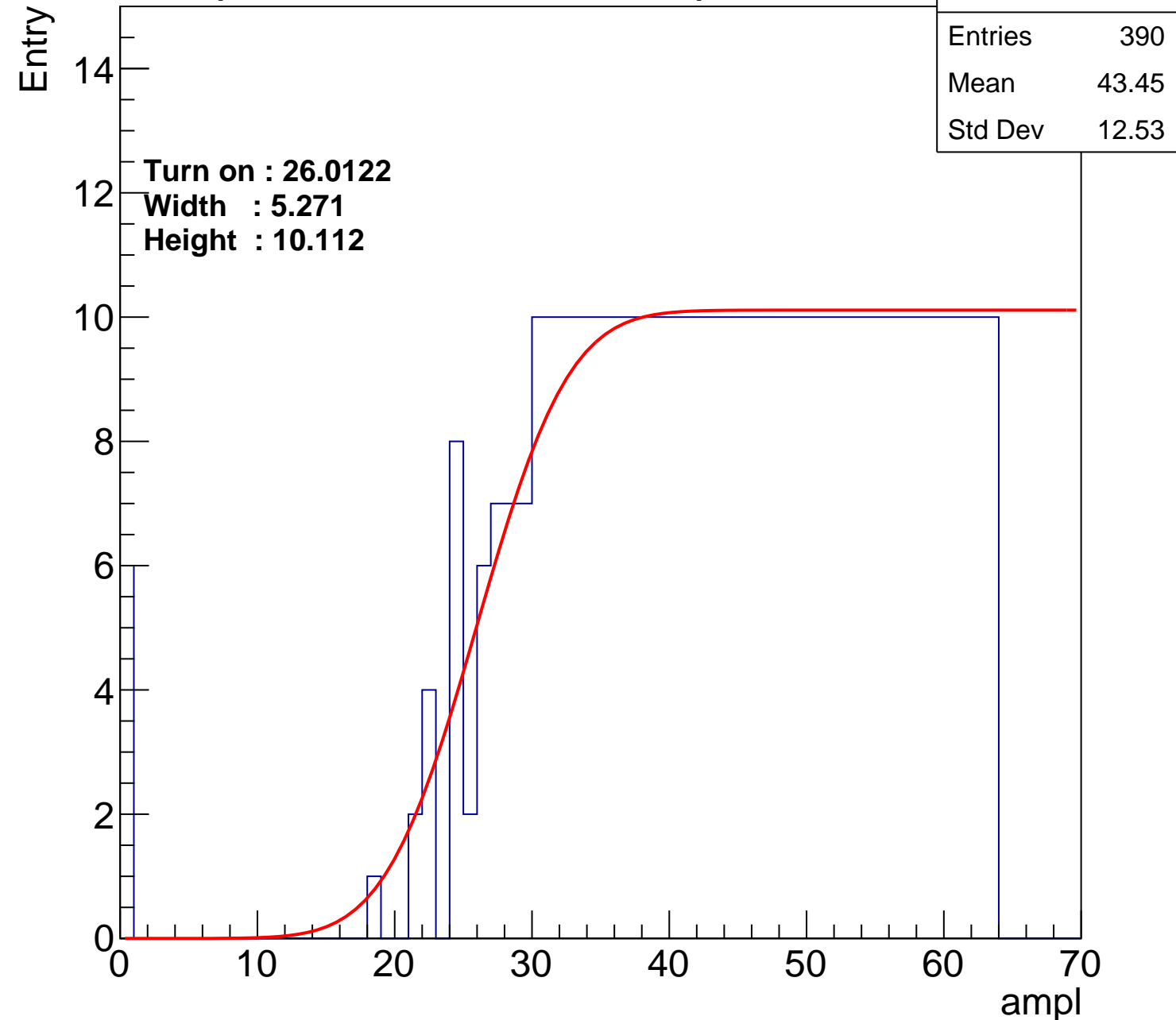
Width : 5.271

Height : 10.112

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch77

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.41
Std Dev	11.88

**Turn on : 28.1227**

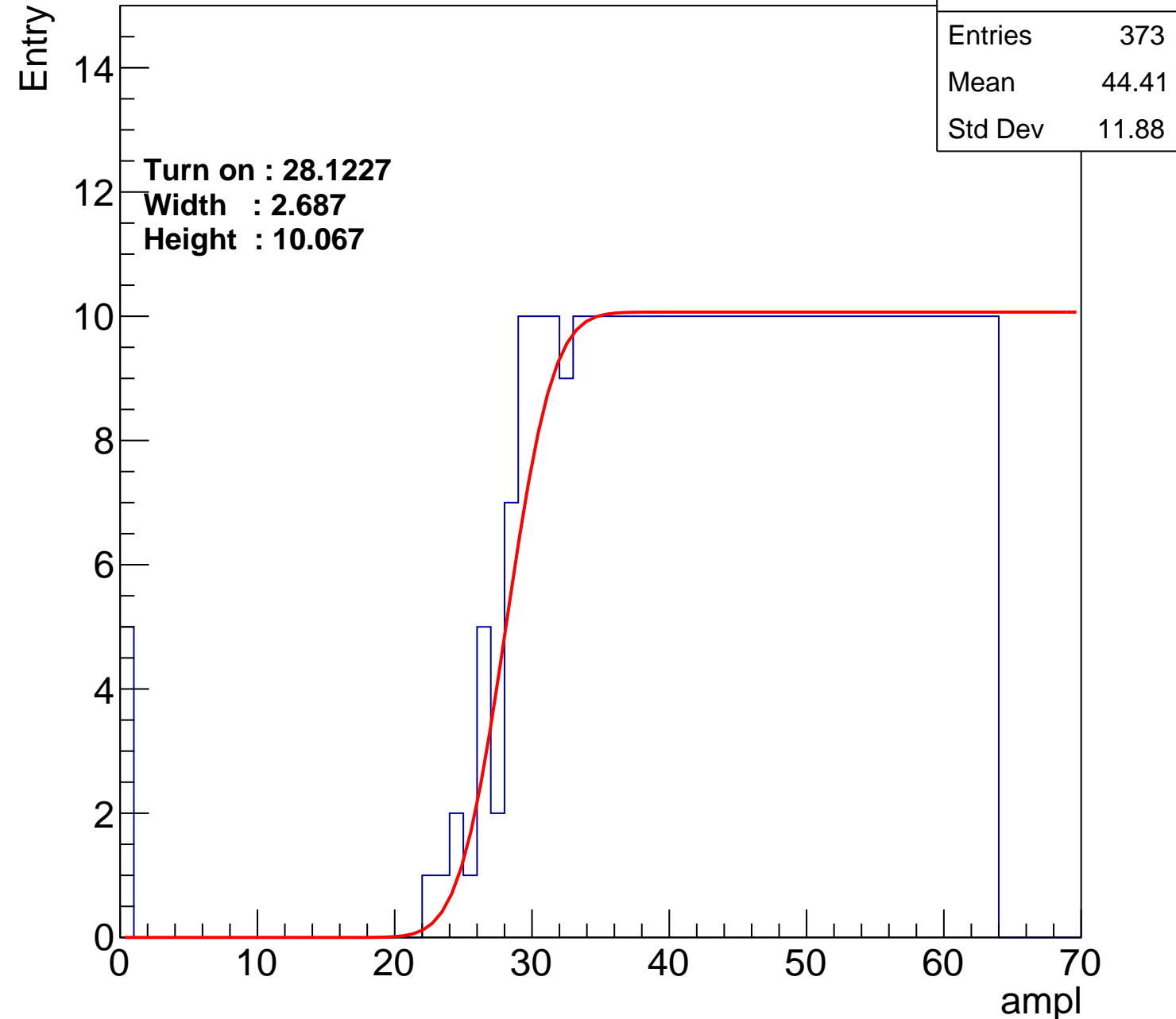
**Width : 2.687**

**Height : 10.067**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch78

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	381
Mean	44.09
Std Dev	11.81

Turn on : 27.1177

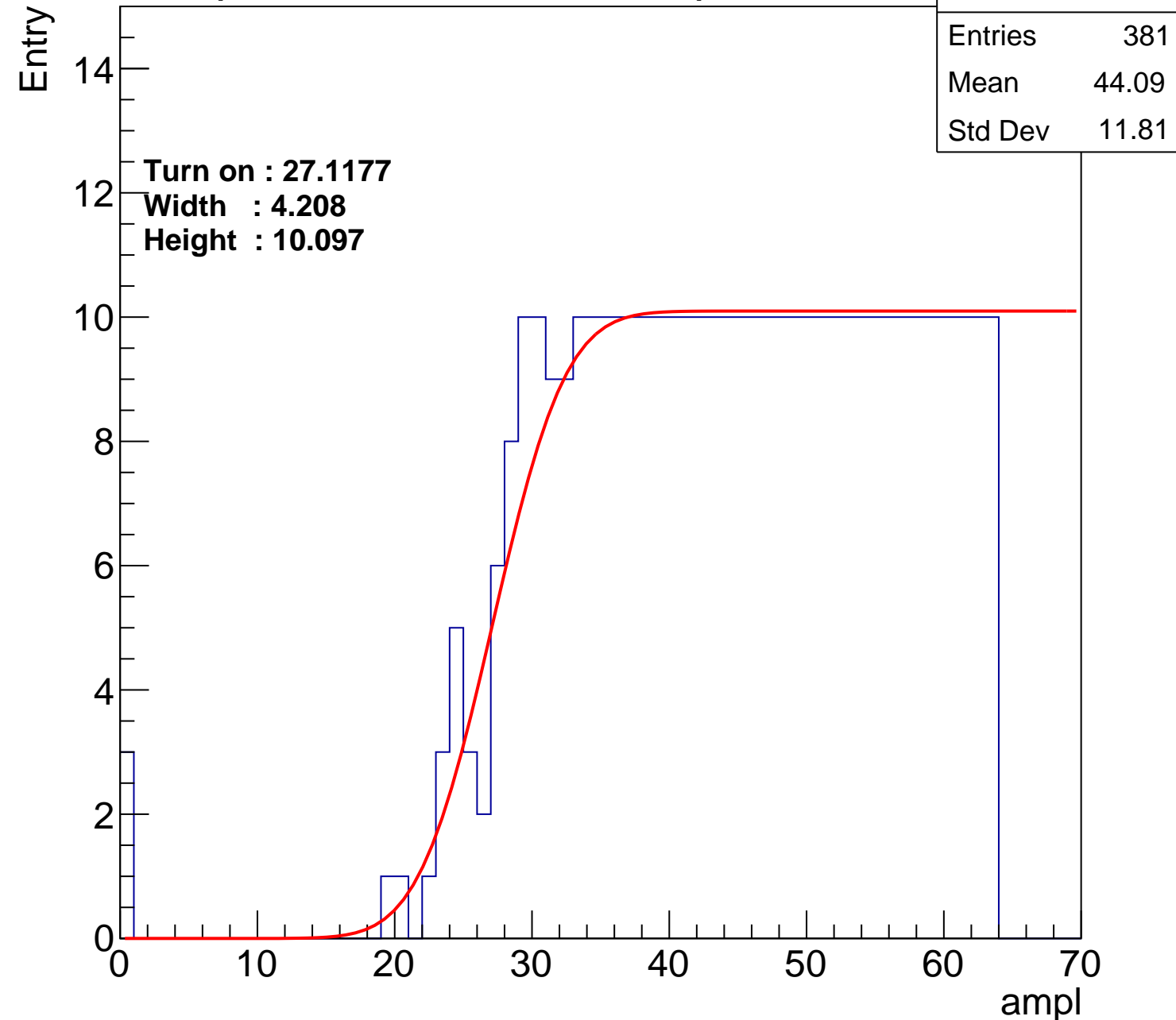
Width : 4.208

Height : 10.097

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch79

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	364
Mean	45.1
Std Dev	10.92

Turn on : 28.1334

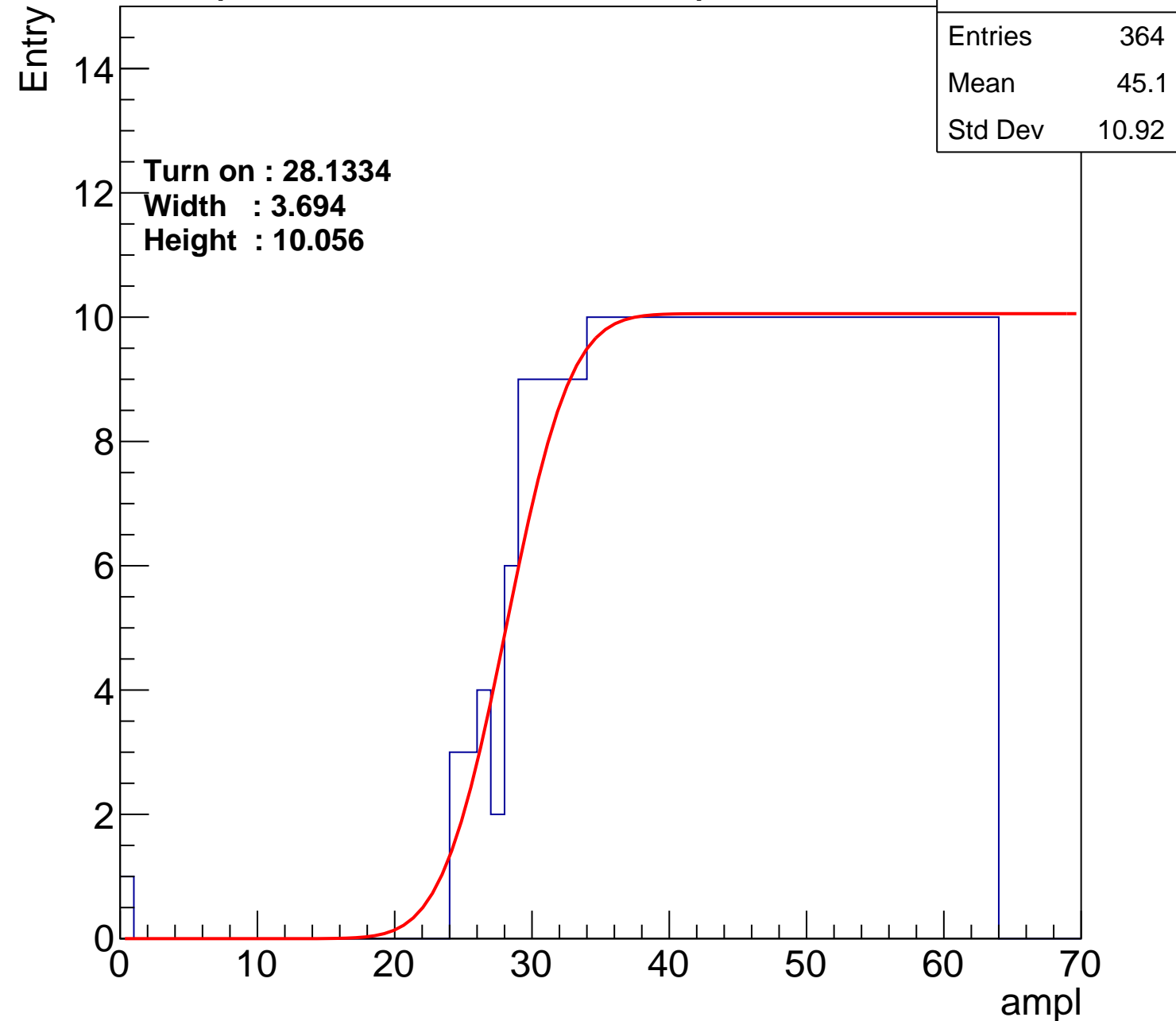
Width : 3.694

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch80

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	401
Mean	43.06
Std Dev	12.38

Turn on : 24.5913

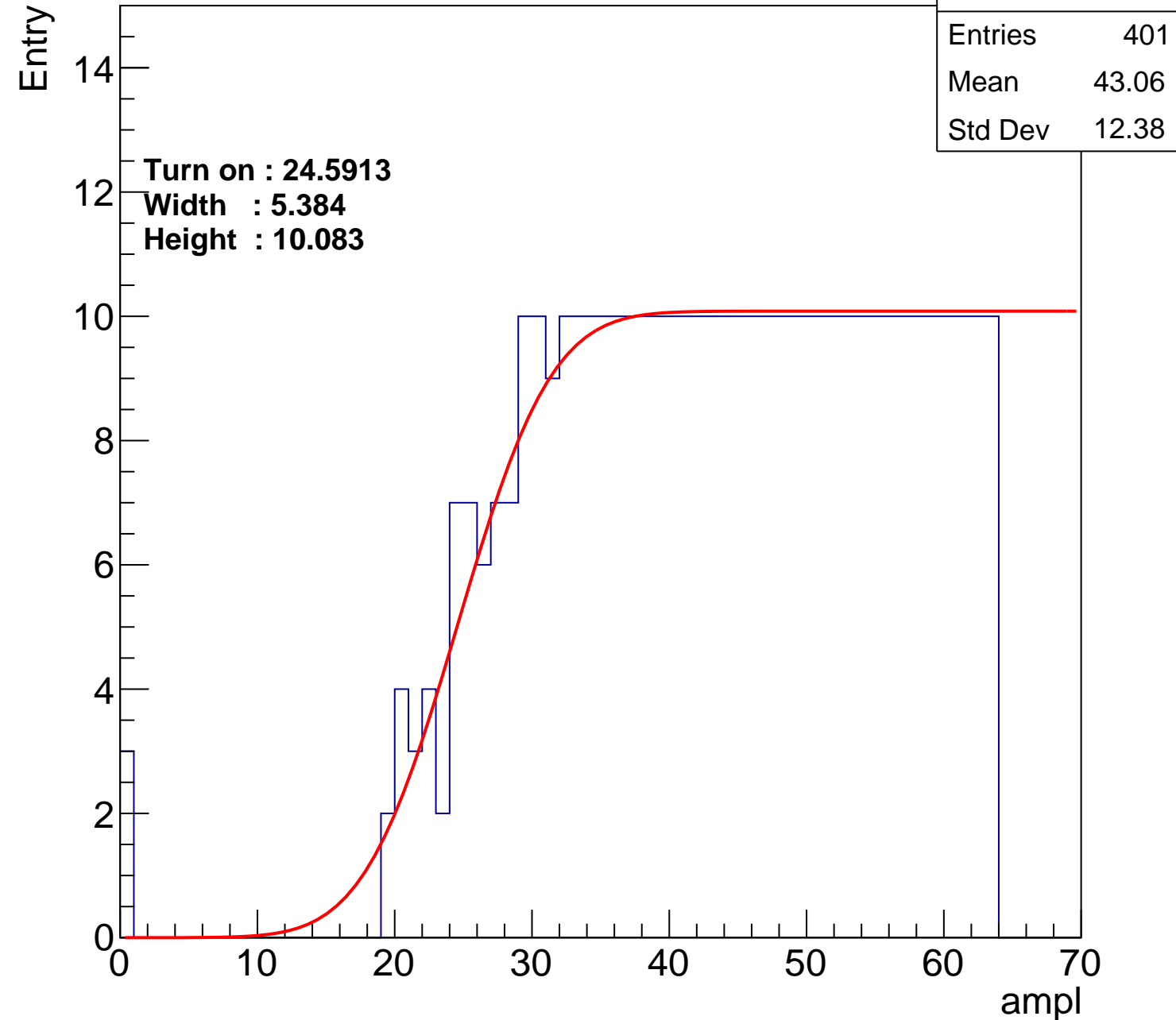
Width : 5.384

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch81

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	374
Mean	44.56
Std Dev	11.37

Turn on : 27.4972

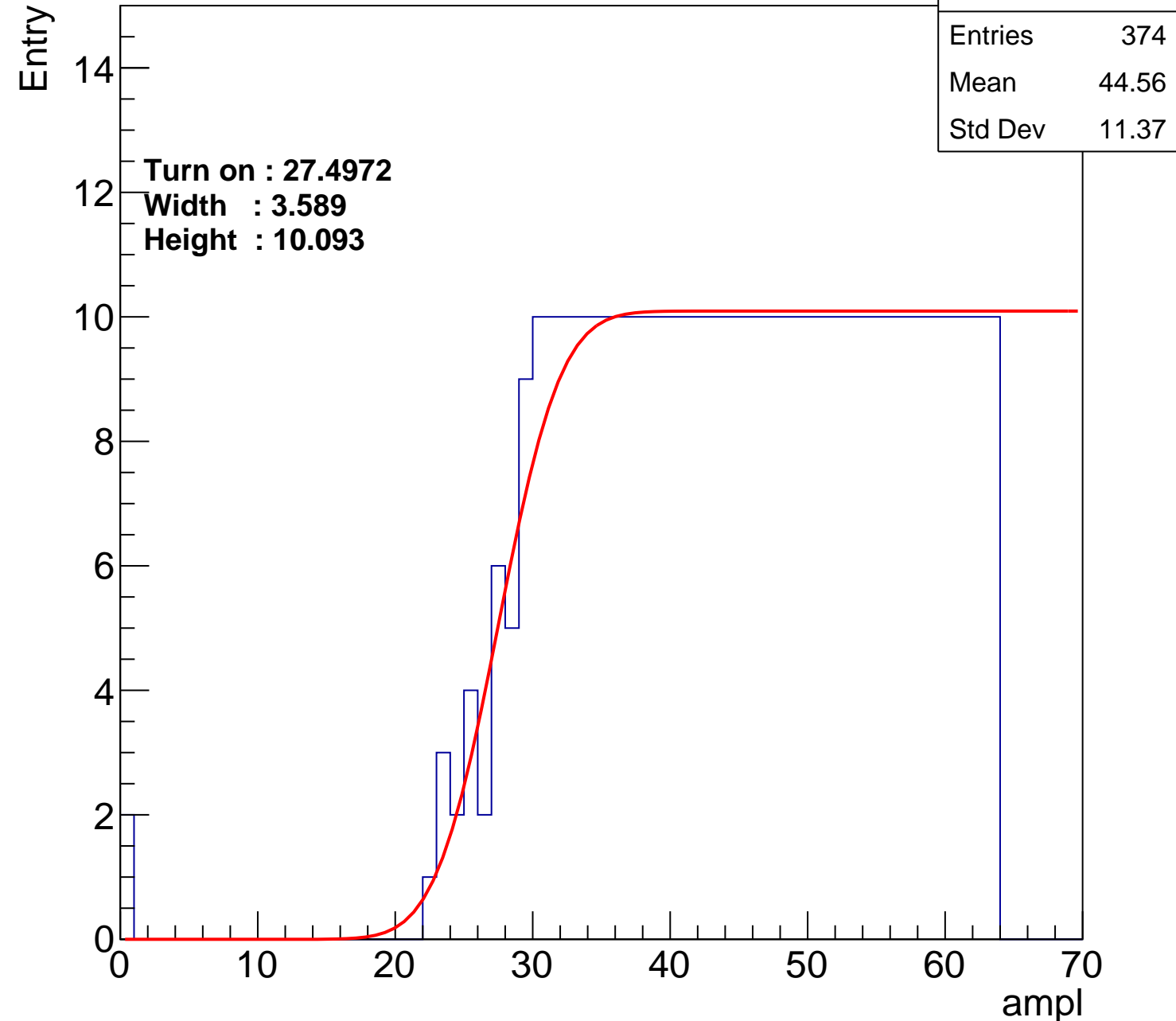
Width : 3.589

Height : 10.093

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch82

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	398
Mean	43.22
Std Dev	12.18

Turn on : 24.3894

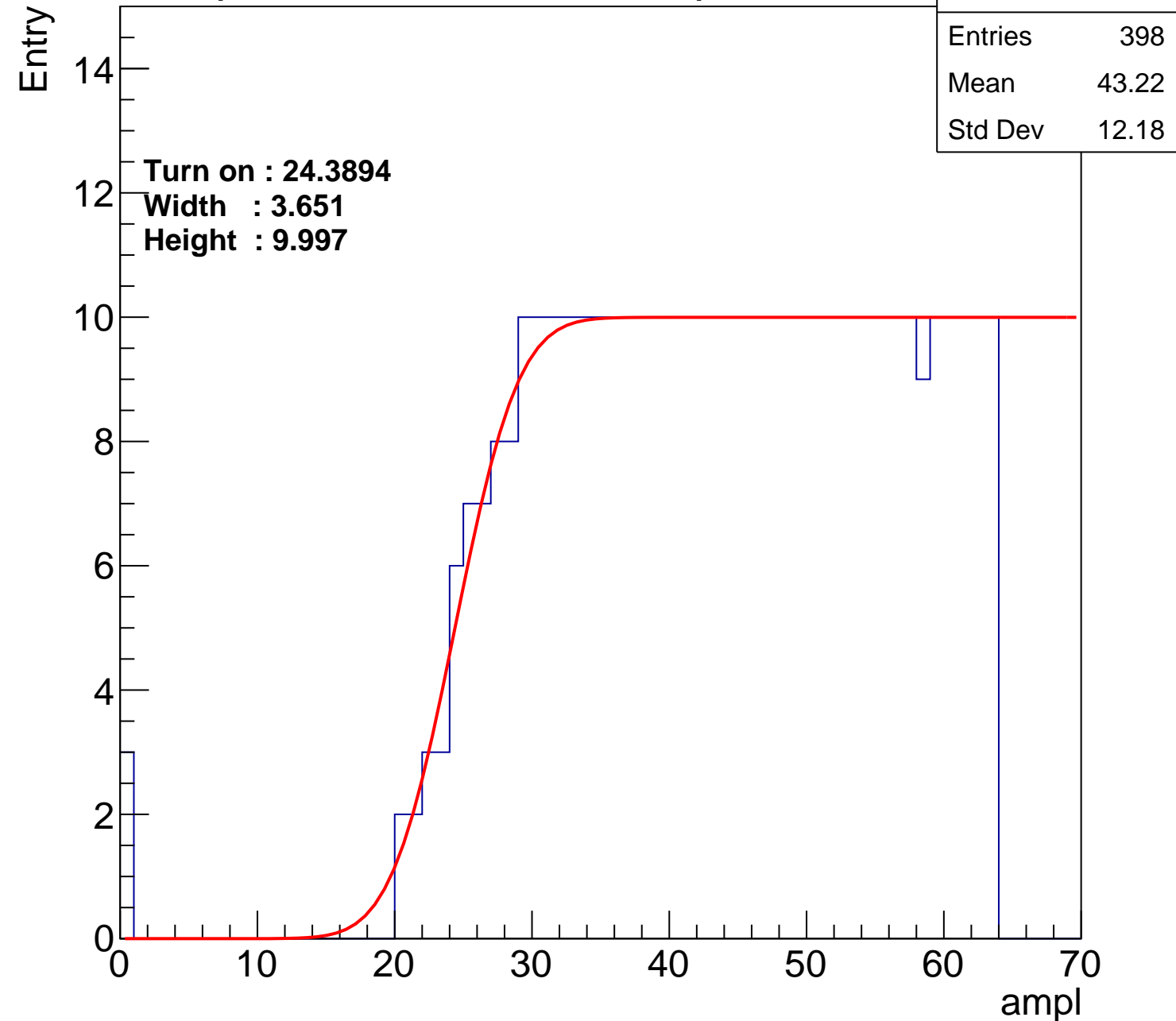
Width : 3.651

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch83

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	370
Mean	44.49
Std Dev	11.92

Turn on : 27.9290

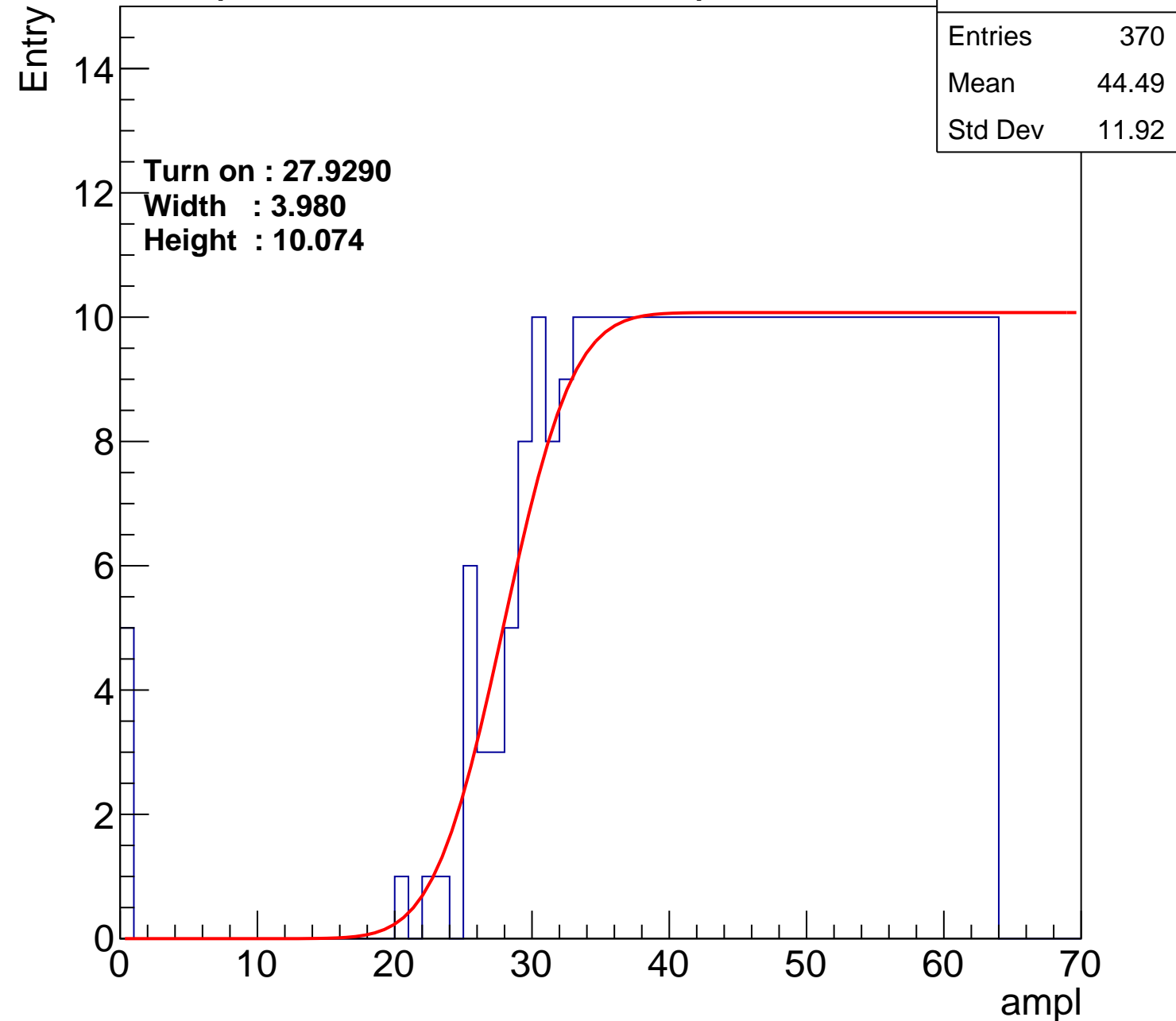
Width : 3.980

Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch84

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	406
Mean	43.04
Std Dev	12.04

Turn on : 23.6037

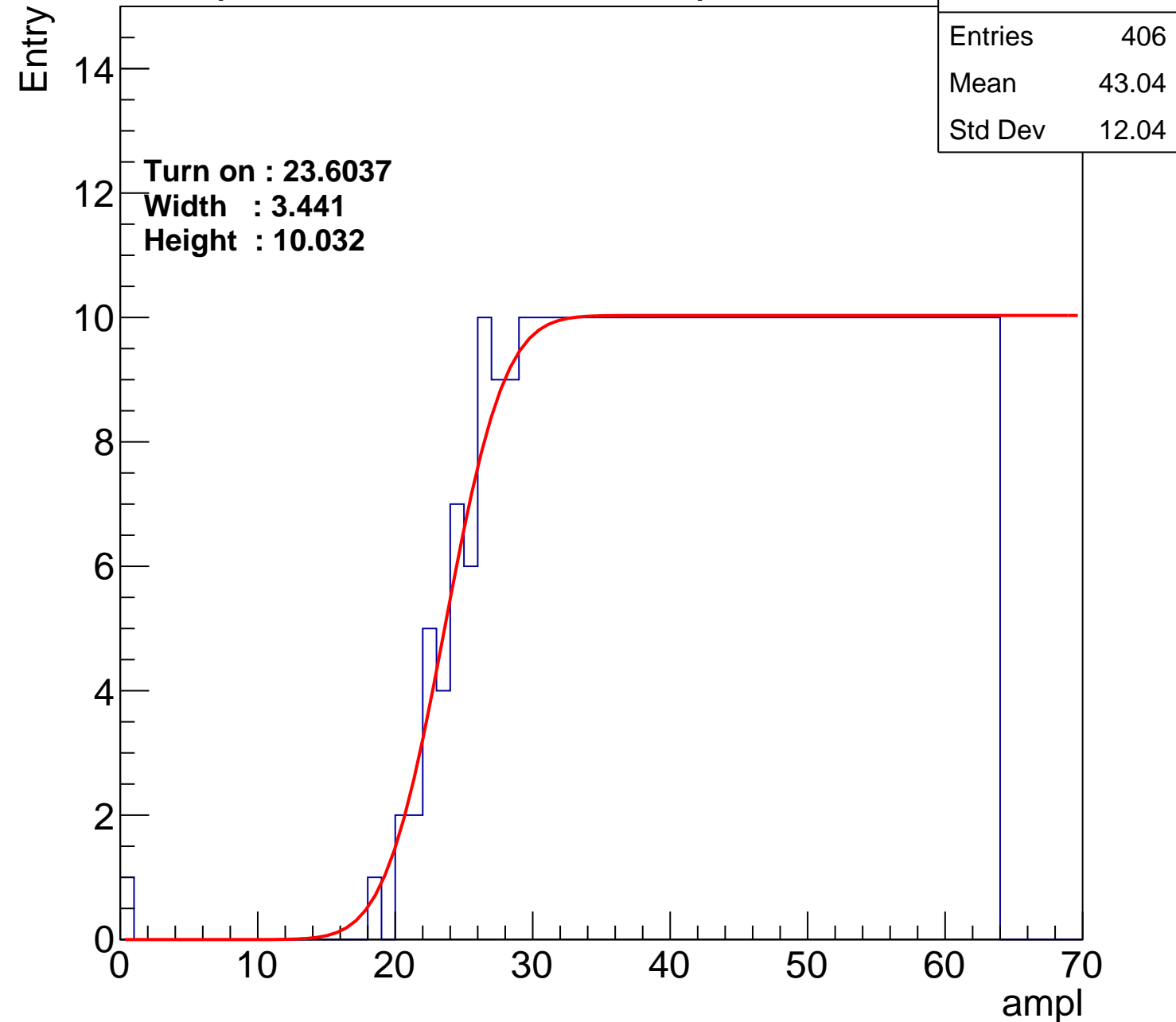
Width : 3.441

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch85

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	377
Mean	44.46
Std Dev	11.28

Turn on : 26.8487

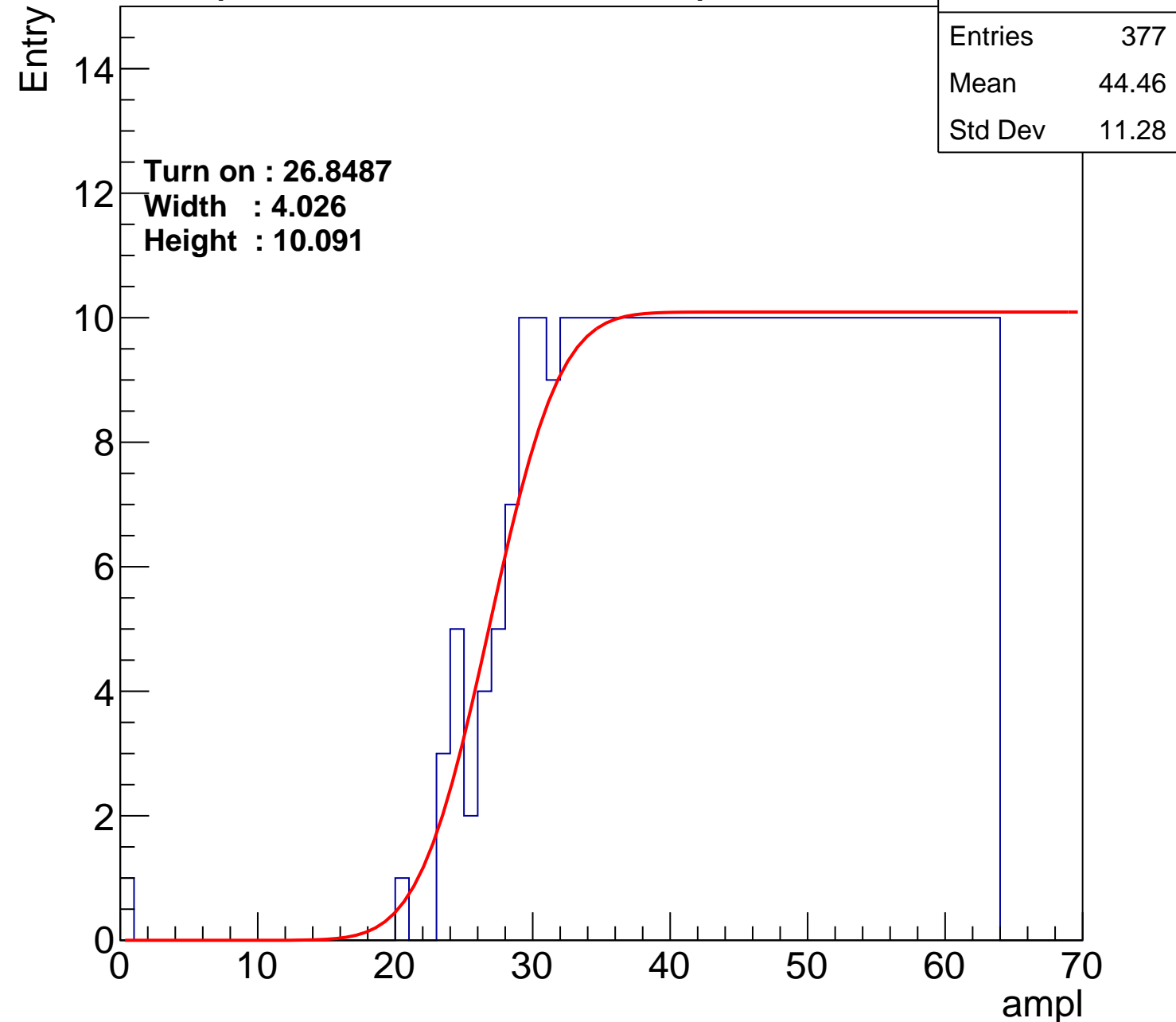
Width : 4.026

Height : 10.091

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch86

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	385
Mean	43.99
Std Dev	11.6

Turn on : 25.5927

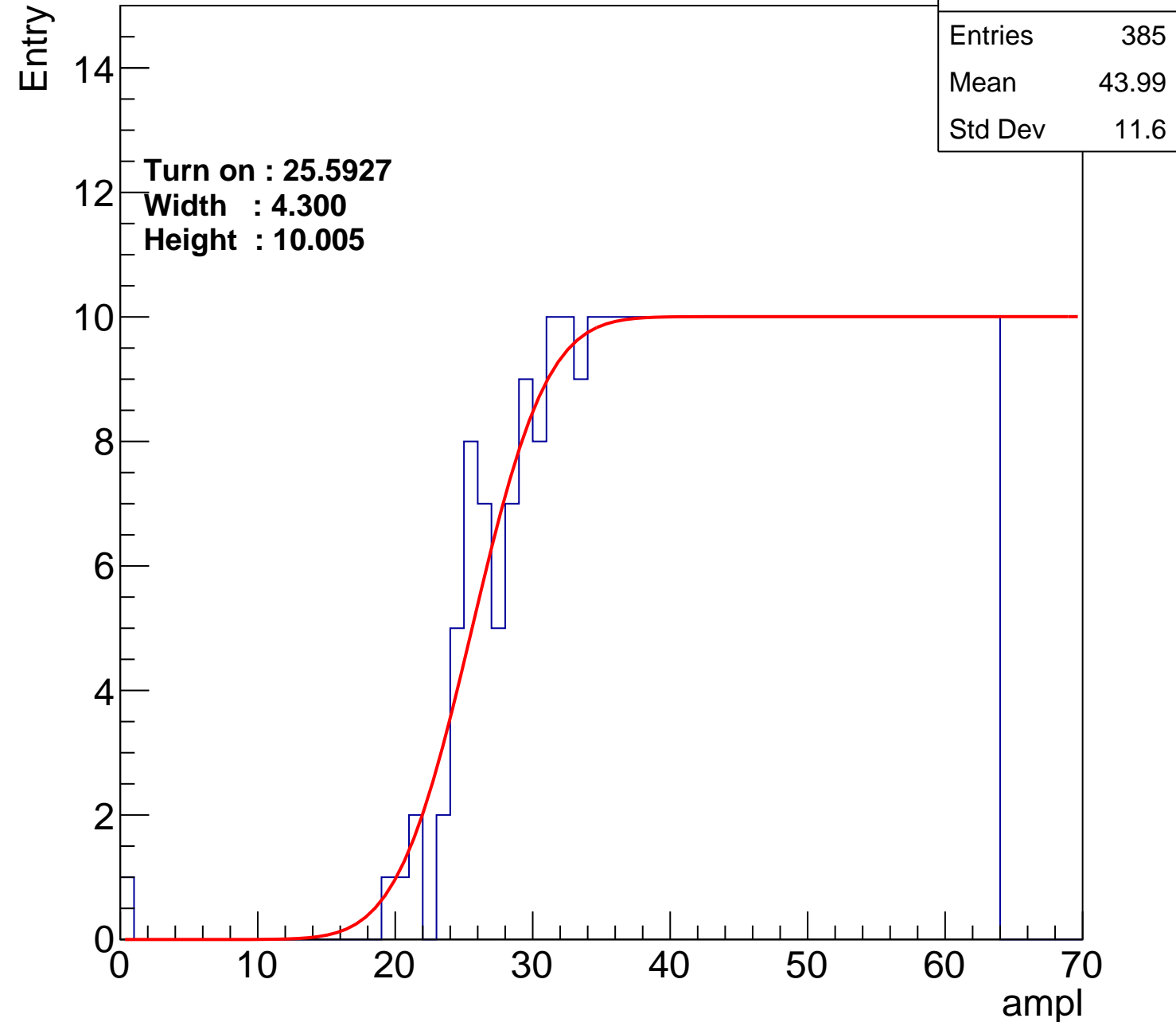
Width : 4.300

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch87

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	400
Mean	43.22
Std Dev	12.19

Turn on : 24.0780

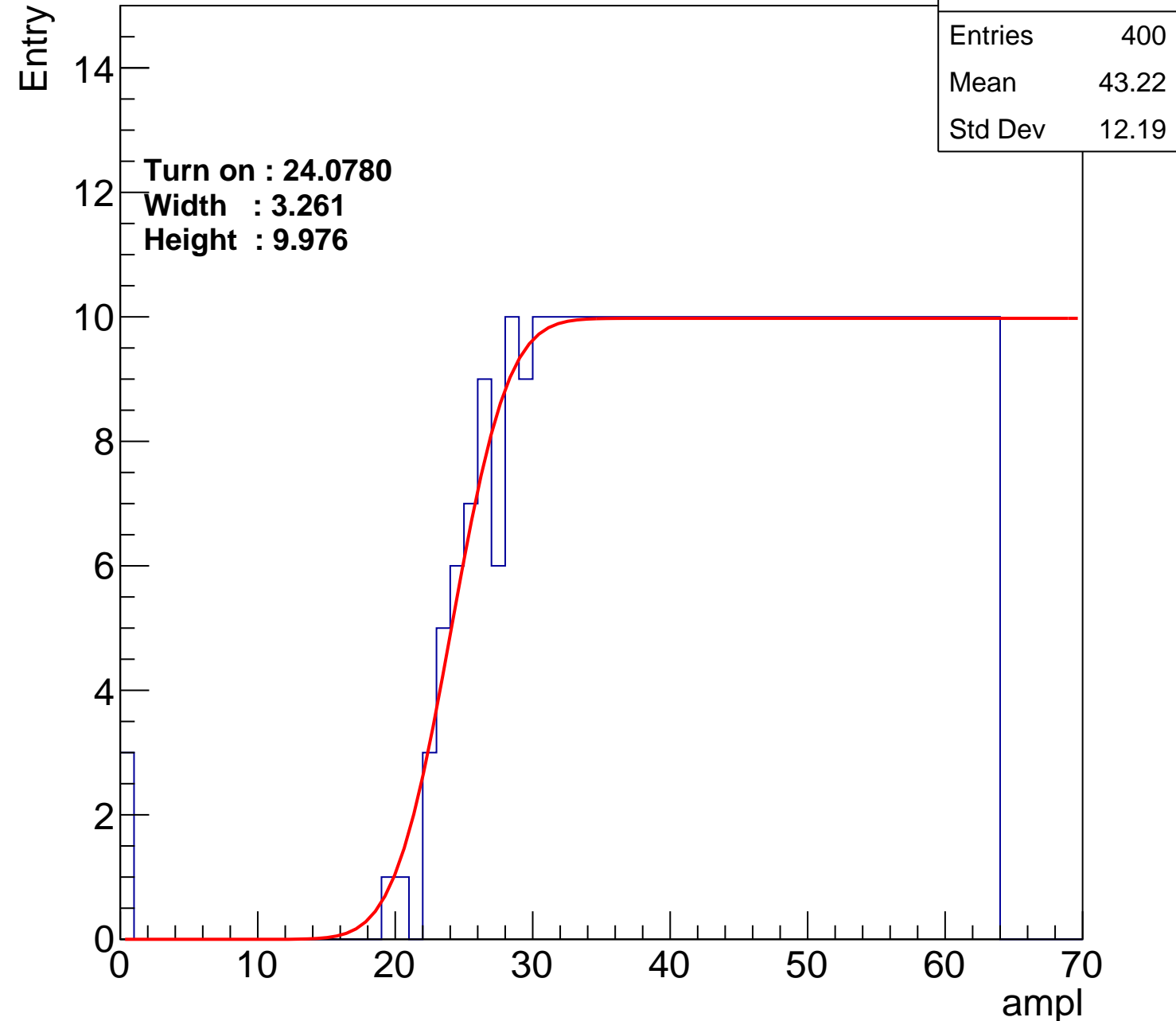
Width : 3.261

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch88

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	378
Mean	44.24
Std Dev	11.73

Turn on : 27.4441

Width : 4.081

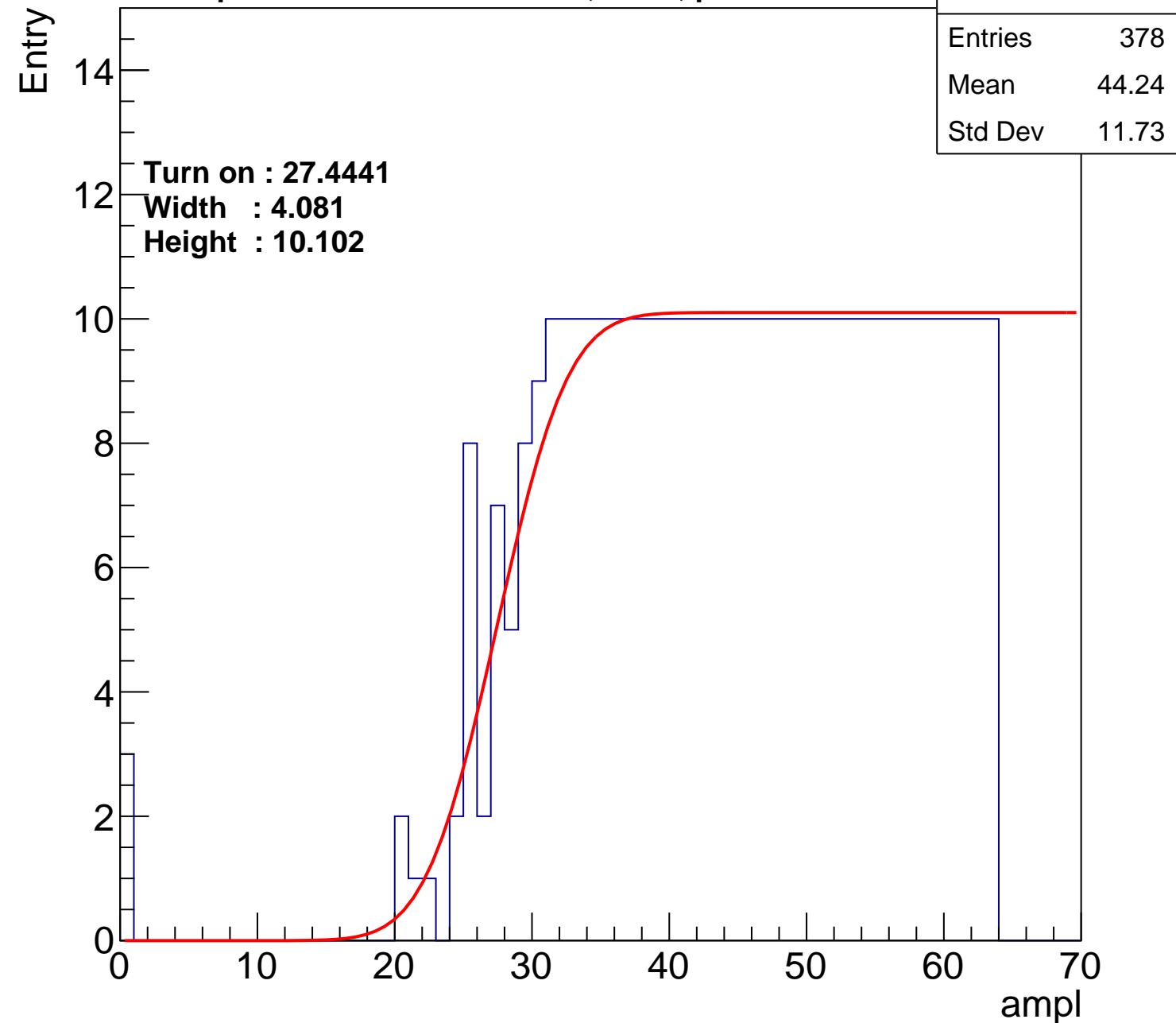
Height : 10.102

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U21-ch89

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	390
Mean	43.67
Std Dev	12

Turn on : 25.8365

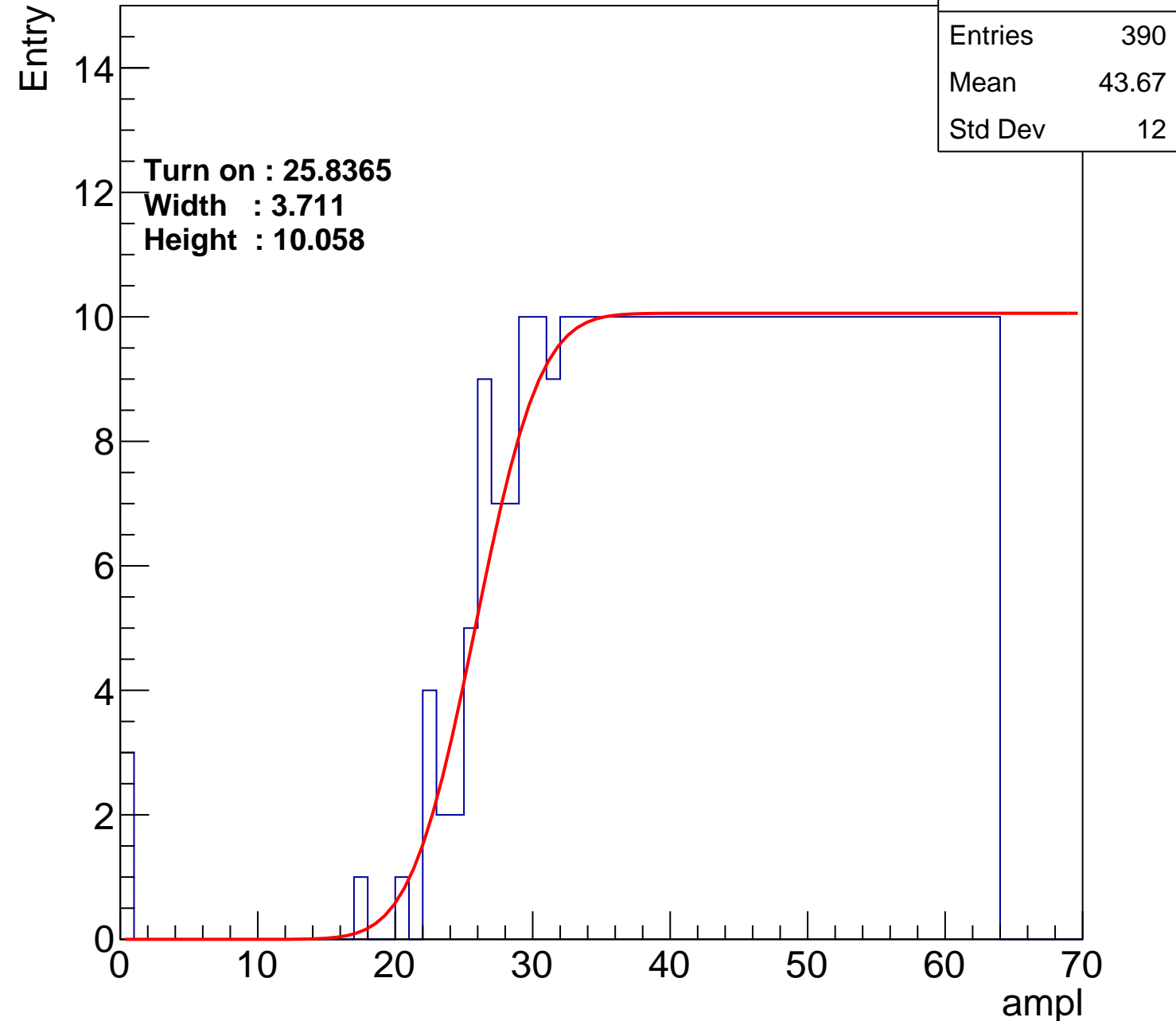
Width : 3.711

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch90

calib\_packv5\_042523\_0143.root, FC#7, port C2

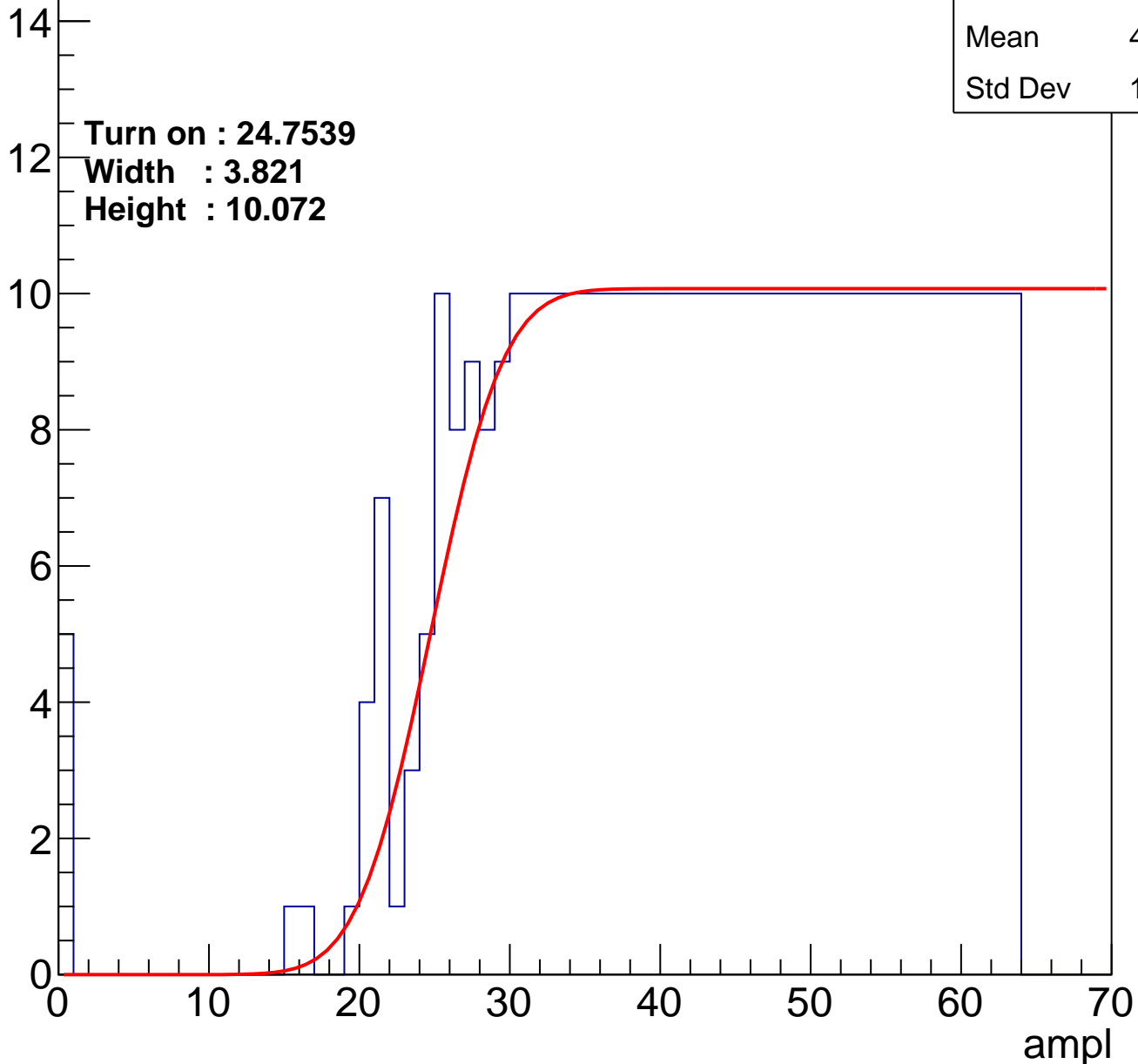
Entries	412
Mean	42.44
Std Dev	12.89

Turn on : 24.7539

Width : 3.821

Height : 10.072

Entry



# B1L103S, U21-ch91

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	376
Mean	44.39
Std Dev	11.52

Turn on : 27.4206

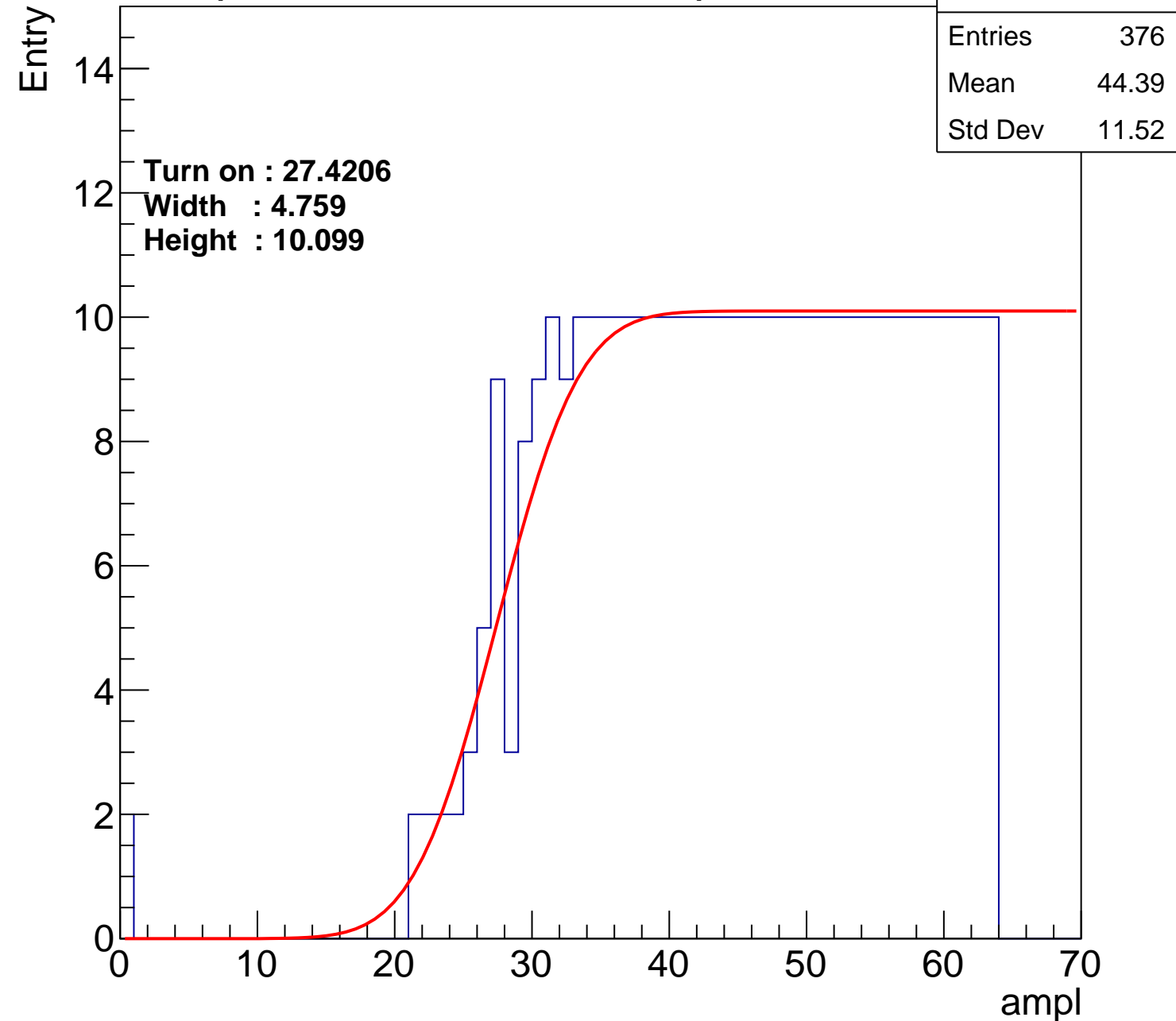
Width : 4.759

Height : 10.099

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch92

calib\_packv5\_042523\_0143.root, FC#7, port C2

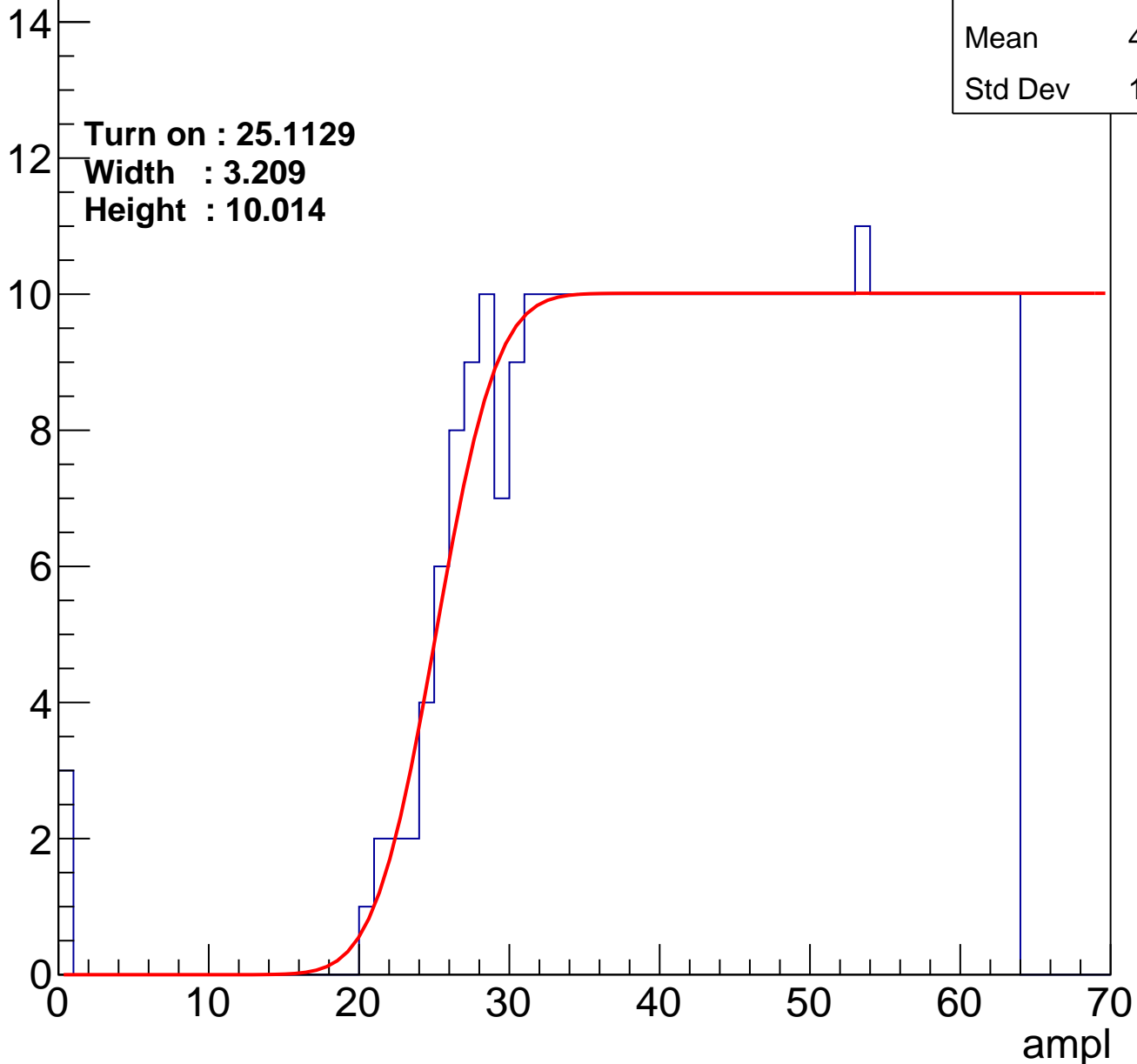
Entries	394
Mean	43.57
Std Dev	12.03

Turn on : 25.1129

Width : 3.209

Height : 10.014

Entry



# B1L103S, U21-ch93

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.4605

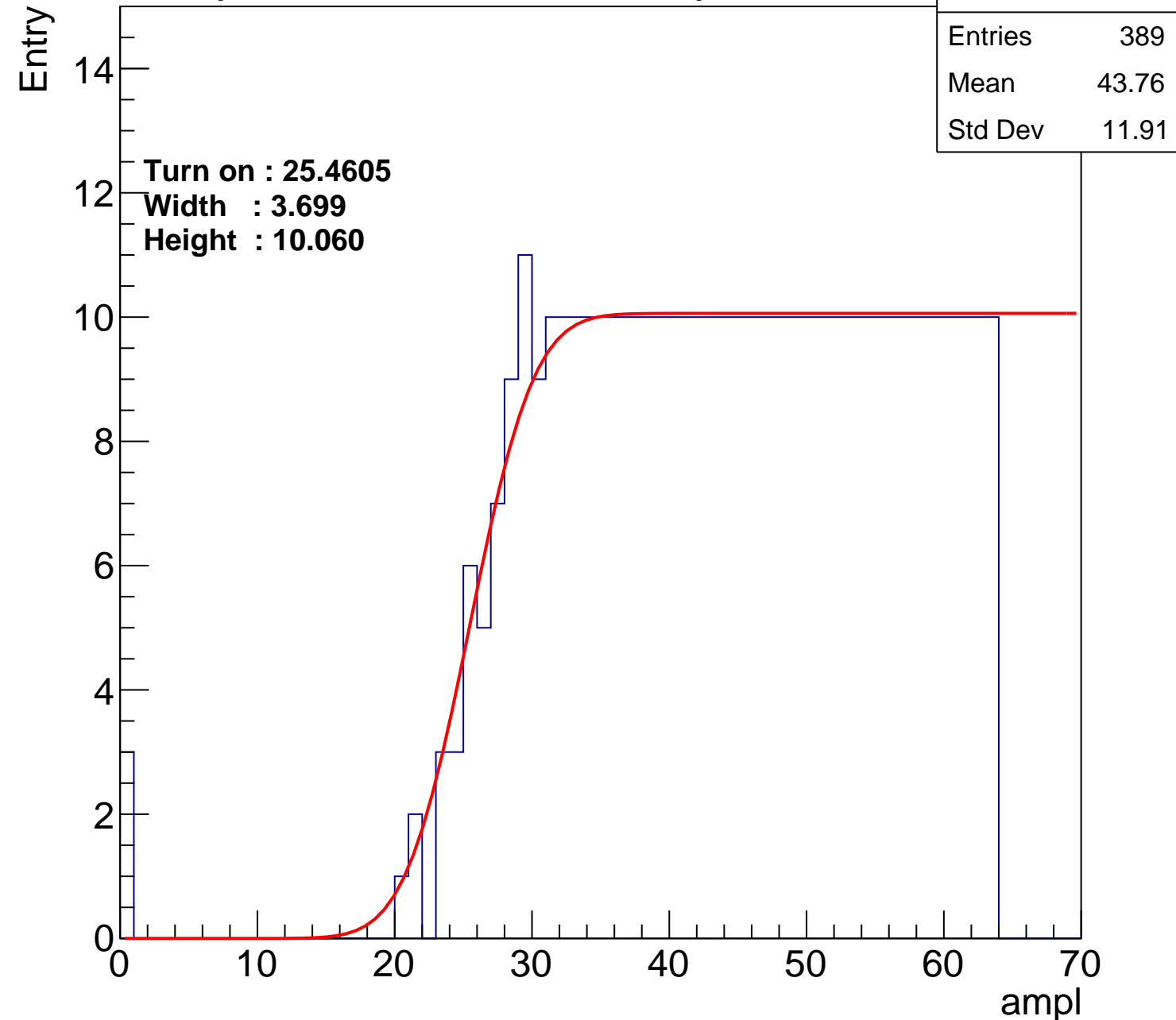
Width : 3.699

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch94

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	389
Mean	43.44
Std Dev	12.72

Turn on : 26.1894

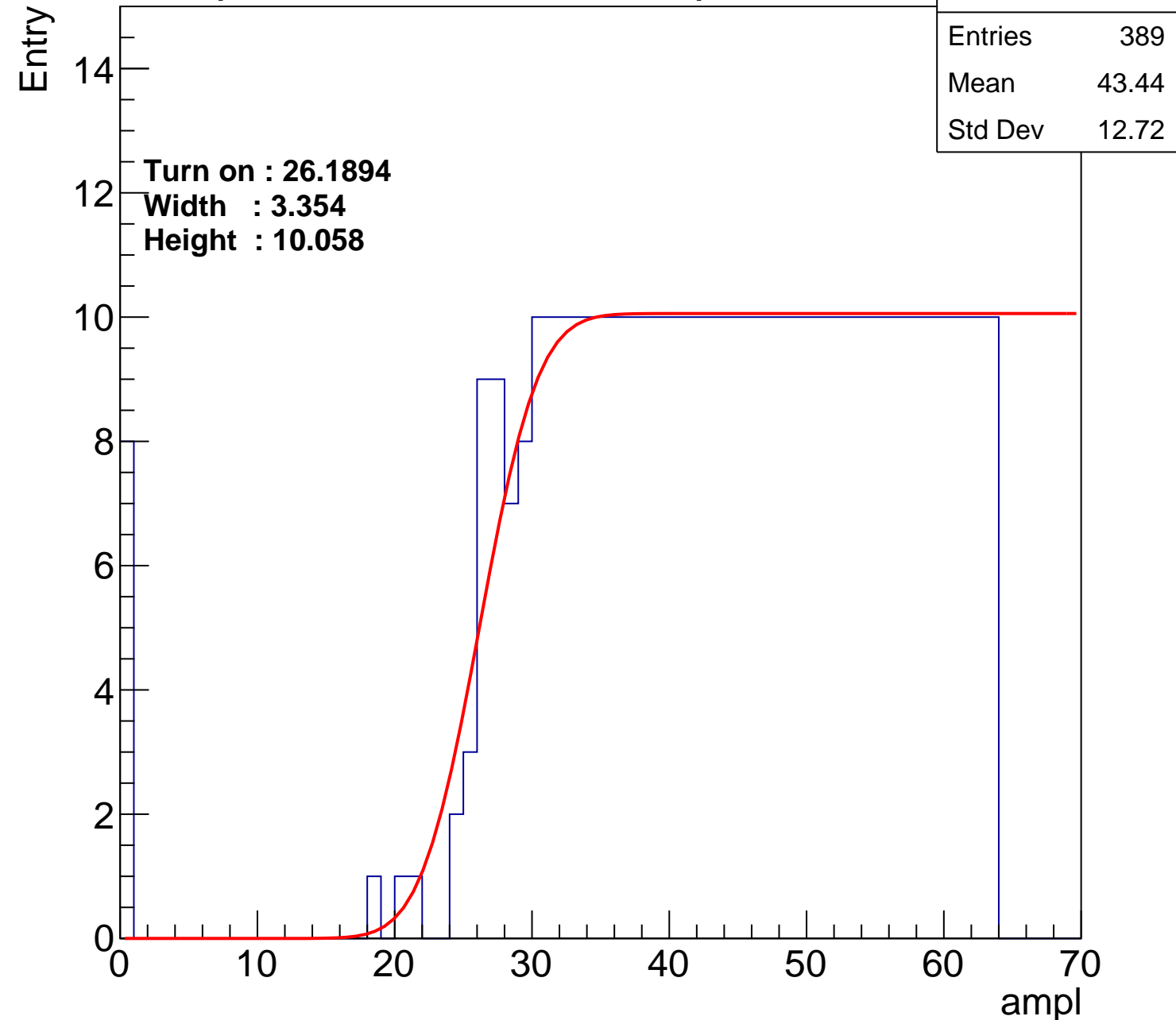
Width : 3.354

Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch95

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.36
Std Dev	11.95

Turn on : 27.3197

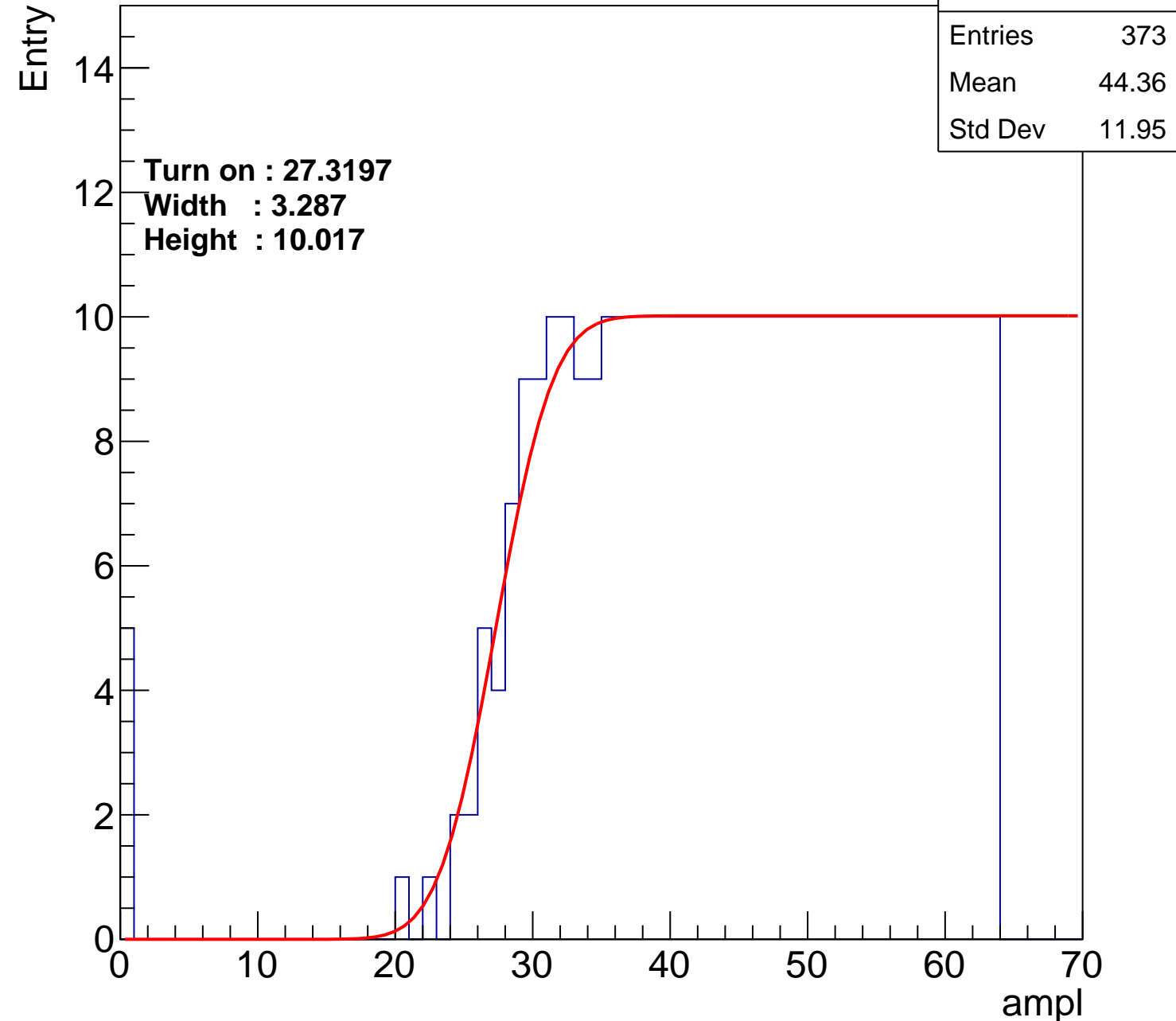
Width : 3.287

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch96

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	397
Mean	43.42
Std Dev	11.96

Turn on : 24.6496

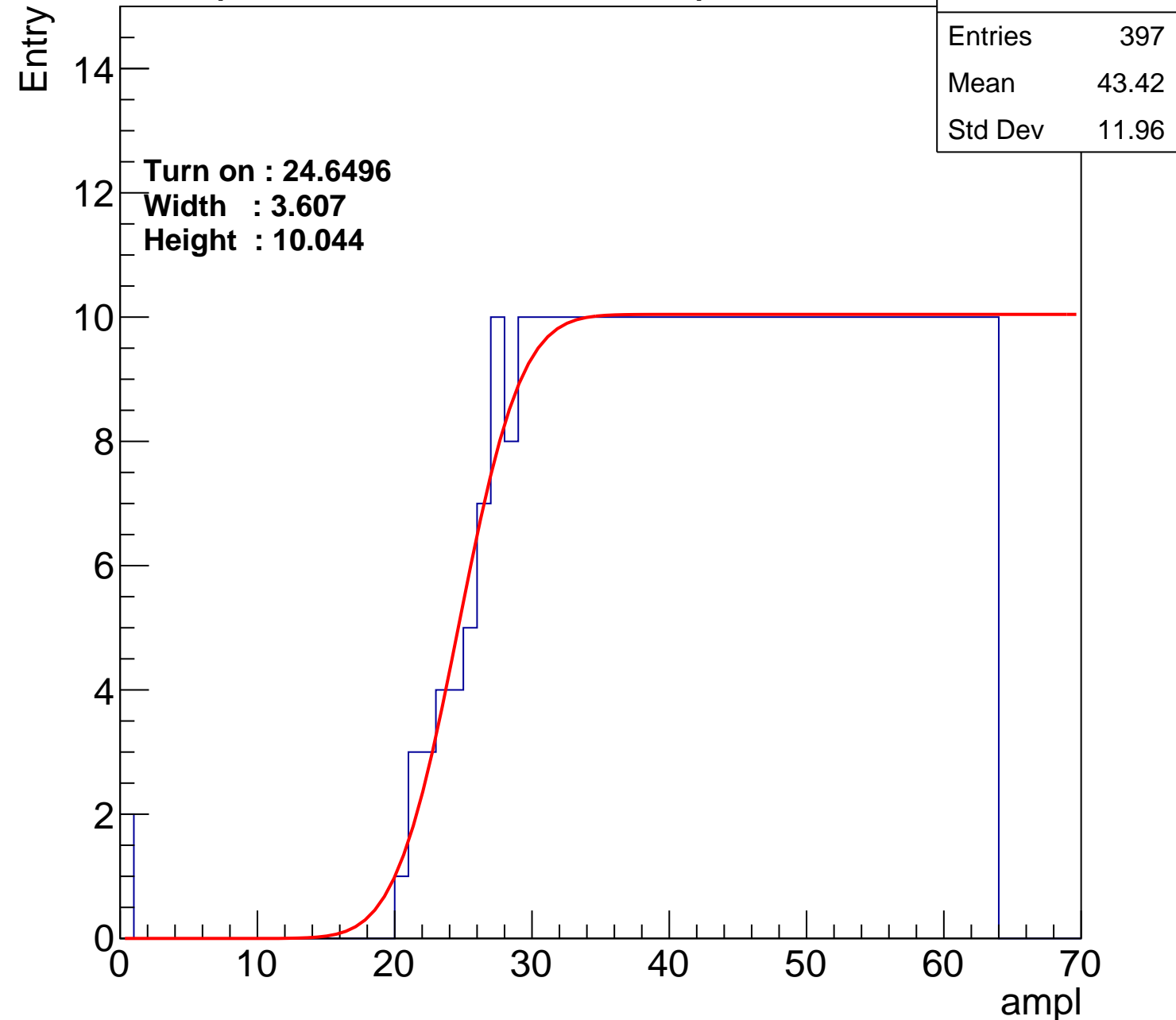
Width : 3.607

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch97

calib\_packv5\_042523\_0143.root, FC#7, port C2

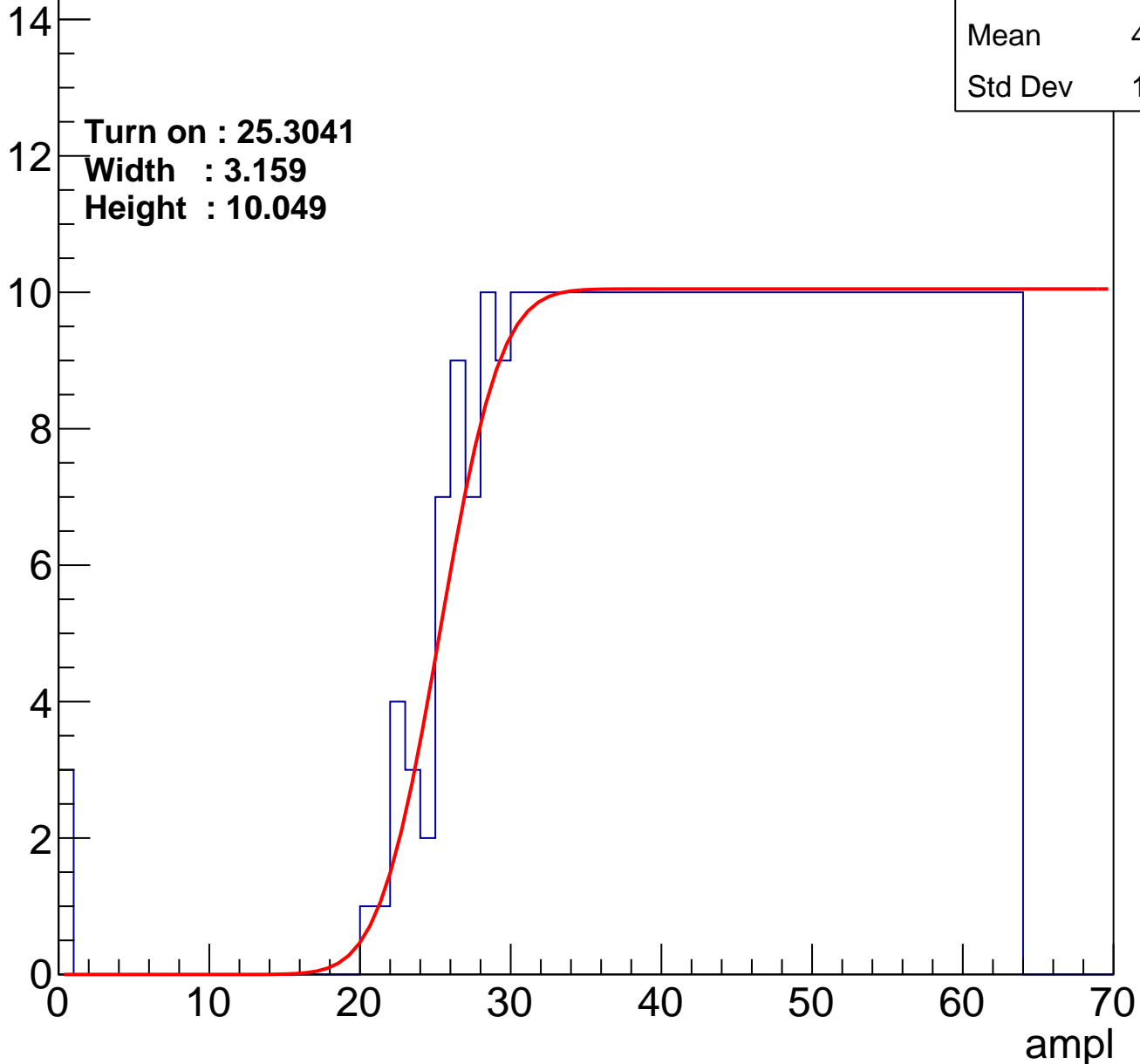
Entries	396
Mean	43.42
Std Dev	12.08

Turn on : 25.3041

Width : 3.159

Height : 10.049

Entry



# B1L103S, U21-ch98

calib\_packv5\_042523\_0143.root, FC#7, port C2

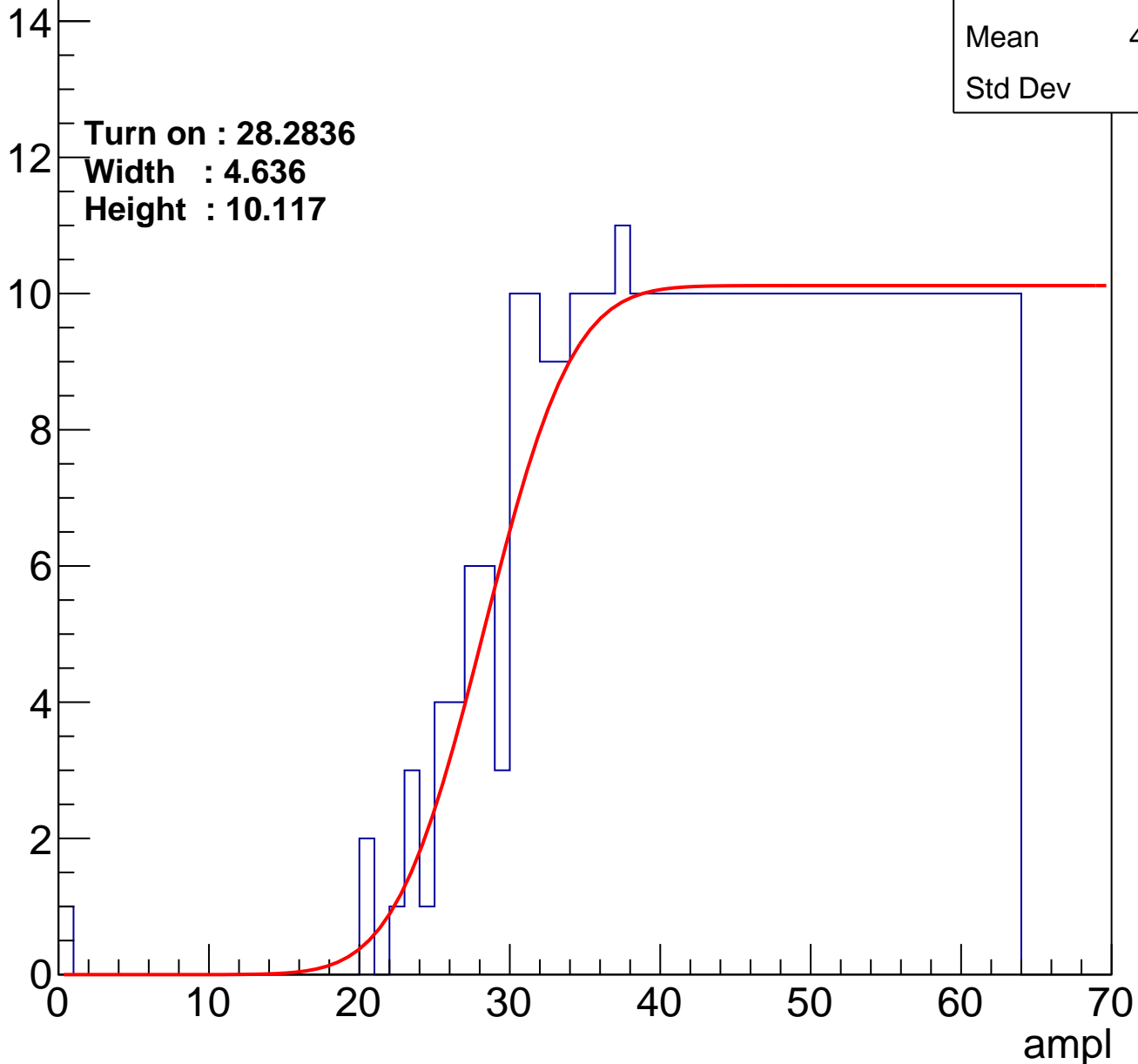
Entries	370
Mean	44.75
Std Dev	11.2

Turn on : 28.2836

Width : 4.636

Height : 10.117

Entry



# B1L103S, U21-ch99

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	388
Mean	43.7
Std Dev	12.12

Turn on : 25.8236

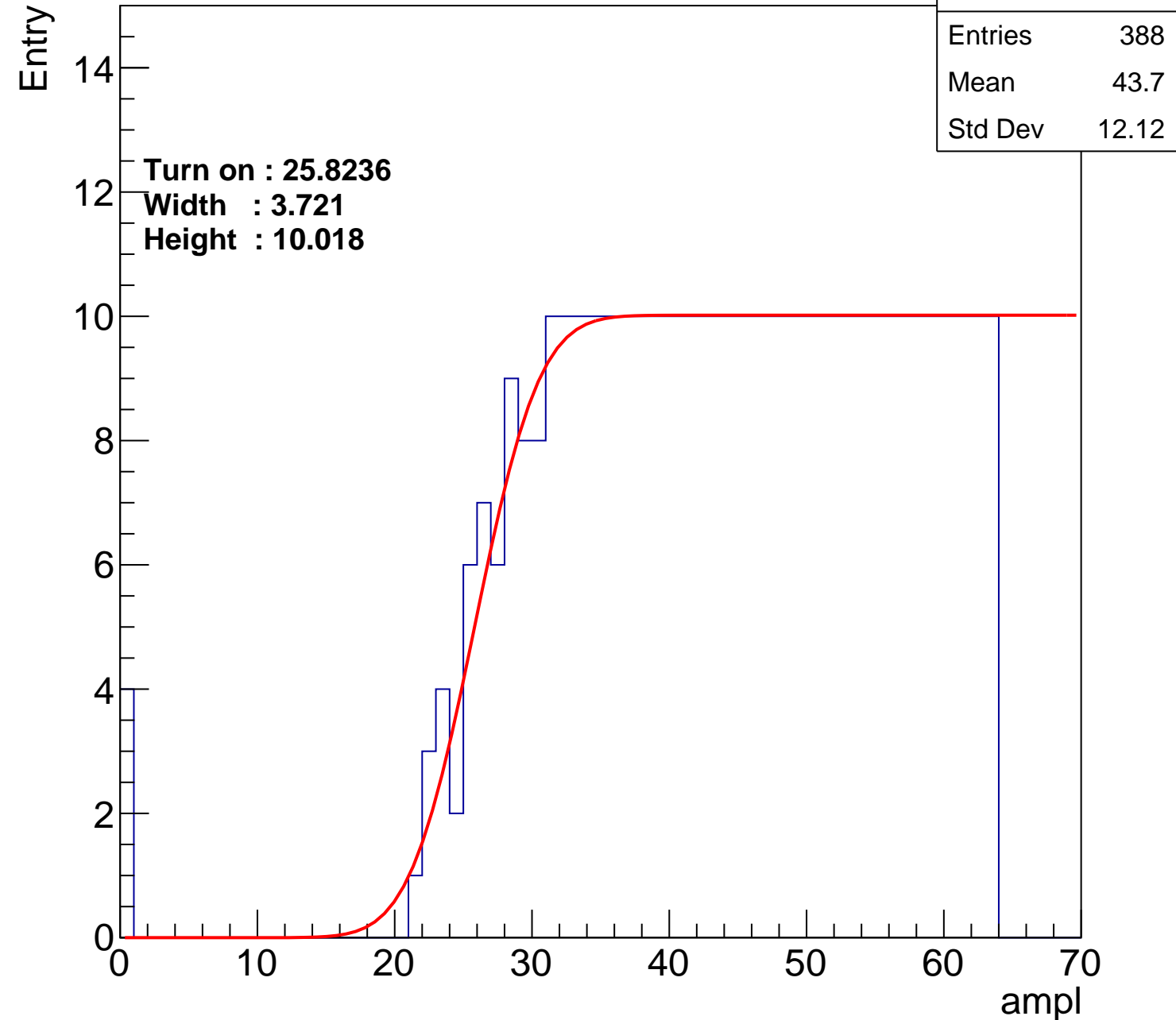
Width : 3.721

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch100

calib\_packv5\_042523\_0143.root, FC#7, port C2

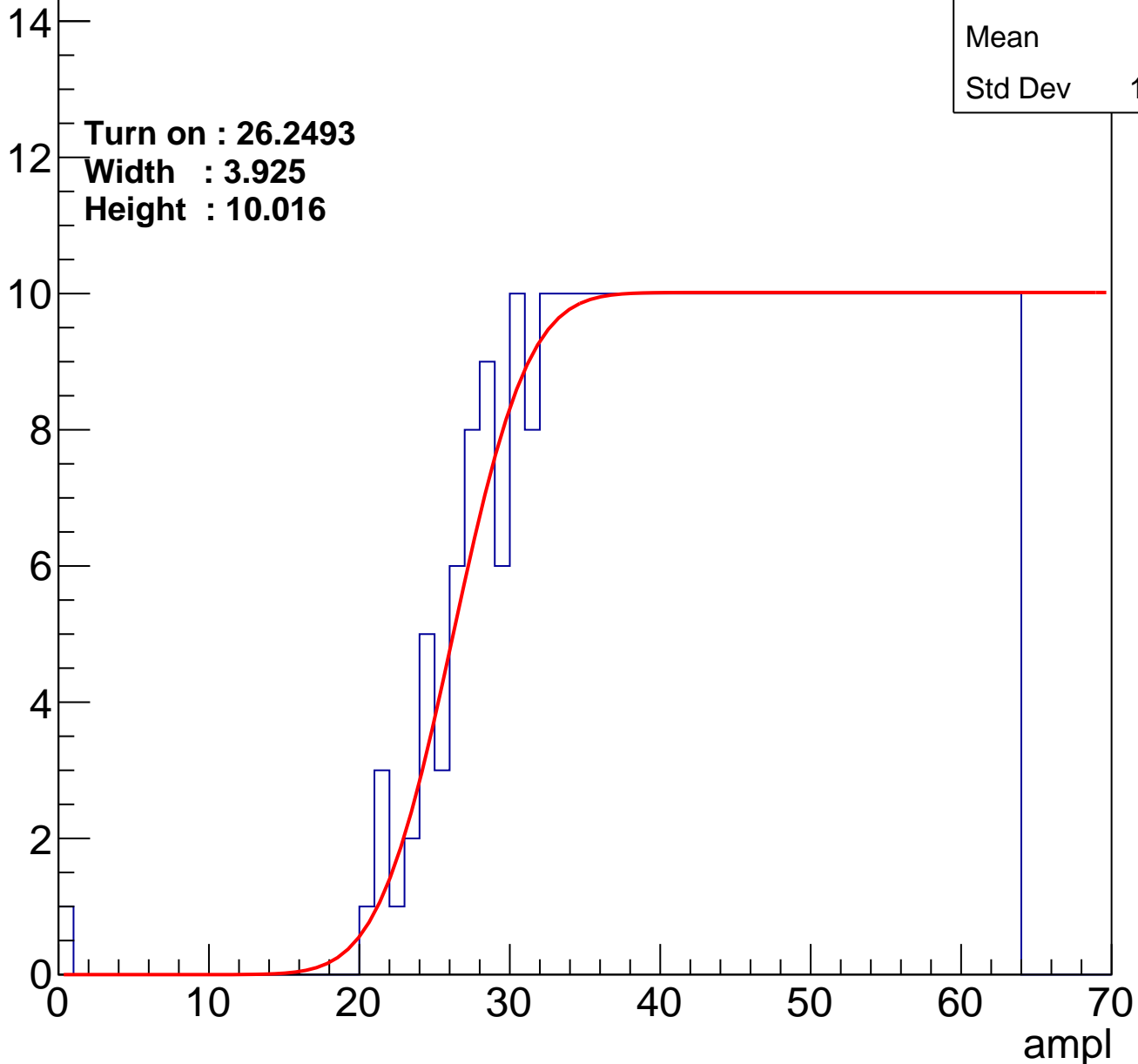
Entries	383
Mean	44.1
Std Dev	11.54

Turn on : 26.2493

Width : 3.925

Height : 10.016

Entry



# B1L103S, U21-ch101

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	389
Mean	43.8
Std Dev	11.77

Turn on : 25.6207

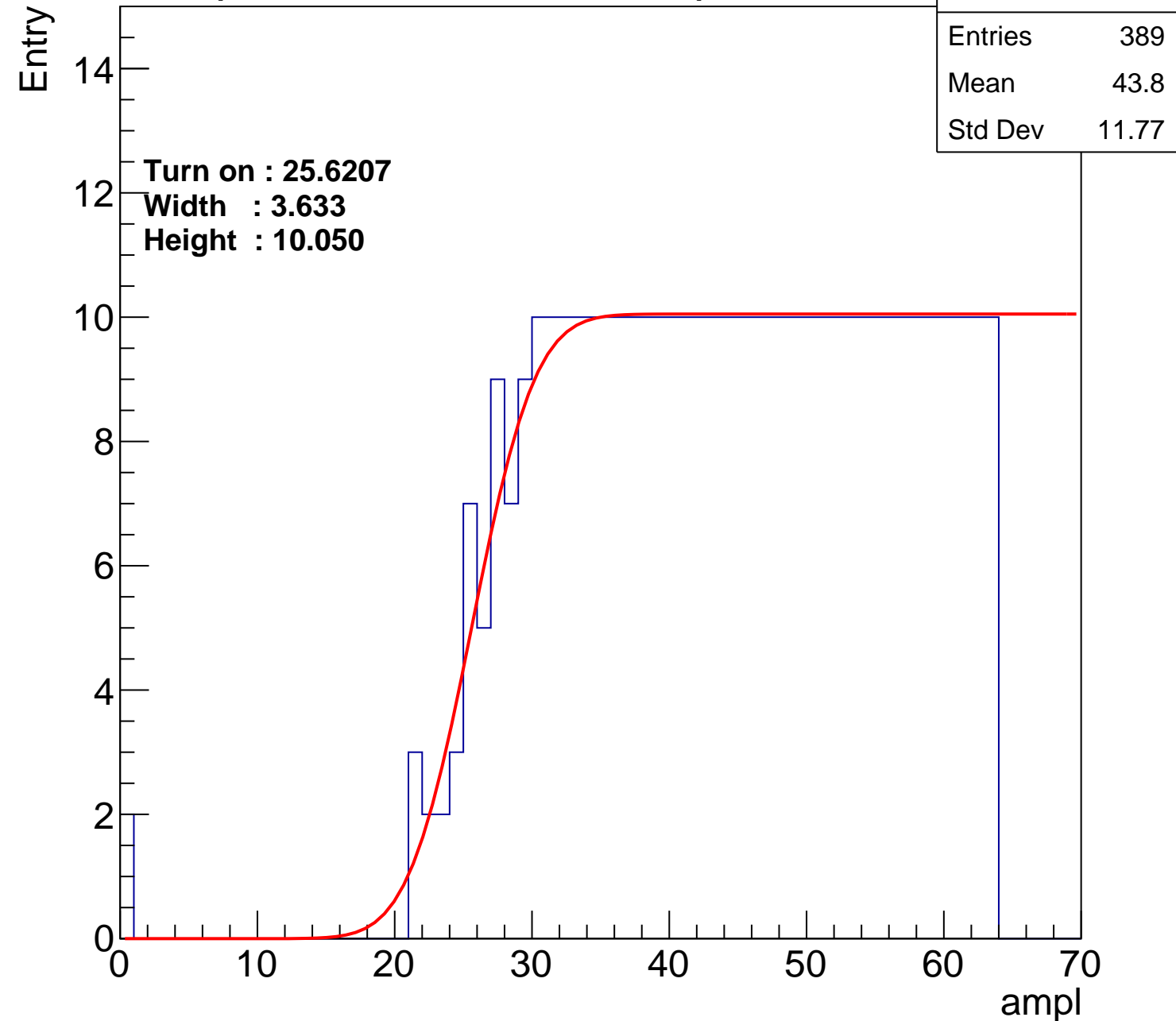
Width : 3.633

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch102

calib\_packv5\_042523\_0143.root, FC#7, port C2

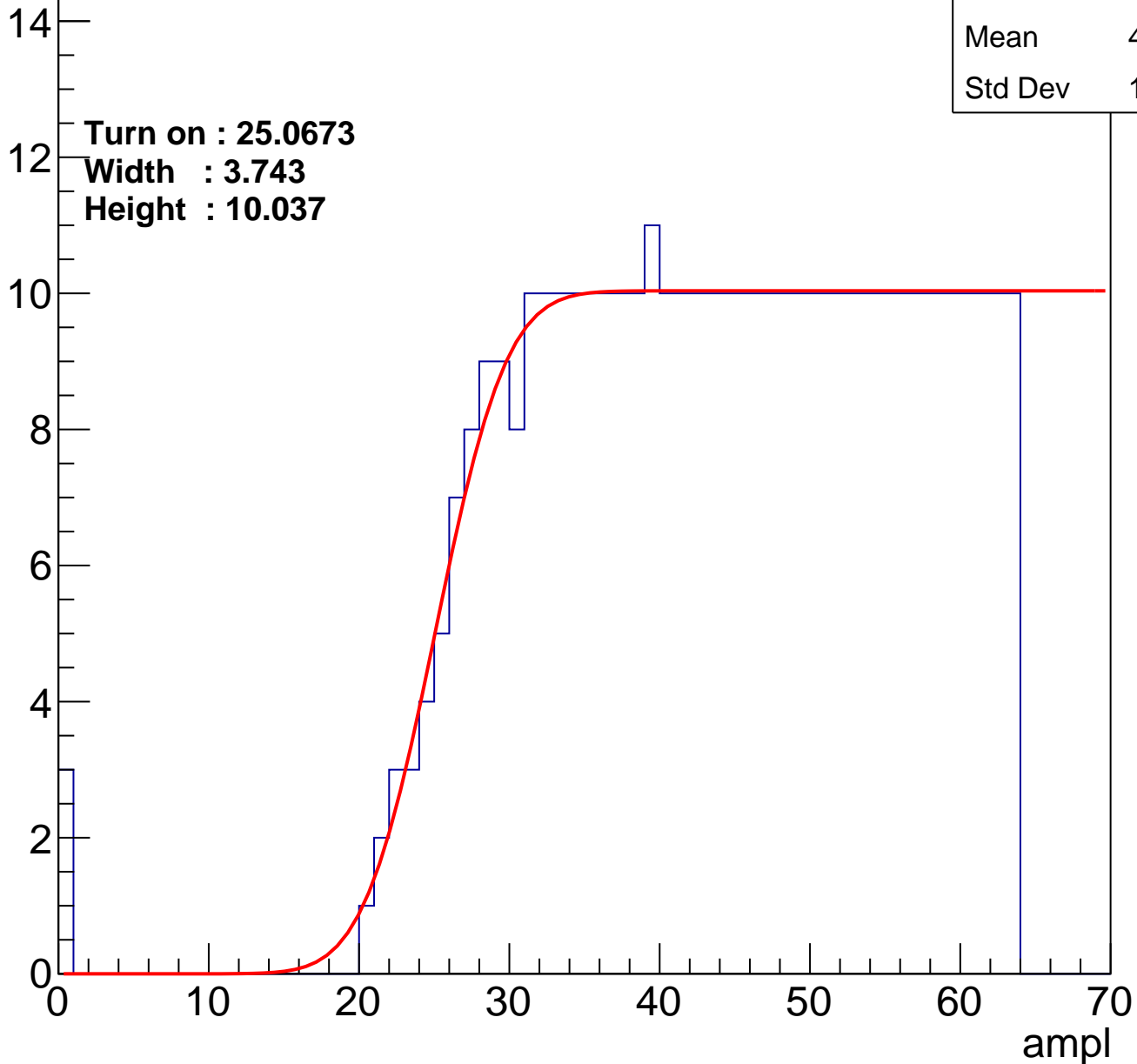
Entries	393
Mean	43.56
Std Dev	12.04

Turn on : 25.0673

Width : 3.743

Height : 10.037

Entry





# B1L103S, U21-ch103

calib\_packv5\_042523\_0143.root, FC#7, port C2

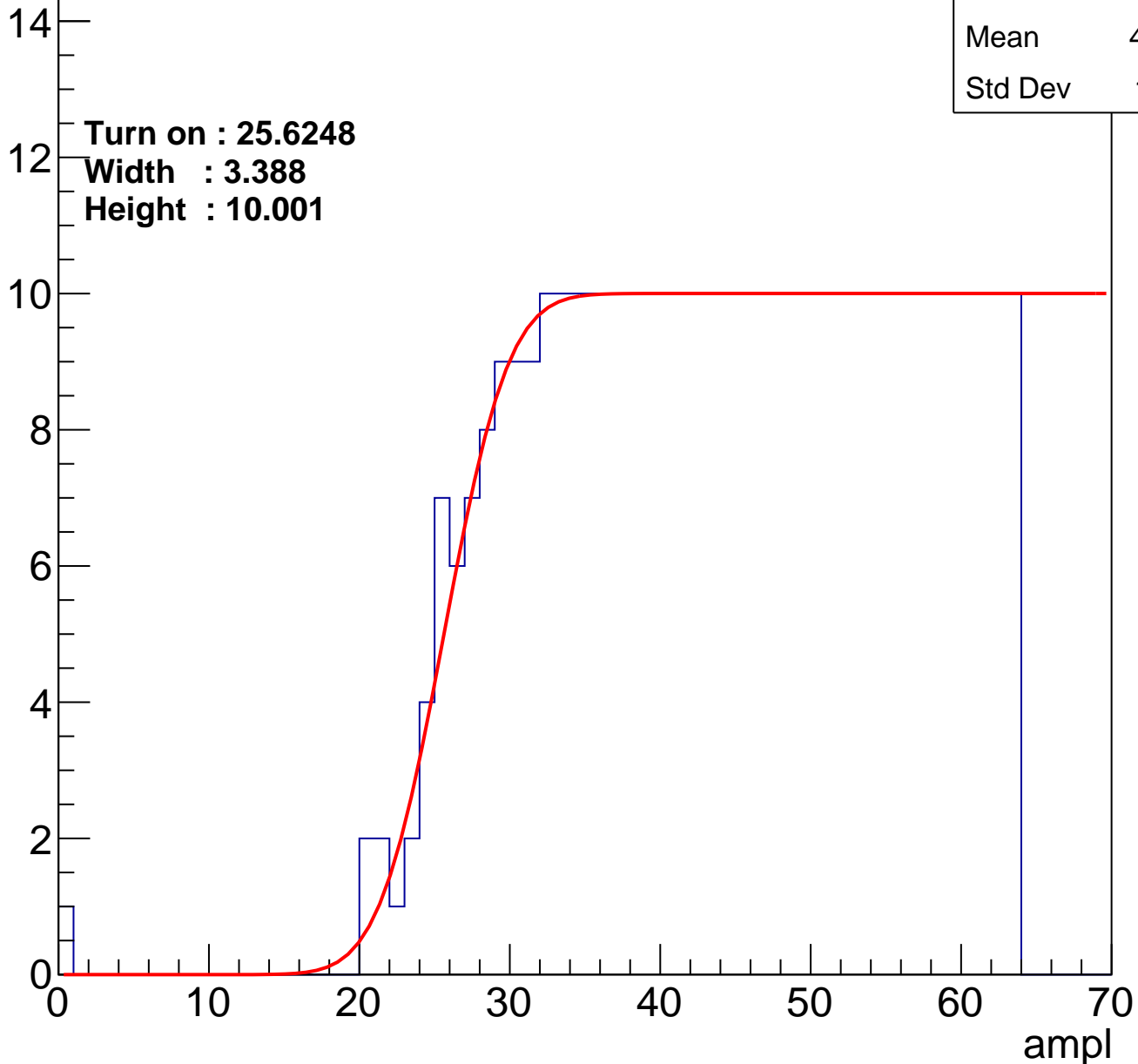
Entries	387
Mean	43.93
Std Dev	11.61

Turn on : 25.6248

Width : 3.388

Height : 10.001

Entry



# B1L103S, U21-ch104

calib\_packv5\_042523\_0143.root, FC#7, port C2

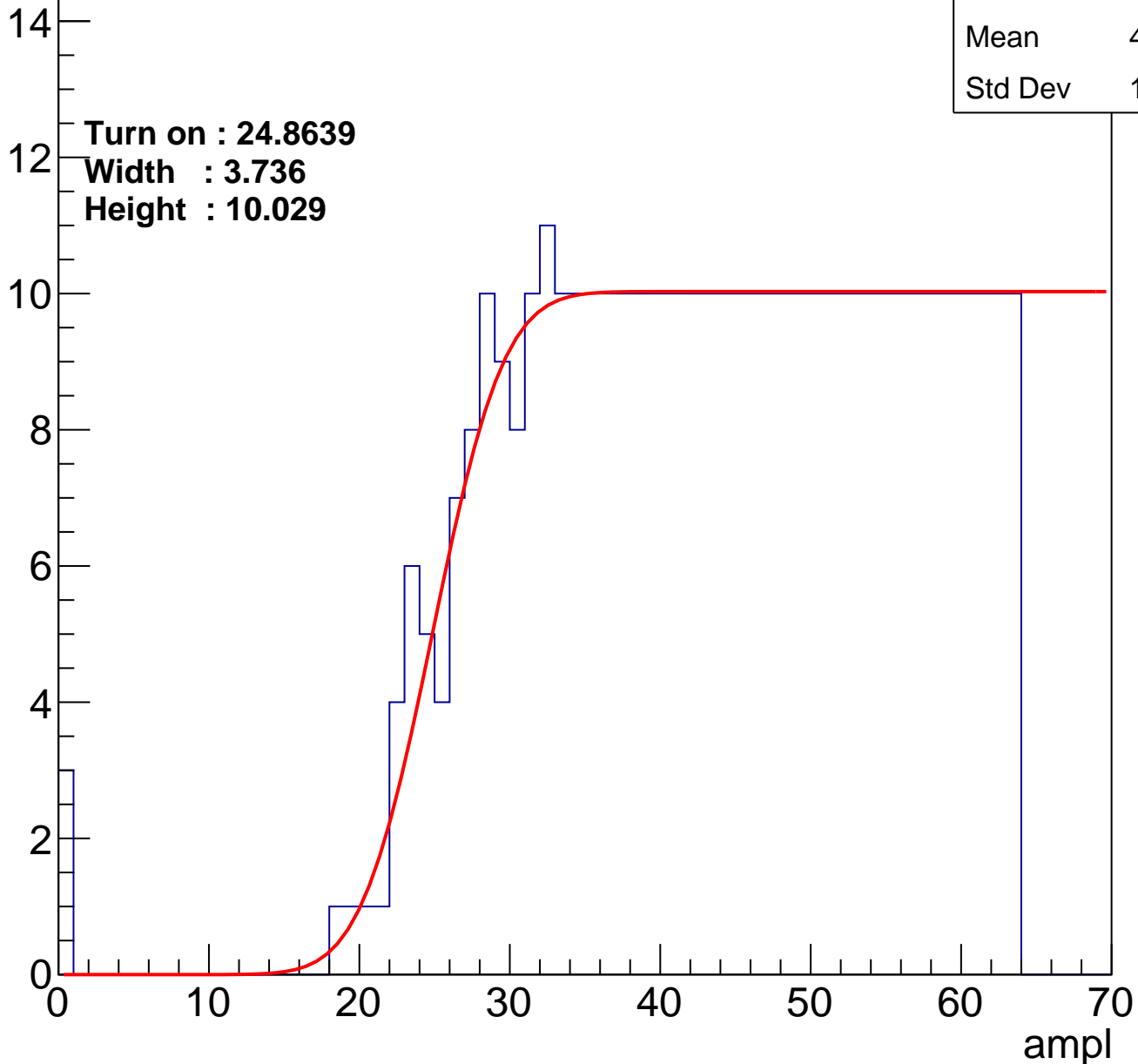
Entries	399
Mean	43.22
Std Dev	12.24

Turn on : 24.8639

Width : 3.736

Height : 10.029

Entry



# B1L103S, U21-ch105

calib\_packv5\_042523\_0143.root, FC#7, port C2

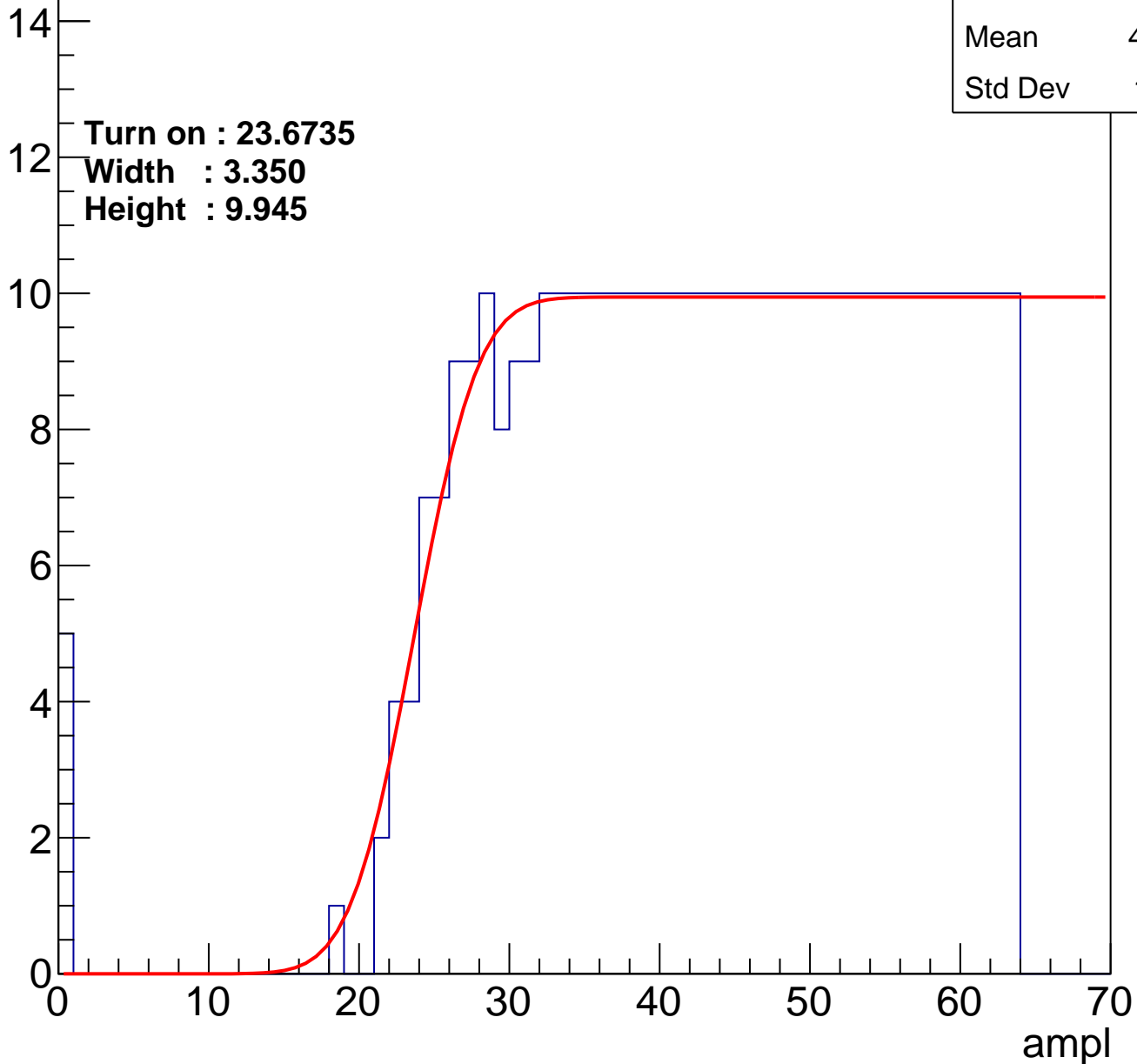
Entries	404
Mean	42.87
Std Dev	12.61

Turn on : 23.6735

Width : 3.350

Height : 9.945

Entry



# B1L103S, U21-ch106

calib\_packv5\_042523\_0143.root, FC#7, port C2

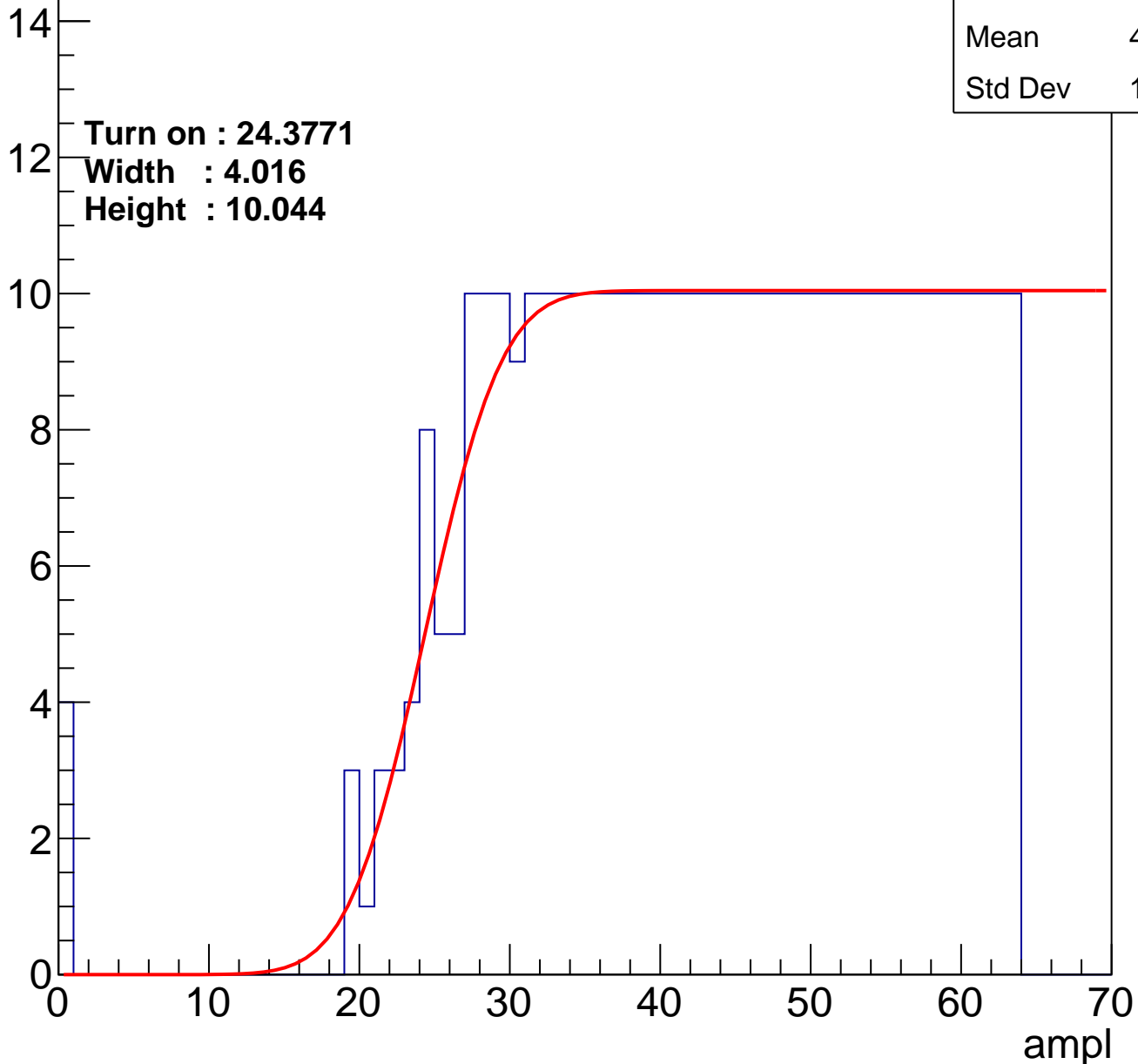
Entries	405
Mean	42.88
Std Dev	12.52

Turn on : 24.3771

Width : 4.016

Height : 10.044

Entry



# B1L103S, U21-ch107

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	383
Mean	43.72
Std Dev	12.53

Turn on : 26.3313

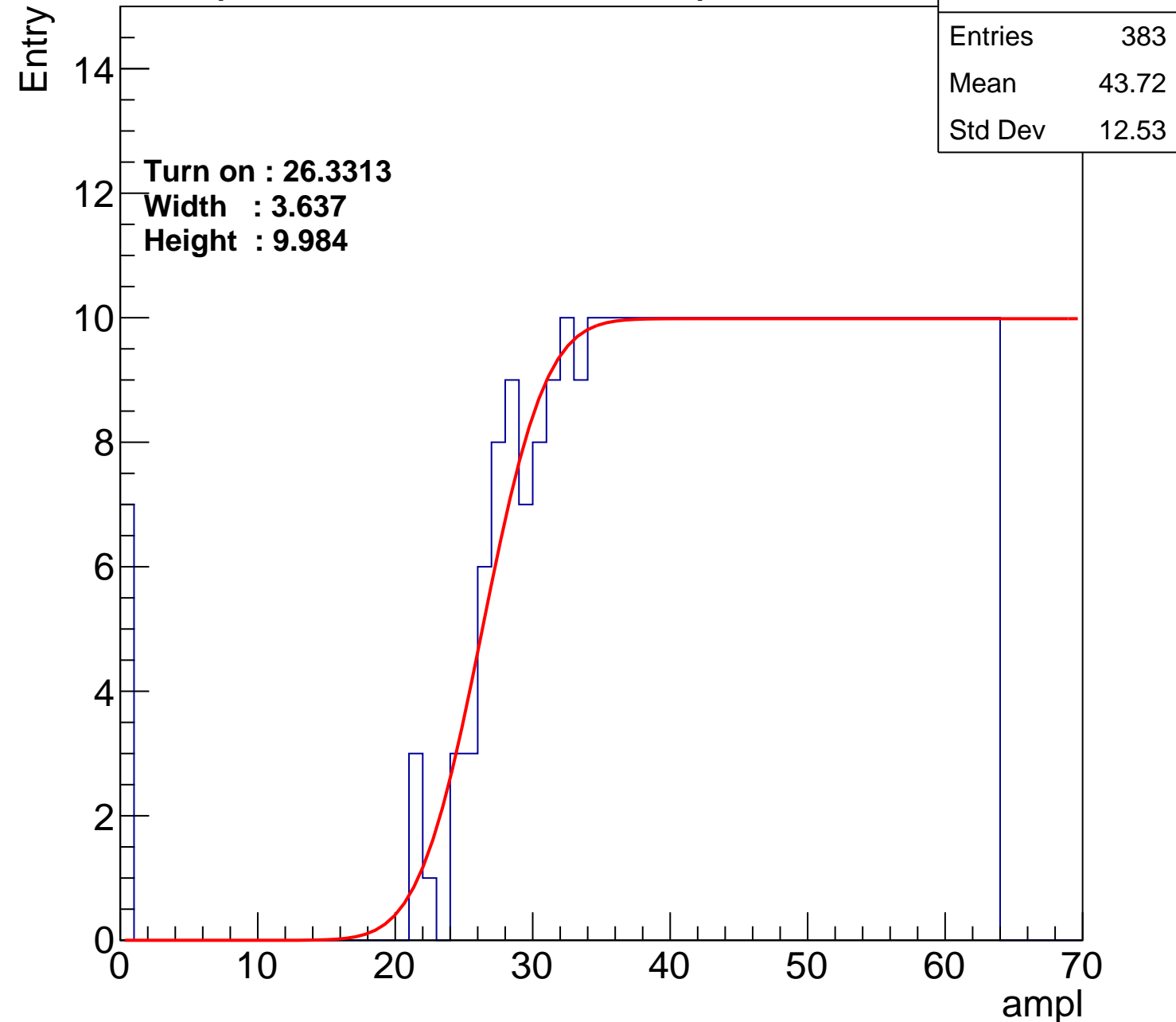
Width : 3.637

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch108

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	407
Mean	42.69
Std Dev	12.81

Turn on : 24.6162

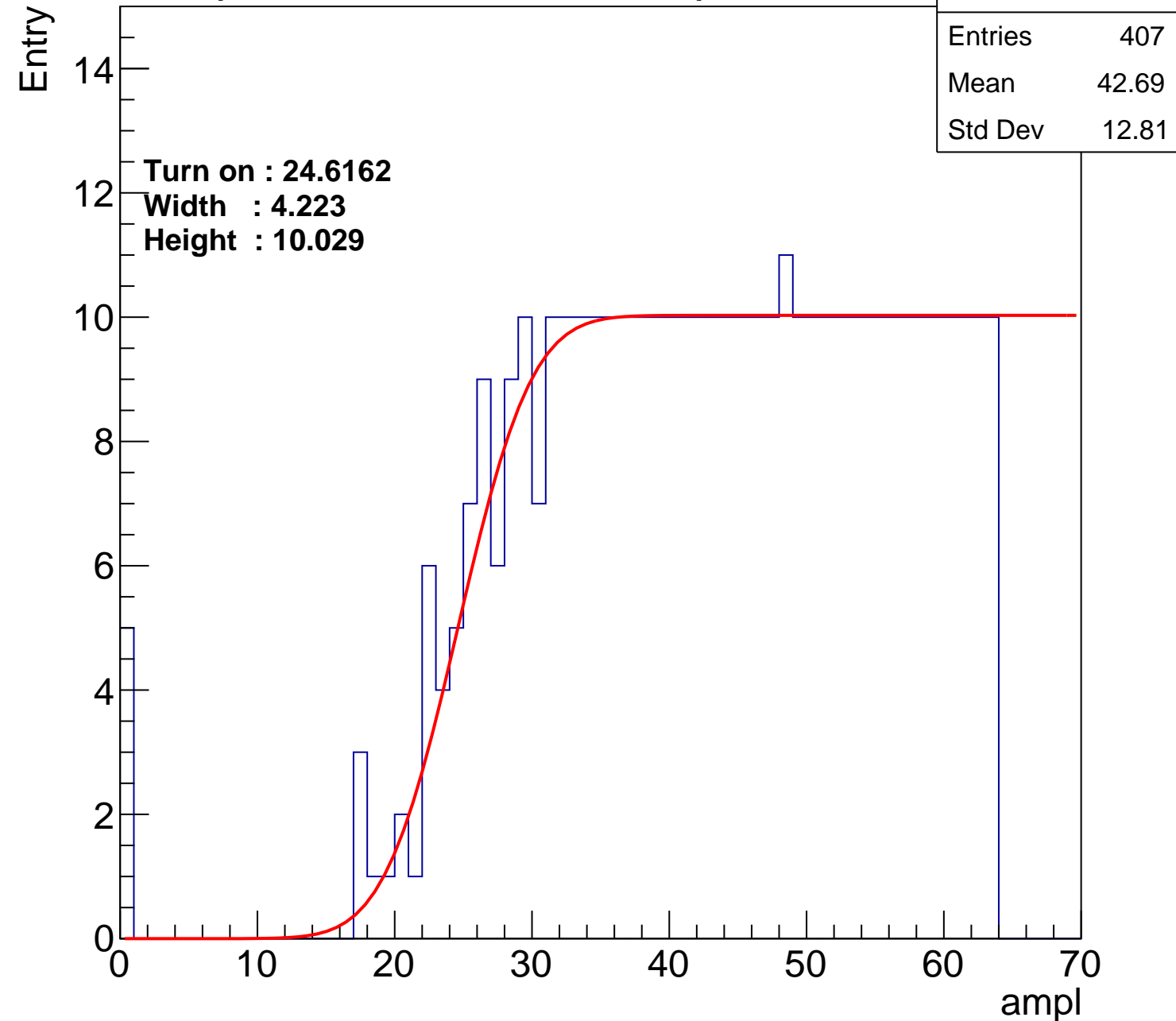
Width : 4.223

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch109

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	384
Mean	43.9
Std Dev	11.96

Turn on : 25.8180

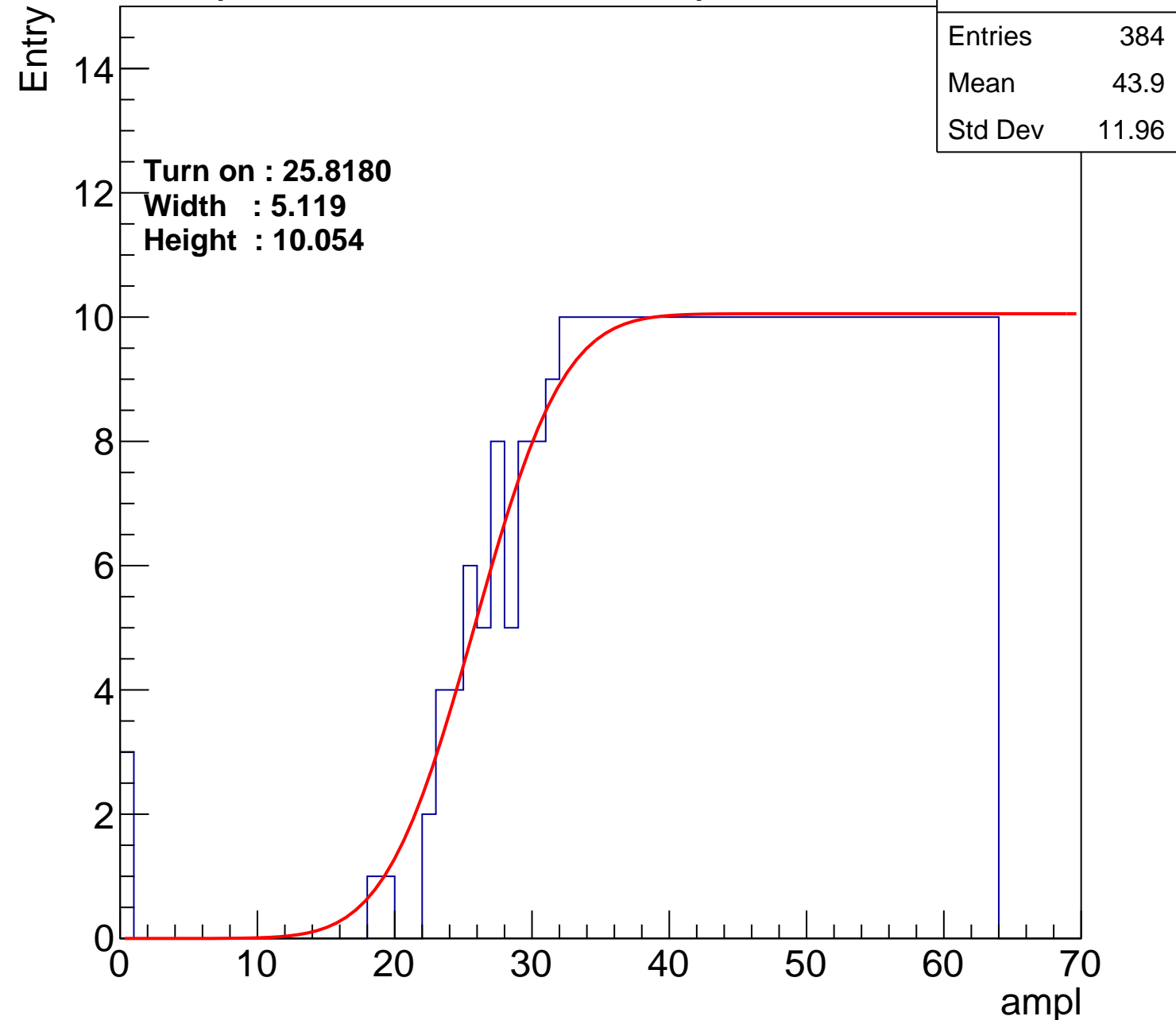
Width : 5.119

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch110

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	401
Mean	42.94
Std Dev	12.72

Turn on : 25.1935

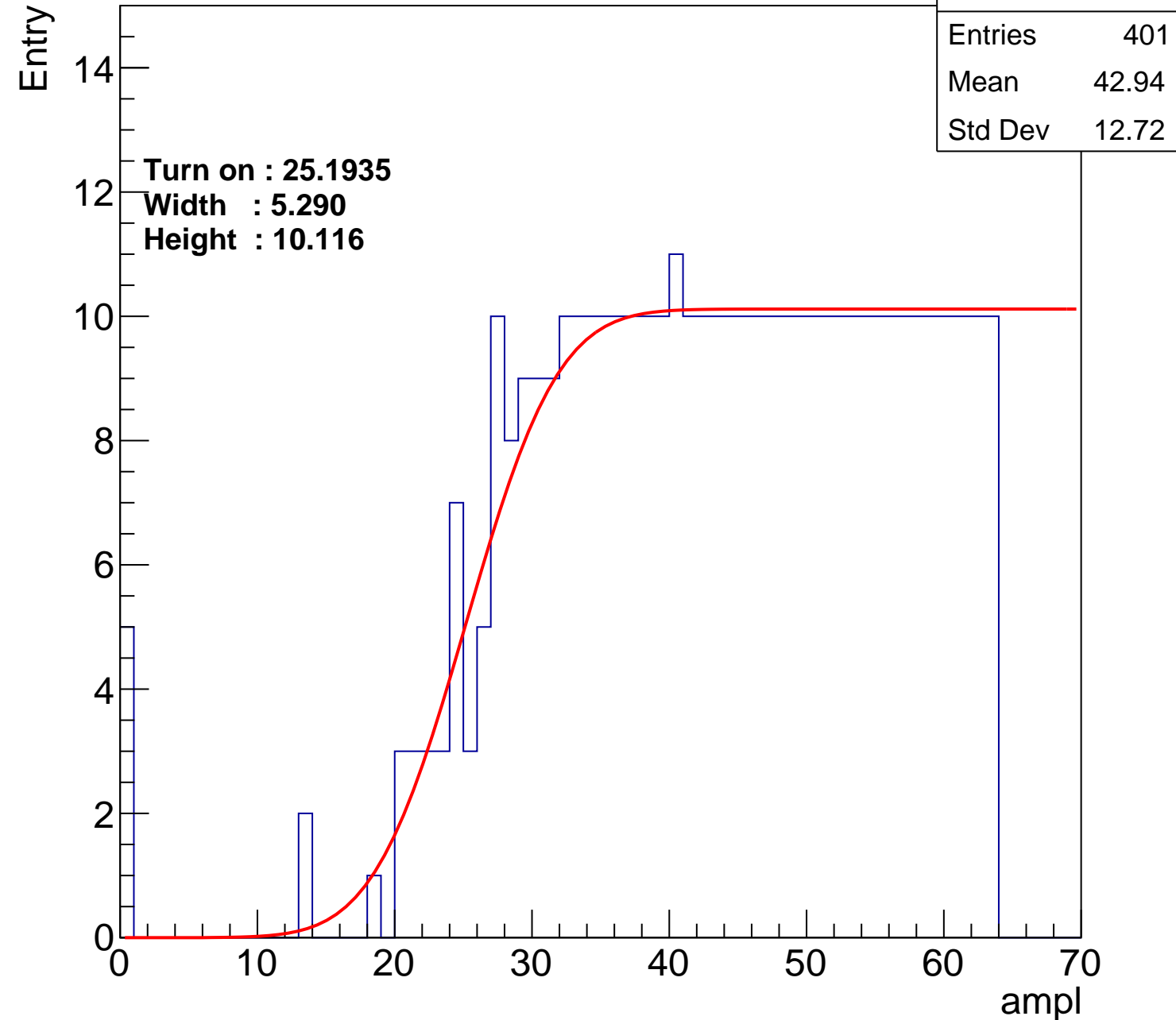
Width : 5.290

Height : 10.116

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U21-ch111

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	356
Mean	45.37
Std Dev	10.93

Turn on : 28.7238

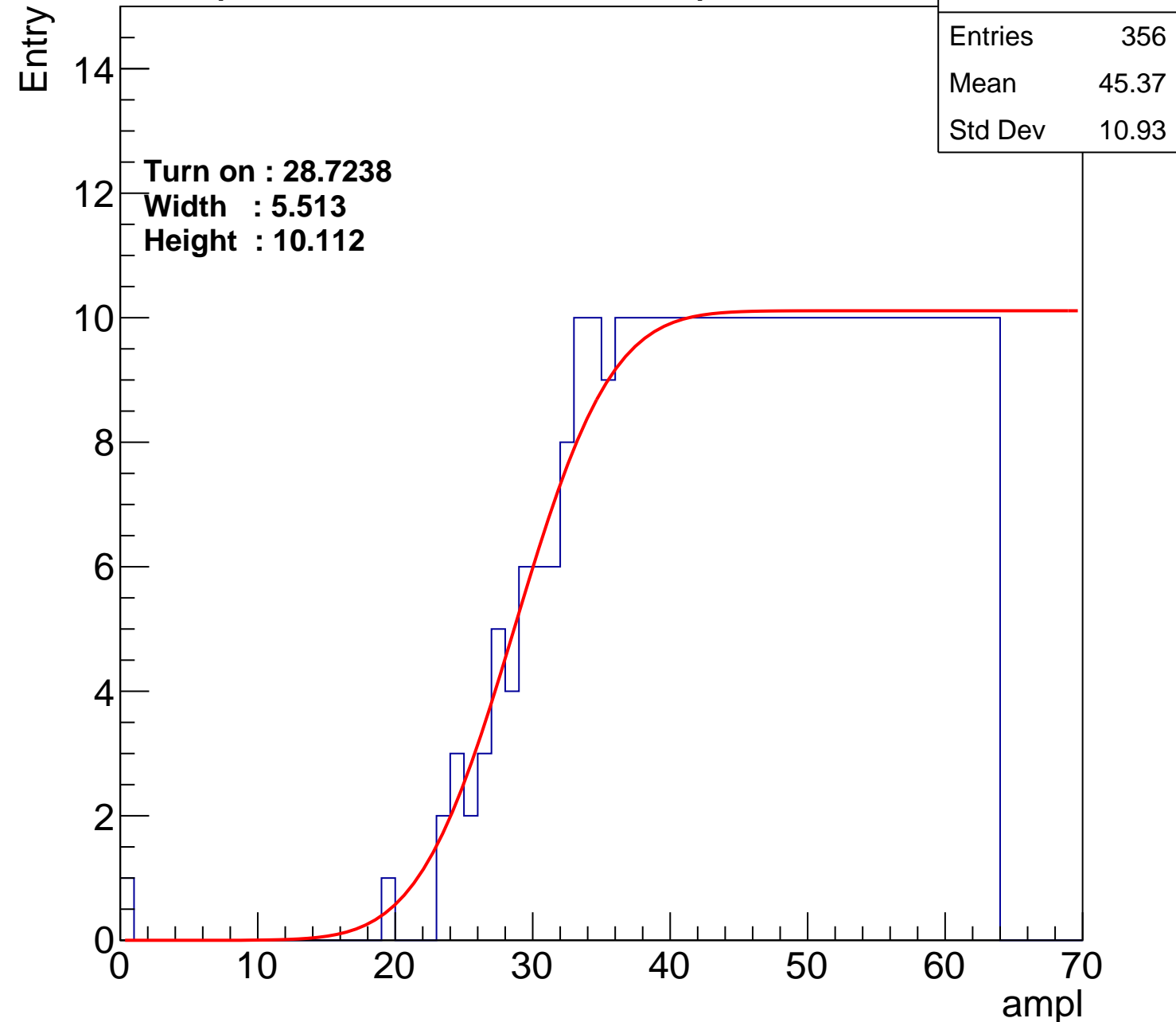
Width : 5.513

Height : 10.112

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch112

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	393
Mean	43.48
Std Dev	12.42

Turn on : 25.6117

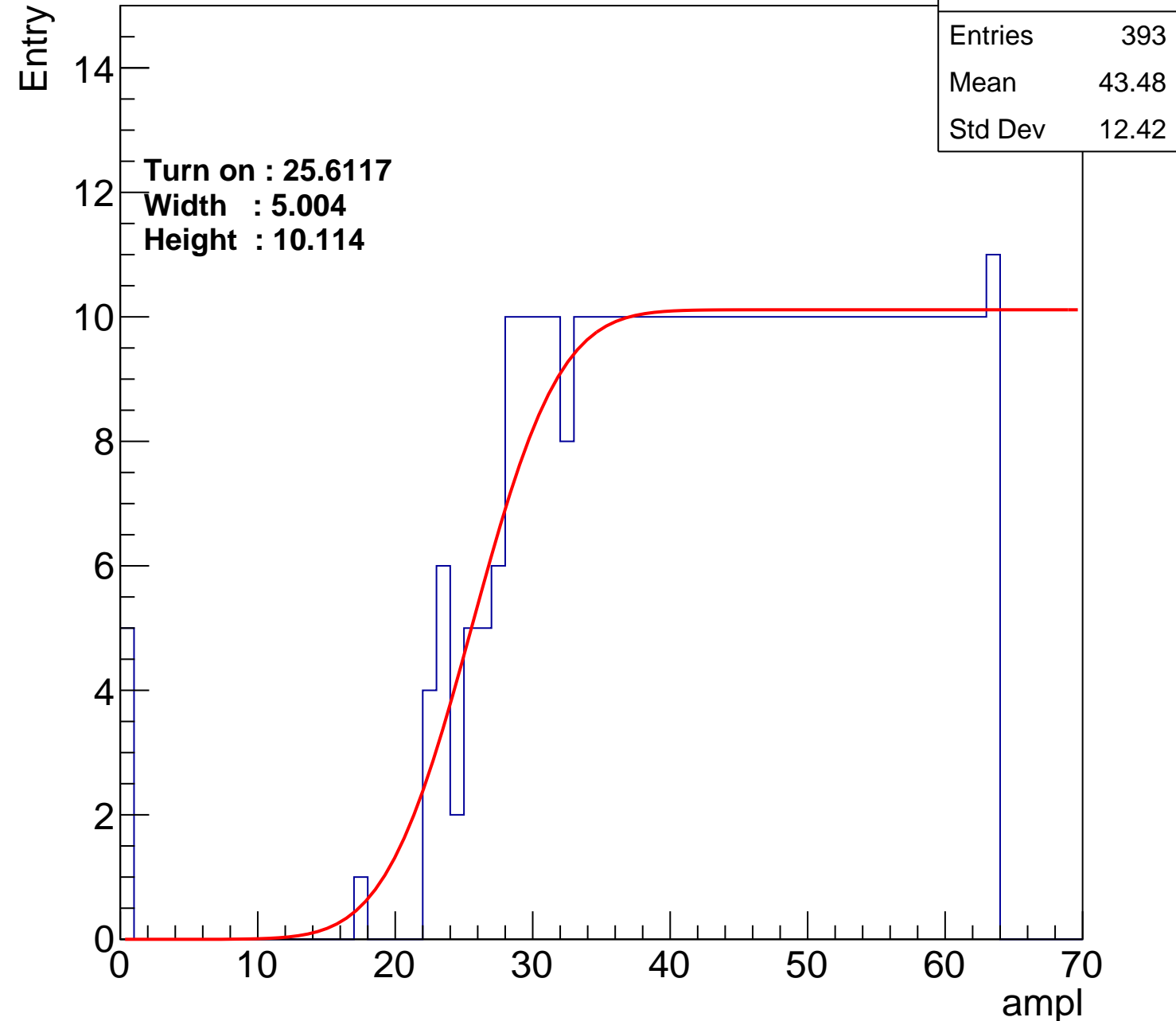
Width : 5.004

Height : 10.114

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch113

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	383
Mean	44.13
Std Dev	11.58

Turn on : 26.1919

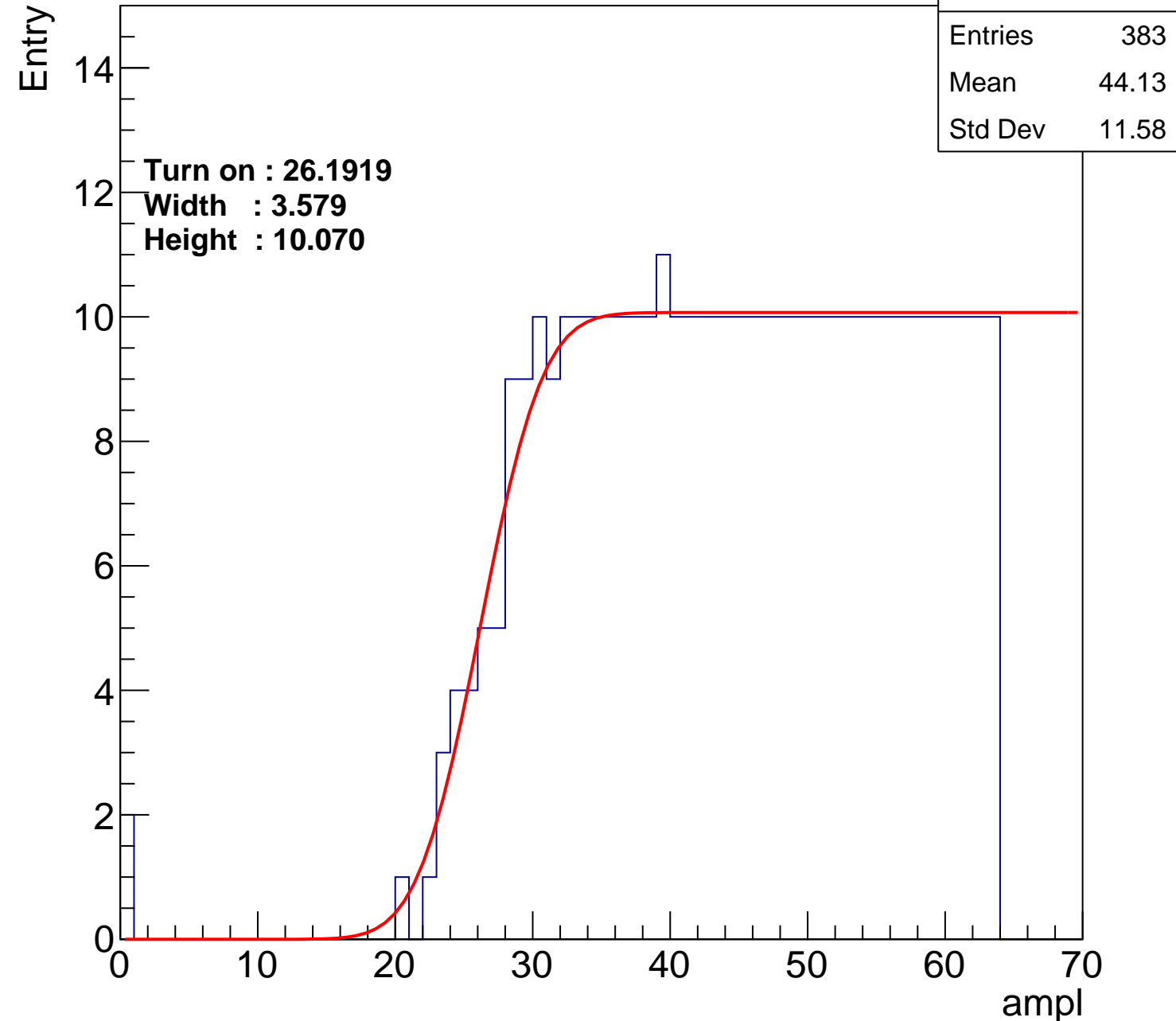
Width : 3.579

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch114

calib\_packv5\_042523\_0143.root, FC#7, port C2

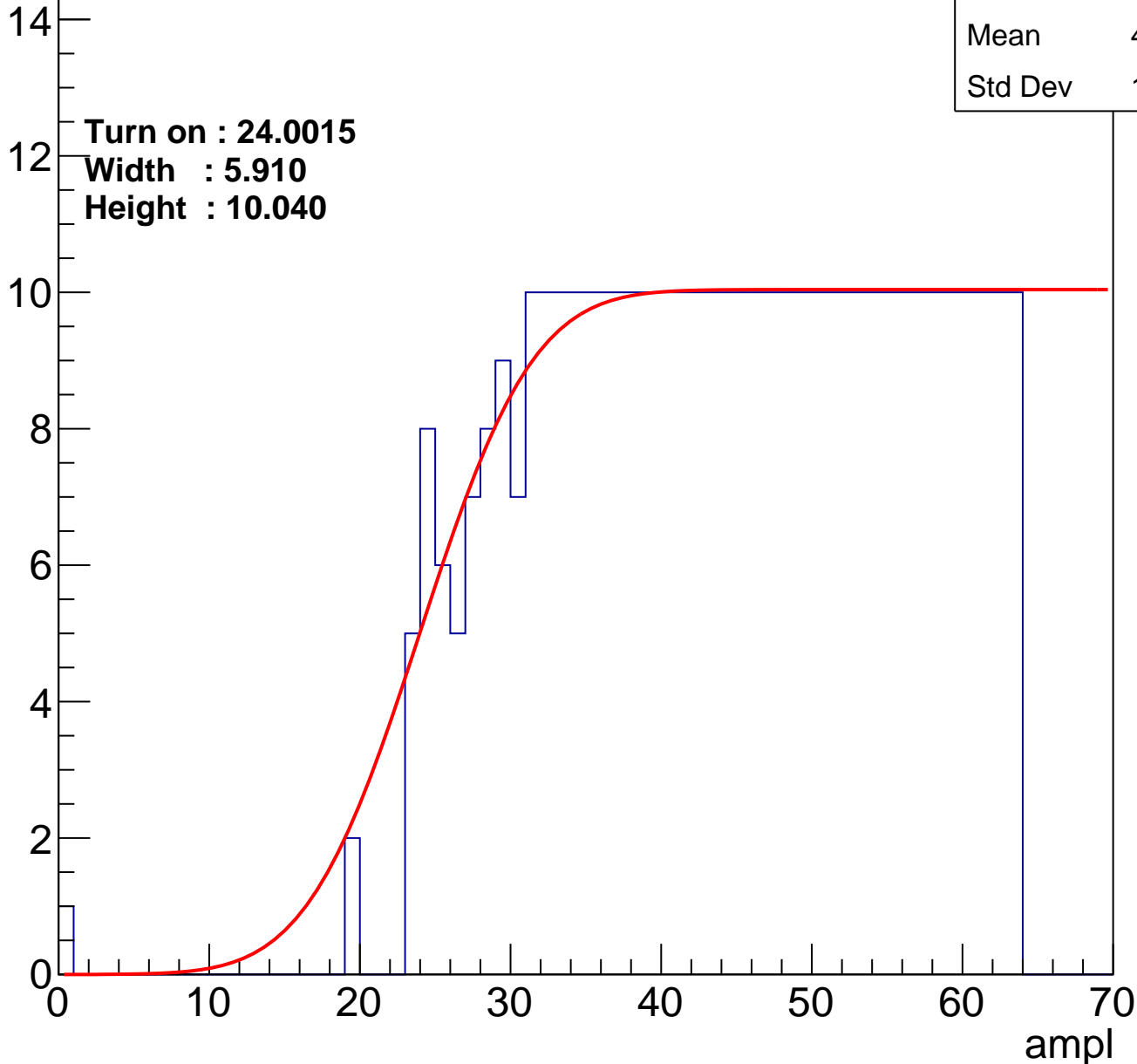
Entries	388
Mean	43.86
Std Dev	11.66

Turn on : 24.0015

Width : 5.910

Height : 10.040

Entry



# B1L103S, U21-ch115

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	394
Mean	43.33
Std Dev	12.46

Turn on : 26.1539

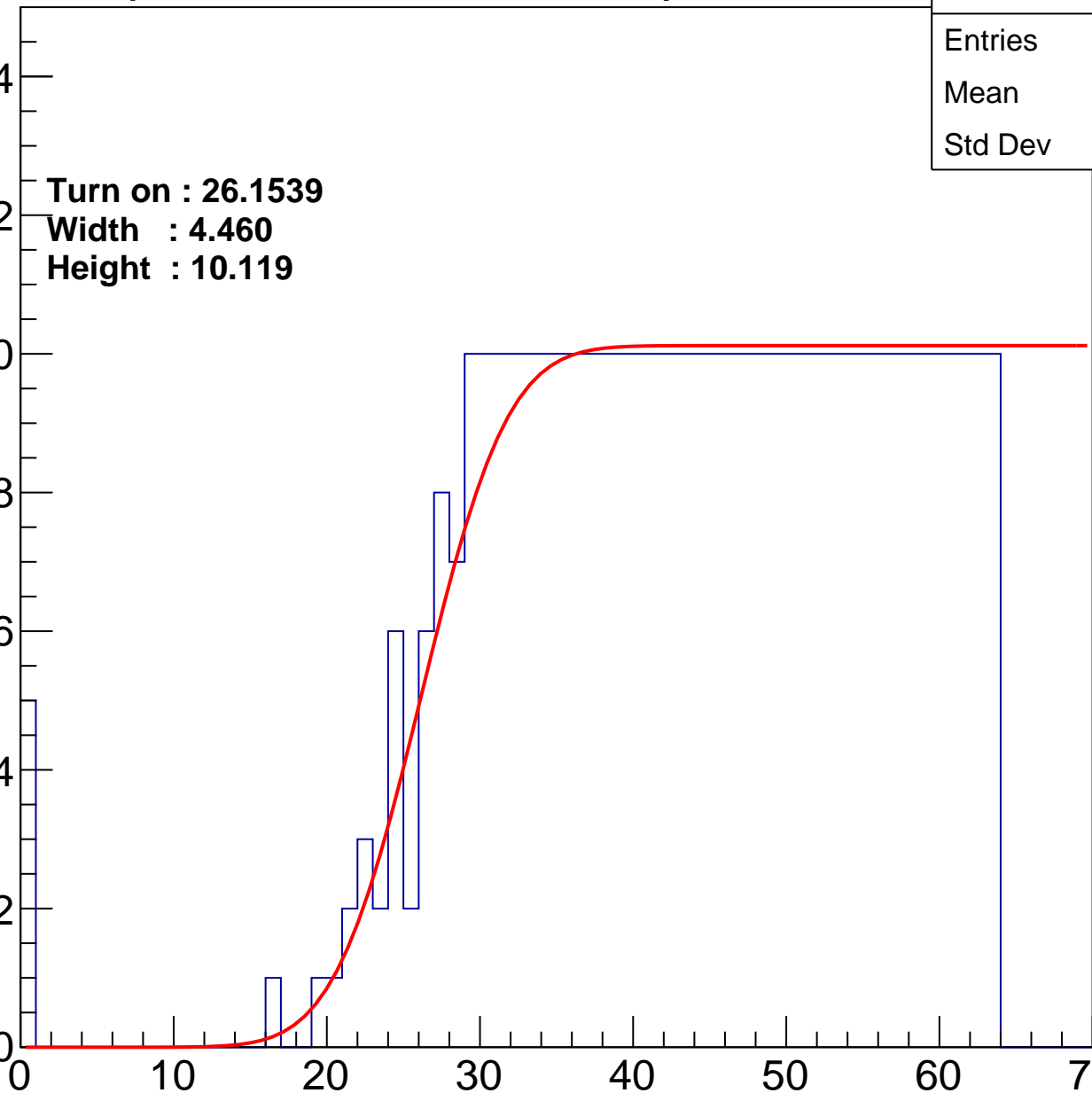
Width : 4.460

Height : 10.119

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch116

calib\_packv5\_042523\_0143.root, FC#7, port C2

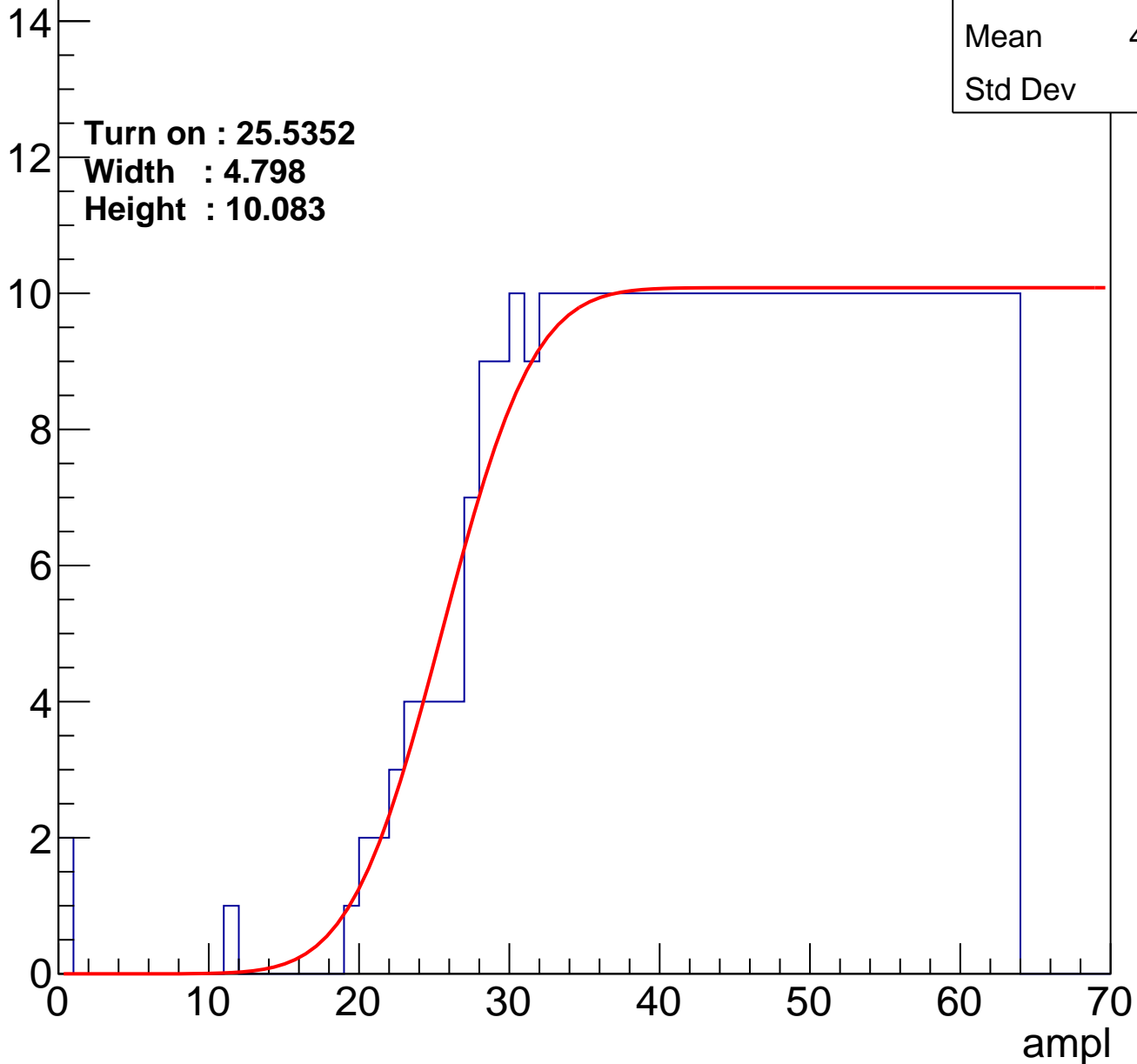
Entries	391
Mean	43.61
Std Dev	12

Turn on : 25.5352

Width : 4.798

Height : 10.083

Entry



# B1L103S, U21-ch117

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	372
Mean	44.71
Std Dev	11.15

Turn on : 27.0275

Width : 3.453

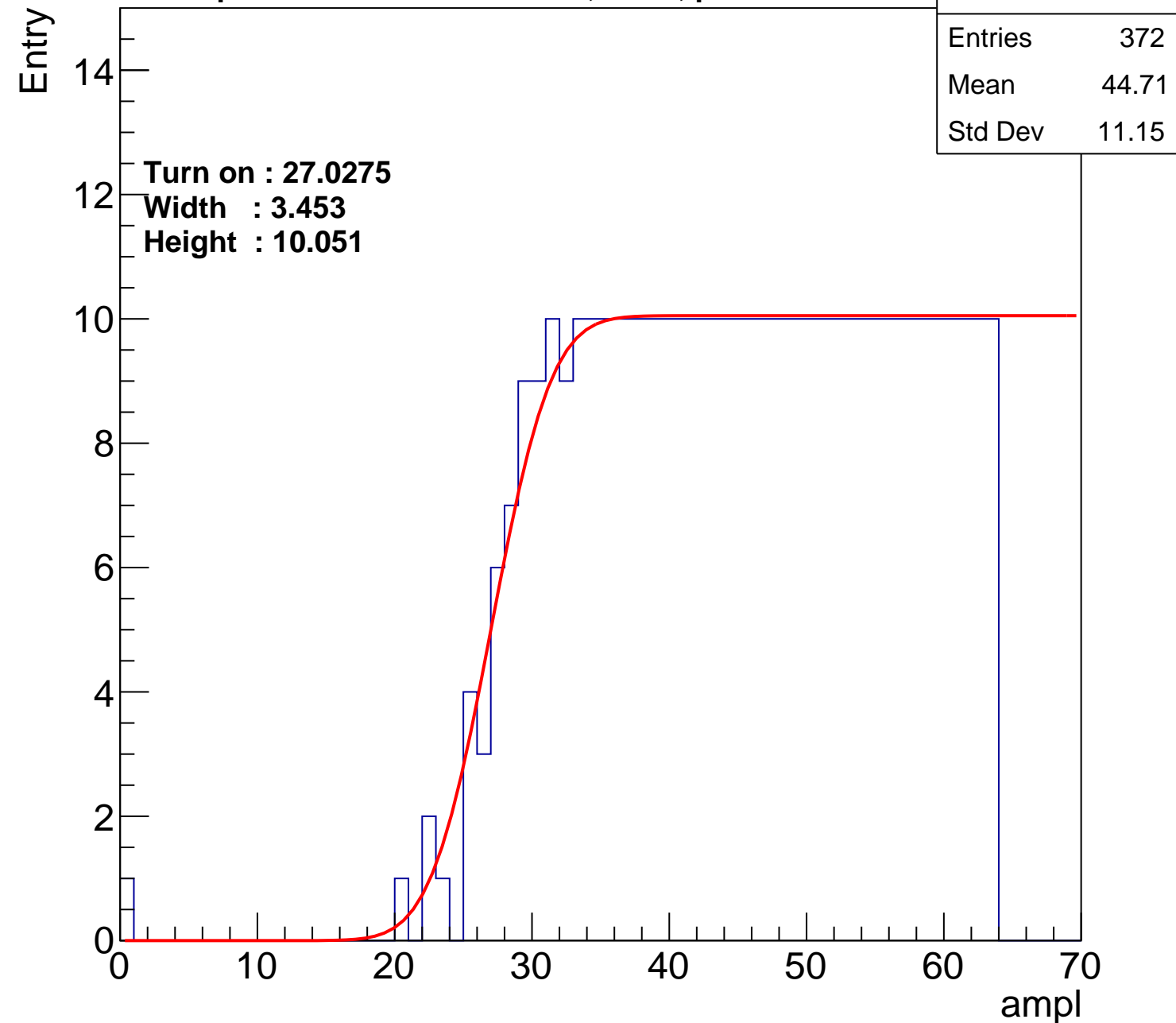
Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U21-ch118

calib\_packv5\_042523\_0143.root, FC#7, port C2

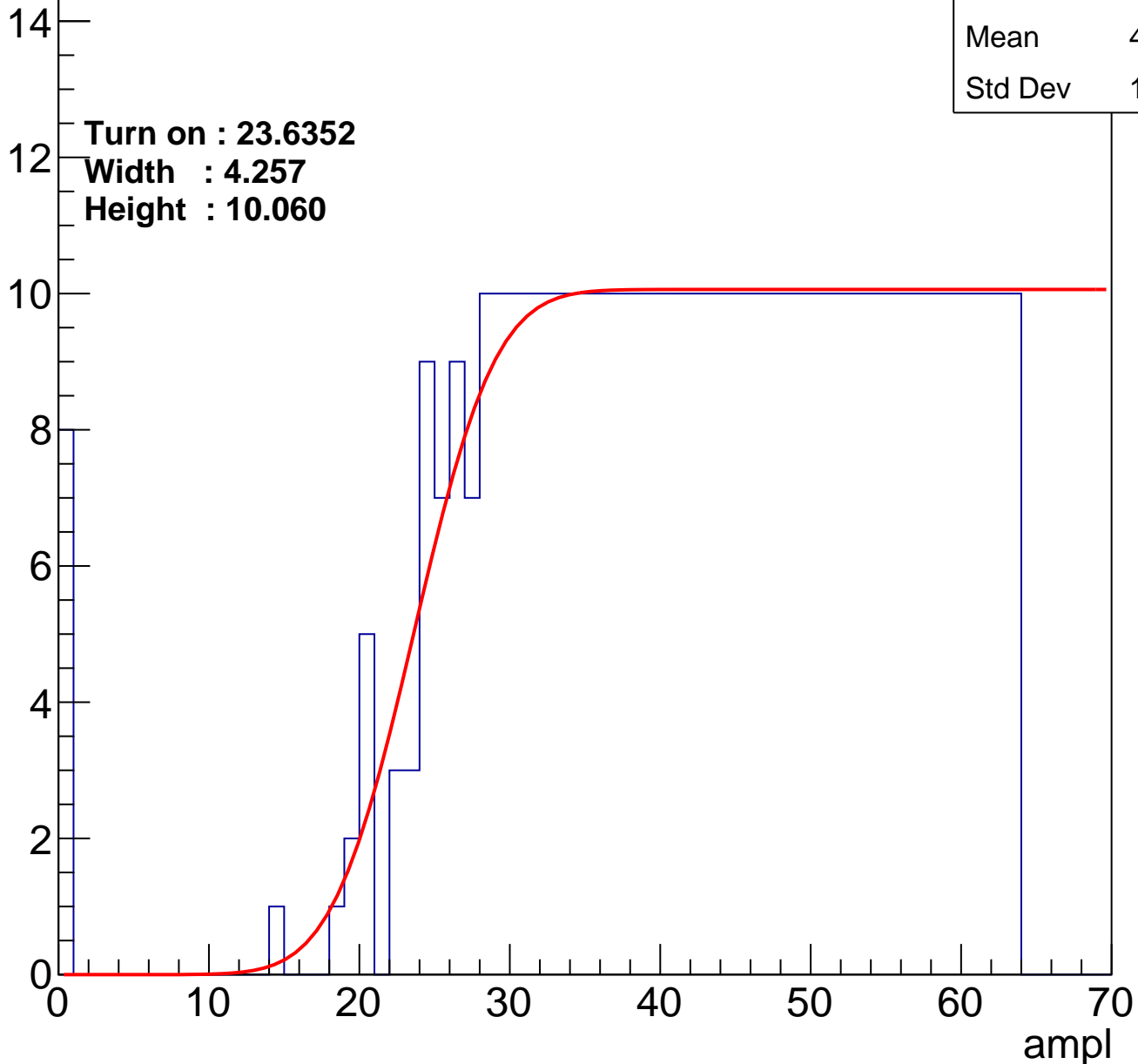
Entries	415
Mean	42.17
Std Dev	13.29

Turn on : 23.6352

Width : 4.257

Height : 10.060

Entry





# B1L103S, U21-ch119

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	377
Mean	44.24
Std Dev	11.79

Turn on : 27.5061

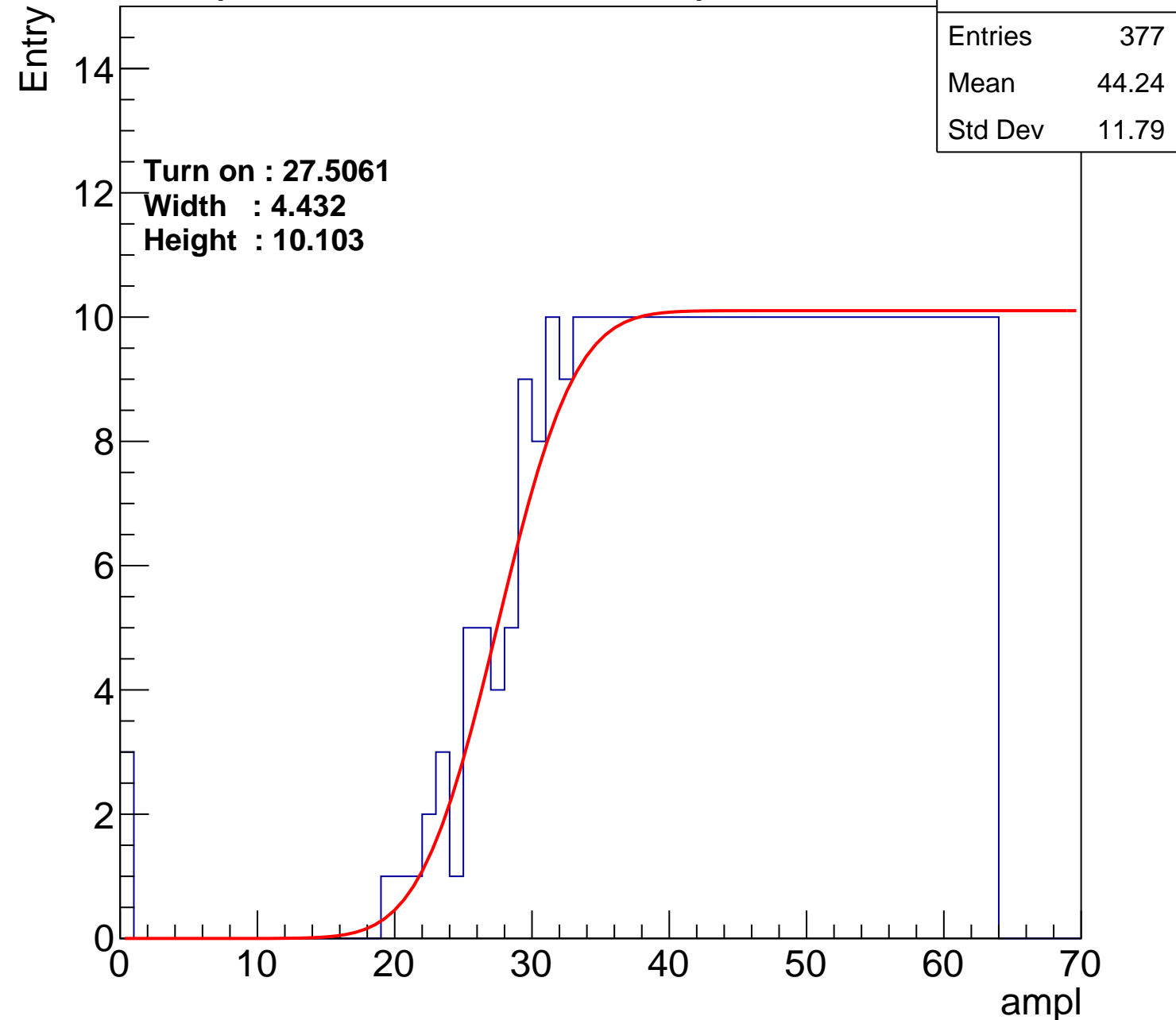
Width : 4.432

Height : 10.103

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch120

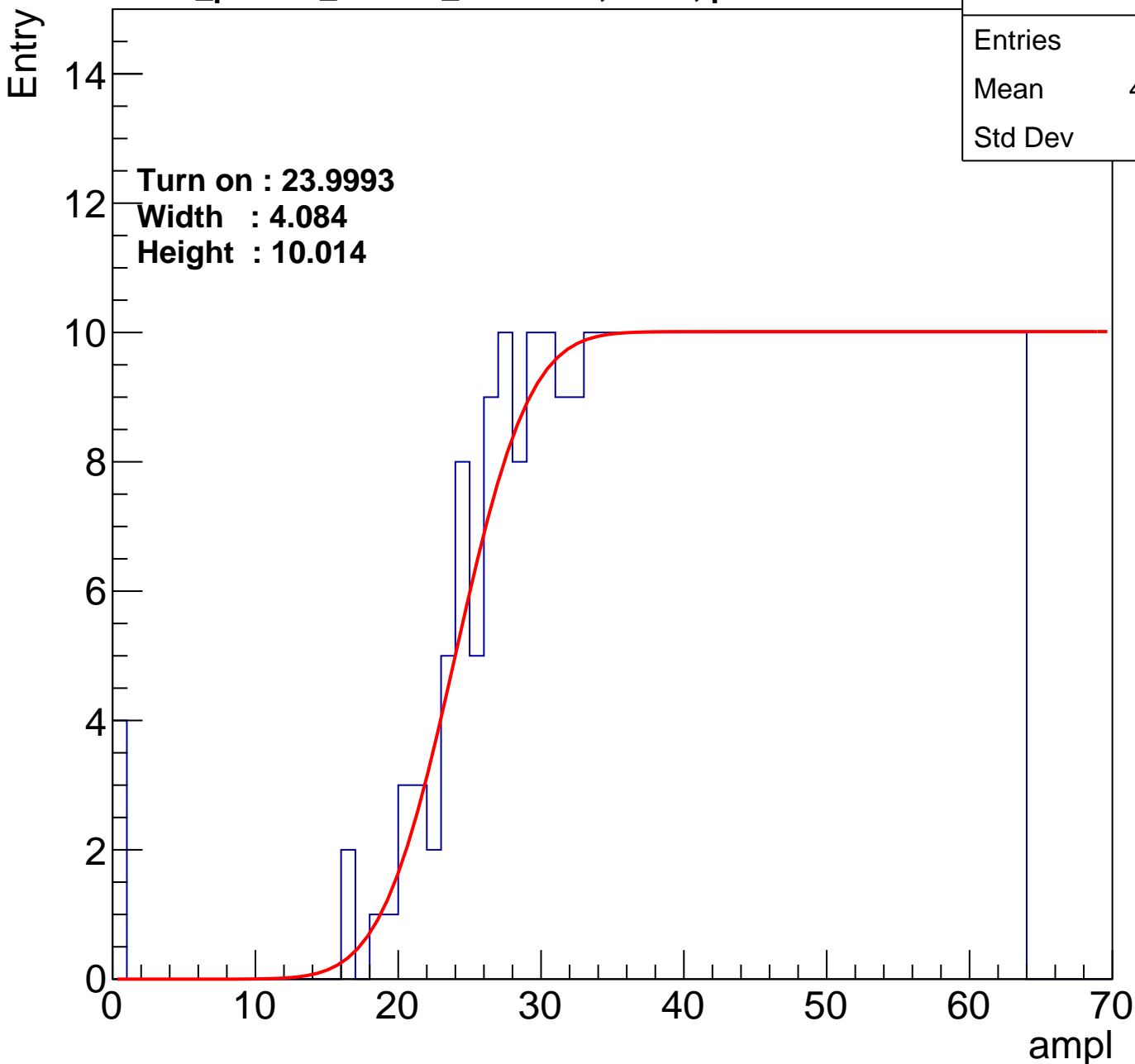
**calib\_packv5\_042523\_0143.root, FC#7, port C2**

Entries	409
Mean	42.62
Std Dev	12.7

**Turn on : 23.9993**

**Width : 4.084**

**Height : 10.014**



# B1L103S, U21-ch121

calib\_packv5\_042523\_0143.root, FC#7, port C2

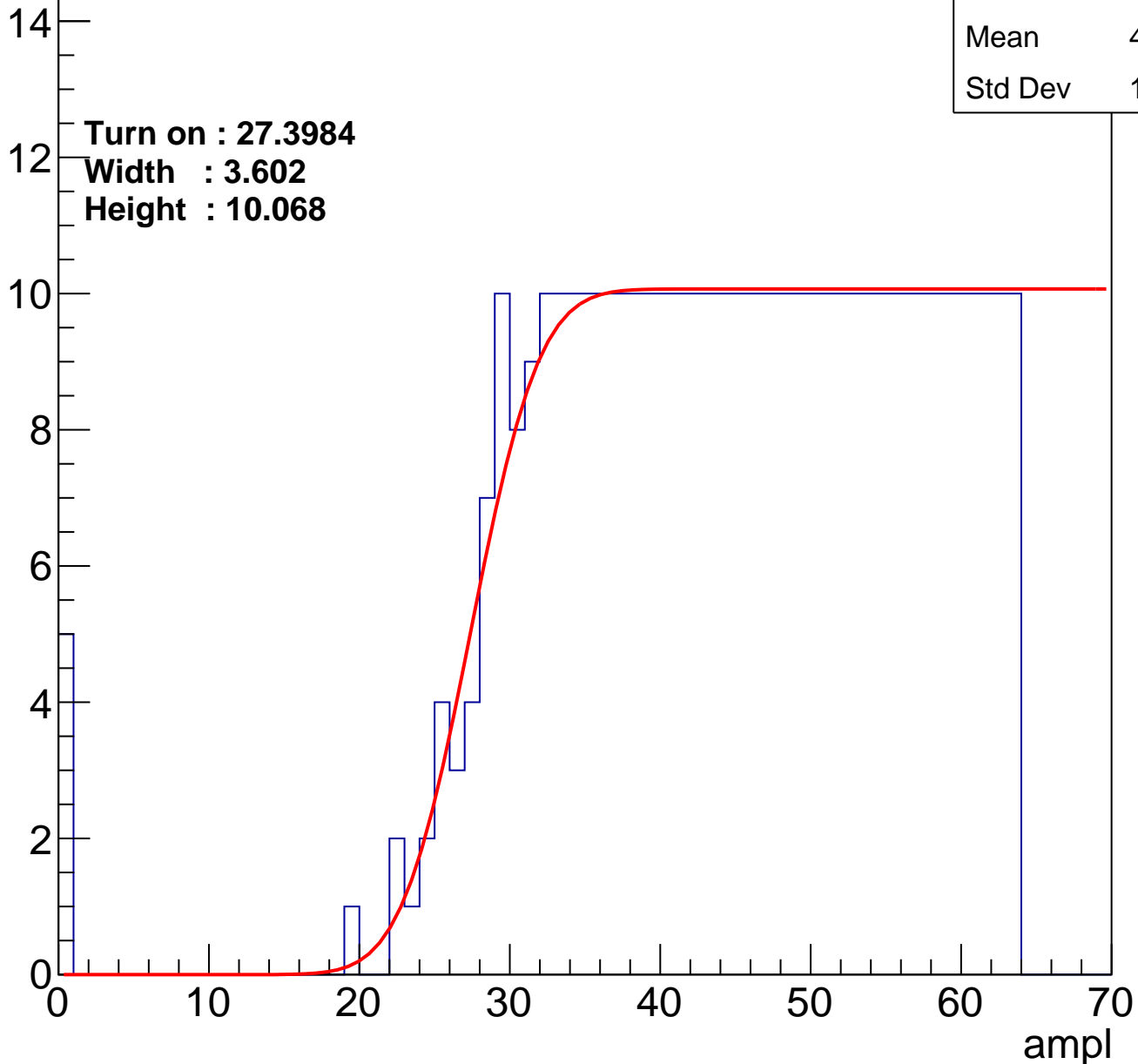
Entries	376
Mean	44.22
Std Dev	12.03

Turn on : 27.3984

Width : 3.602

Height : 10.068

Entry



# B1L103S, U21-ch122

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	396
Mean	43.32
Std Dev	12.22

Turn on : 25.4187

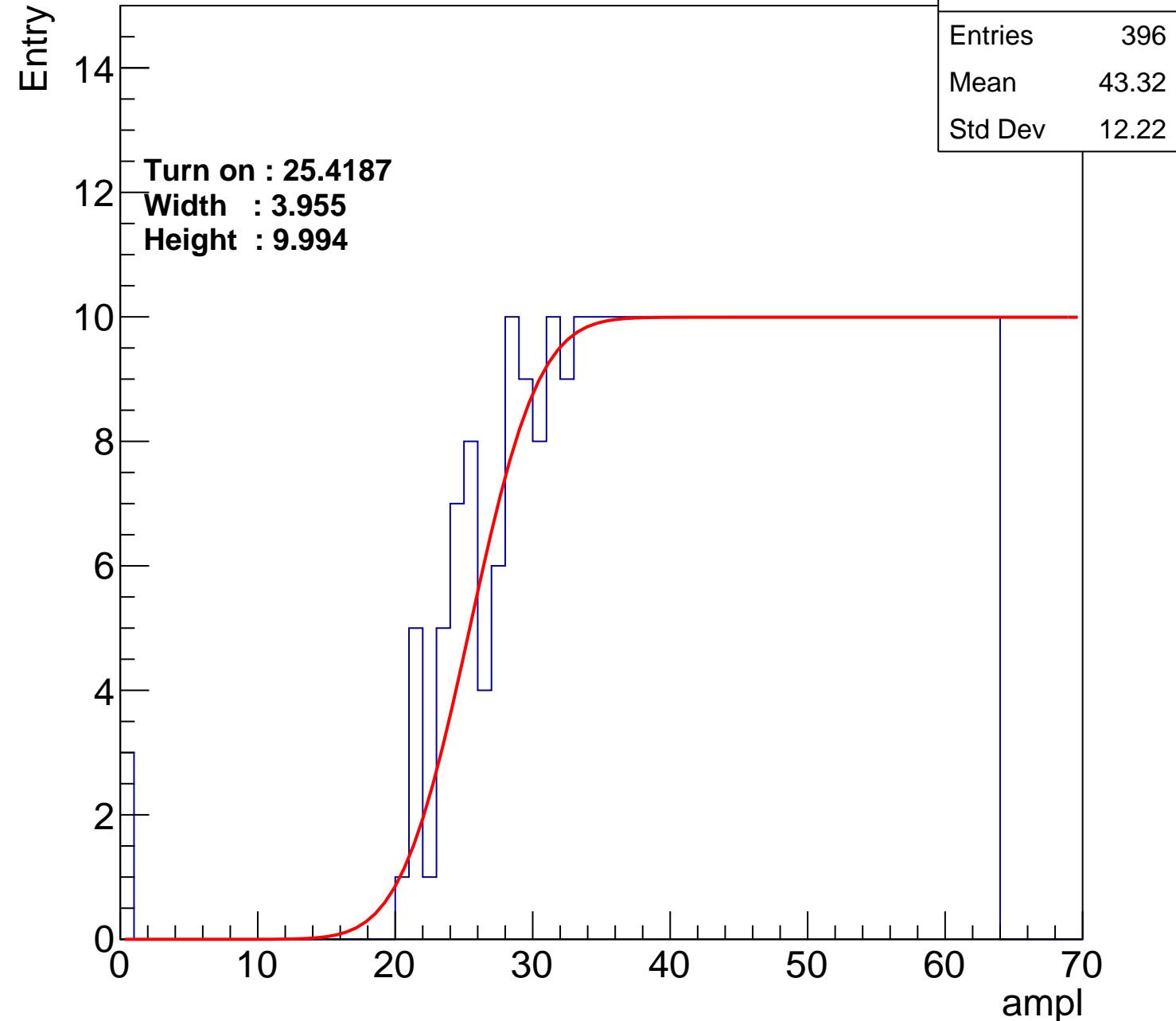
Width : 3.955

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch123

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	379
Mean	44.19
Std Dev	11.86

Turn on : 26.7685

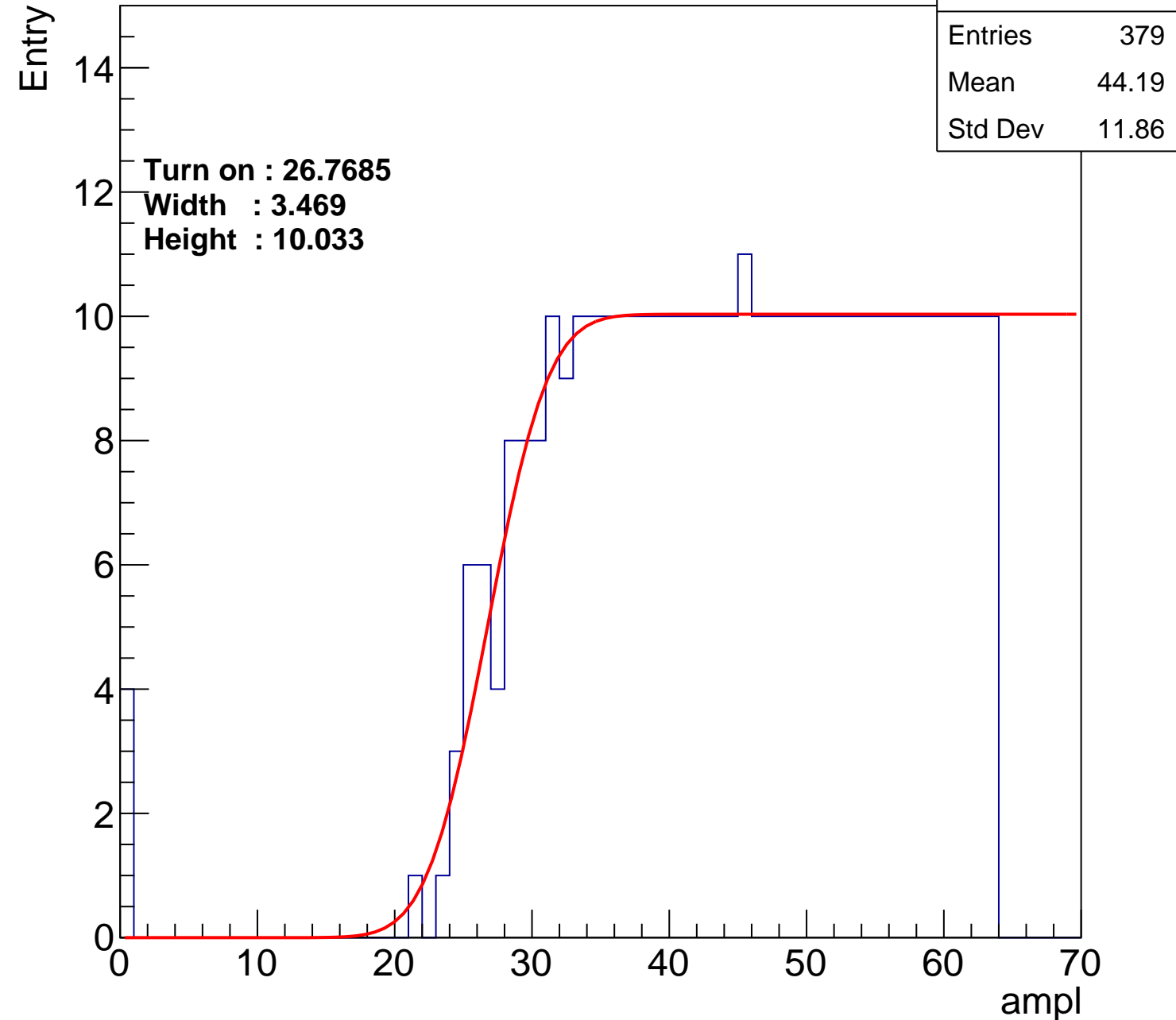
Width : 3.469

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch124

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	396
Mean	43.35
Std Dev	12.26

Turn on : 24.9812

Width : 2.117

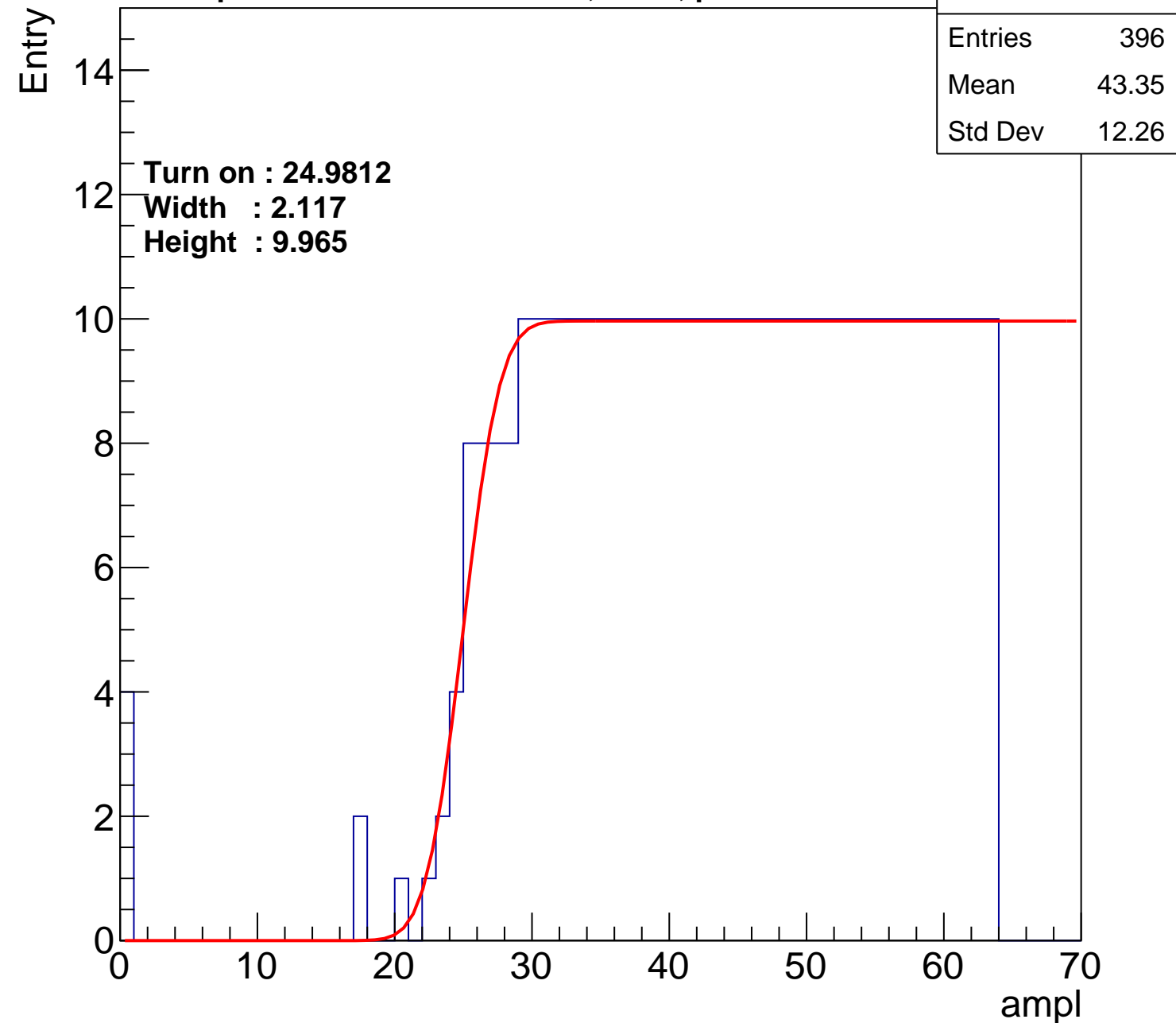
Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U21-ch125

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	384
Mean	43.93
Std Dev	12.01

Turn on : 25.8889

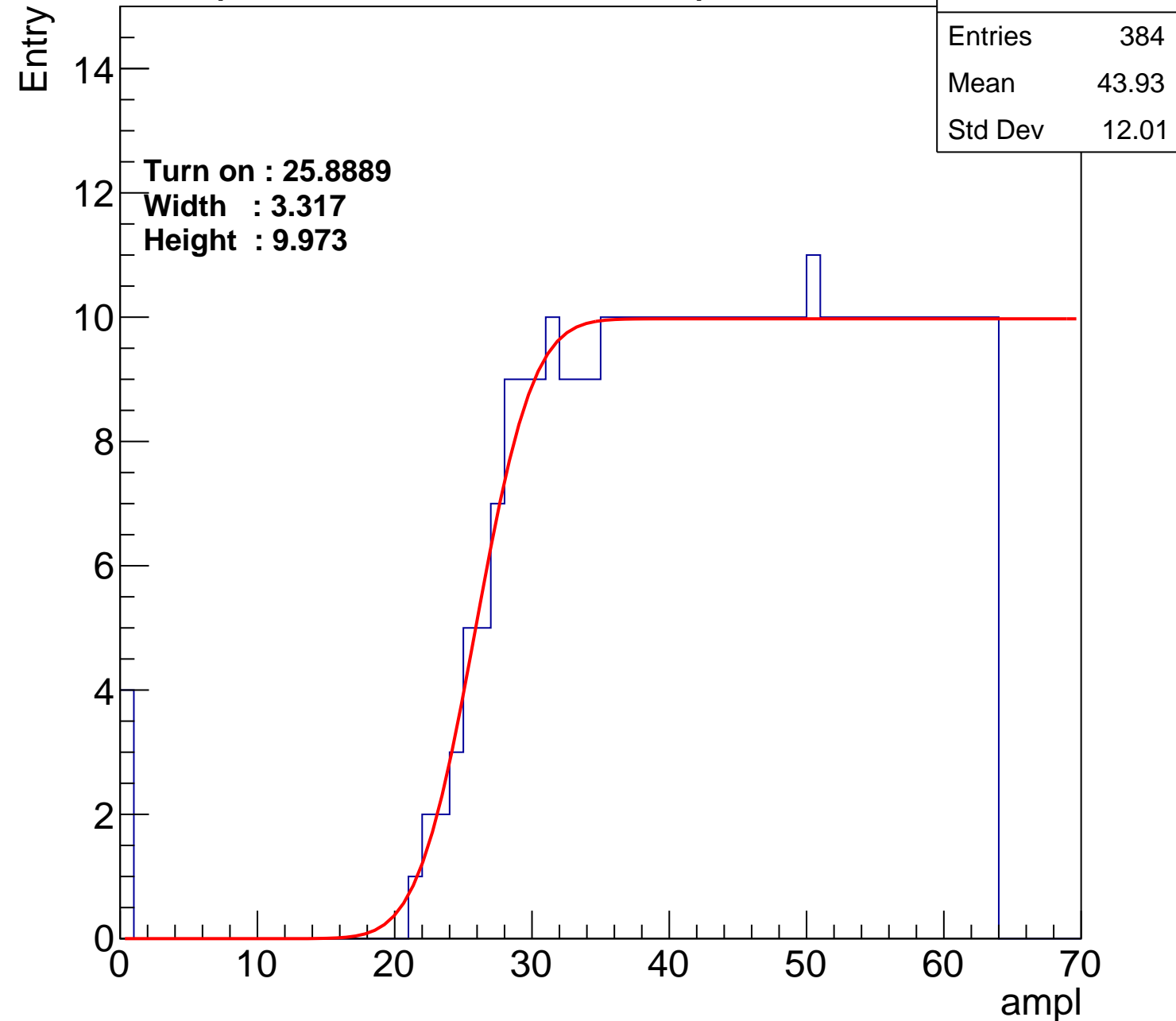
Width : 3.317

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U21-ch126

calib\_packv5\_042523\_0143.root, FC#7, port C2

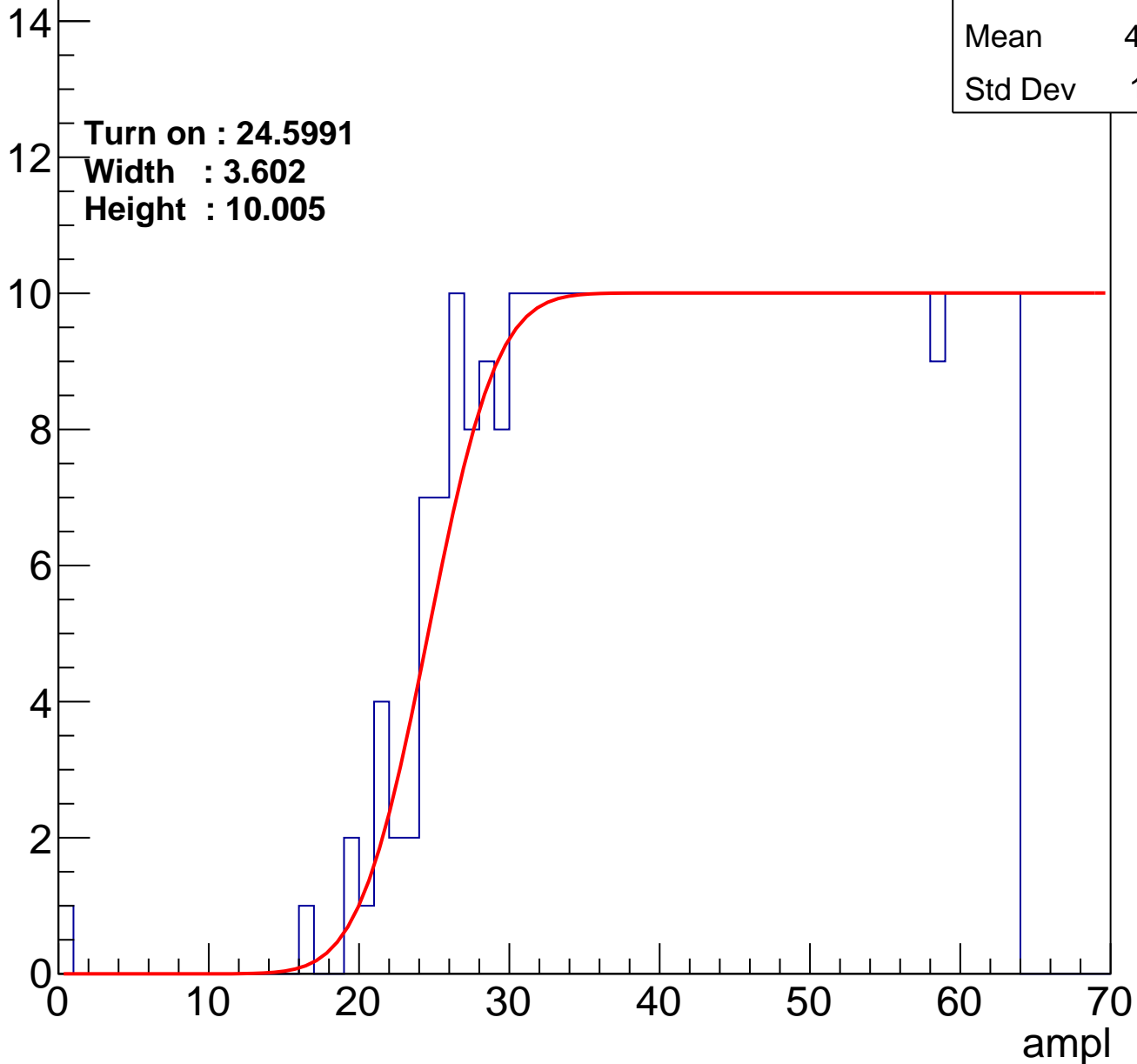
Entries	401
Mean	43.15
Std Dev	12.01

Turn on : 24.5991

Width : 3.602

Height : 10.005

Entry





# B1L103S, U21-ch127

calib\_packv5\_042523\_0143.root, FC#7, port C2

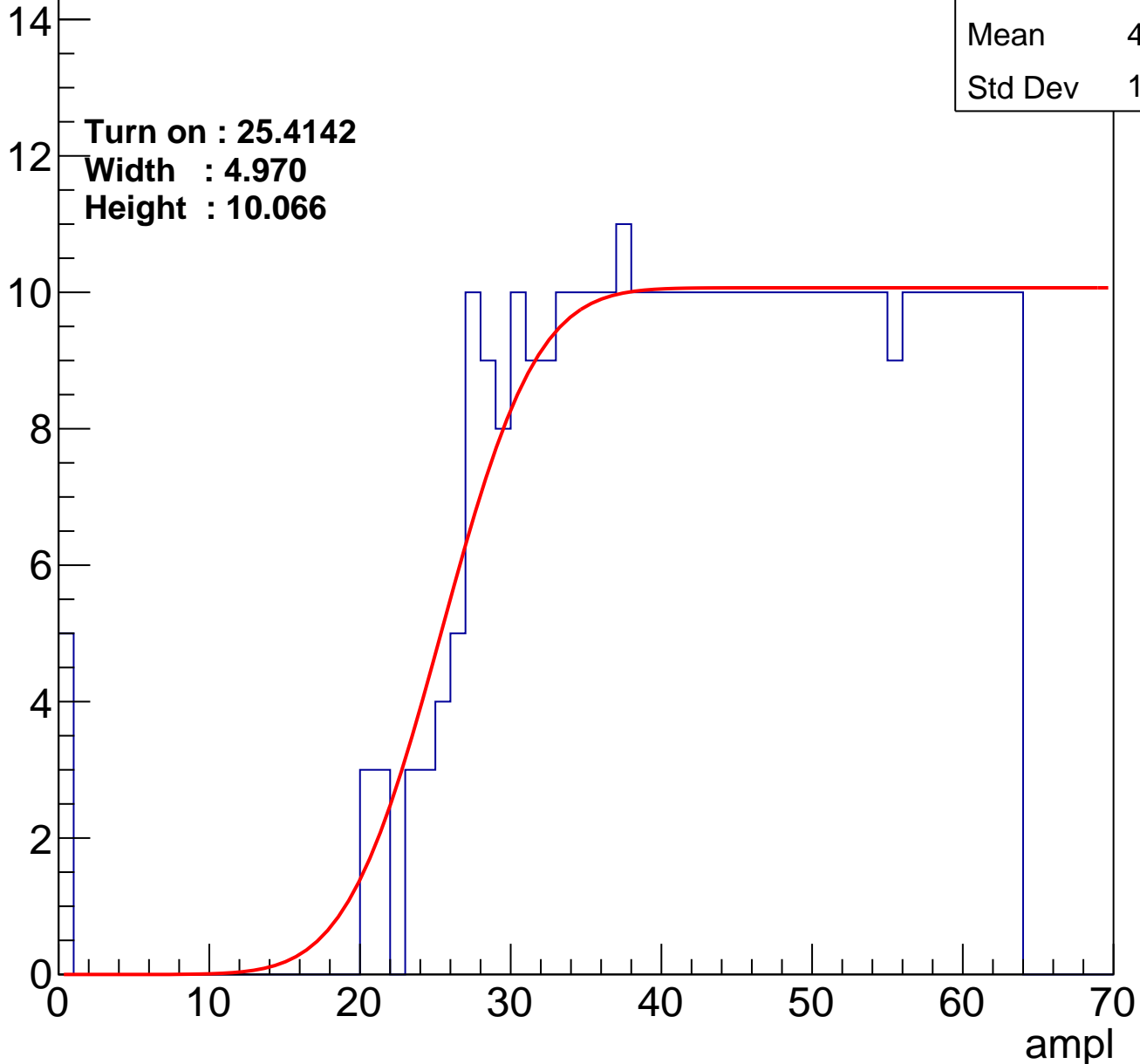
Entries	391
Mean	43.42
Std Dev	12.38

Turn on : 25.4142

Width : 4.970

Height : 10.066

Entry



# B1L103S, U21-ch127

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	391
Mean	43.42
Std Dev	12.38

Turn on : 25.4142

Width : 4.970

Height : 10.066

Entry

