



# B1L103S, U14-ch0

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.49
Std Dev	11.63

**Turn on : 26.9770**

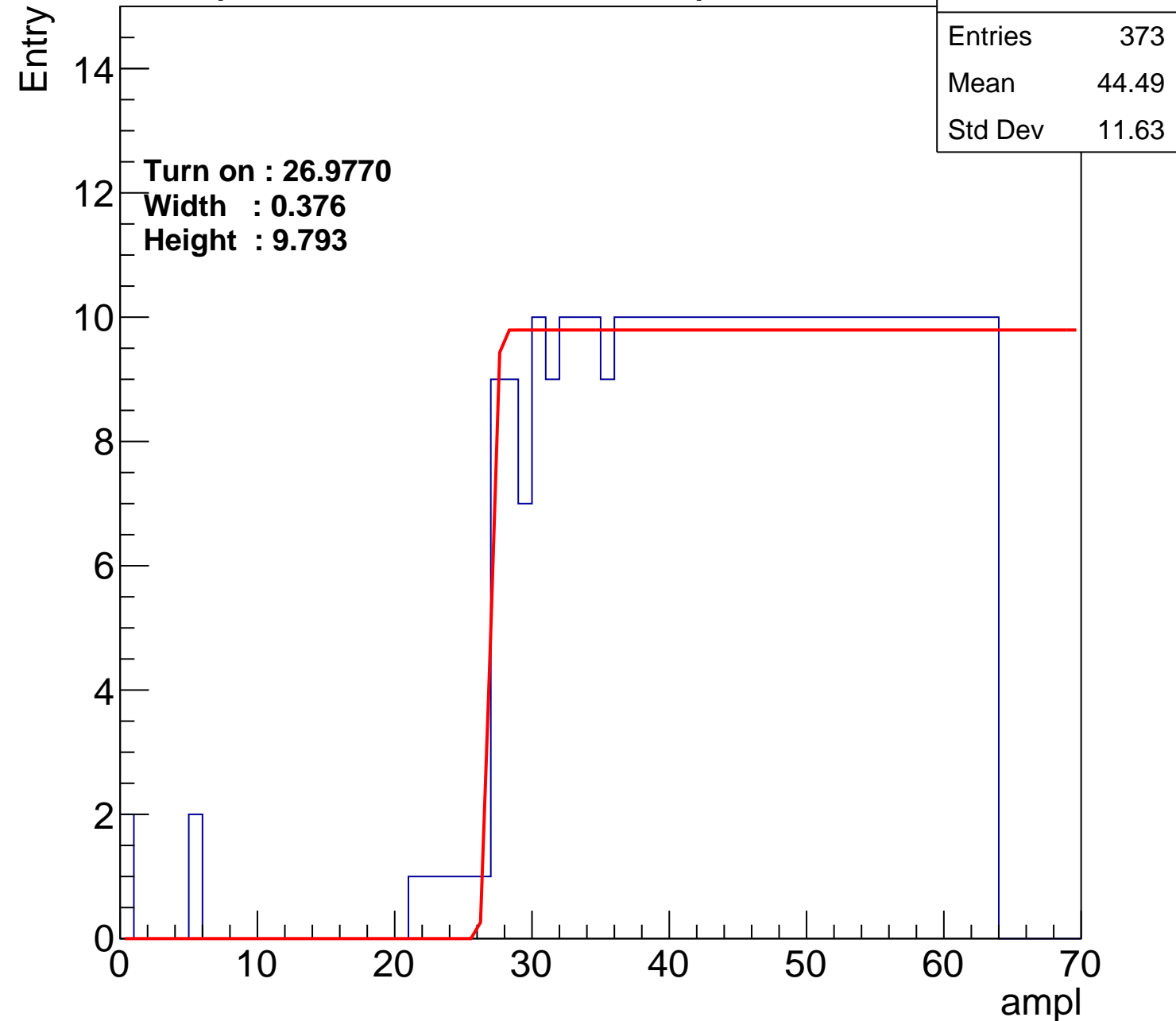
**Width : 0.376**

**Height : 9.793**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch1

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	396
Mean	43.32
Std Dev	12.24

Turn on : 24.7620

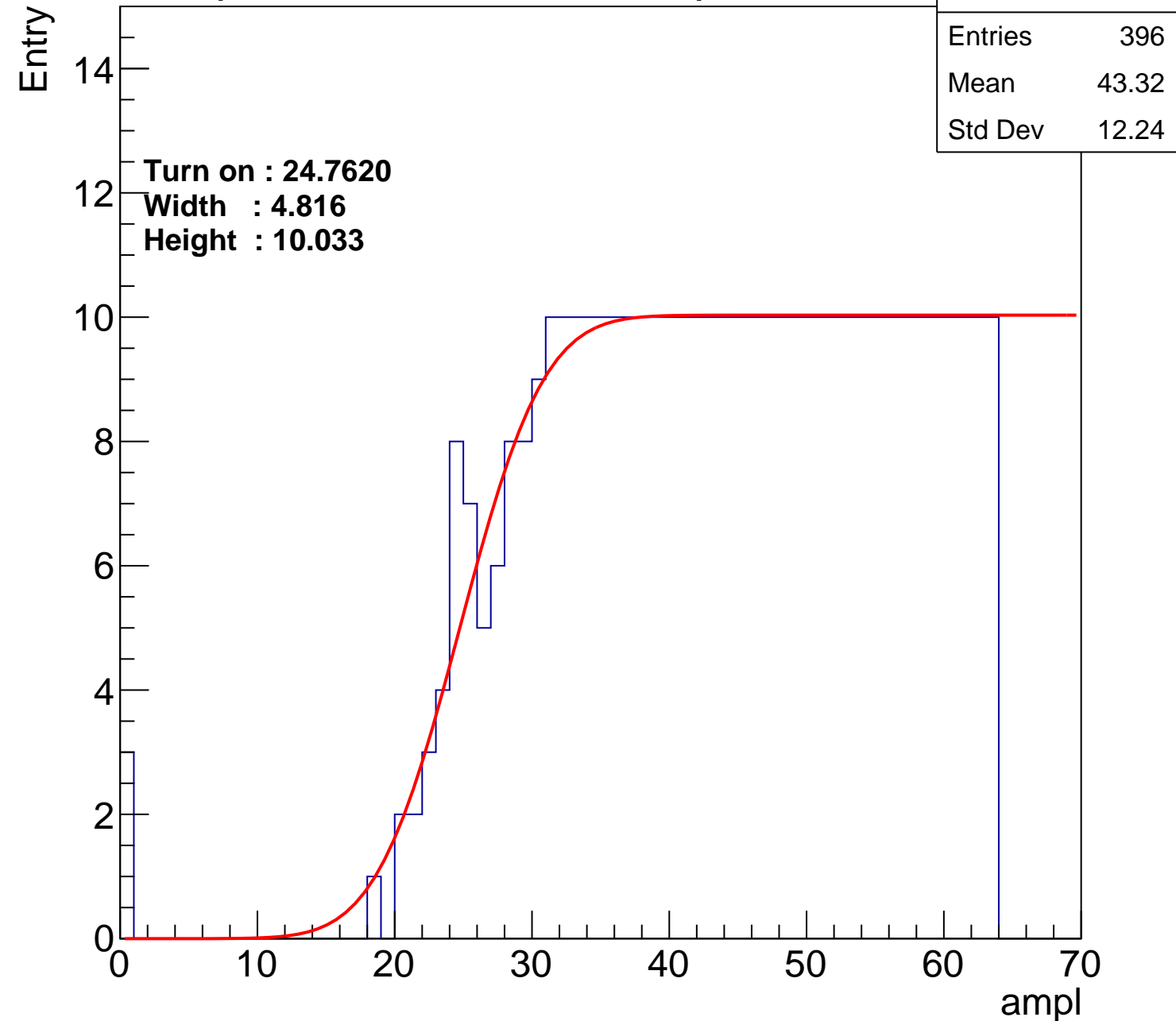
Width : 4.816

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch2

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	382
Mean	44.08
Std Dev	11.71

**Turn on : 26.2708**

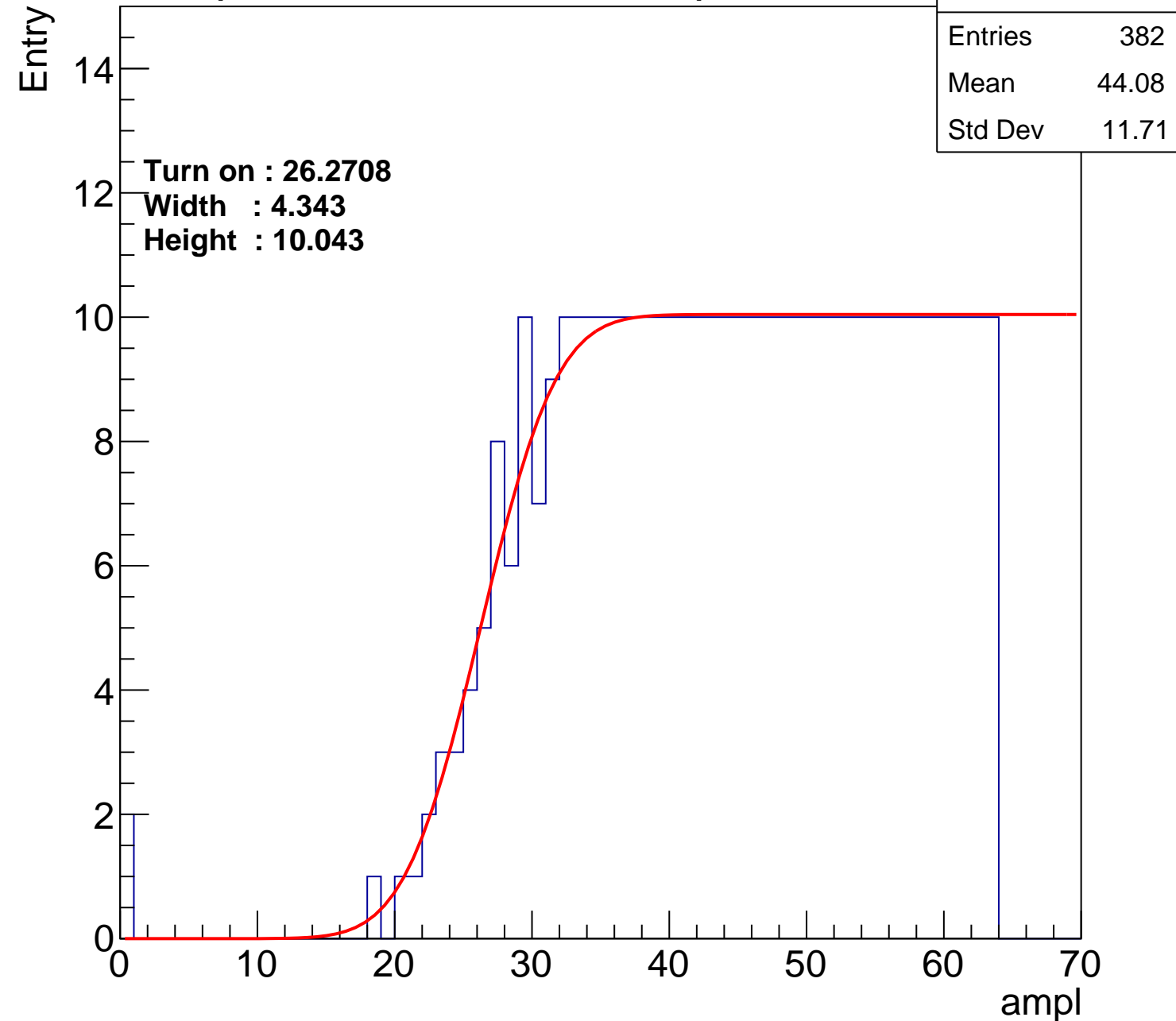
**Width : 4.343**

**Height : 10.043**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch3

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	391
Mean	43.68
Std Dev	11.93

**Turn on : 25.5970**

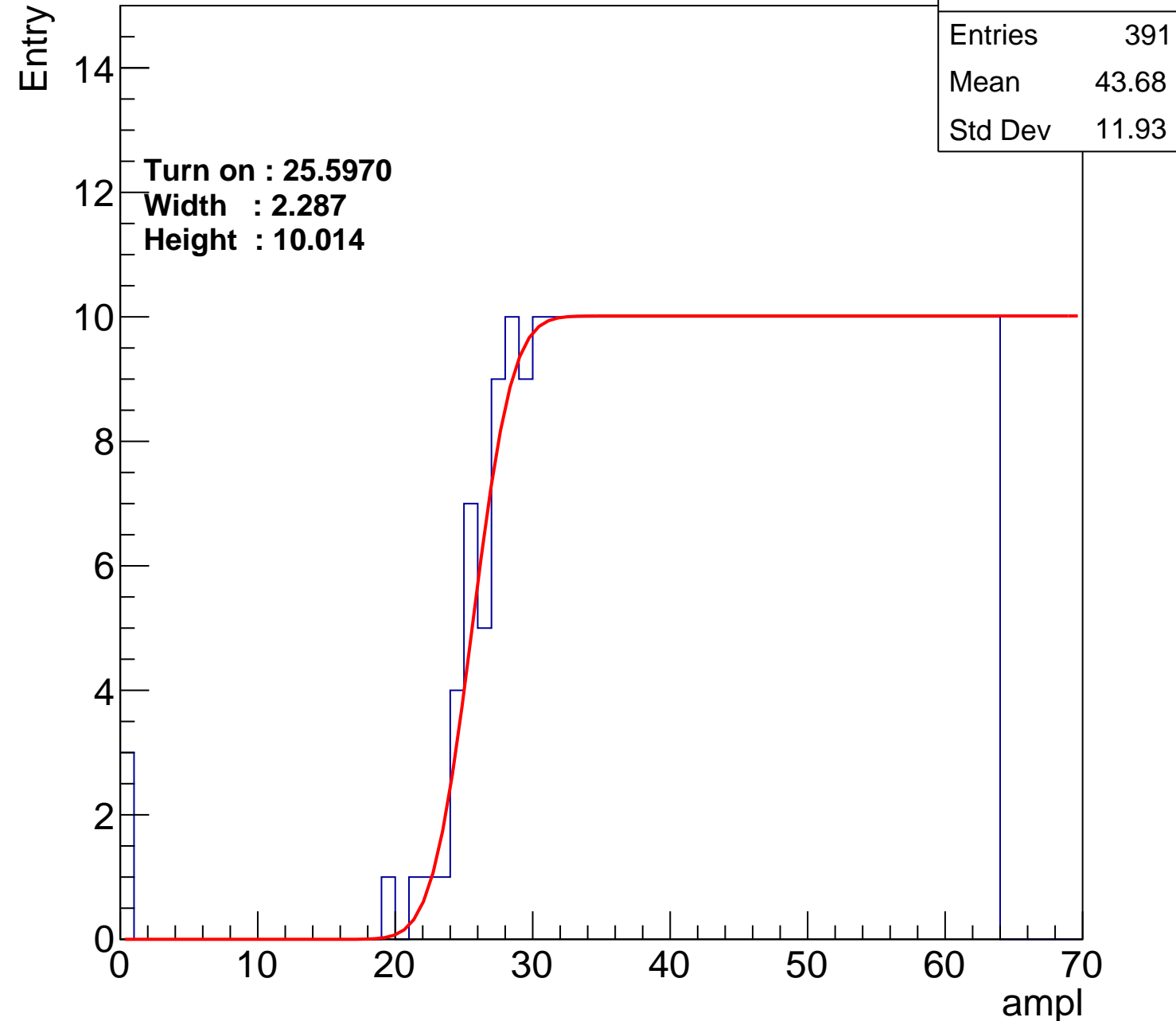
**Width : 2.287**

**Height : 10.014**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch4

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	366
Mean	44.69
Std Dev	11.83

Turn on : 27.9651

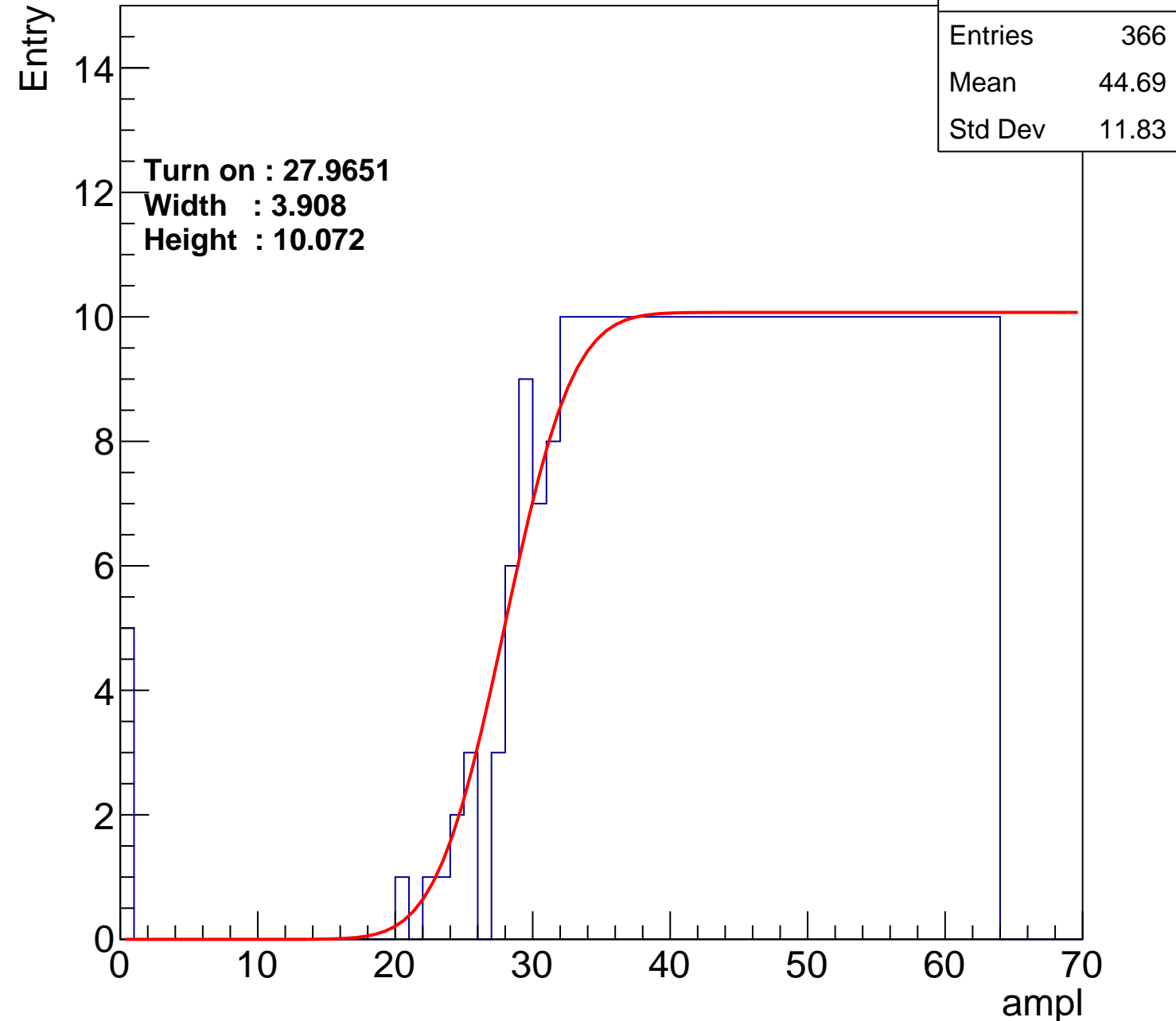
Width : 3.908

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch5

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	385
Mean	43.94
Std Dev	11.85

**Turn on : 25.9435**

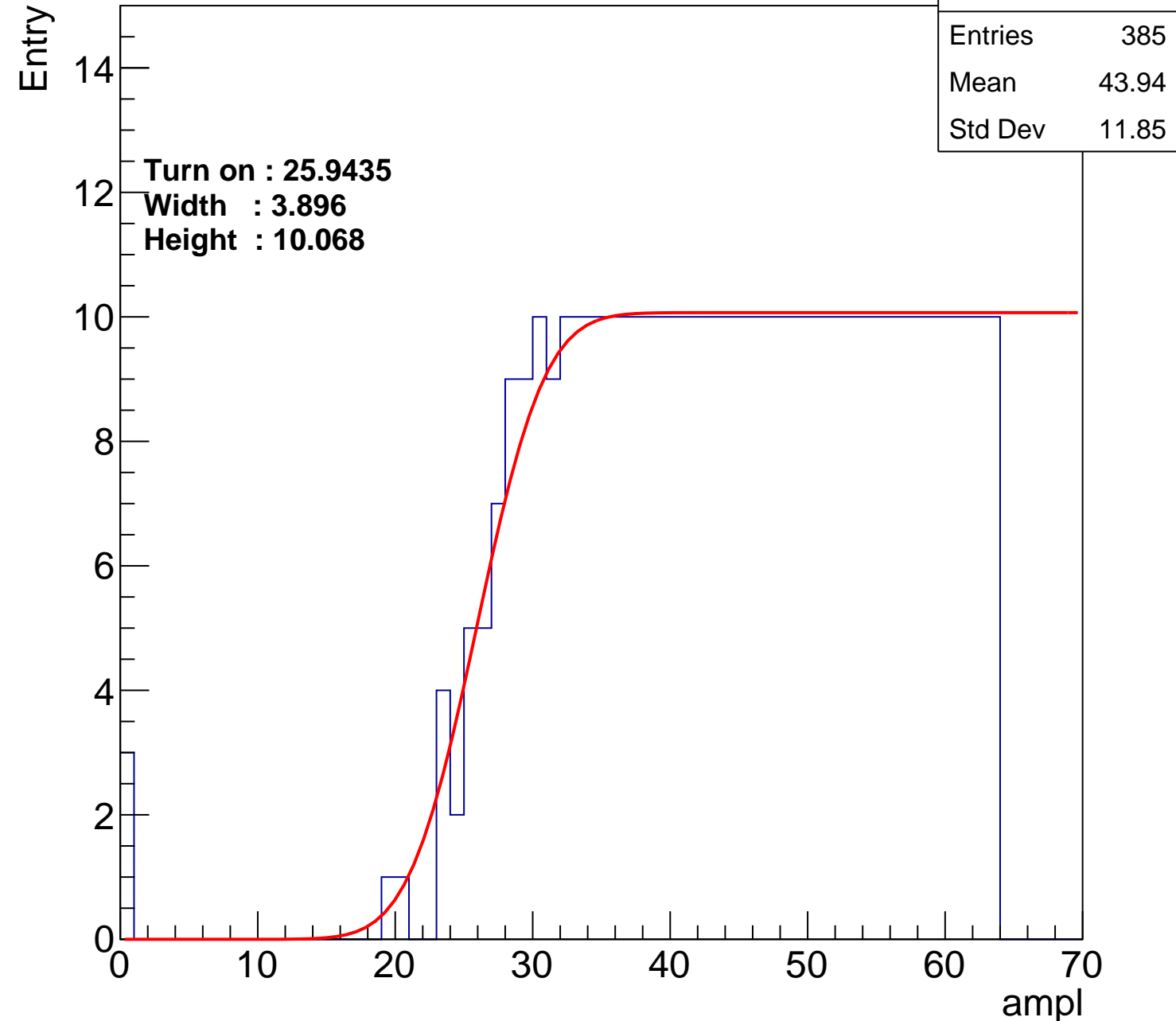
**Width : 3.896**

**Height : 10.068**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch6

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.49
Std Dev	11.68

Turn on : 27.4615

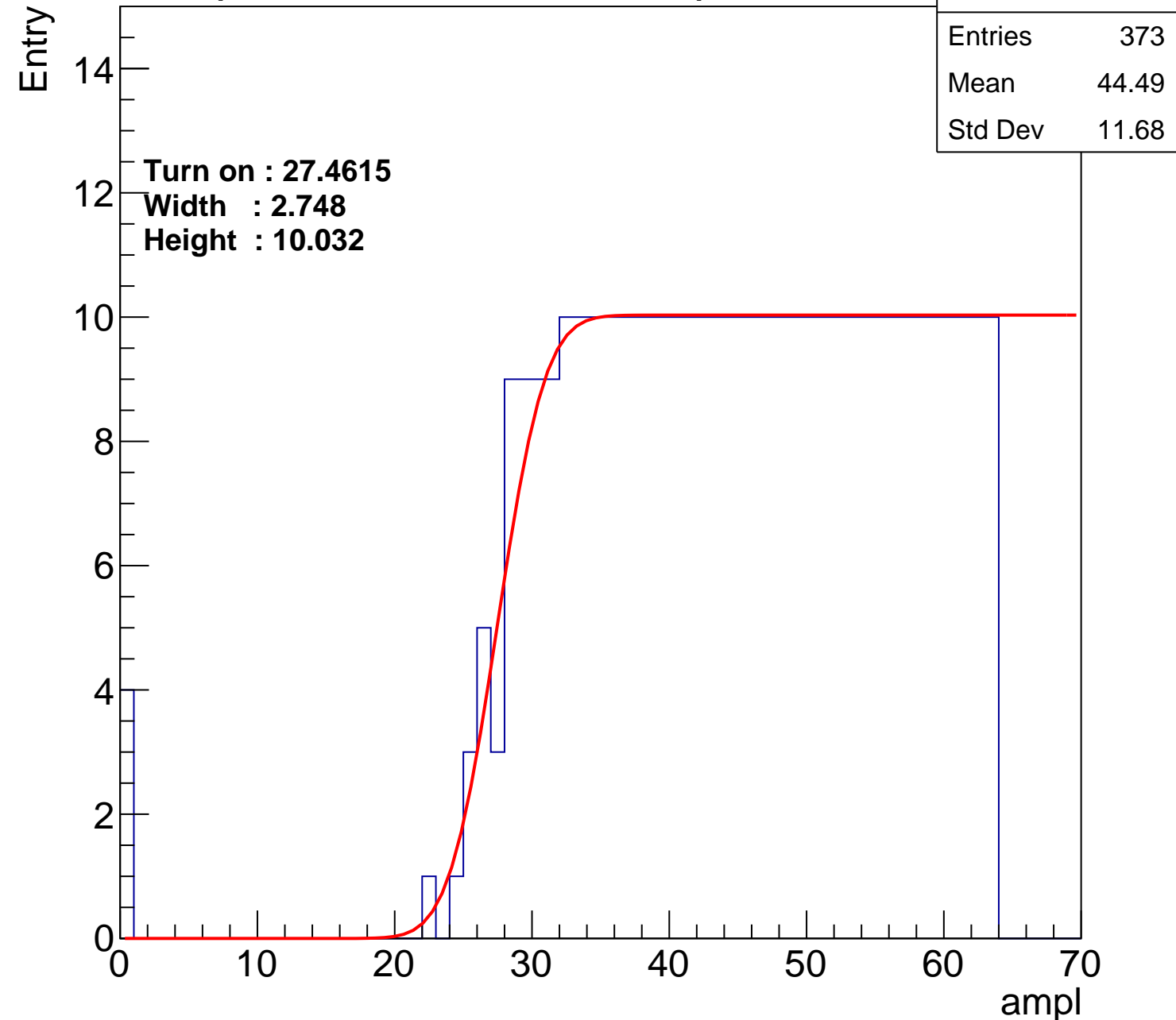
Width : 2.748

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U14-ch7

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	375
Mean	44.44
Std Dev	11.57

Turn on : 27.4925

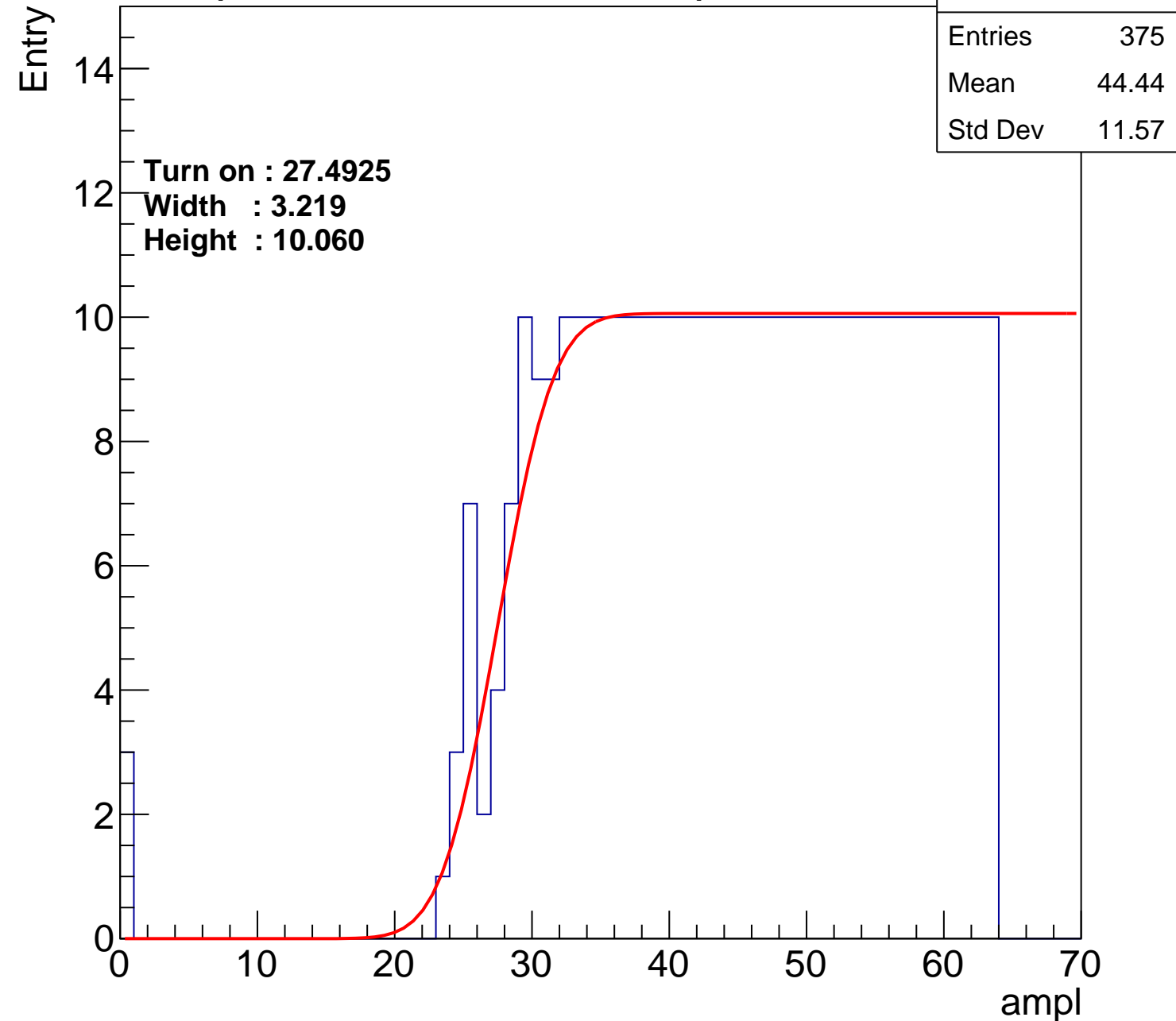
Width : 3.219

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch8

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	381
Mean	44.11
Std Dev	11.78

Turn on : 26.3101

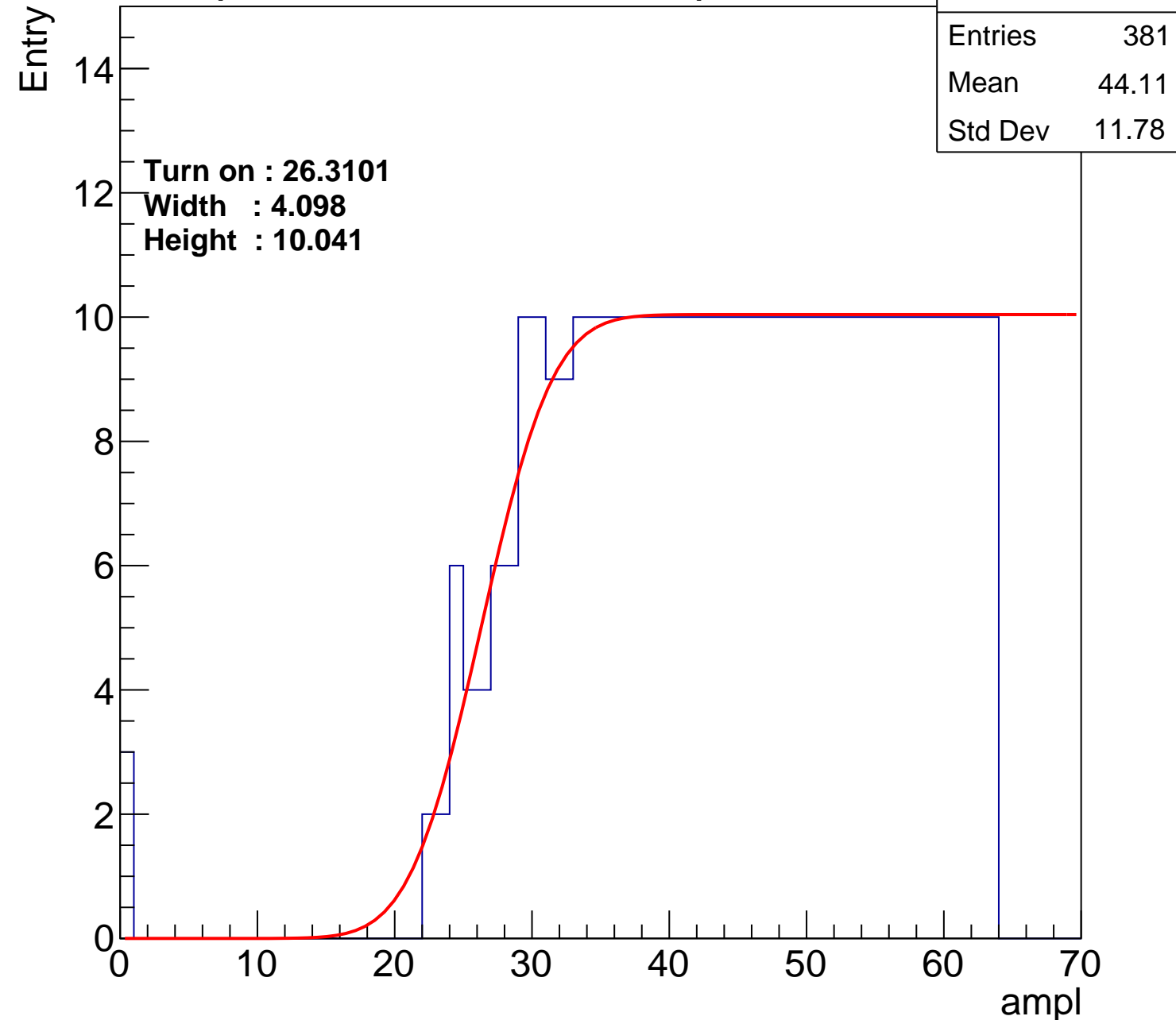
Width : 4.098

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch9

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	364
Mean	45.15
Std Dev	10.85

**Turn on : 27.5607**

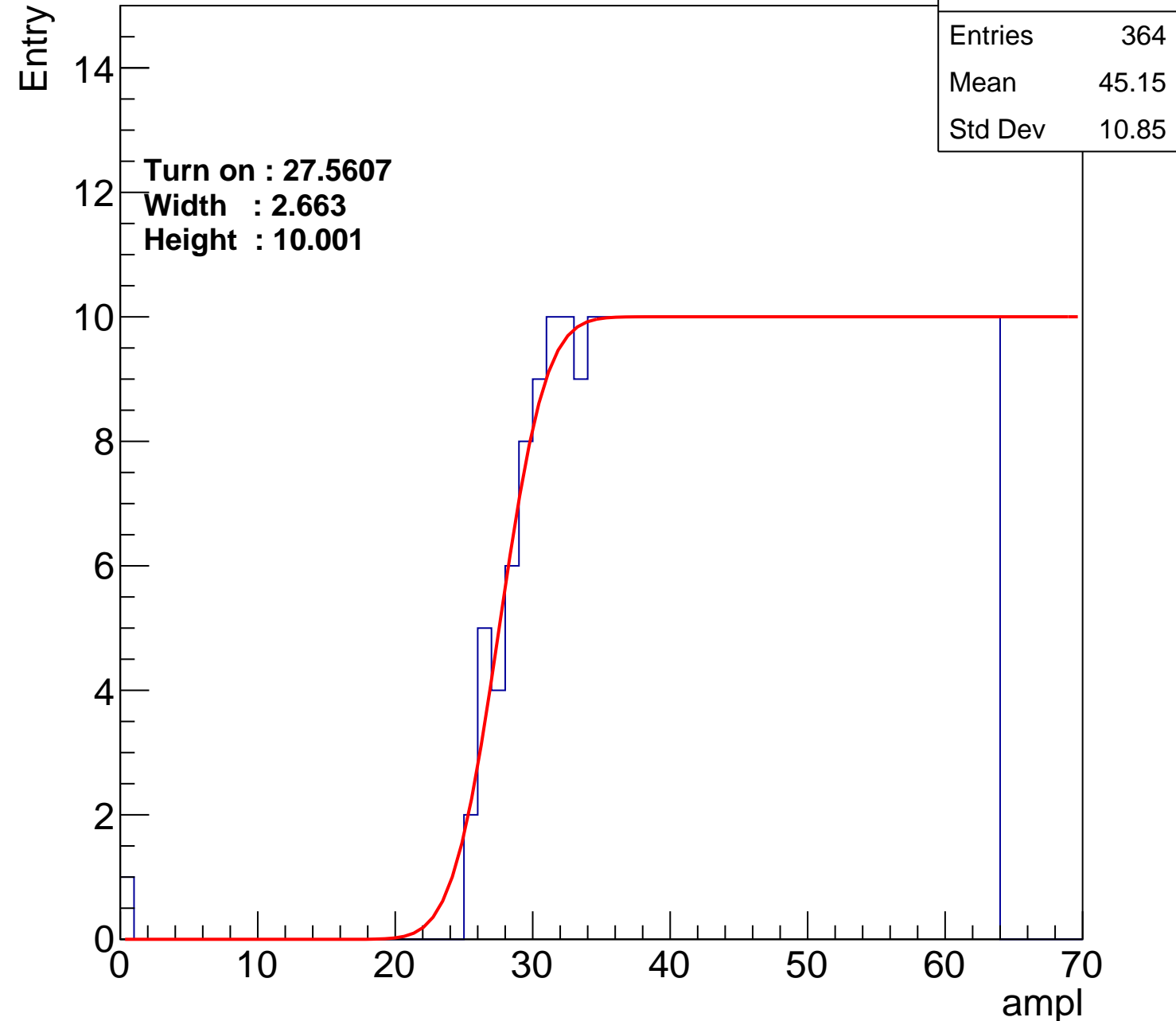
**Width : 2.663**

**Height : 10.001**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch10

calib\_packv5\_042523\_0143.root, FC#7, port C2

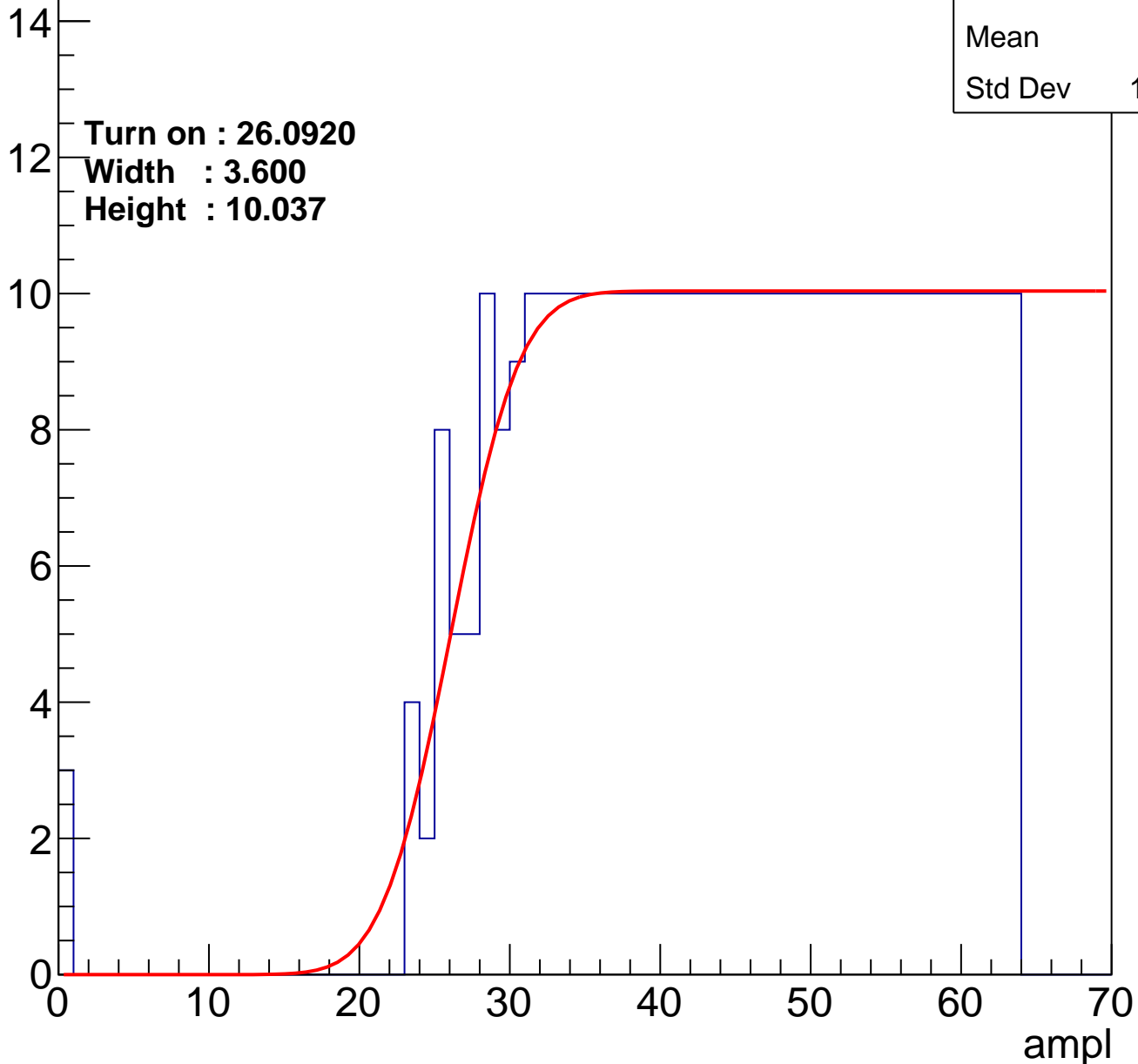
Entries	384
Mean	44
Std Dev	11.79

Turn on : 26.0920

Width : 3.600

Height : 10.037

Entry



# B1L103S, U14-ch11

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	361
Mean	45.18
Std Dev	11.05

Turn on : 27.9260

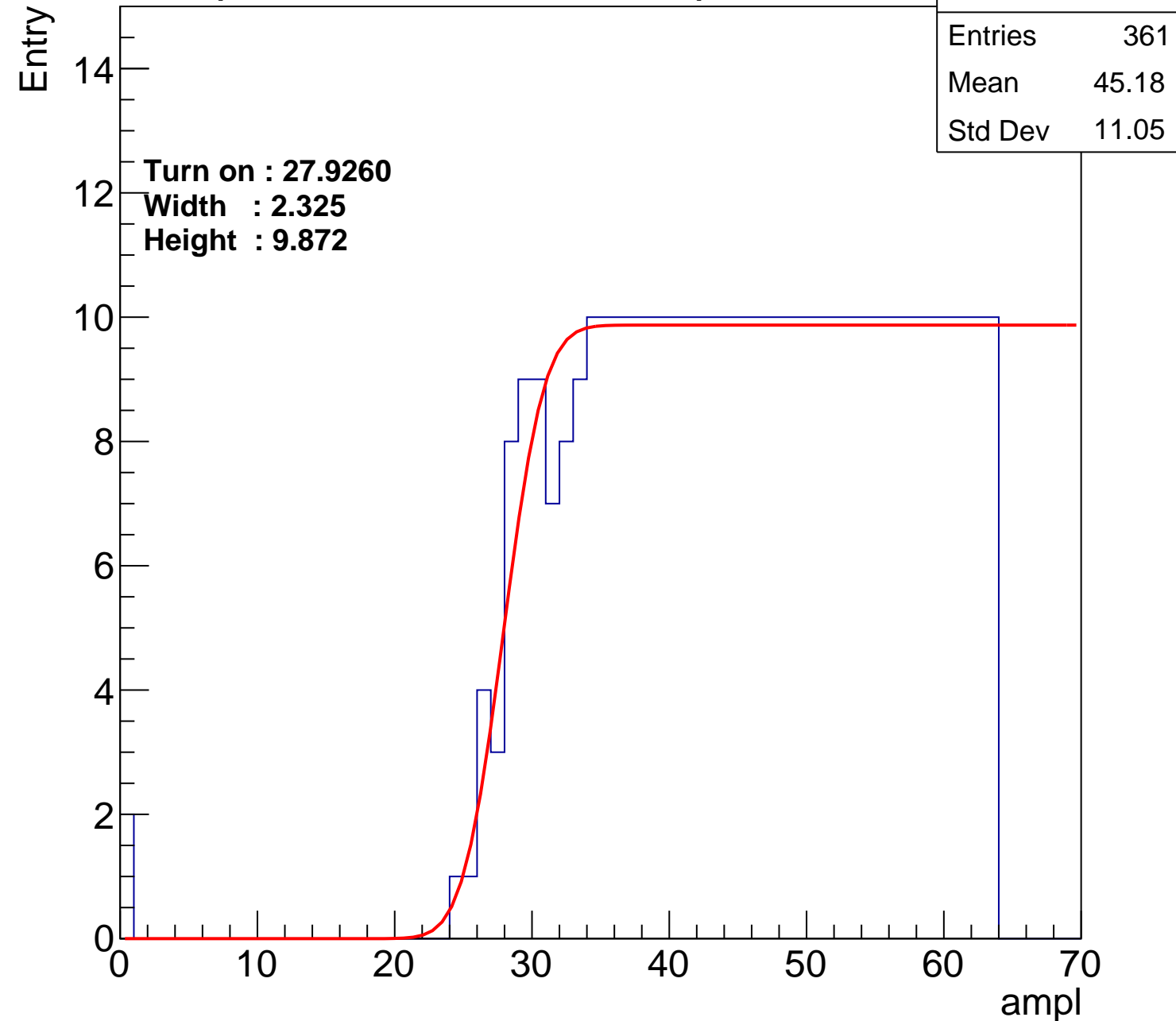
Width : 2.325

Height : 9.872

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch12

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	378
Mean	44.22
Std Dev	11.83

Turn on : 26.7002

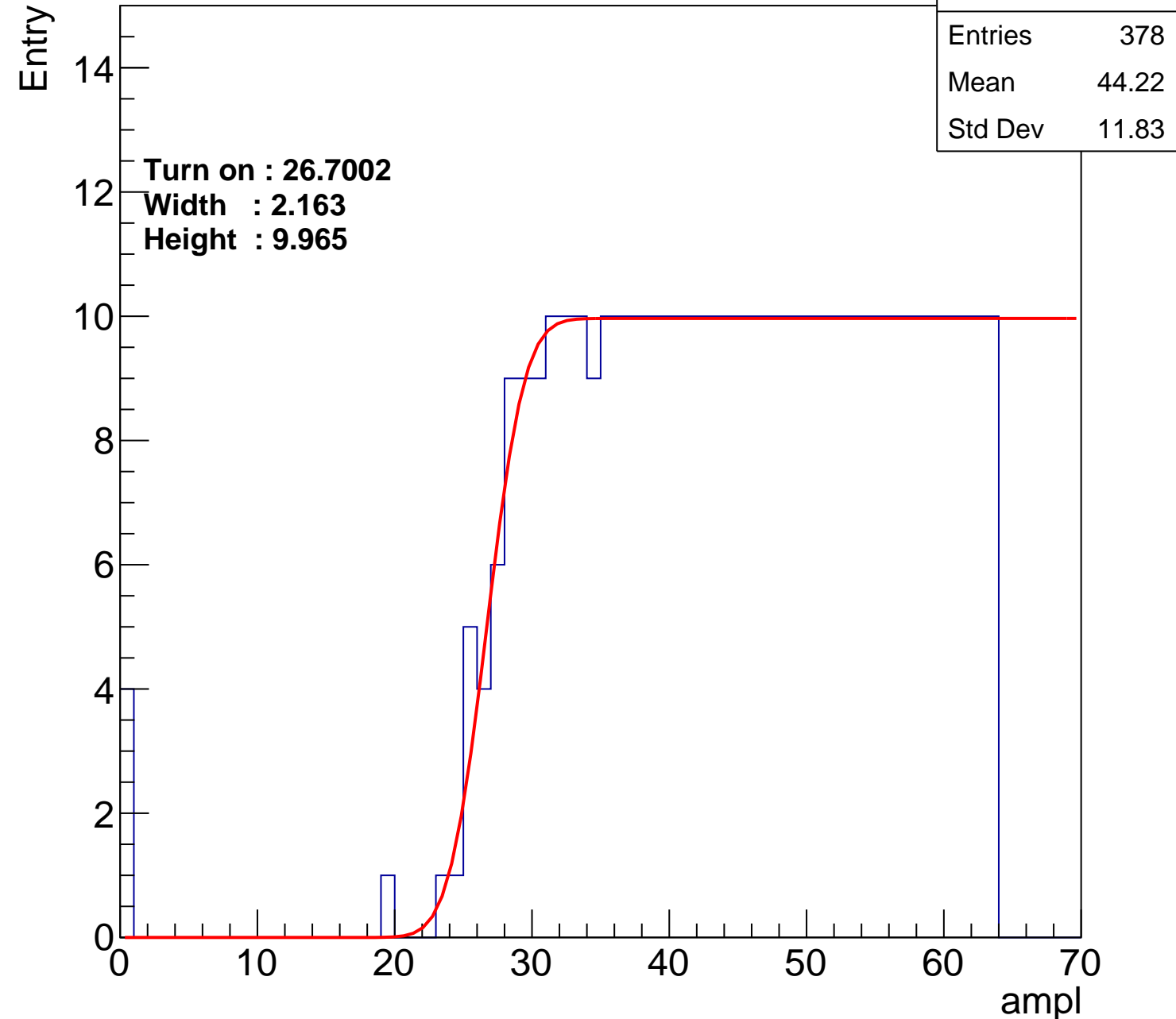
Width : 2.163

Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch13

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	369
Mean	44.66
Std Dev	11.63

Turn on : 28.0118

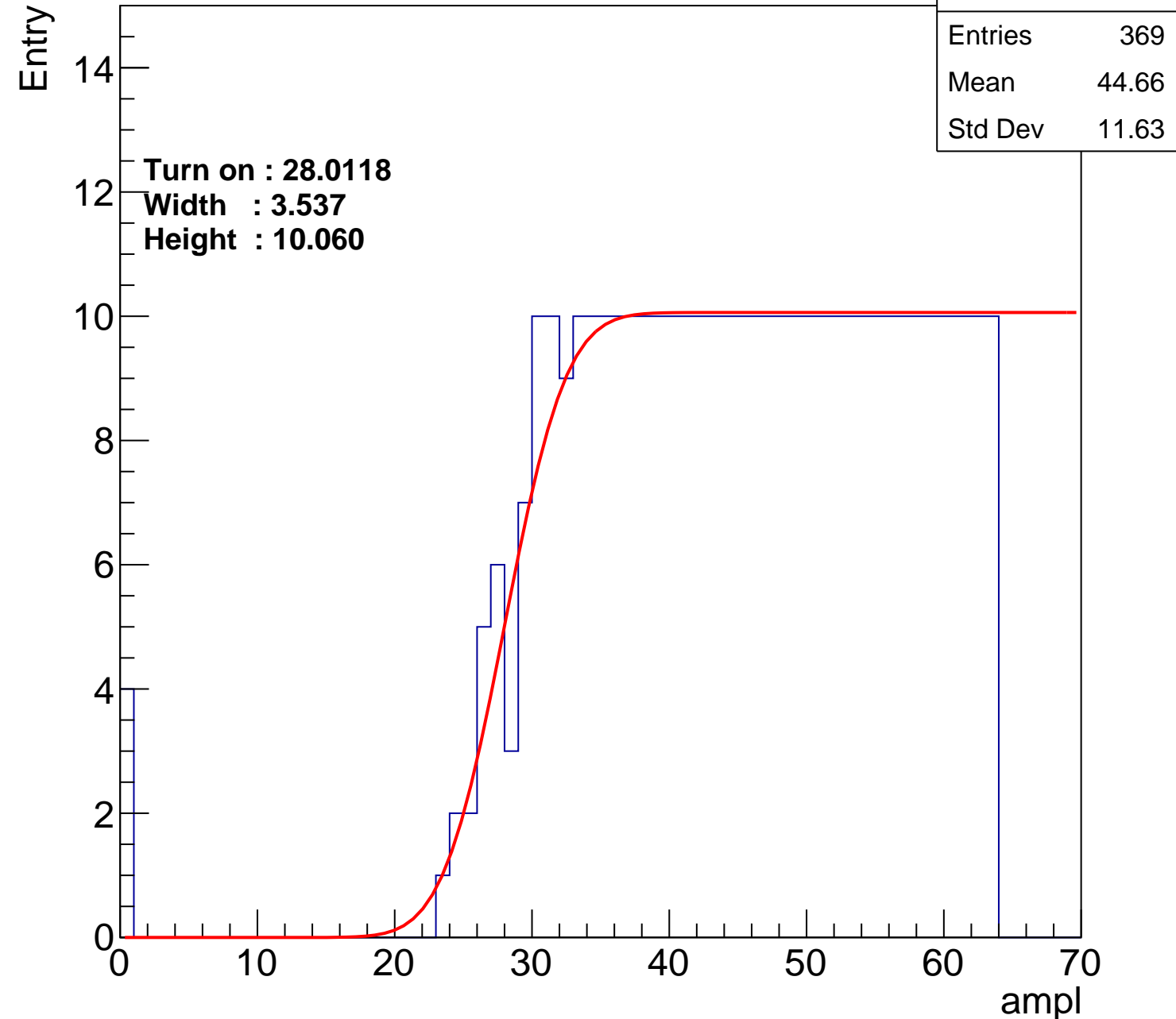
Width : 3.537

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch14

calib\_packv5\_042523\_0143.root, FC#7, port C2

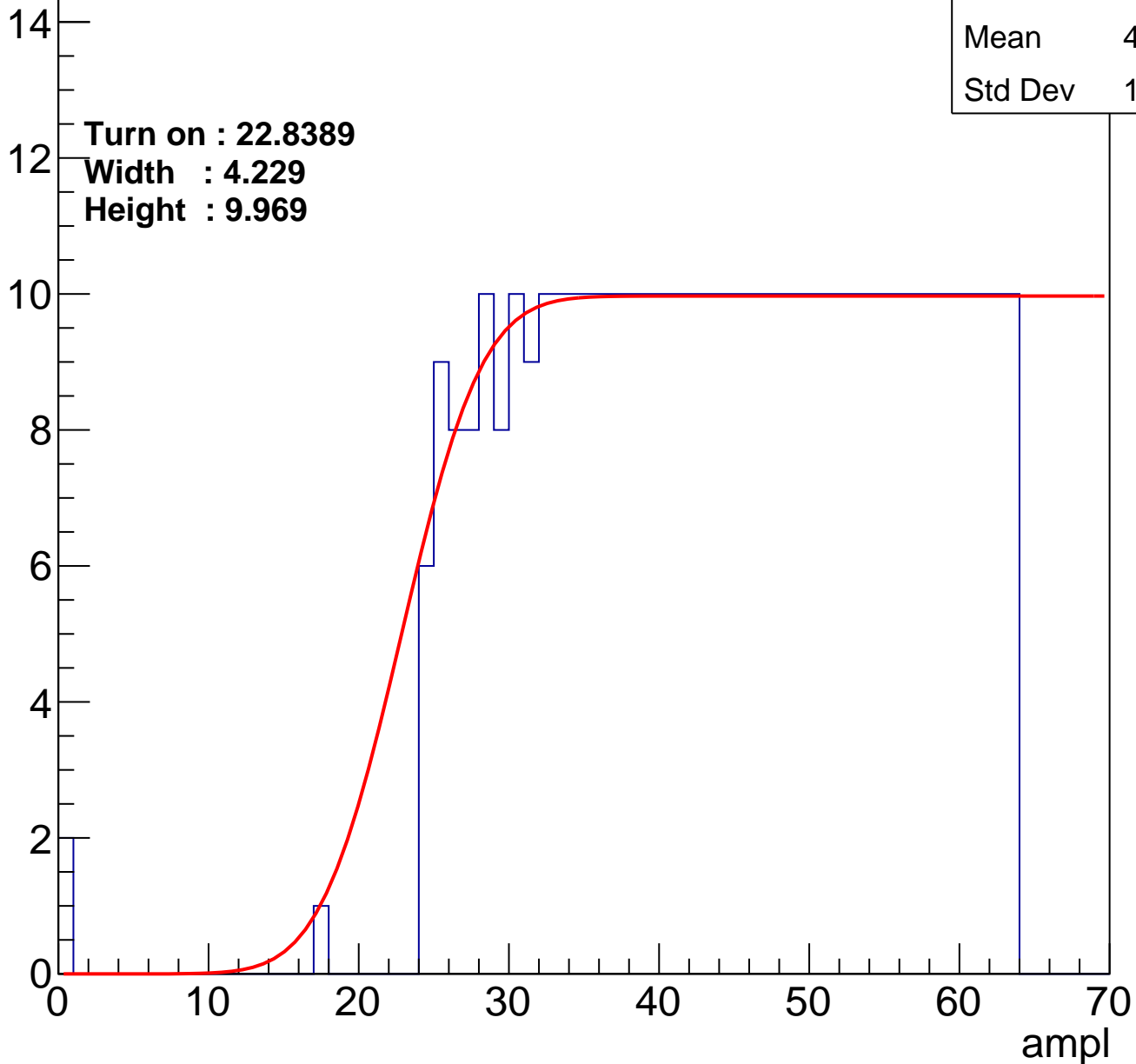
Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 22.8389

Width : 4.229

Height : 9.969

Entry





# B1L103S, U14-ch15

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	375
Mean	44.42
Std Dev	11.61

Turn on : 26.9597

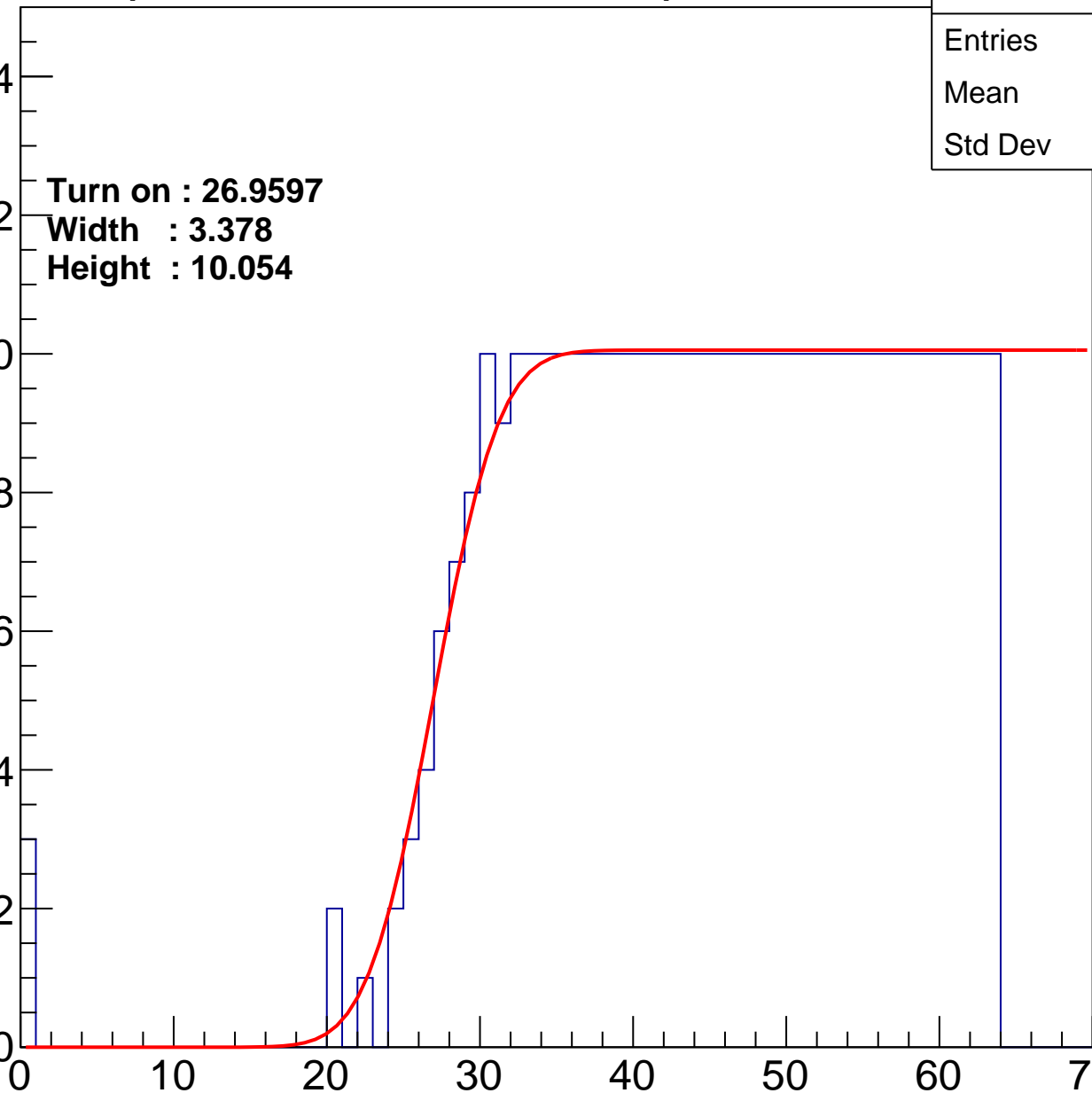
Width : 3.378

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch16

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	376
Mean	44.32
Std Dev	11.72

Turn on : 27.2361

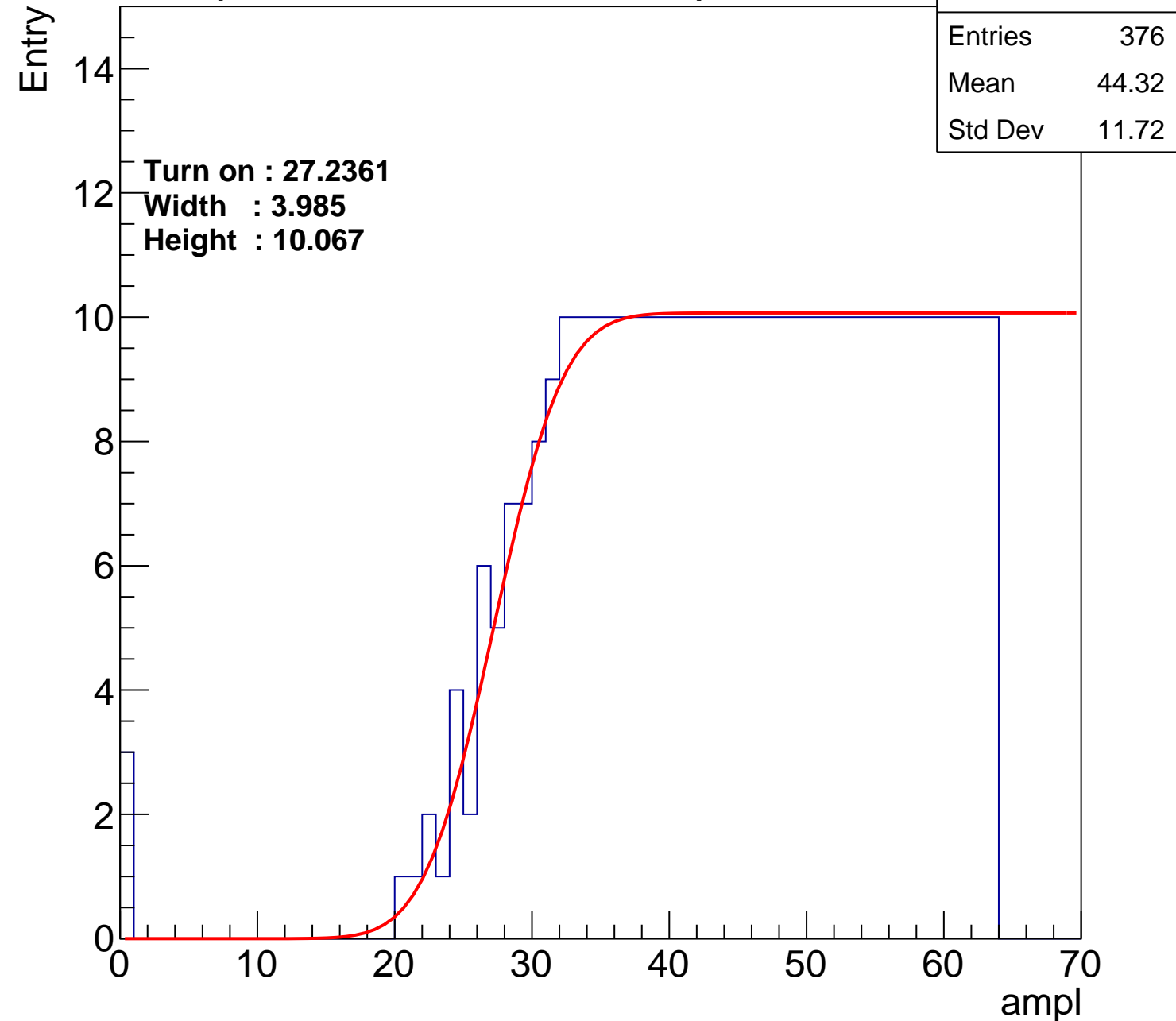
Width : 3.985

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#7, port C2**

**calib\_packv5\_042523\_0143.root, FC#7, port C2**

Turn on : 27.2118  
Width : 2.797  
Height : 10.043



# B1L103S, U14-ch18

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	401
Mean	43.16
Std Dev	12.23

Turn on : 24.1392

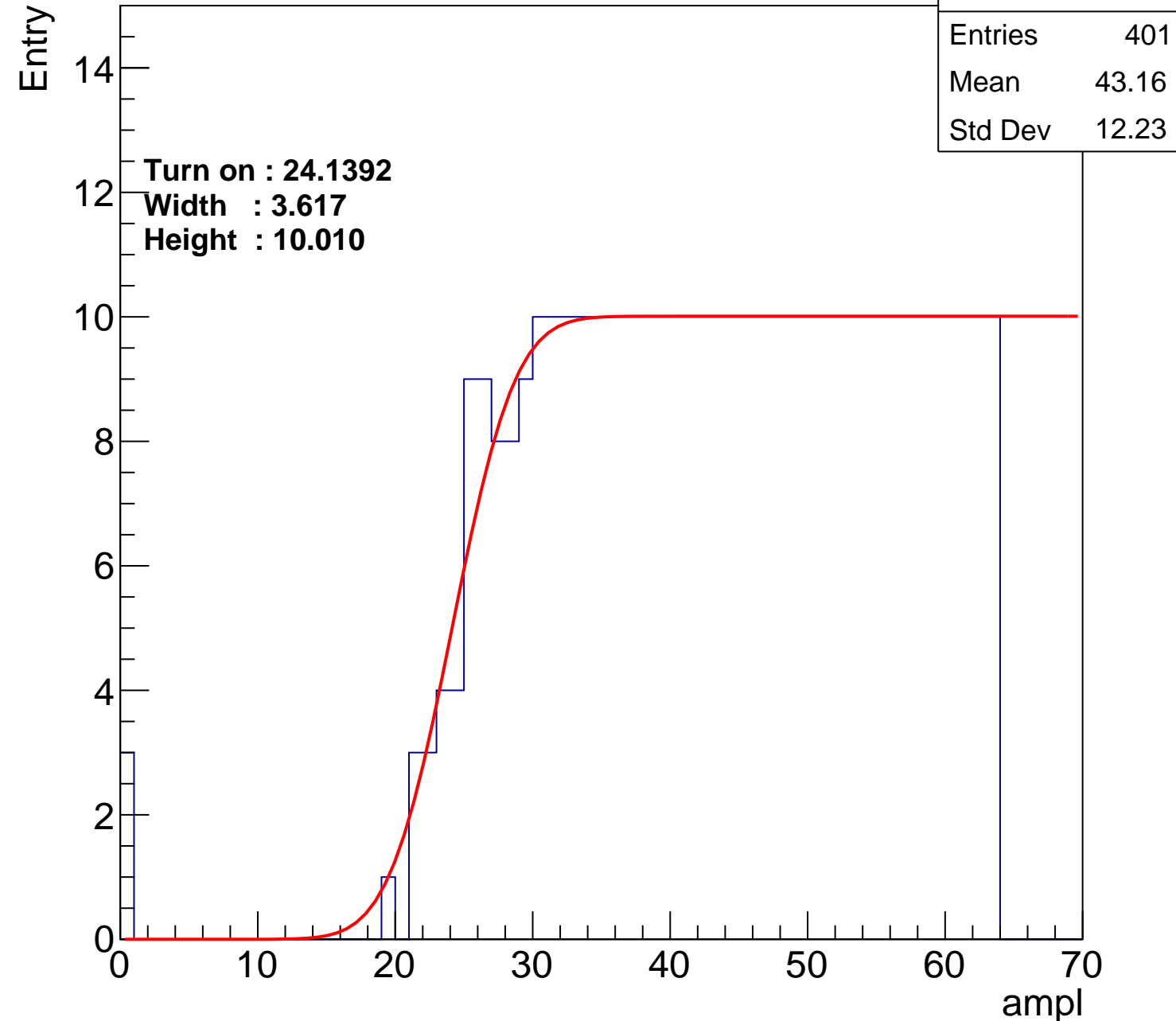
Width : 3.617

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch19

calib\_packv5\_042523\_0143.root, FC#7, port C2

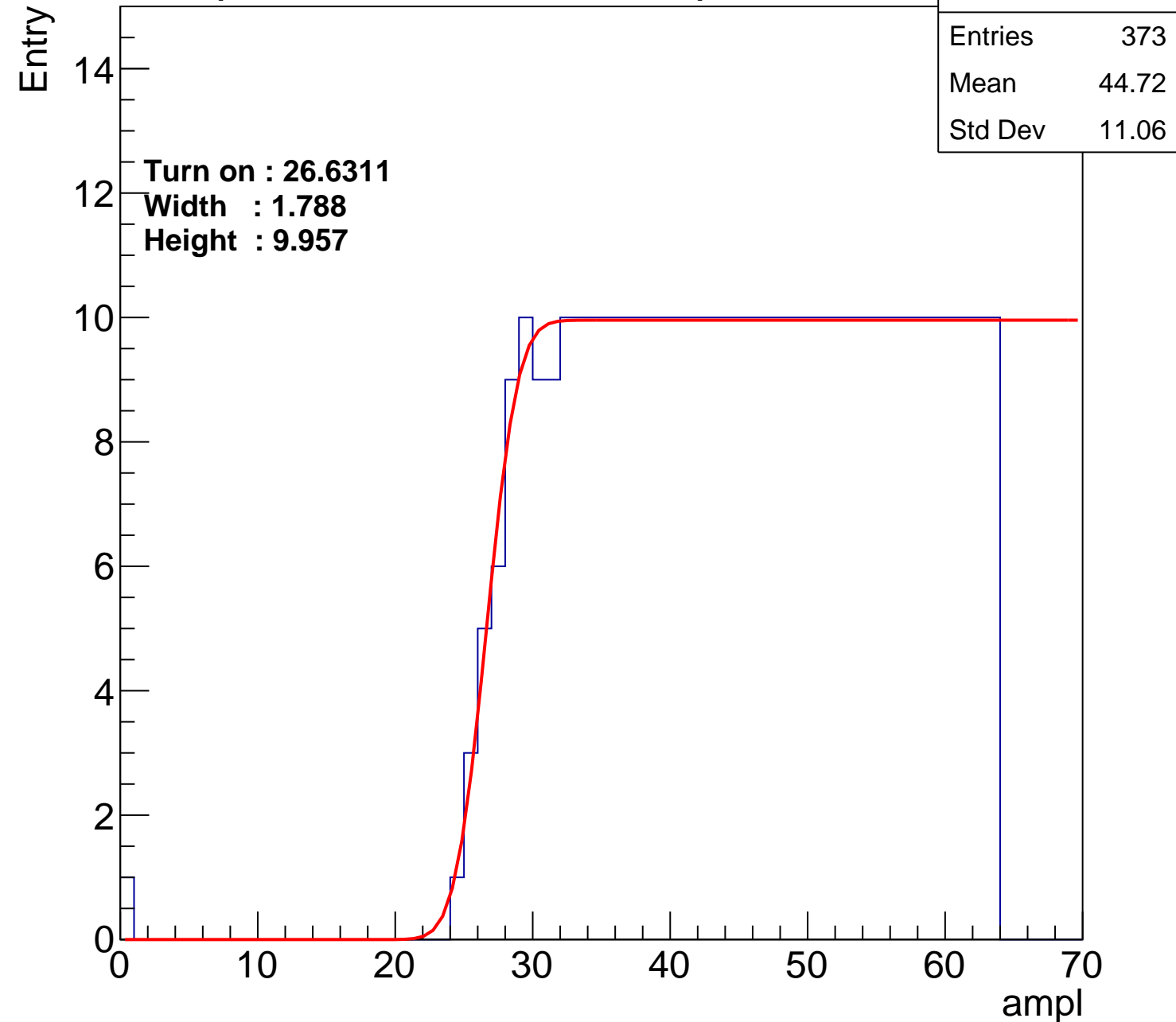
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.6311  
Width : 1.788  
Height : 9.957

Entries	373
Mean	44.72
Std Dev	11.06

ampl



# B1L103S, U14-ch20

calib\_packv5\_042523\_0143.root, FC#7, port C2

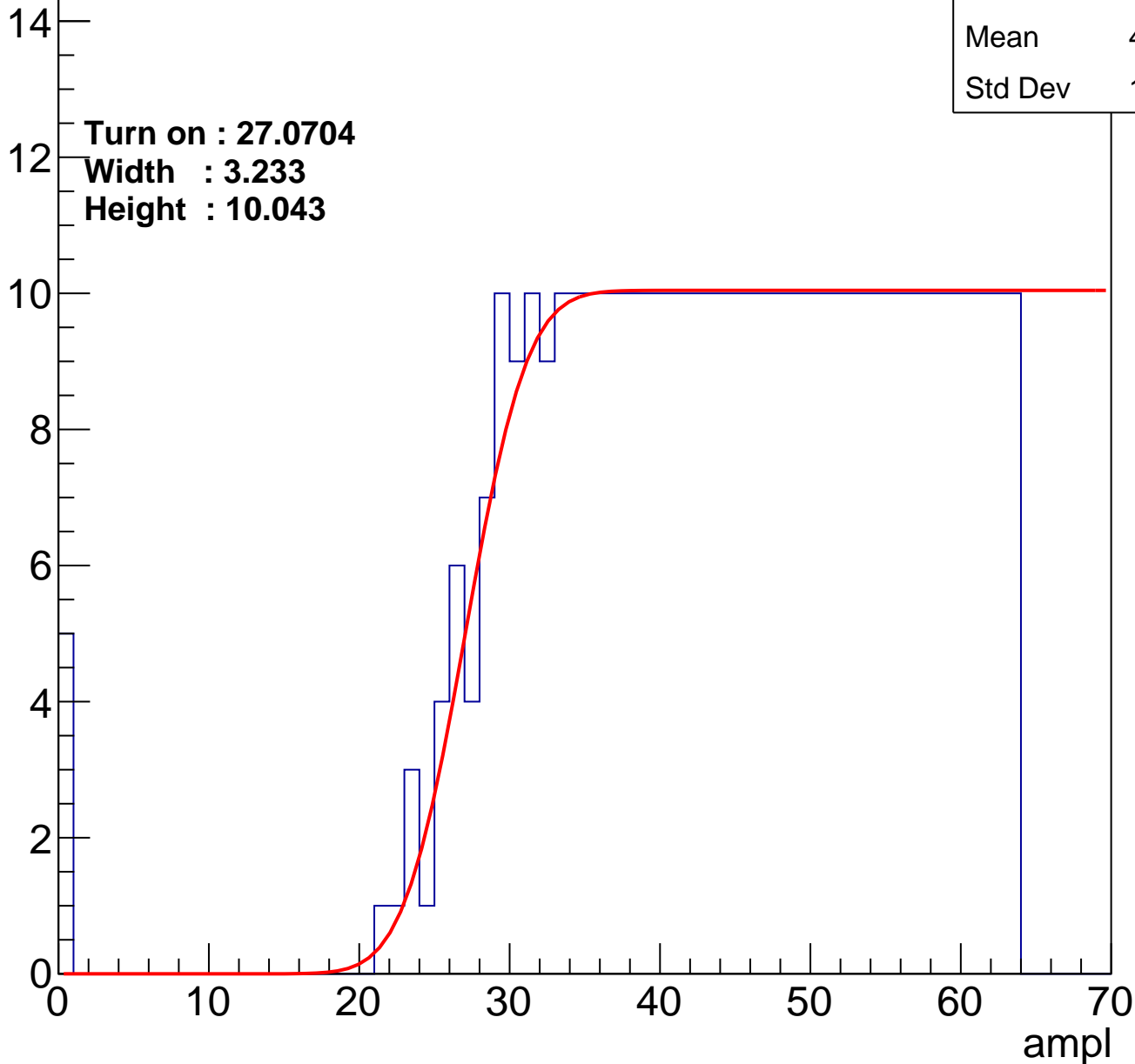
Entry

Entries	380
Mean	44.04
Std Dev	12.09

Turn on : 27.0704

Width : 3.233

Height : 10.043



# B1L103S, U14-ch21

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 28.2761

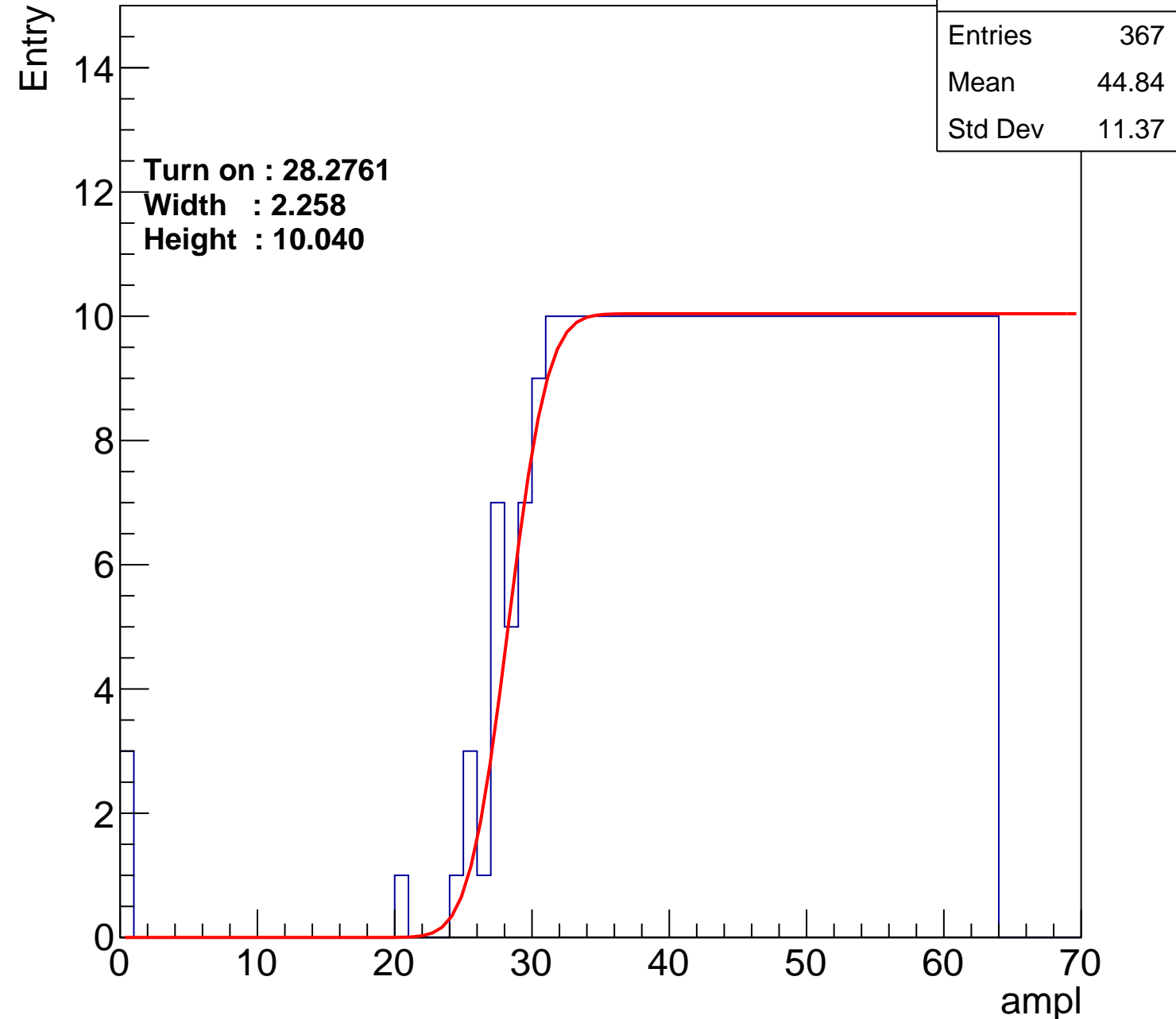
Width : 2.258

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch22

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	371
Mean	44.62
Std Dev	11.51

Turn on : 27.0369

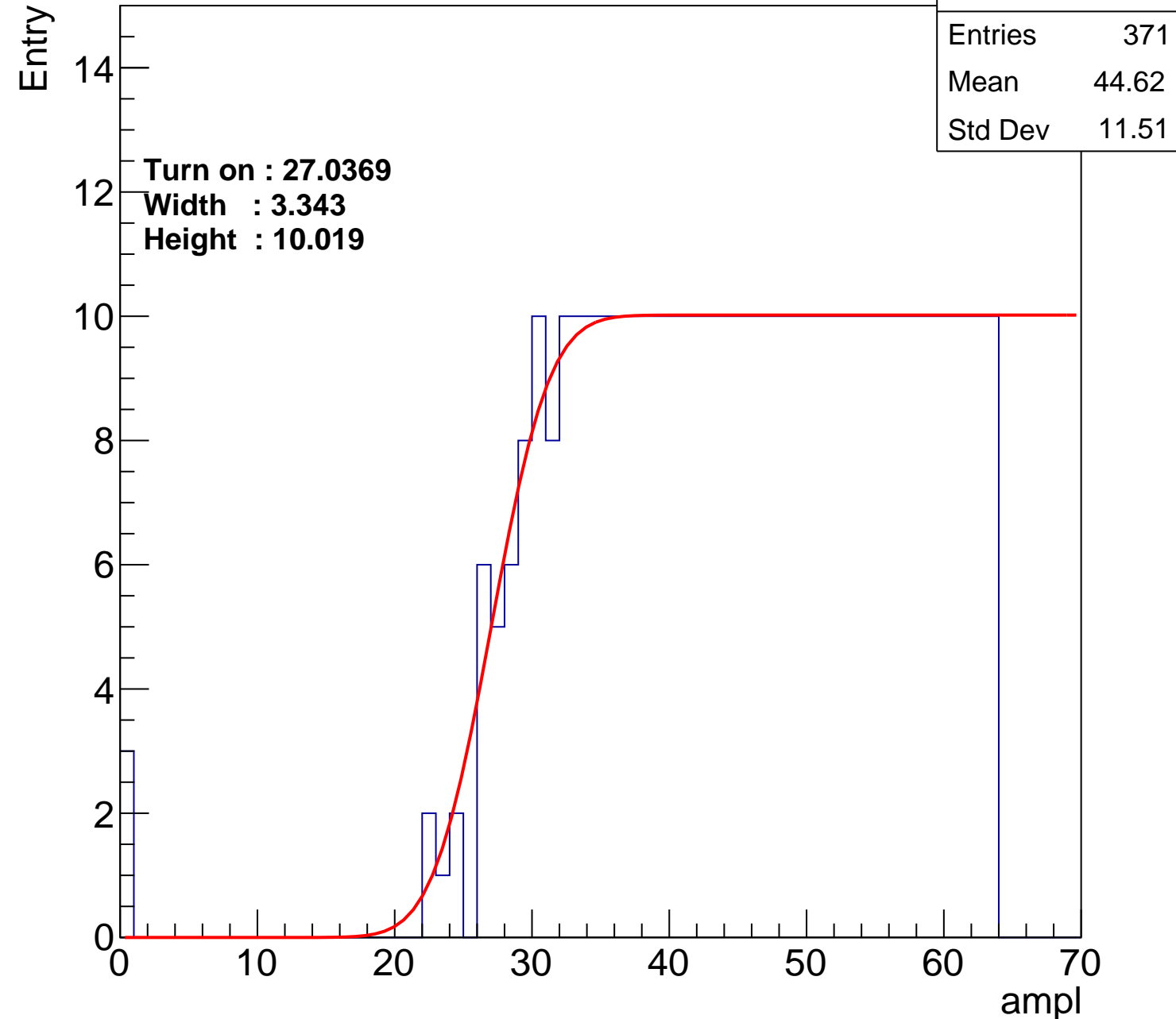
Width : 3.343

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U14-ch23

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	383
Mean	44.05
Std Dev	11.77

Turn on : 26.1531

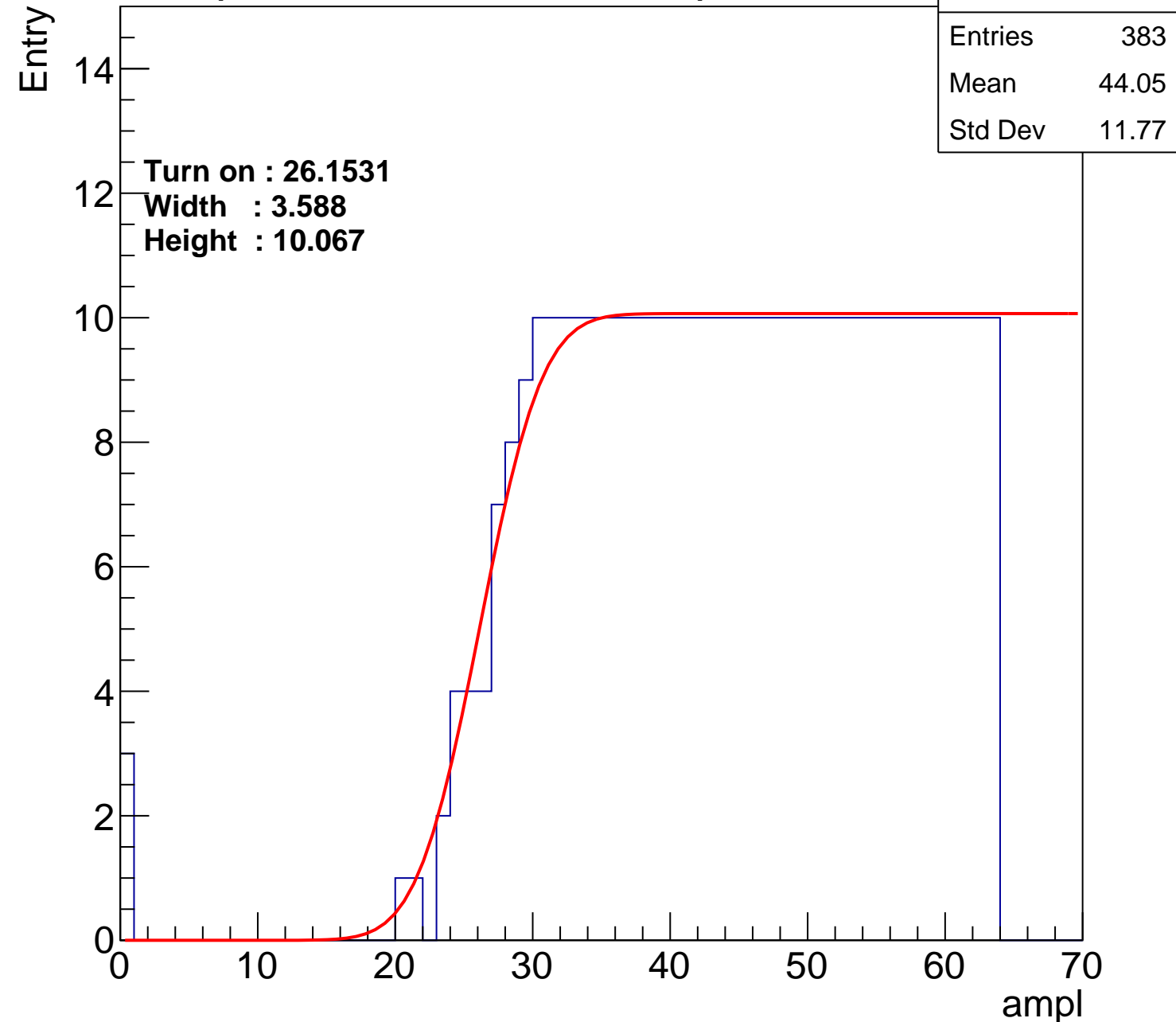
Width : 3.588

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch24

calib\_packv5\_042523\_0143.root, FC#7, port C2

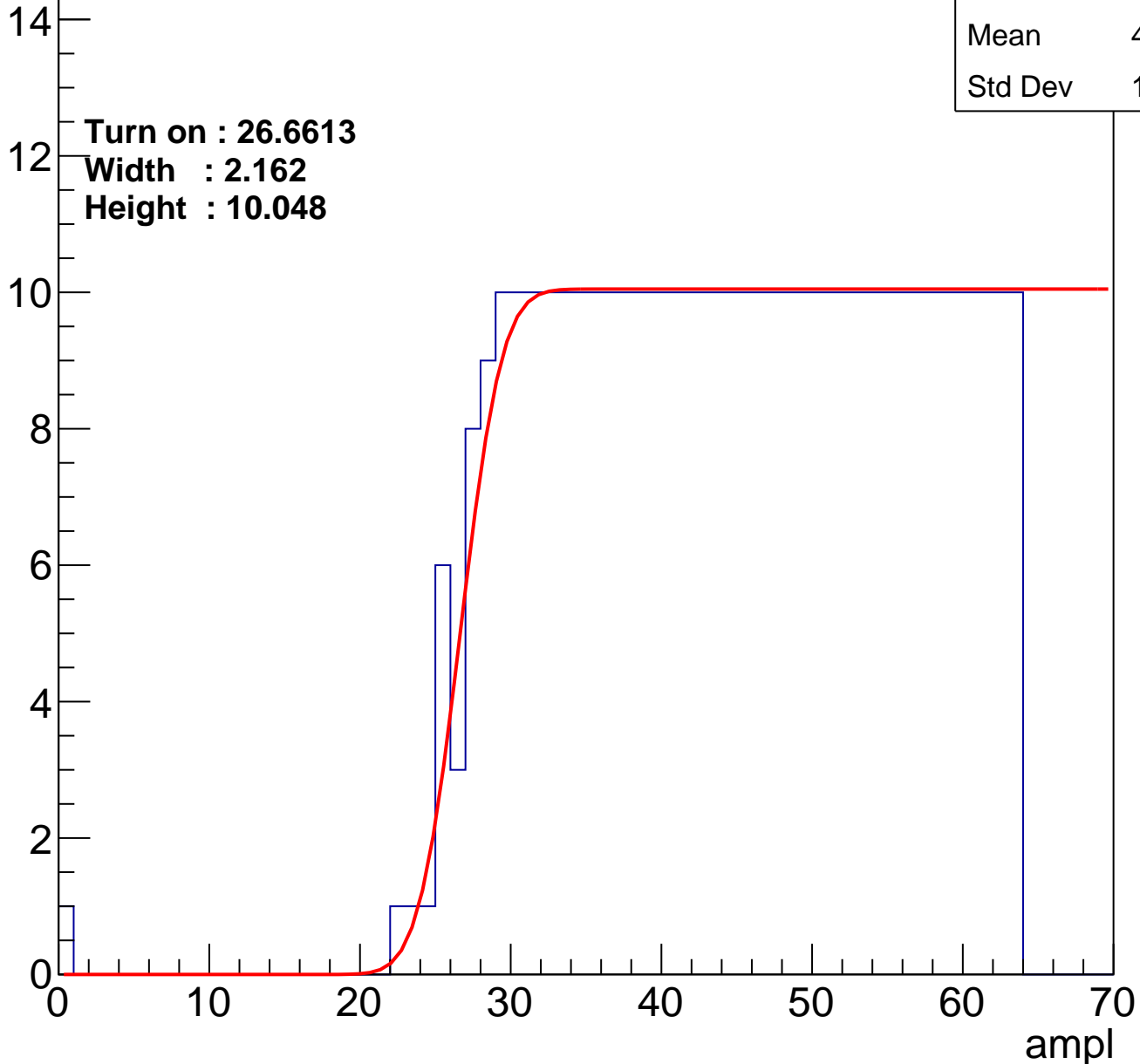
Entries	380
Mean	44.38
Std Dev	11.25

Turn on : 26.6613

Width : 2.162

Height : 10.048

Entry



**calib\_packv5\_042523\_0143.root, FC#7, port C2**

**calib\_packv5\_042523\_0143.root, FC#7, port C2**

Turn on : 28.3670  
Width : 2.442  
Height : 9.940



# B1L103S, U14-ch26

calib\_packv5\_042523\_0143.root, FC#7, port C2

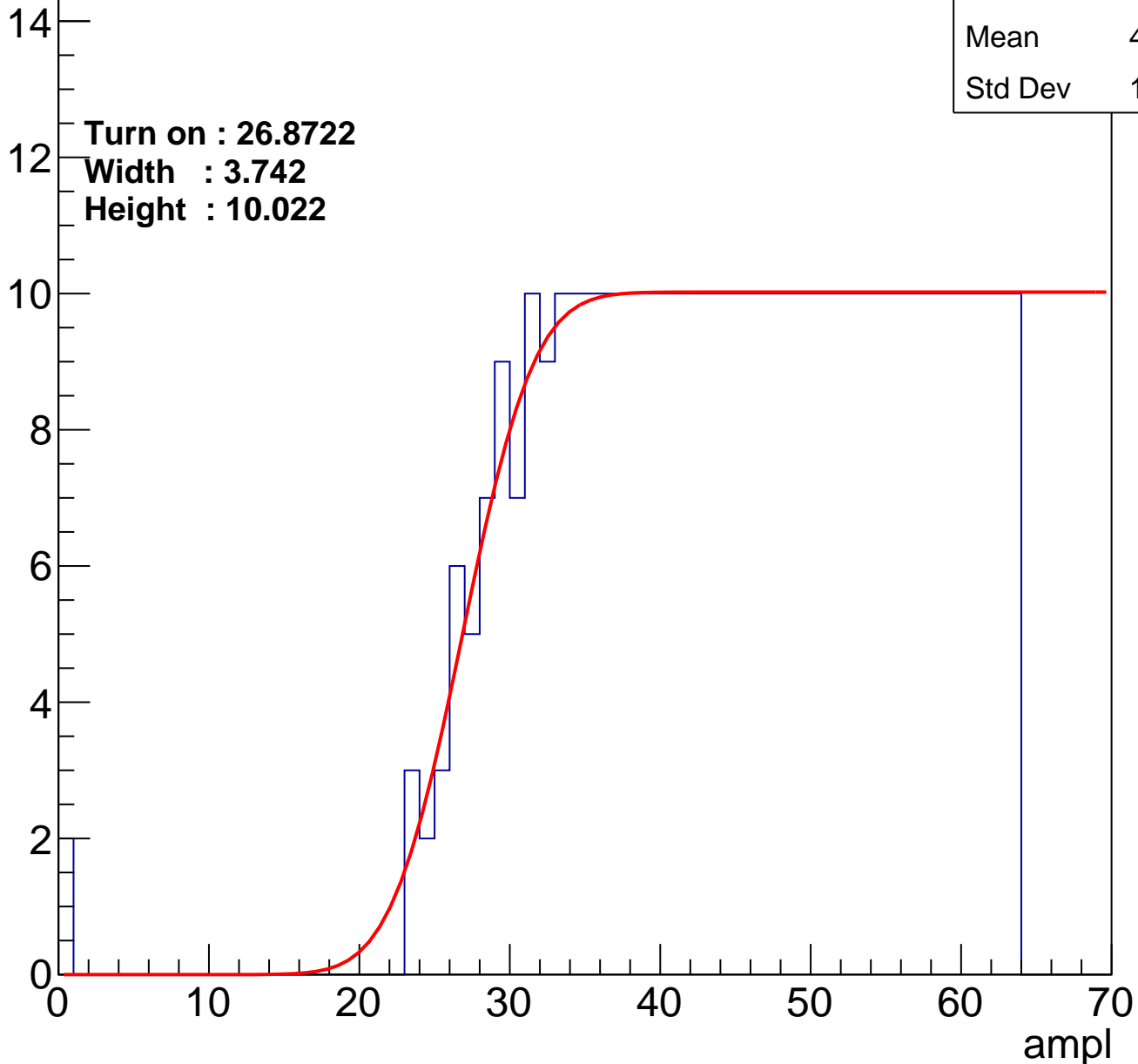
Entries	373
Mean	44.58
Std Dev	11.38

Turn on : 26.8722

Width : 3.742

Height : 10.022

Entry



# B1L103S, U14-ch27

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	379
Mean	44.24
Std Dev	11.75

Turn on : 26.5781

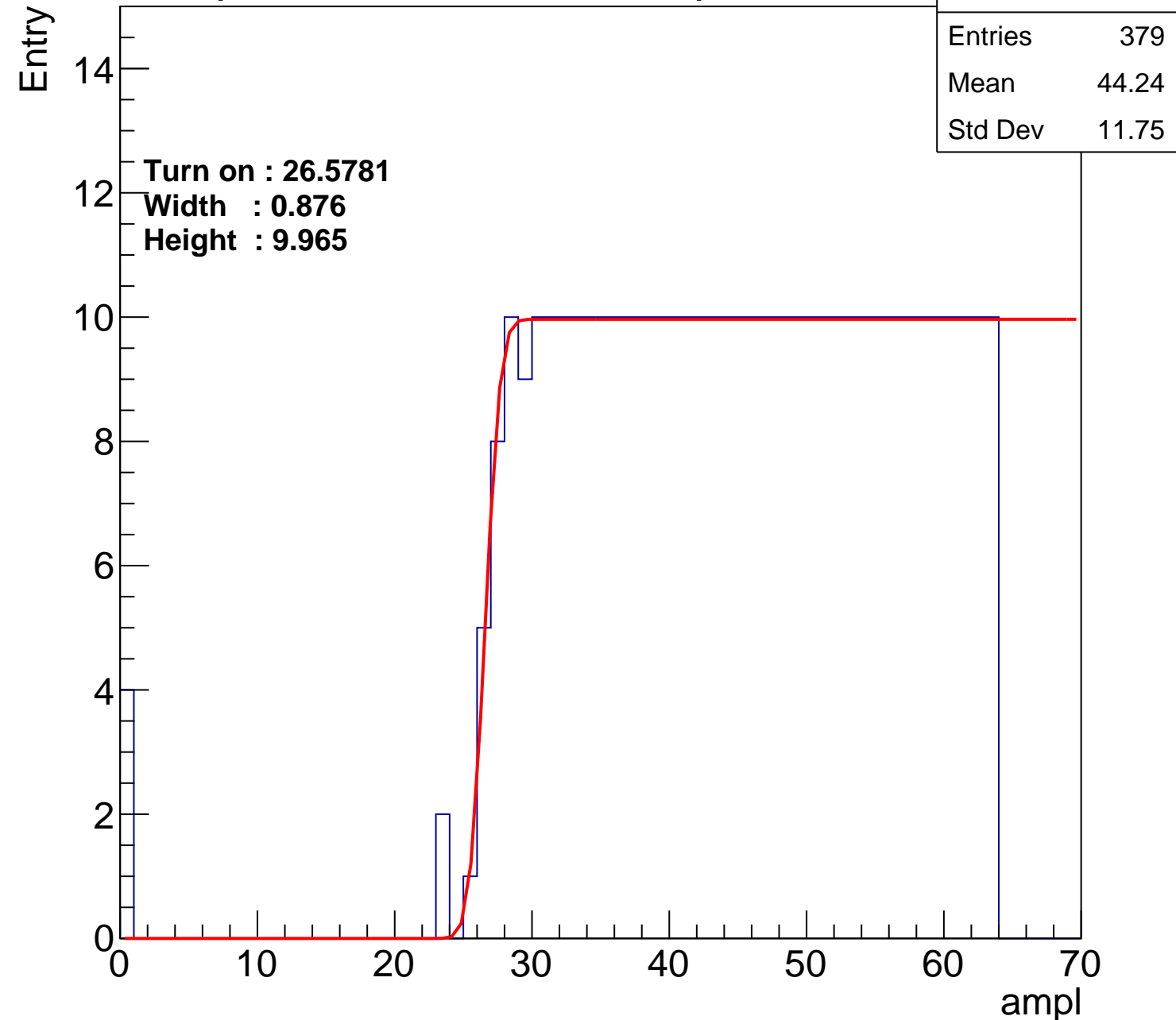
Width : 0.876

Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch28

calib\_packv5\_042523\_0143.root, FC#7, port C2

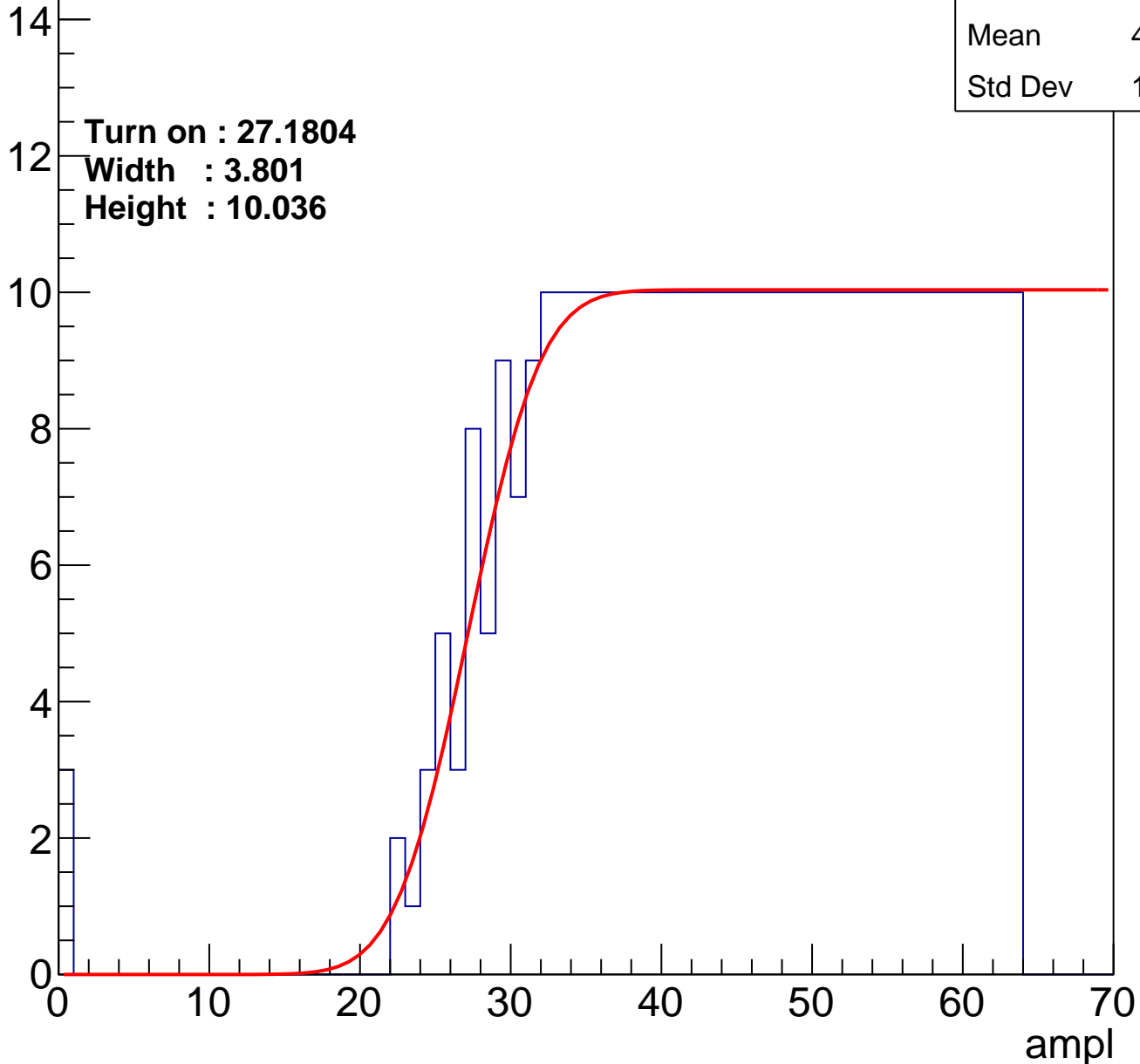
Entries	375
Mean	44.39
Std Dev	11.64

Turn on : 27.1804

Width : 3.801

Height : 10.036

Entry



# B1L103S, U14-ch29

calib\_packv5\_042523\_0143.root, FC#7, port C2

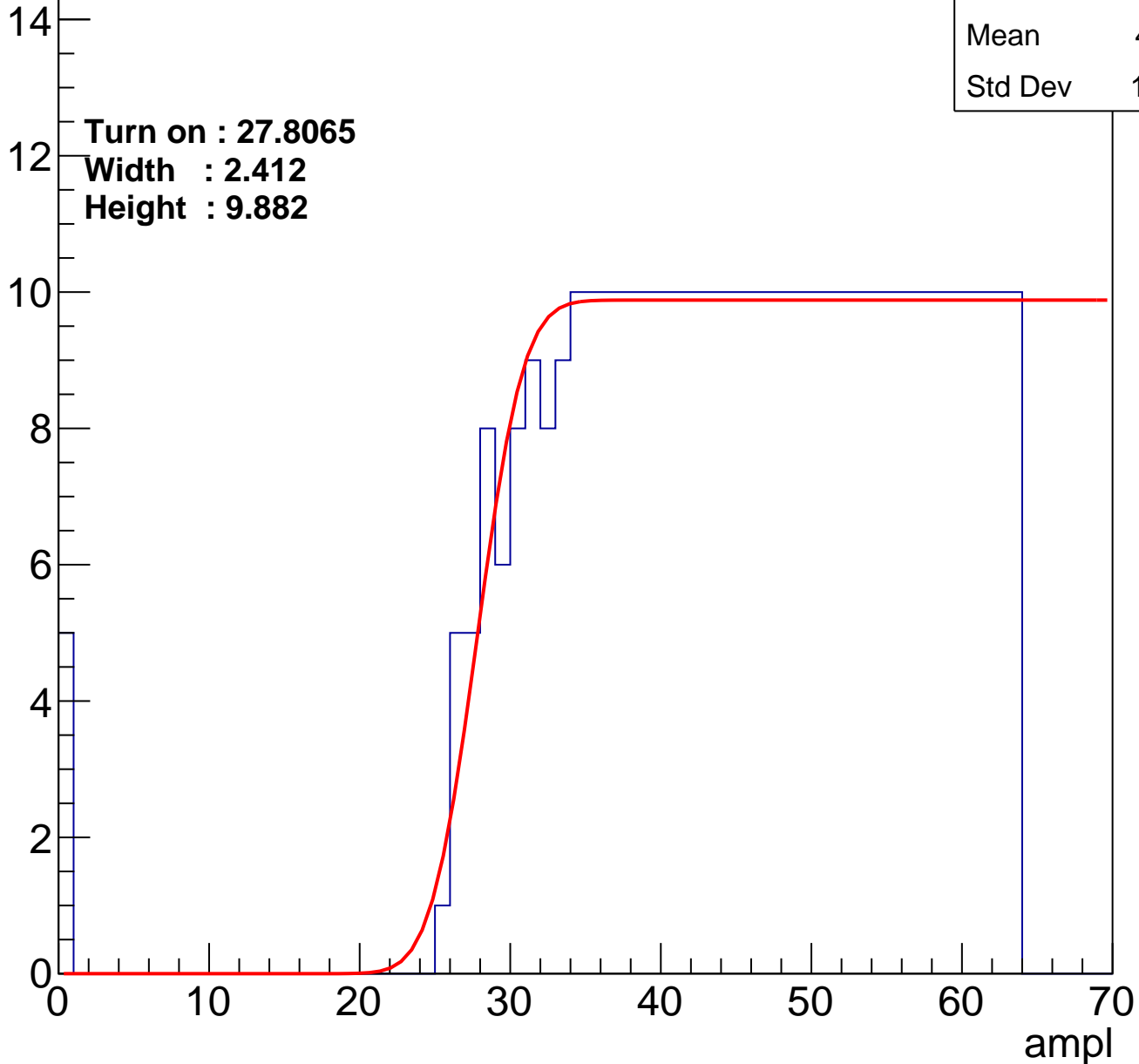
Entries	364
Mean	44.81
Std Dev	11.74

Turn on : 27.8065

Width : 2.412

Height : 9.882

Entry



# B1L103S, U14-ch30

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	386
Mean	43.8
Std Dev	12.08

Turn on : 26.4336

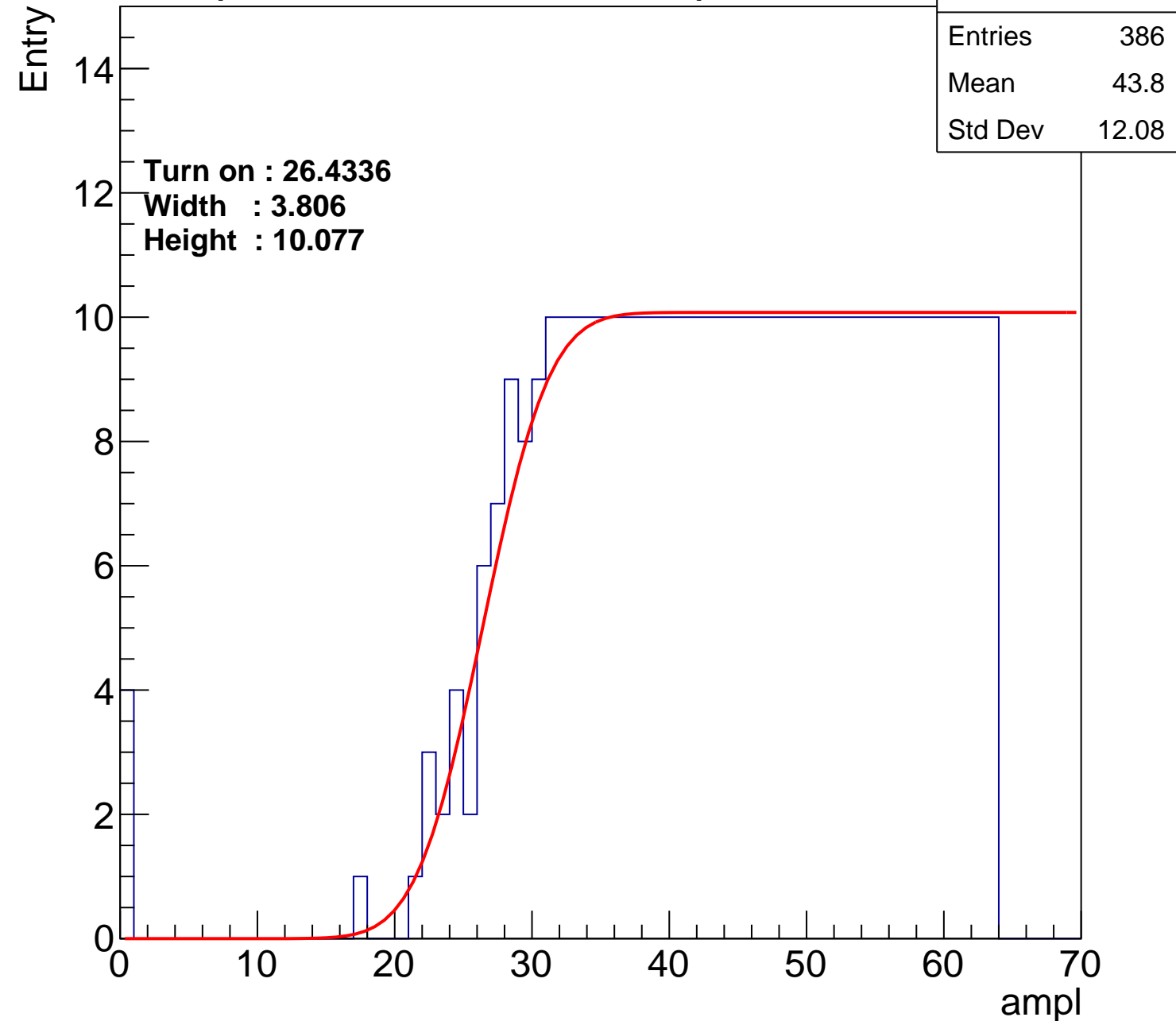
Width : 3.806

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U14-ch31

calib\_packv5\_042523\_0143.root, FC#7, port C2

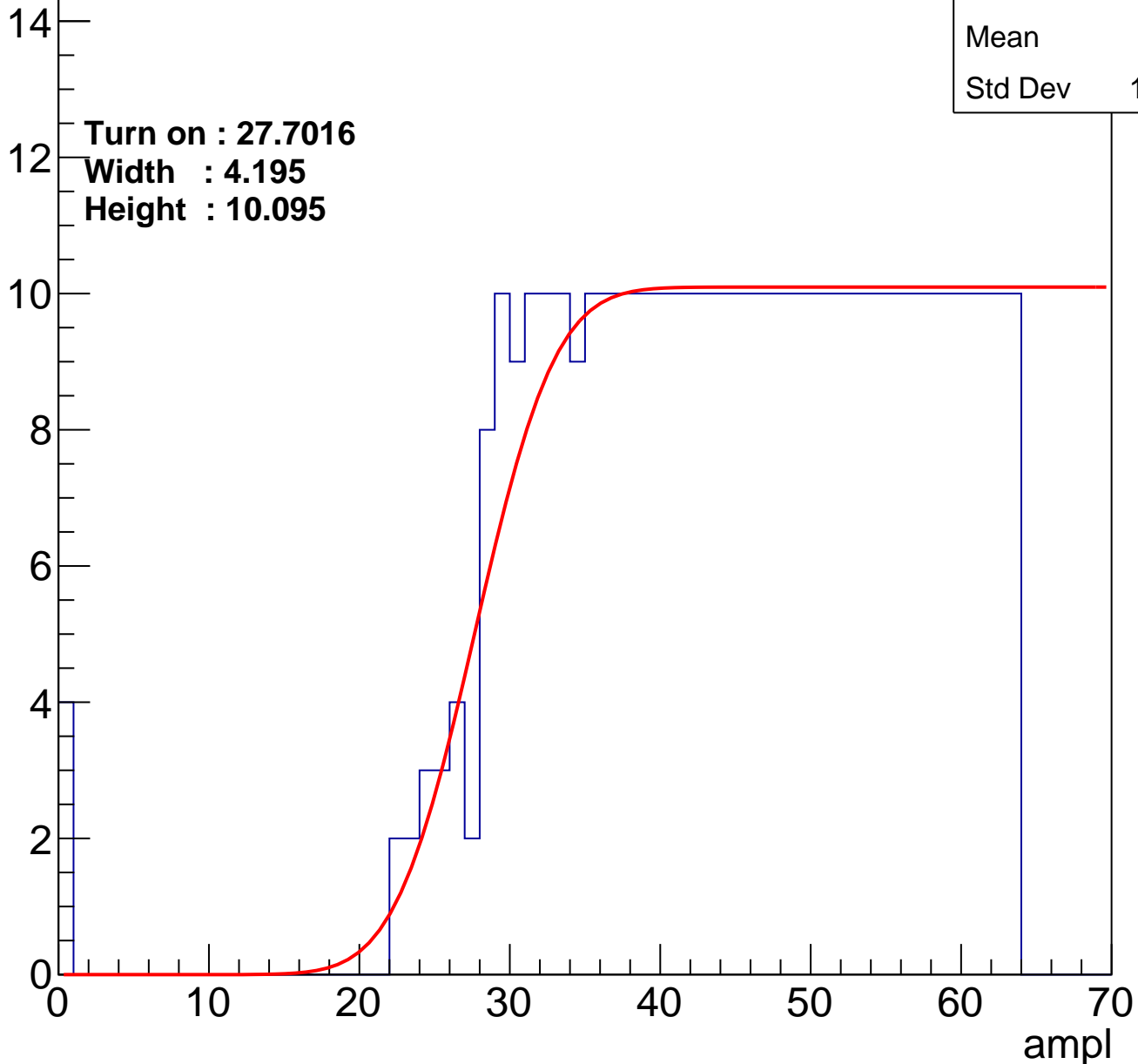
Entries	376
Mean	44.3
Std Dev	11.82

Turn on : 27.7016

Width : 4.195

Height : 10.095

Entry



# B1L103S, U14-ch32

calib\_packv5\_042523\_0143.root, FC#7, port C2

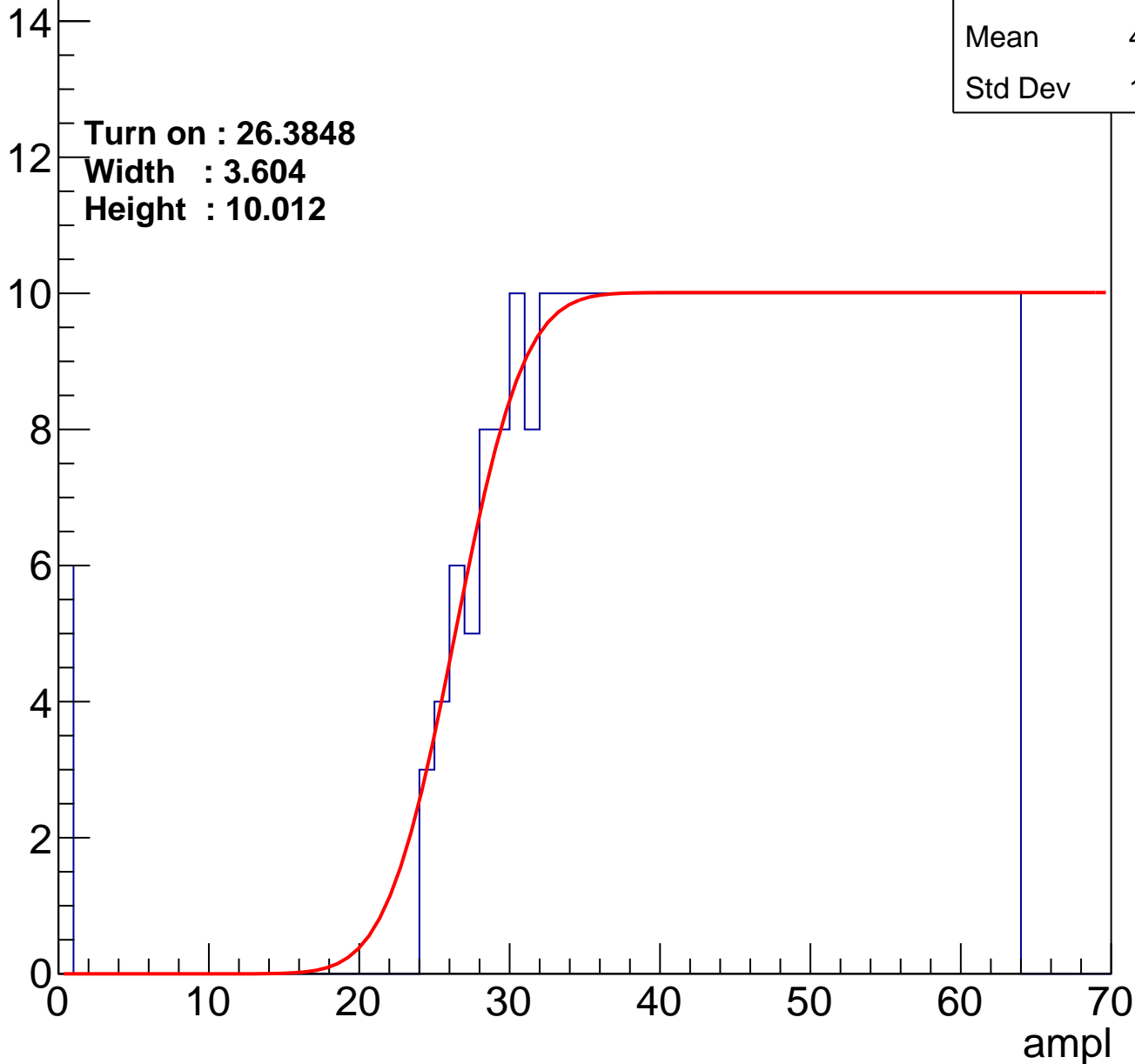
Entries	378
Mean	44.09
Std Dev	12.17

**Turn on : 26.3848**

**Width : 3.604**

**Height : 10.012**

Entry



# B1L103S, U14-ch33

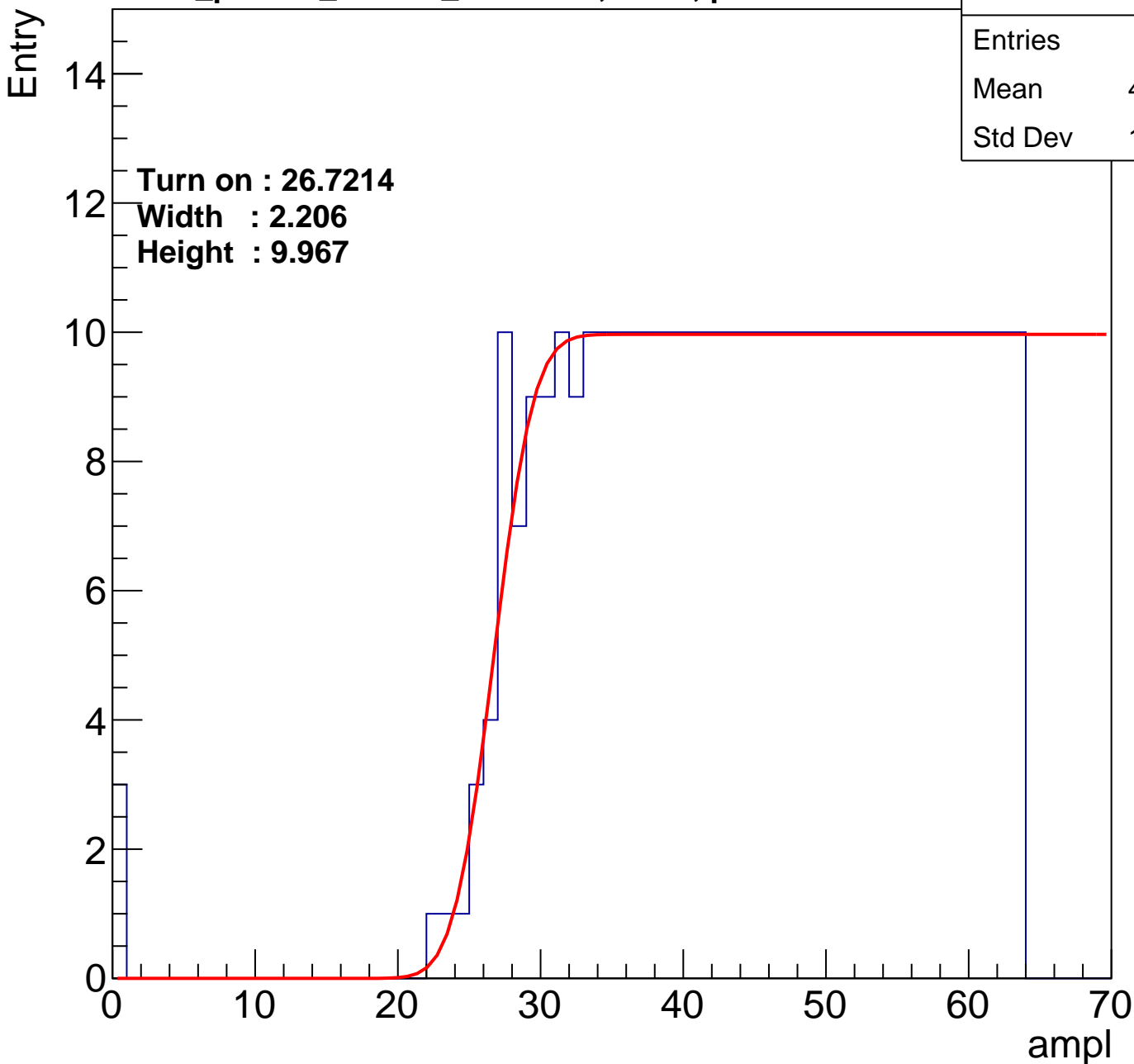
**calib\_packv5\_042523\_0143.root, FC#7, port C2**

**Turn on : 26.7214**

**Width : 2.206**

**Height : 9.967**

Entries	377
Mean	44.36
Std Dev	11.59



# B1L103S, U14-ch34

calib\_packv5\_042523\_0143.root, FC#7, port C2

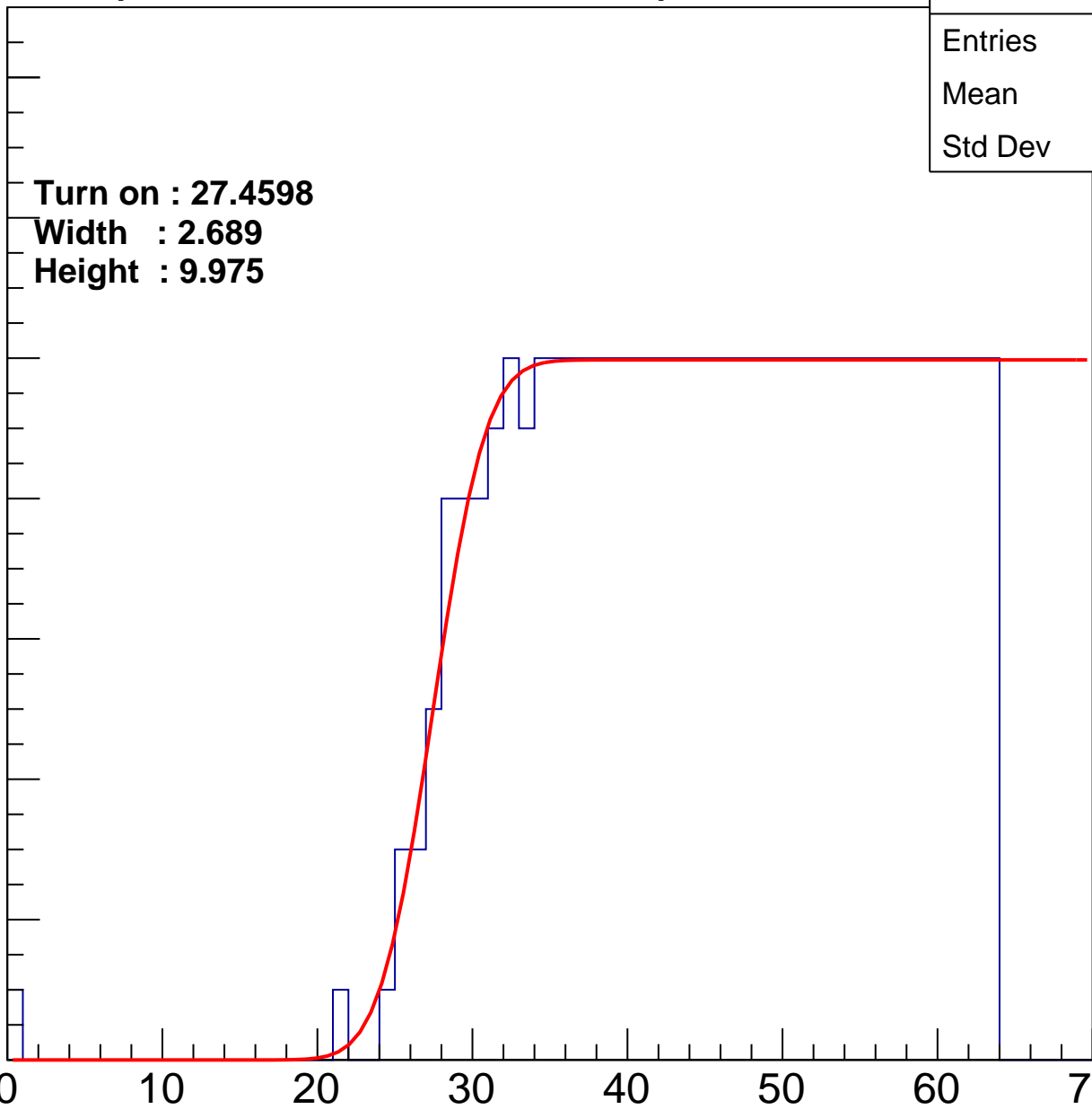
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.4598  
Width : 2.689  
Height : 9.975

Entries	366
Mean	45.01
Std Dev	10.97

ampl



# B1L103S, U14-ch35

calib\_packv5\_042523\_0143.root, FC#7, port C2

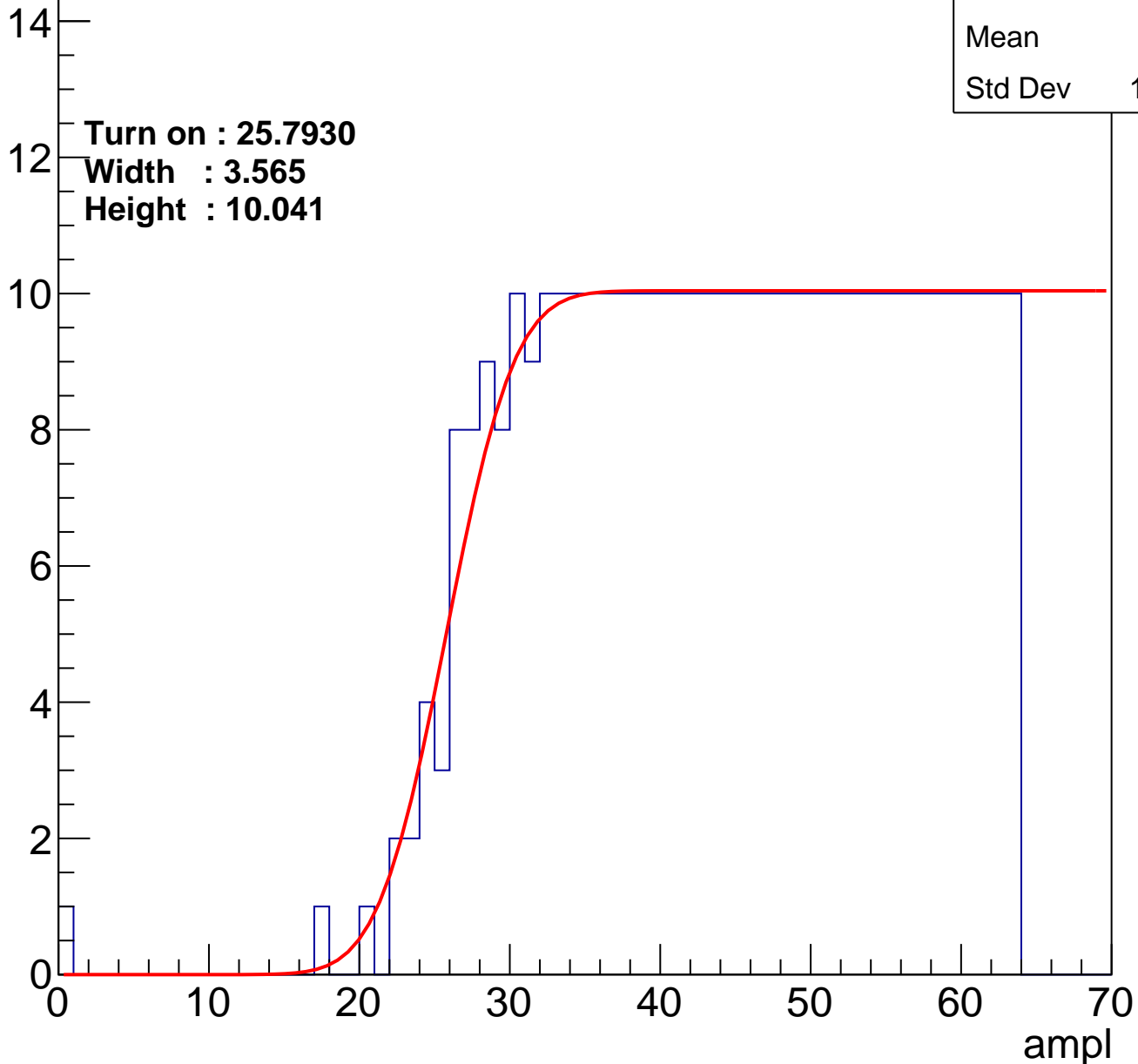
Entries	386
Mean	44
Std Dev	11.55

Turn on : 25.7930

Width : 3.565

Height : 10.041

Entry



# B1L103S, U14-ch36

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	374
Mean	44.47
Std Dev	11.58

Turn on : 27.2016

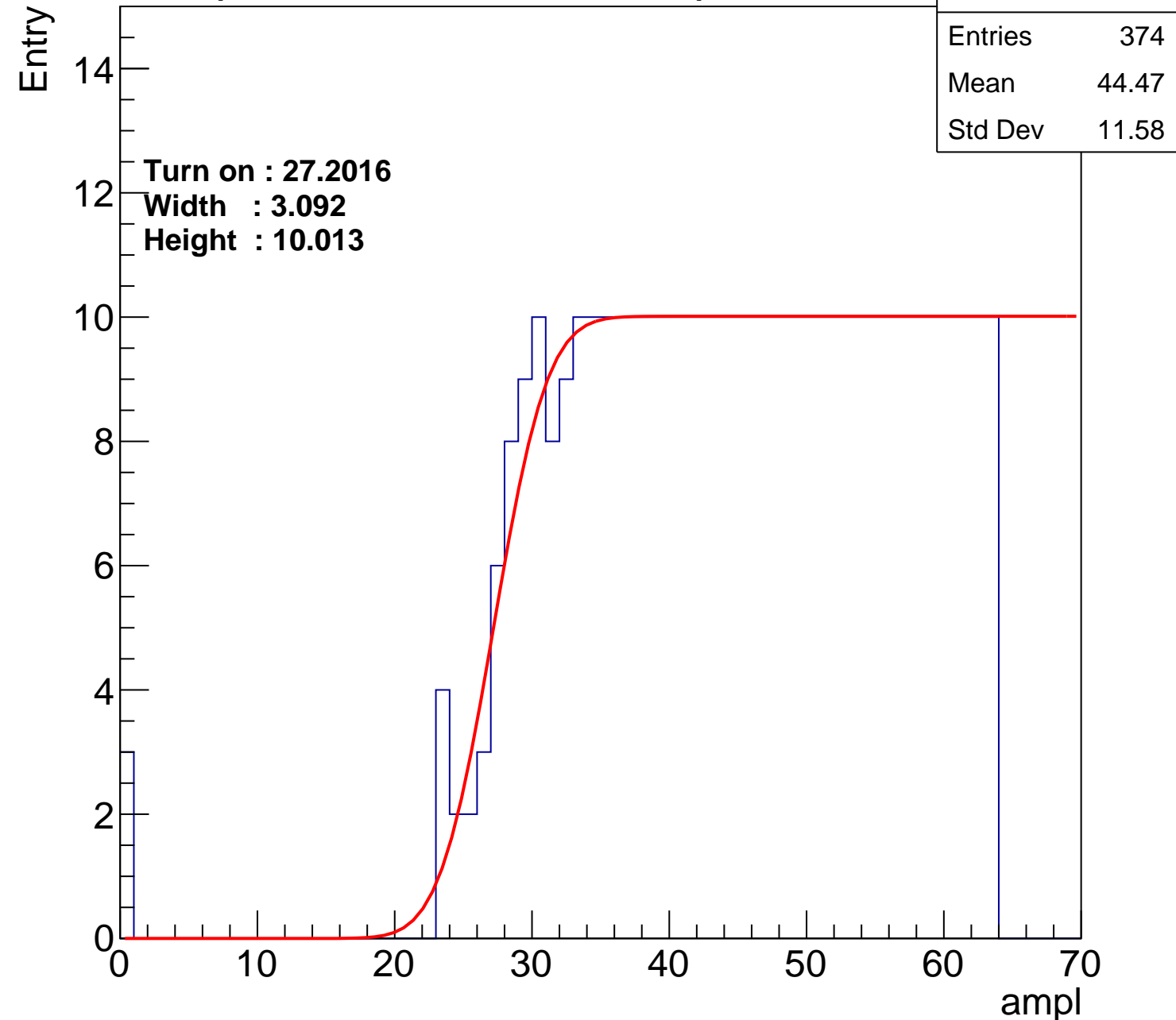
Width : 3.092

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch37

calib\_packv5\_042523\_0143.root, FC#7, port C2

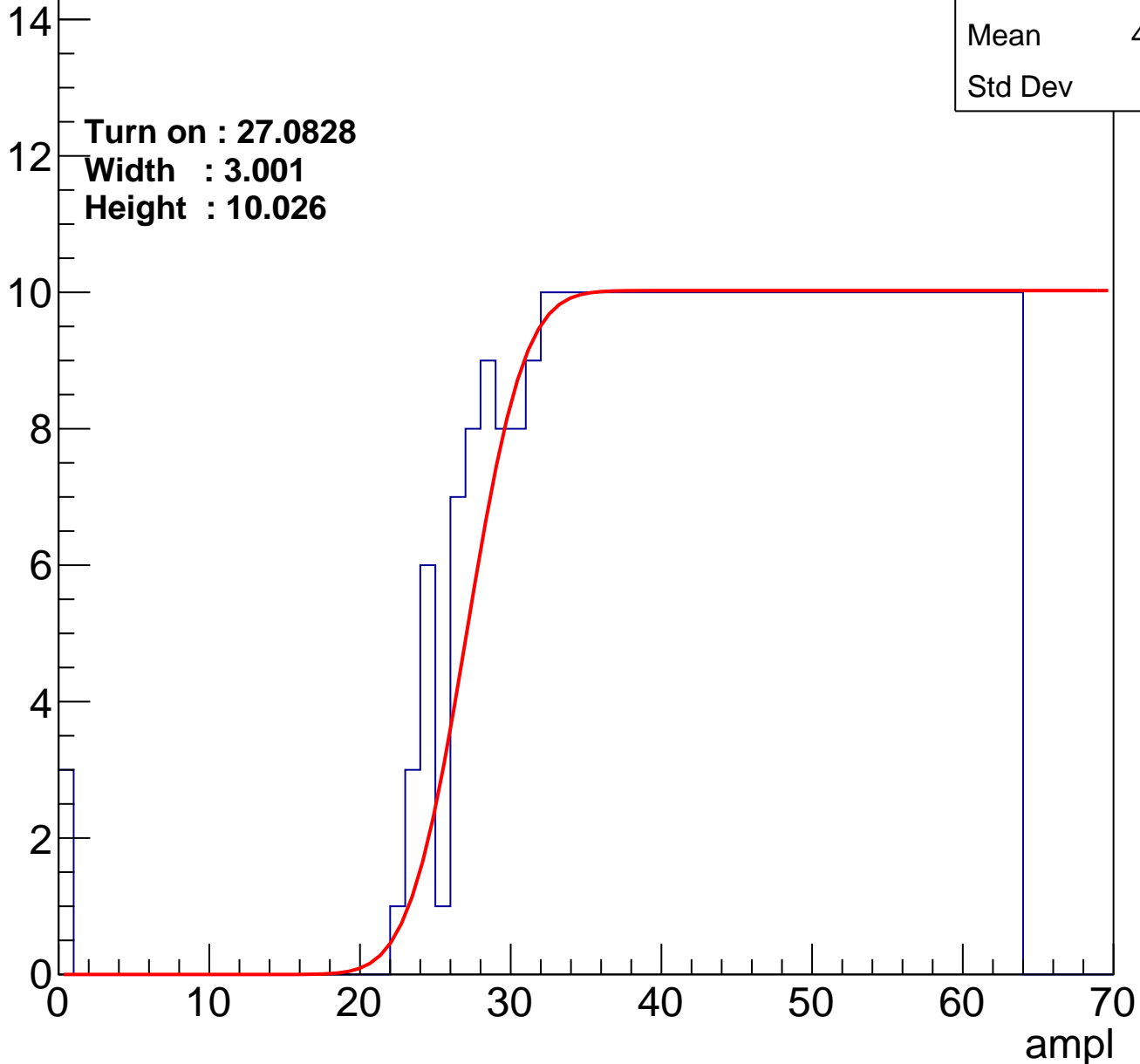
Entries	383
Mean	44.02
Std Dev	11.8

Turn on : 27.0828

Width : 3.001

Height : 10.026

Entry



# B1L103S, U14-ch38

calib\_packv5\_042523\_0143.root, FC#7, port C2

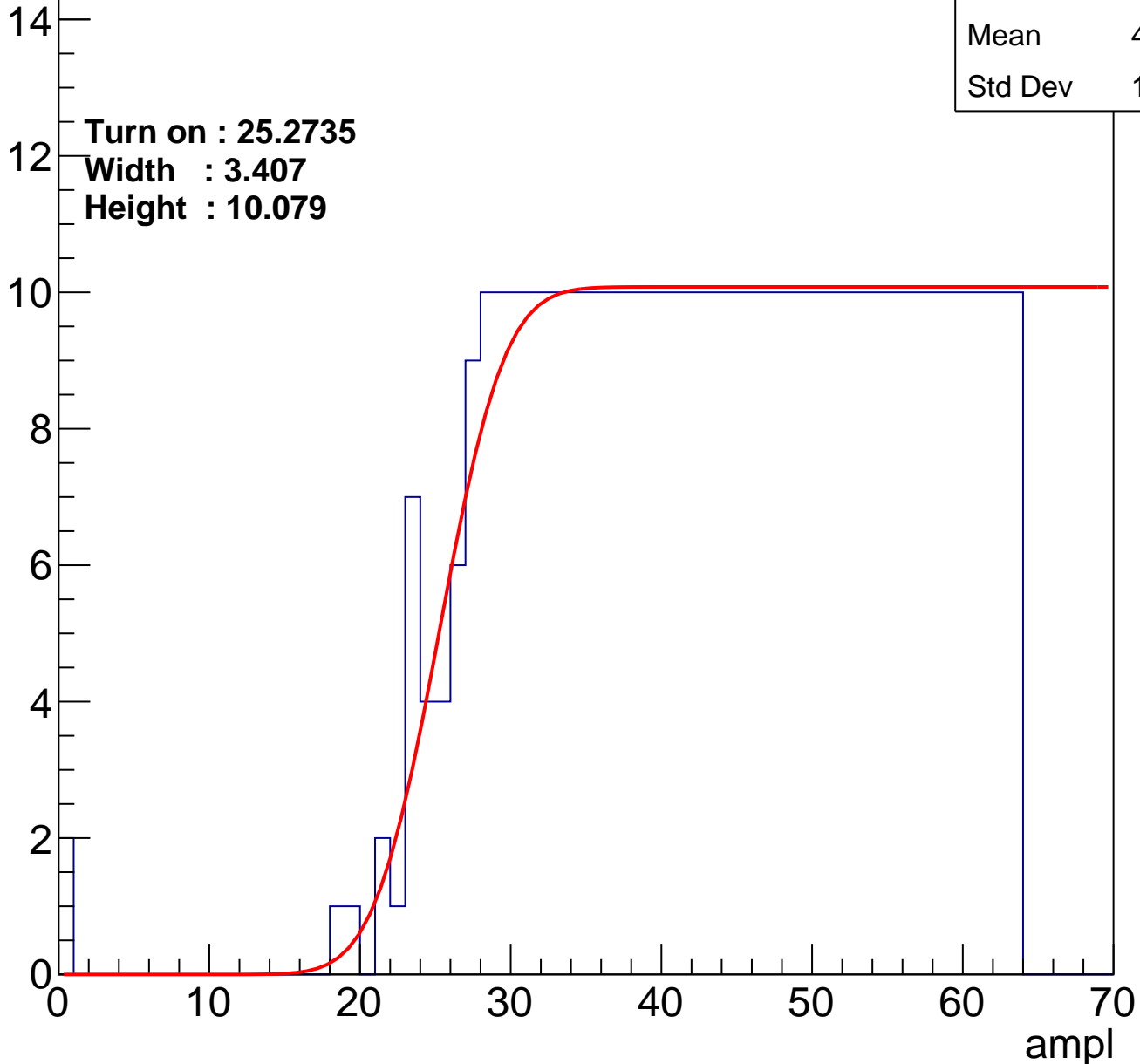
Entries	397
Mean	43.42
Std Dev	11.97

Turn on : 25.2735

Width : 3.407

Height : 10.079

Entry





# B1L103S, U14-ch39

calib\_packv5\_042523\_0143.root, FC#7, port C2

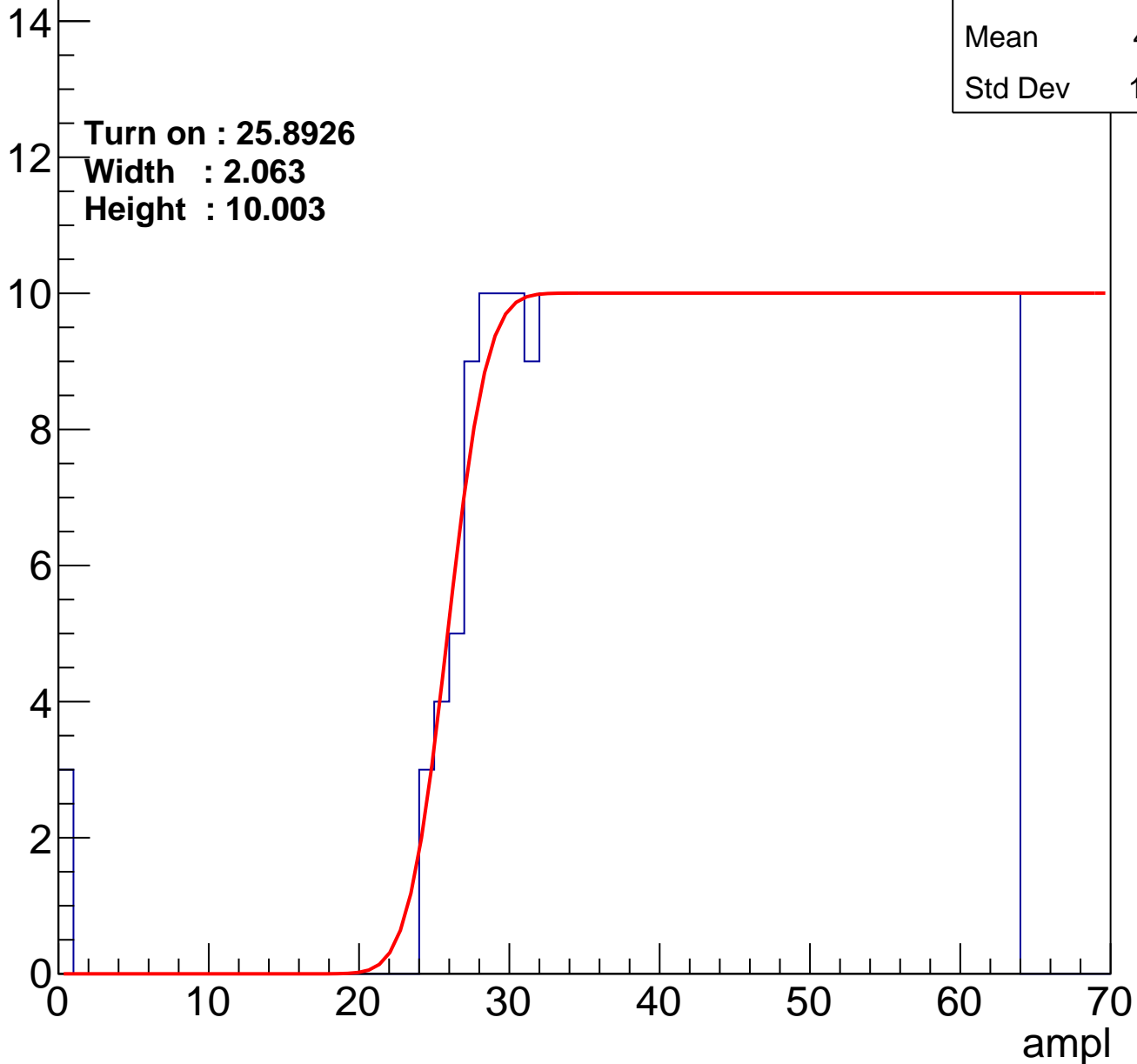
Entries	383
Mean	44.11
Std Dev	11.67

**Turn on : 25.8926**

**Width : 2.063**

**Height : 10.003**

Entry



# B1L103S, U14-ch40

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	385
Mean	43.95
Std Dev	11.82

Turn on : 25.9737

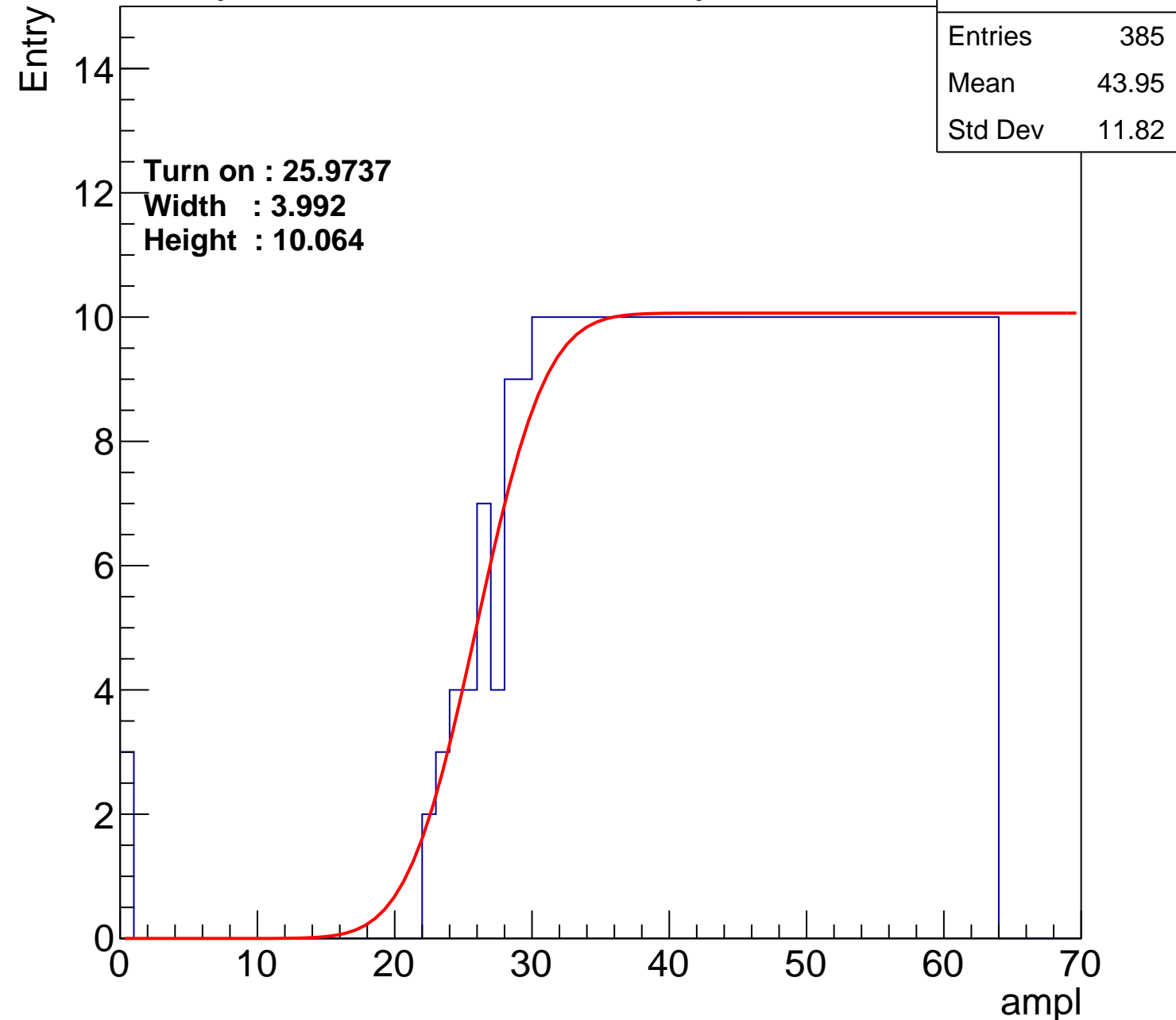
Width : 3.992

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch41

calib\_packv5\_042523\_0143.root, FC#7, port C2

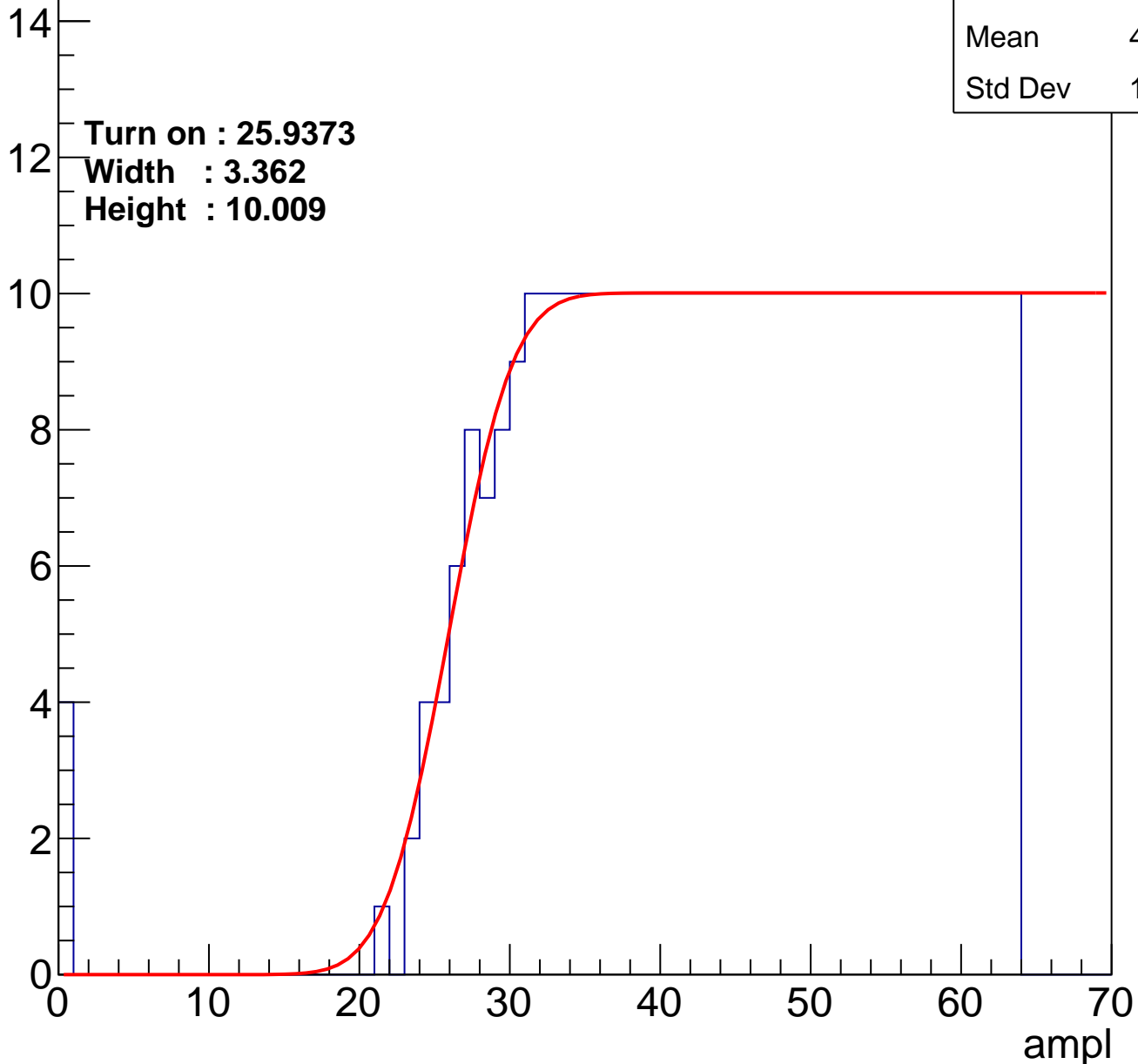
Entries	383
Mean	43.98
Std Dev	11.95

Turn on : 25.9373

Width : 3.362

Height : 10.009

Entry



# B1L103S, U14-ch42

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	383
Mean	44.02
Std Dev	11.89

Turn on : 26.3414

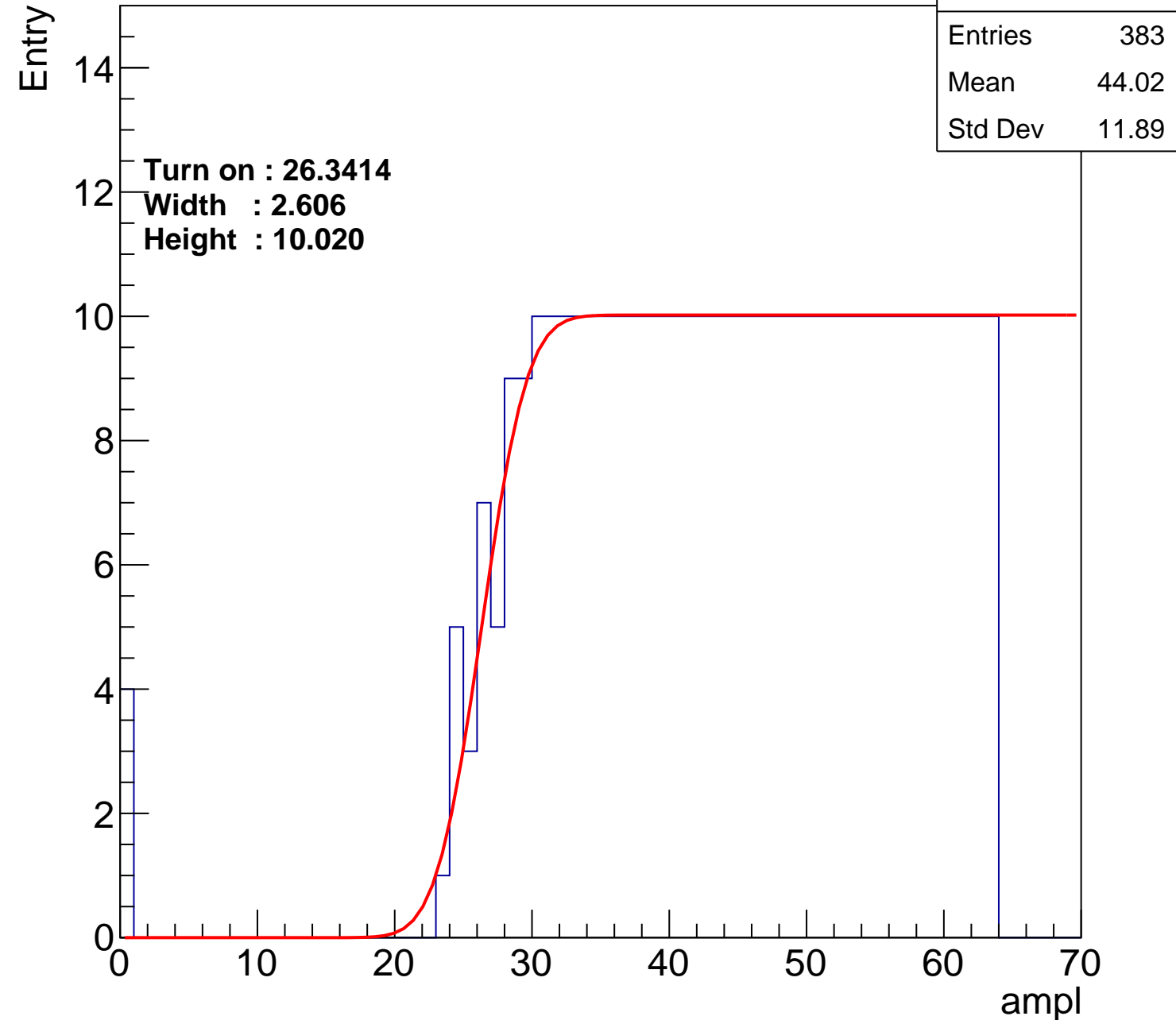
Width : 2.606

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch43

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	386
Mean	43.83
Std Dev	12.03

Turn on : 26.2710

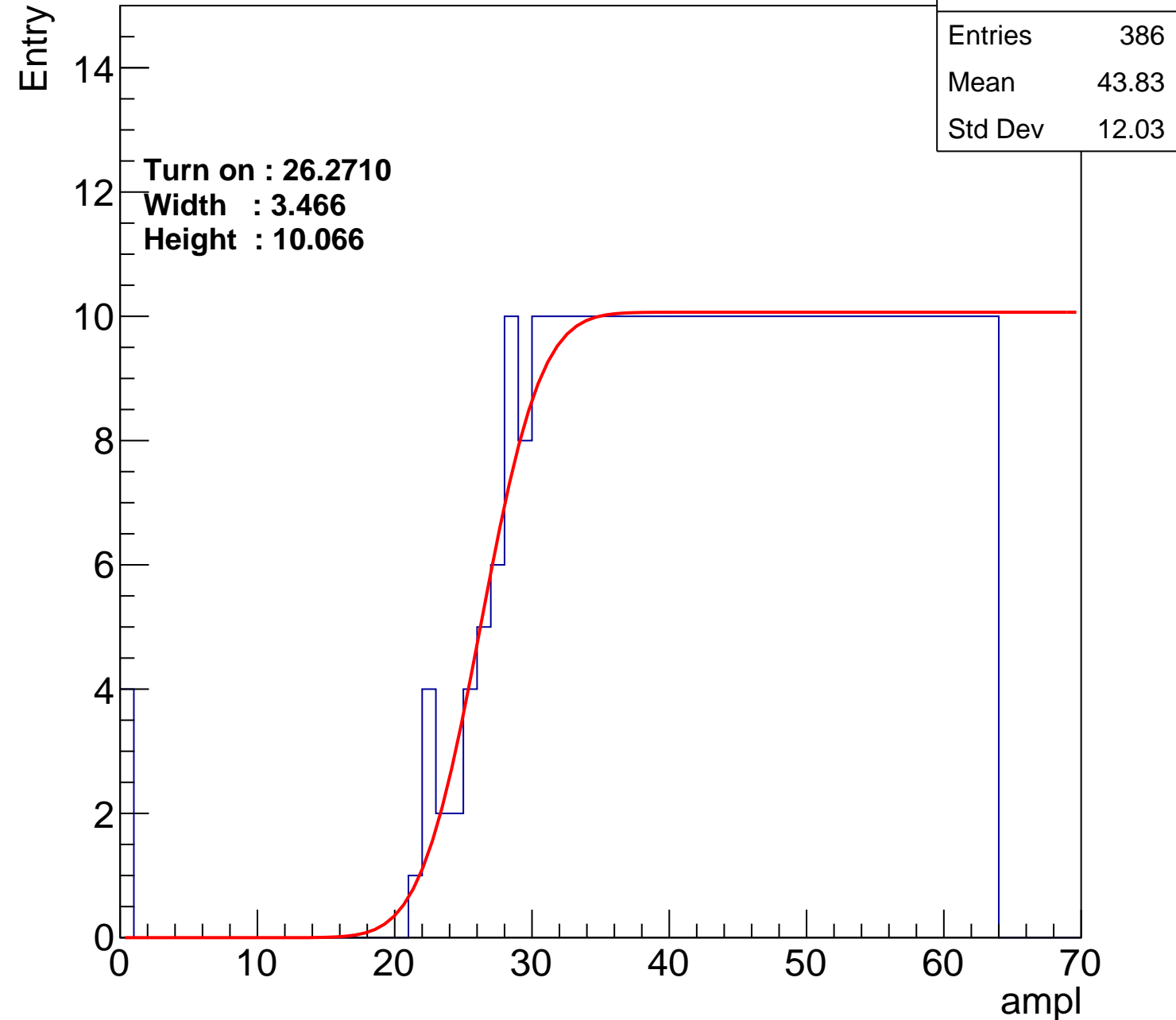
Width : 3.466

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch44

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	397
Mean	43.24
Std Dev	12.42

Turn on : 25.0397

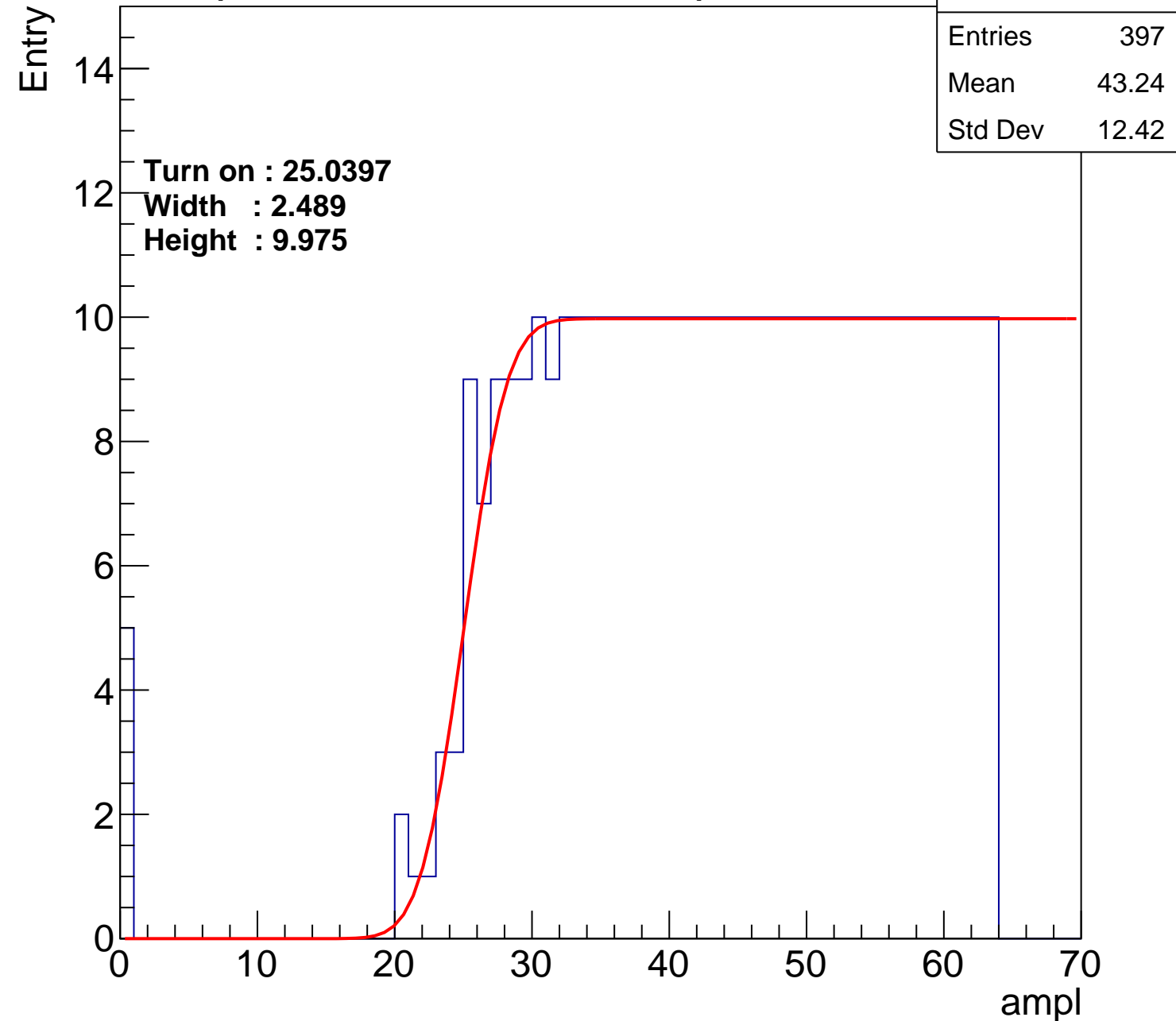
Width : 2.489

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch45

calib\_packv5\_042523\_0143.root, FC#7, port C2

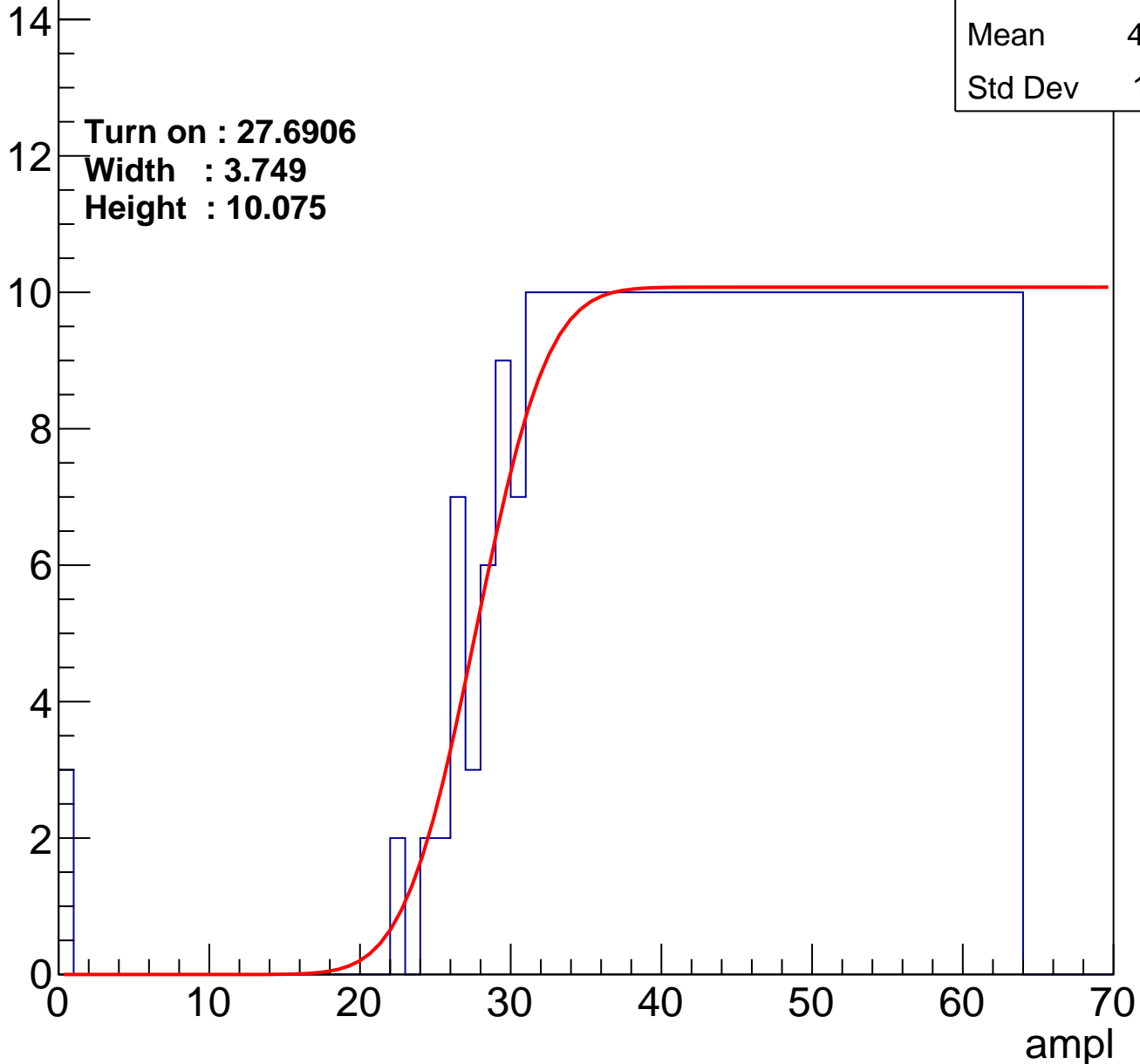
Entries	371
Mean	44.62
Std Dev	11.51

Turn on : 27.6906

Width : 3.749

Height : 10.075

Entry



# B1L103S, U14-ch46

calib\_packv5\_042523\_0143.root, FC#7, port C2

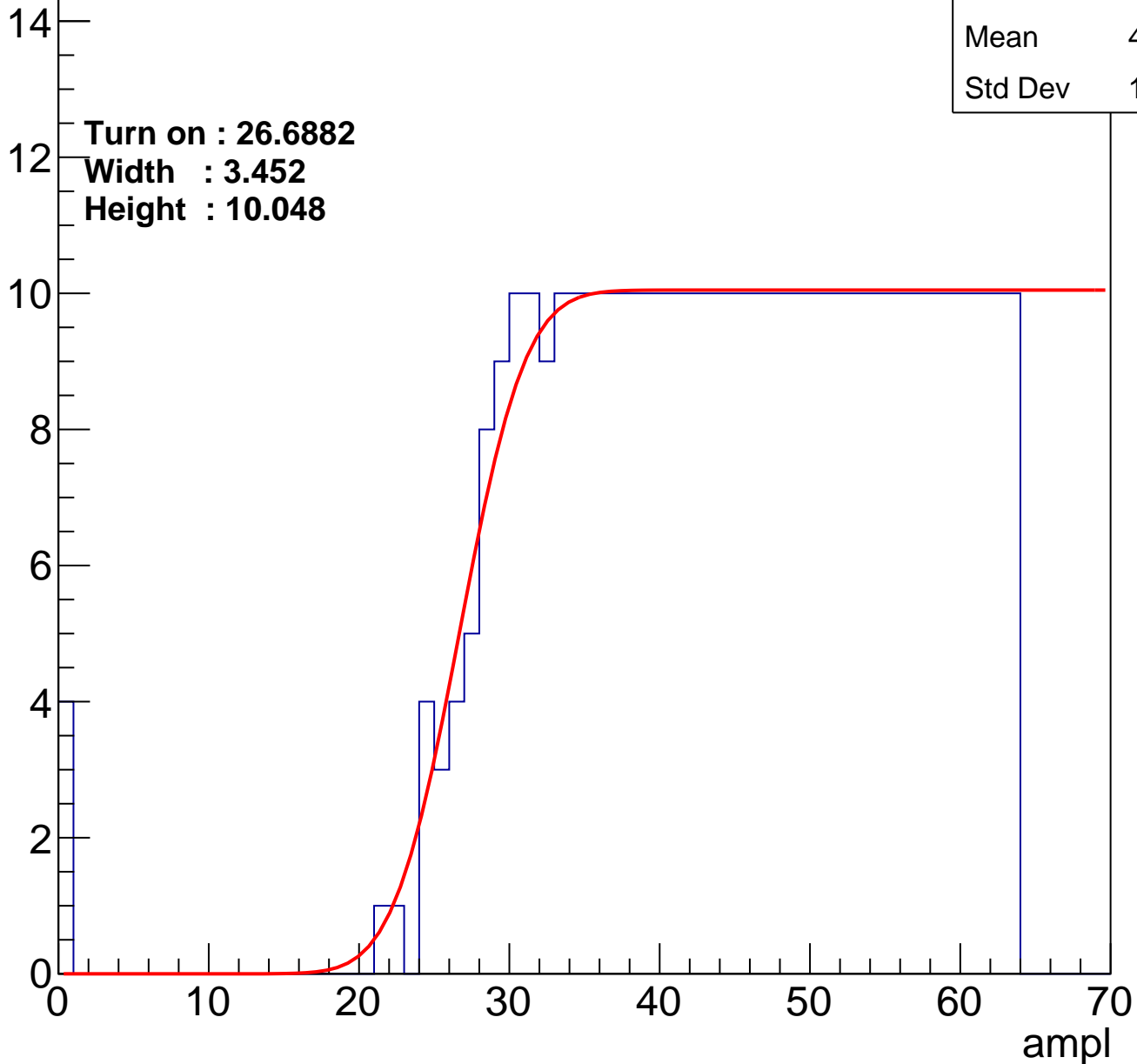
Entries	378
Mean	44.22
Std Dev	11.83

**Turn on : 26.6882**

**Width : 3.452**

**Height : 10.048**

Entry





# B1L103S, U14-ch47

calib\_packv5\_042523\_0143.root, FC#7, port C2

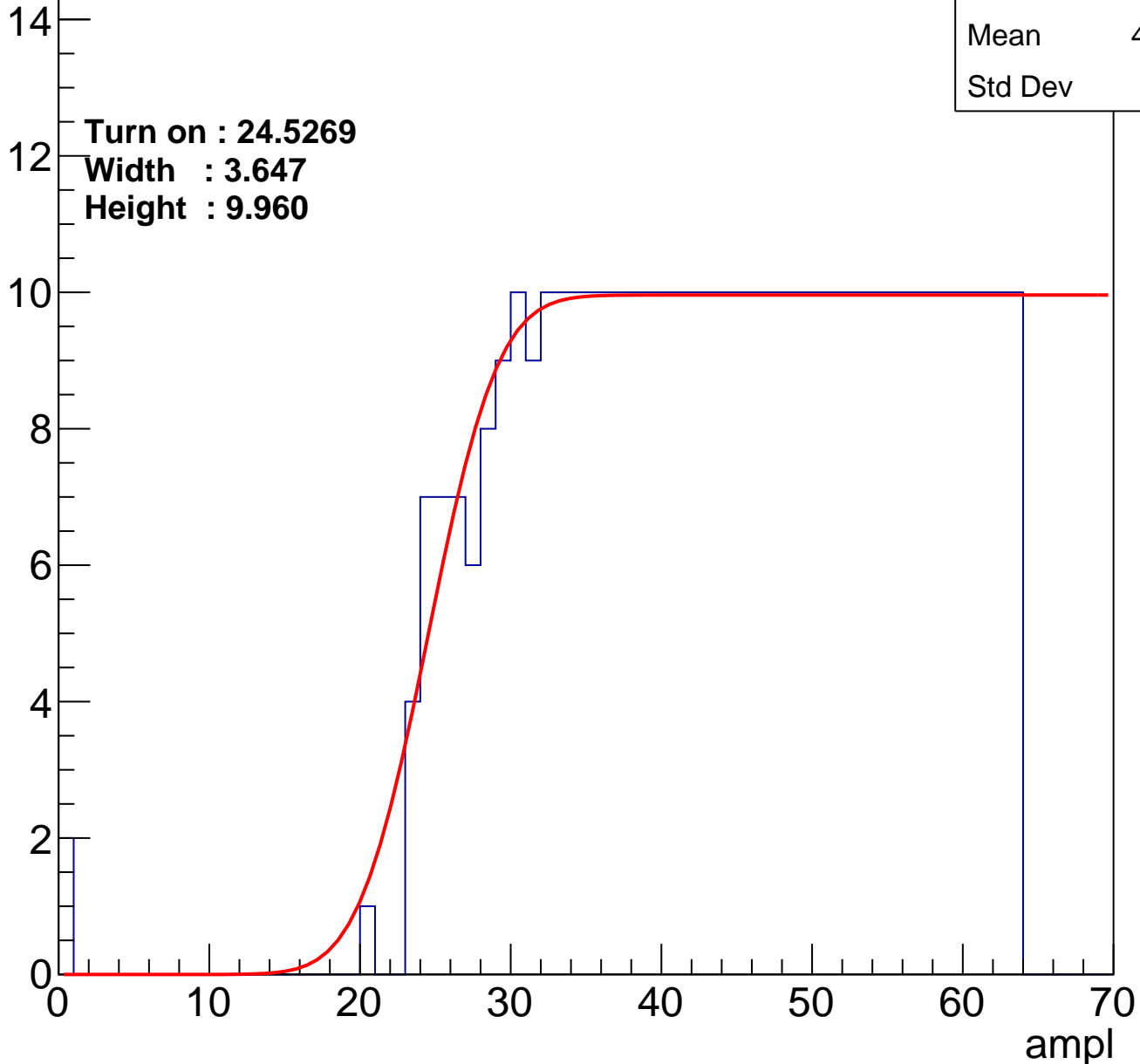
Entries	390
Mean	43.75
Std Dev	11.8

Turn on : 24.5269

Width : 3.647

Height : 9.960

Entry



# B1L103S, U14-ch48

calib\_packv5\_042523\_0143.root, FC#7, port C2

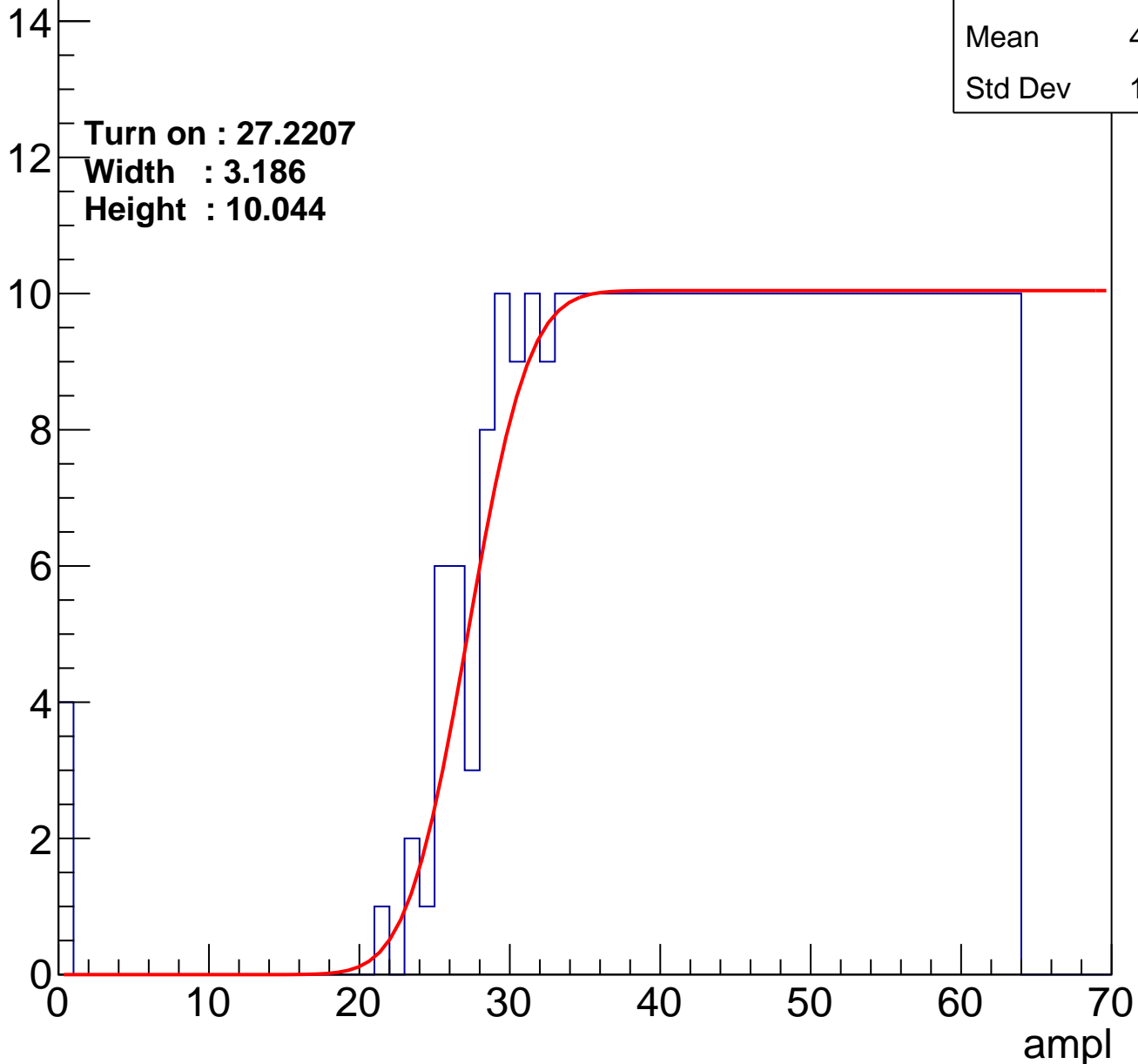
Entries	379
Mean	44.17
Std Dev	11.86

Turn on : 27.2207

Width : 3.186

Height : 10.044

Entry



# B1L103S, U14-ch49

calib\_packv5\_042523\_0143.root, FC#7, port C2

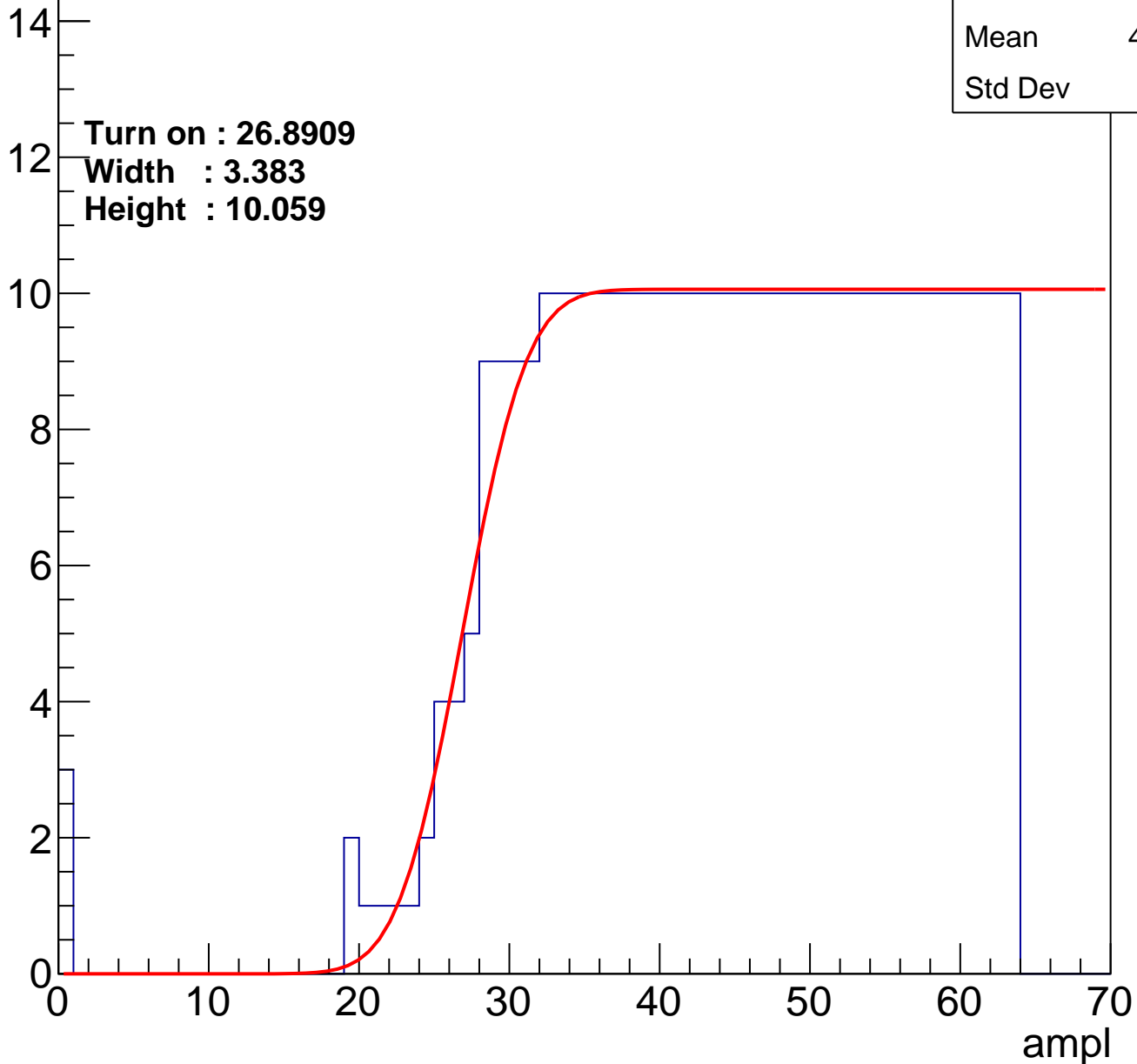
Entries	380
Mean	44.14
Std Dev	11.8

Turn on : 26.8909

Width : 3.383

Height : 10.059

Entry



# B1L103S, U14-ch50

calib\_packv5\_042523\_0143.root, FC#7, port C2

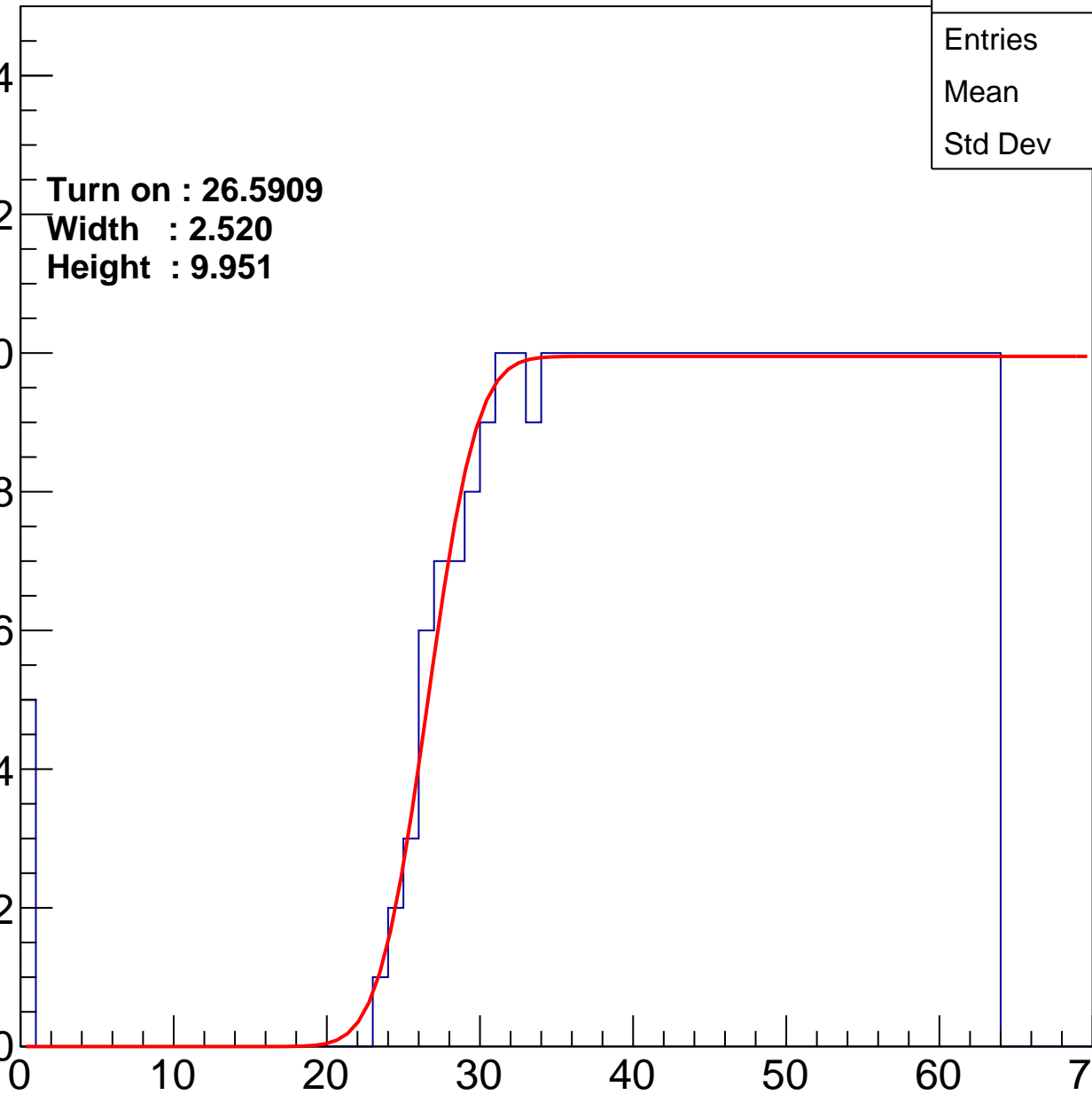
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.5909**  
**Width : 2.520**  
**Height : 9.951**

Entries	377
Mean	44.21
Std Dev	11.98

ampl



# B1L103S, U14-ch51

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	387
Mean	43.86
Std Dev	11.86

Turn on : 25.6180

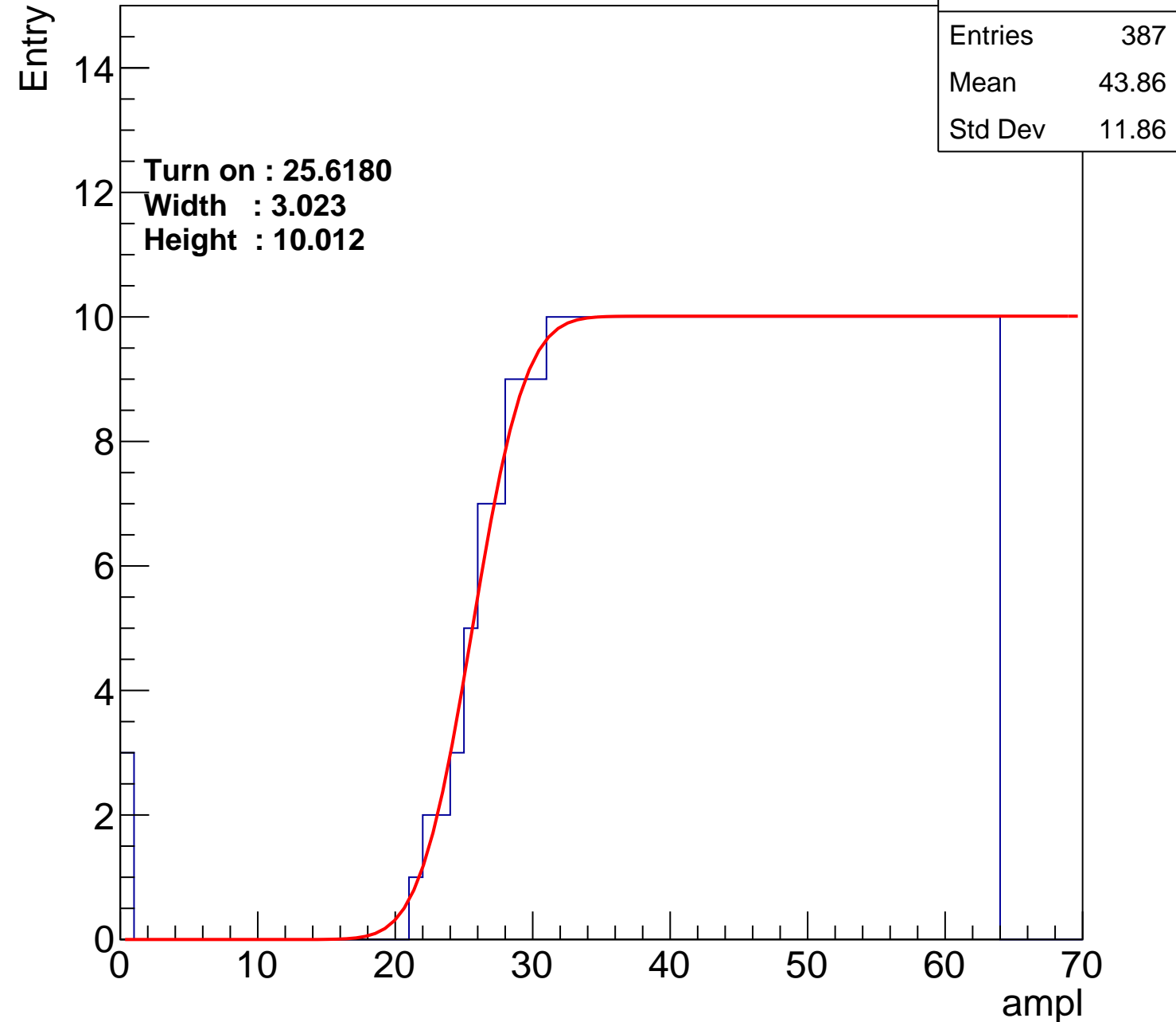
Width : 3.023

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch52

calib\_packv5\_042523\_0143.root, FC#7, port C2

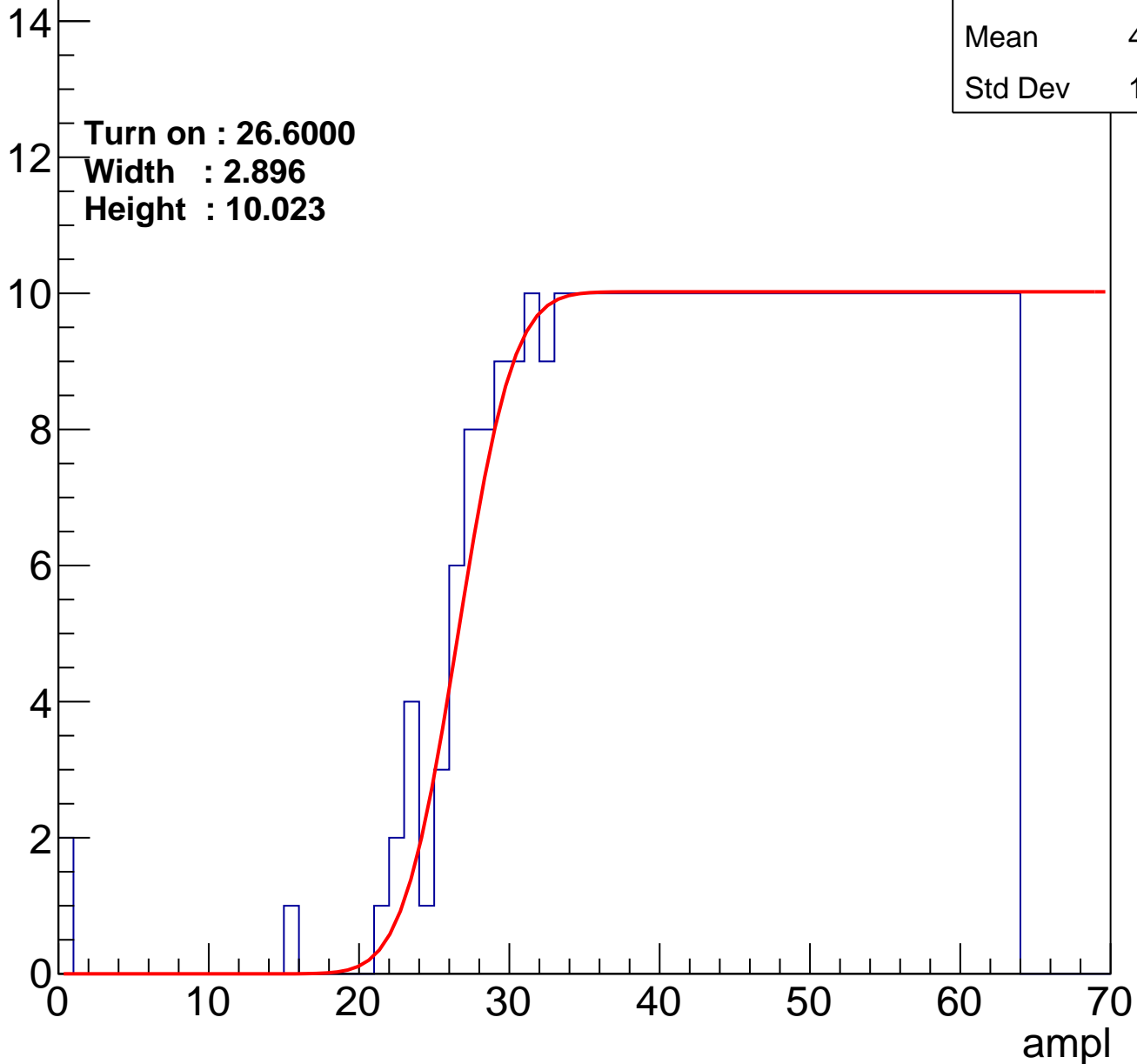
Entries	383
Mean	44.06
Std Dev	11.68

Turn on : 26.6000

Width : 2.896

Height : 10.023

Entry



# B1L103S, U14-ch53

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	393
Mean	43.46
Std Dev	12.23

Turn on : 24.9770

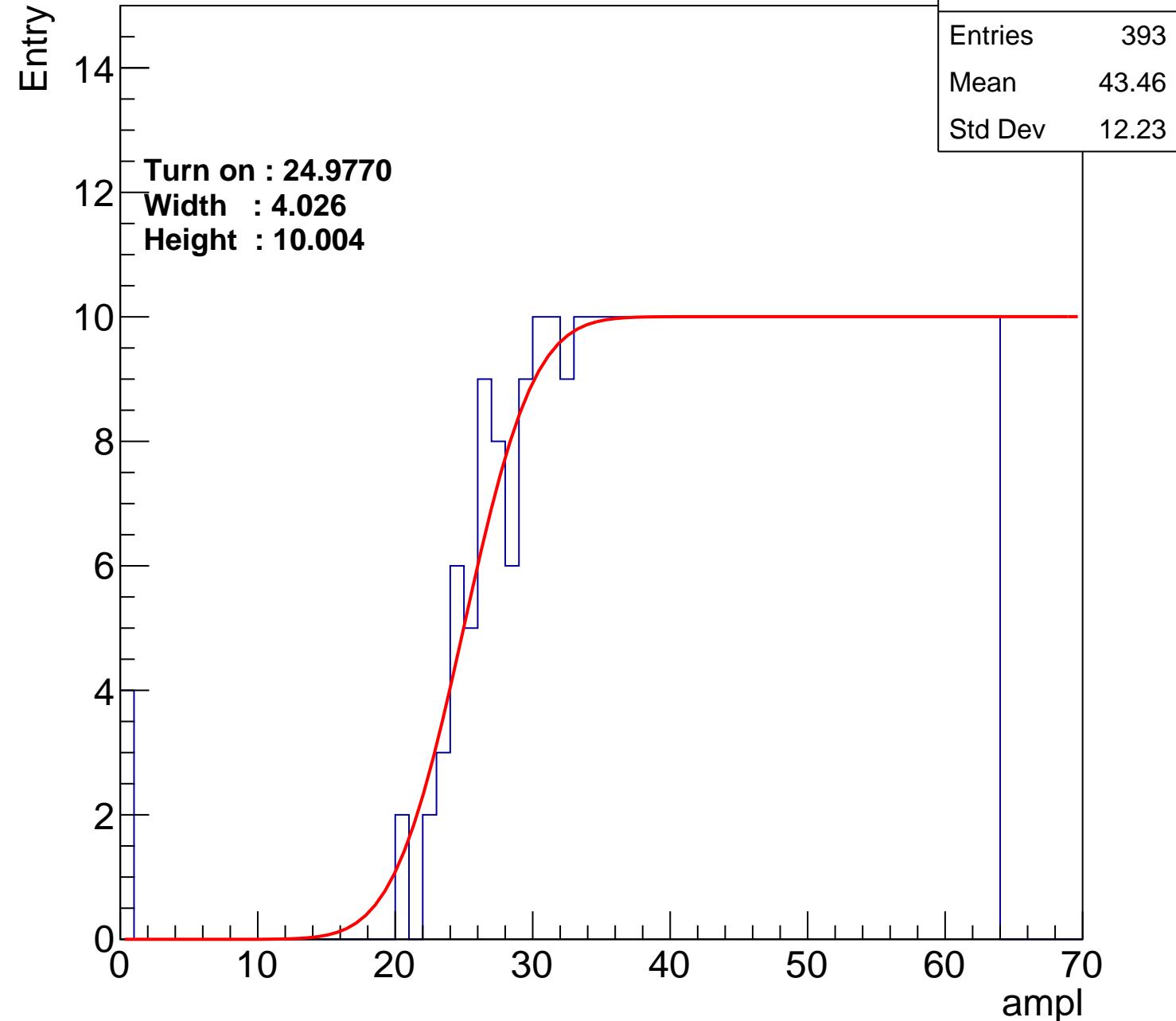
Width : 4.026

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch54

calib\_packv5\_042523\_0143.root, FC#7, port C2

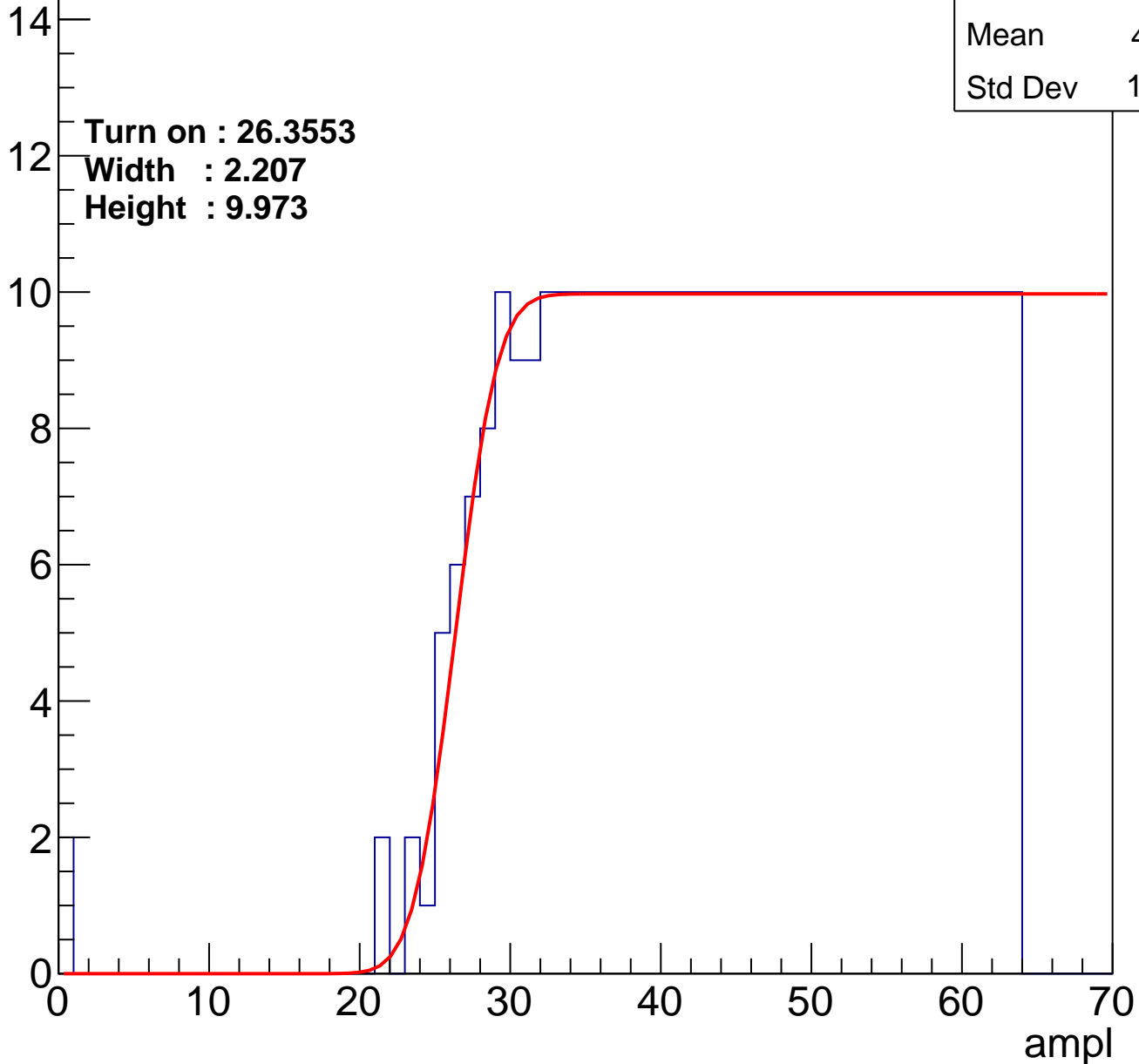
Entries	381
Mean	44.21
Std Dev	11.54

Turn on : 26.3553

Width : 2.207

Height : 9.973

Entry





# B1L103S, U14-ch55

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	368
Mean	44.84
Std Dev	11.22

Turn on : 27.2055

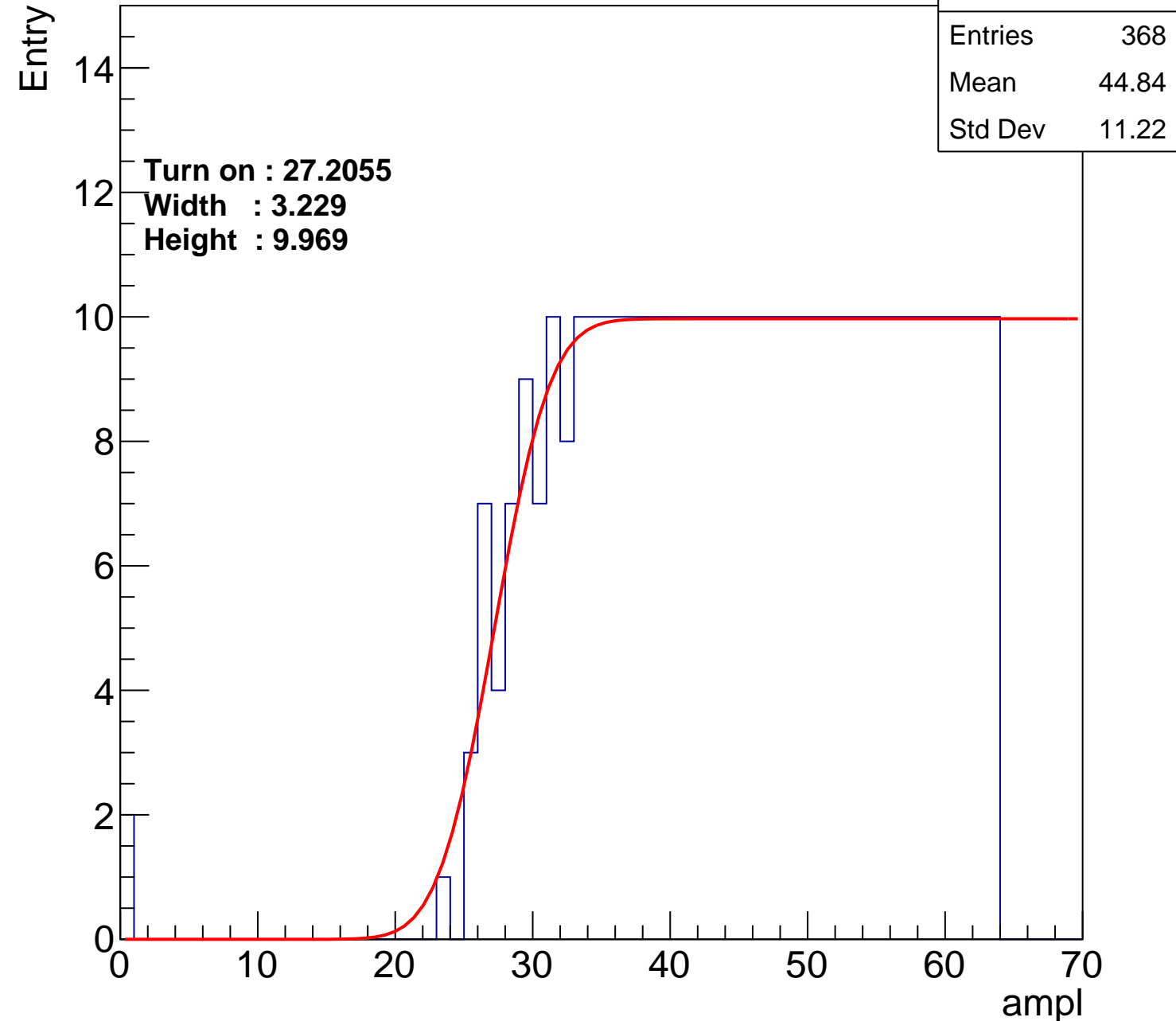
Width : 3.229

Height : 9.969

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch56

calib\_packv5\_042523\_0143.root, FC#7, port C2

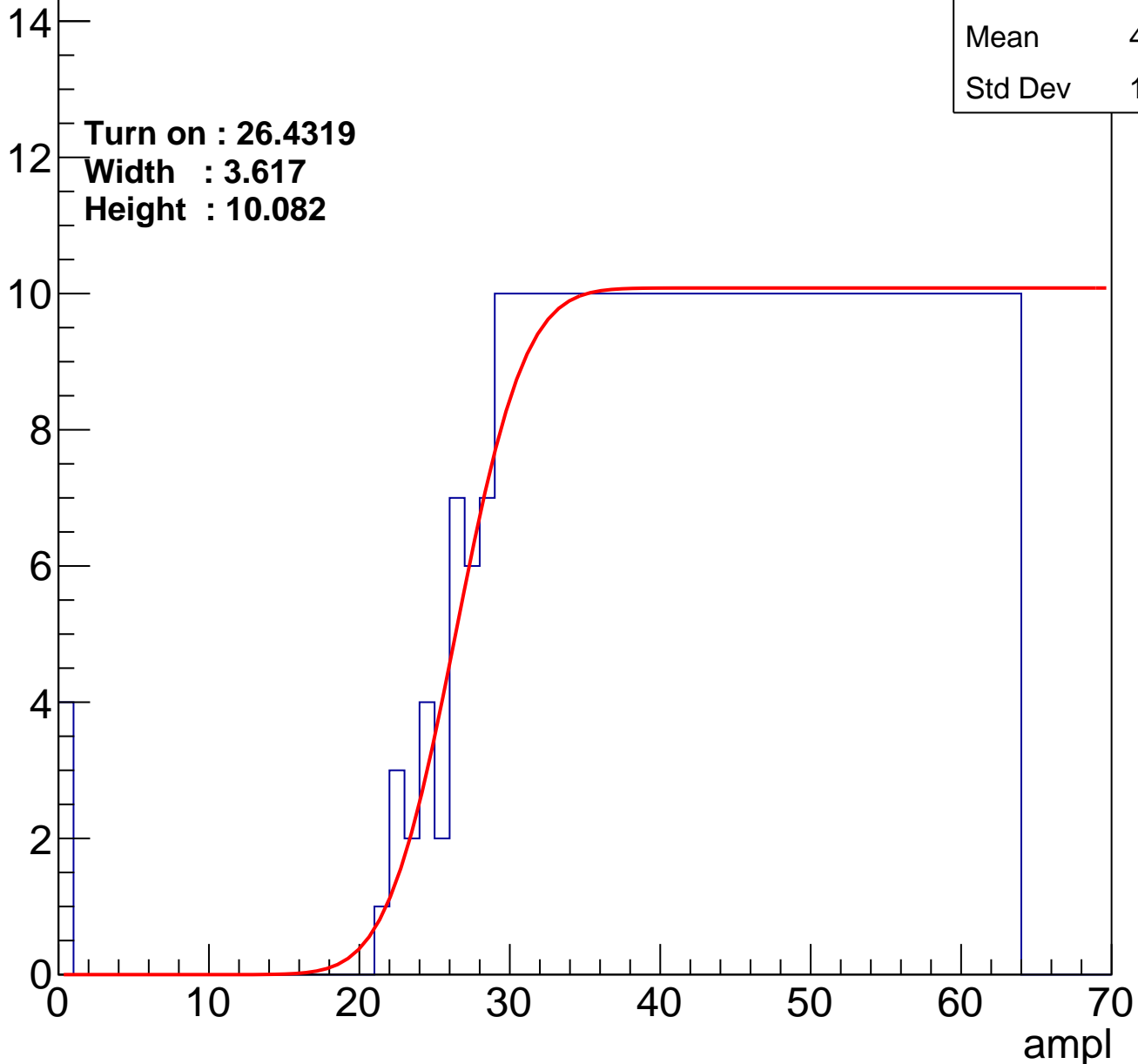
Entries	386
Mean	43.83
Std Dev	12.02

Turn on : 26.4319

Width : 3.617

Height : 10.082

Entry



# B1L103S, U14-ch57

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	369
Mean	44.63
Std Dev	11.68

Turn on : 27.3132

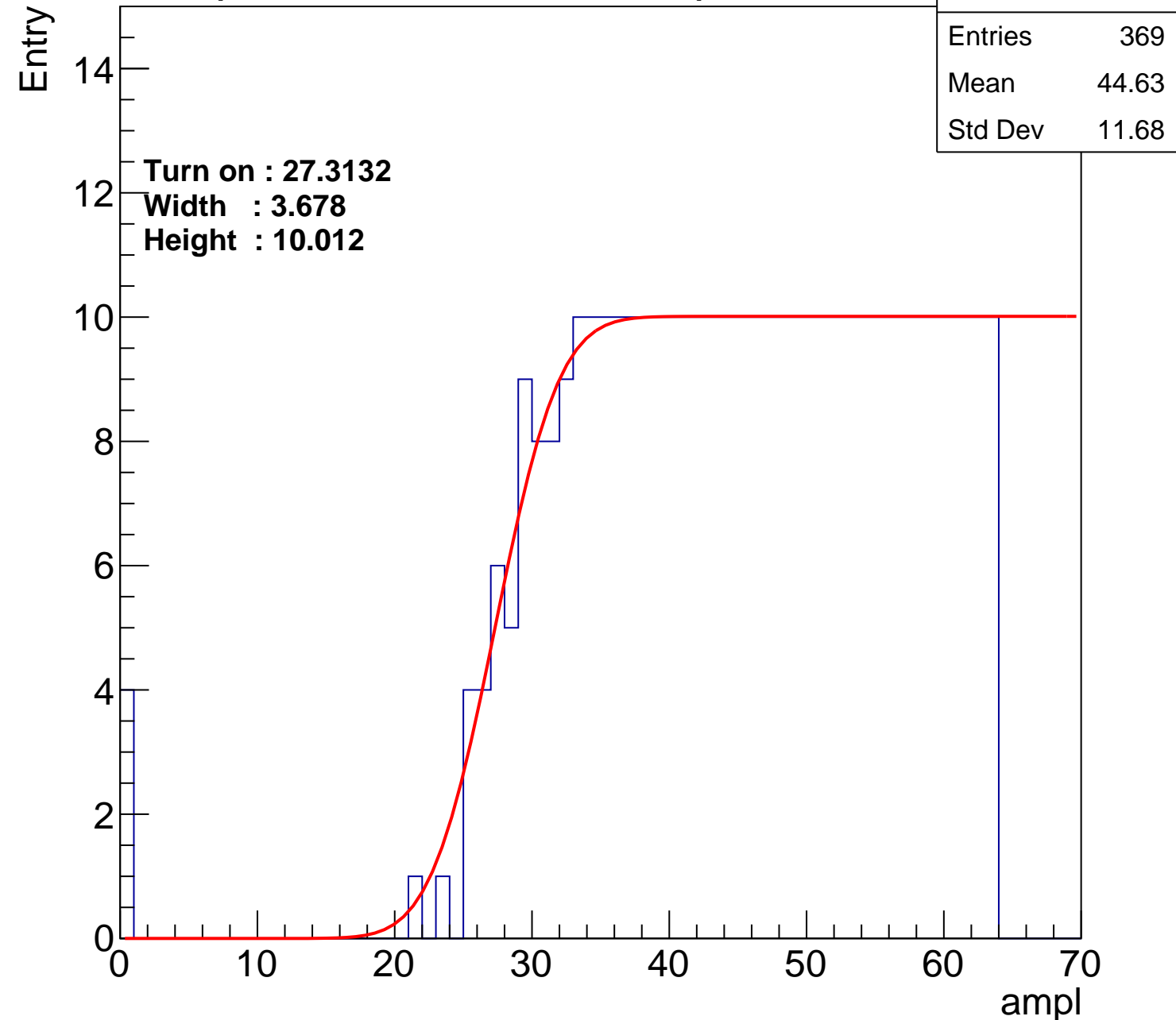
Width : 3.678

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch58

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	392
Mean	43.56
Std Dev	12.13

Turn on : 25.5028

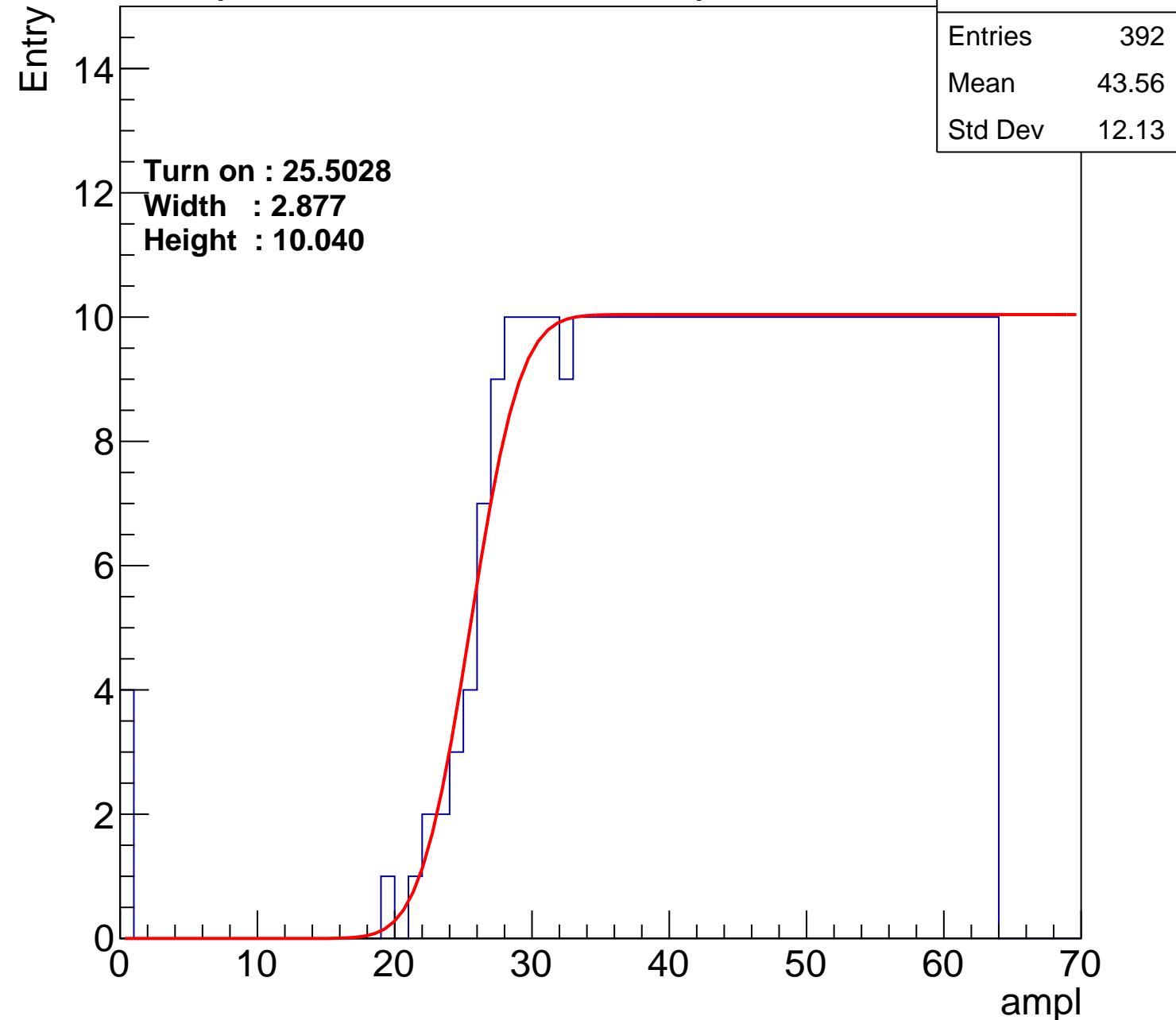
Width : 2.877

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch59

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 26.3796

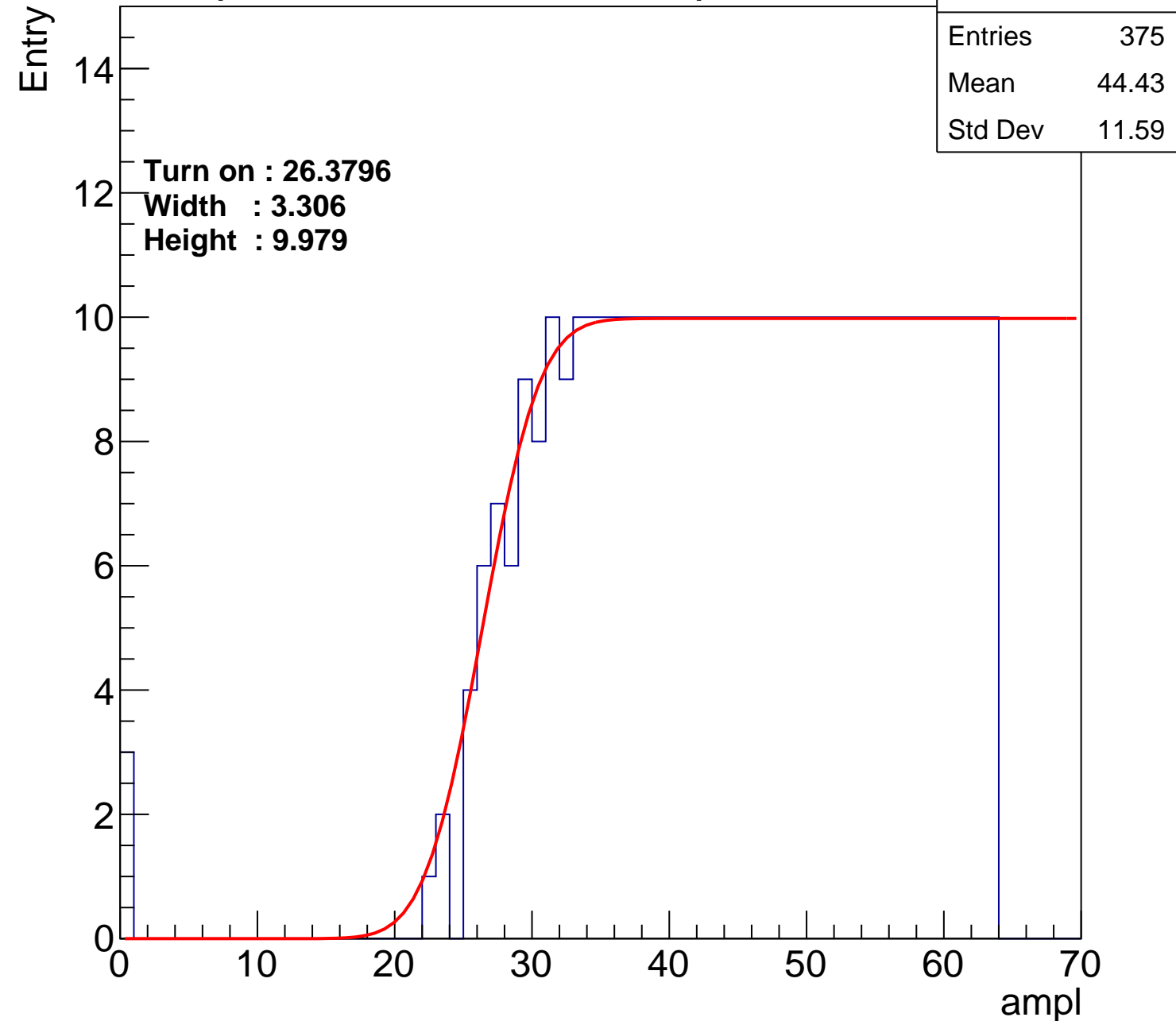
Width : 3.306

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch60

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	381
Mean	43.96
Std Dev	12.22

**Turn on : 26.2185**

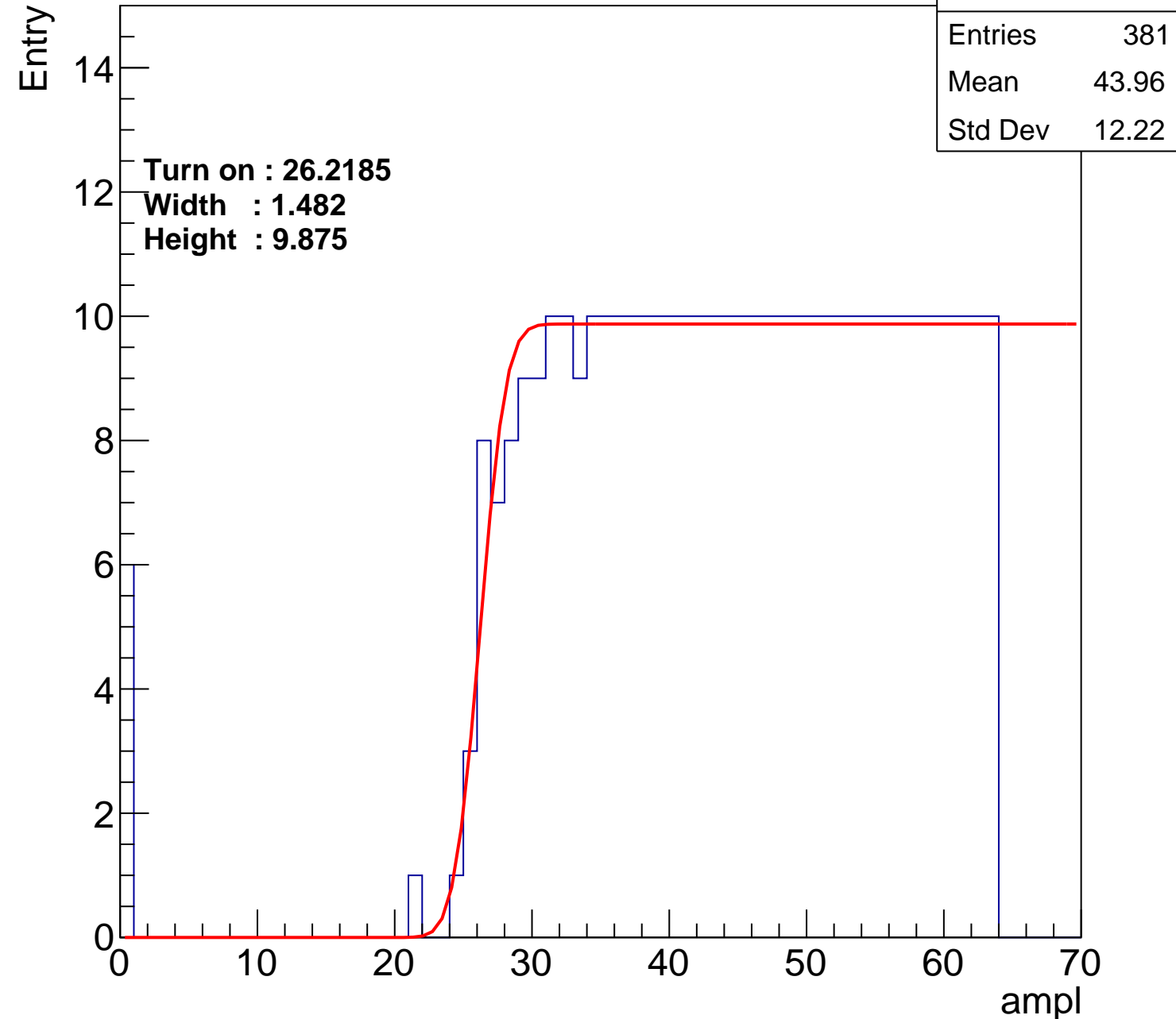
**Width : 1.482**

**Height : 9.875**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch61

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	370
Mean	44.52
Std Dev	11.87

Turn on : 27.8420

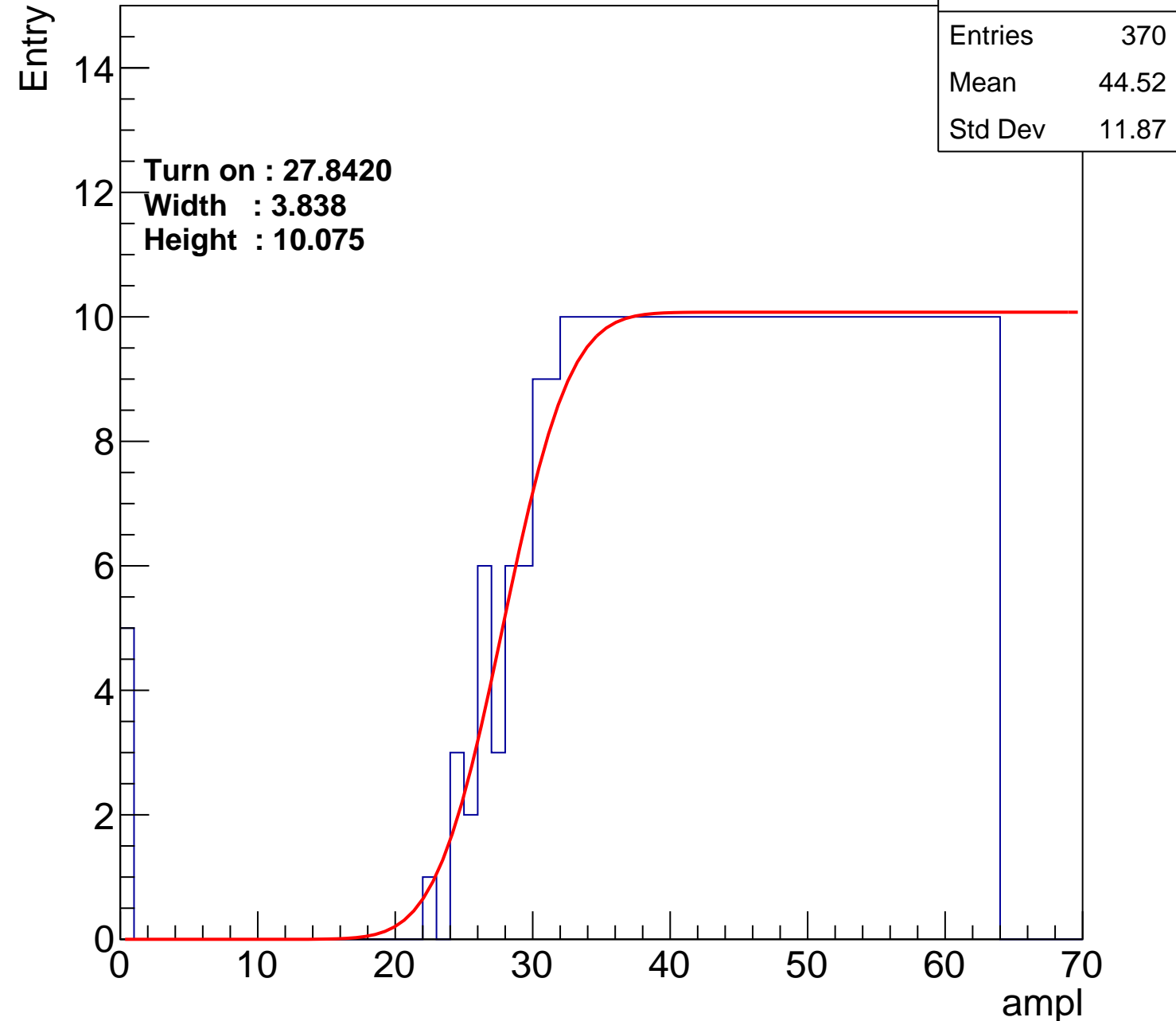
Width : 3.838

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch62

**calib\_packv5\_042523\_0143.root, FC#7, port C2**

Entries	375
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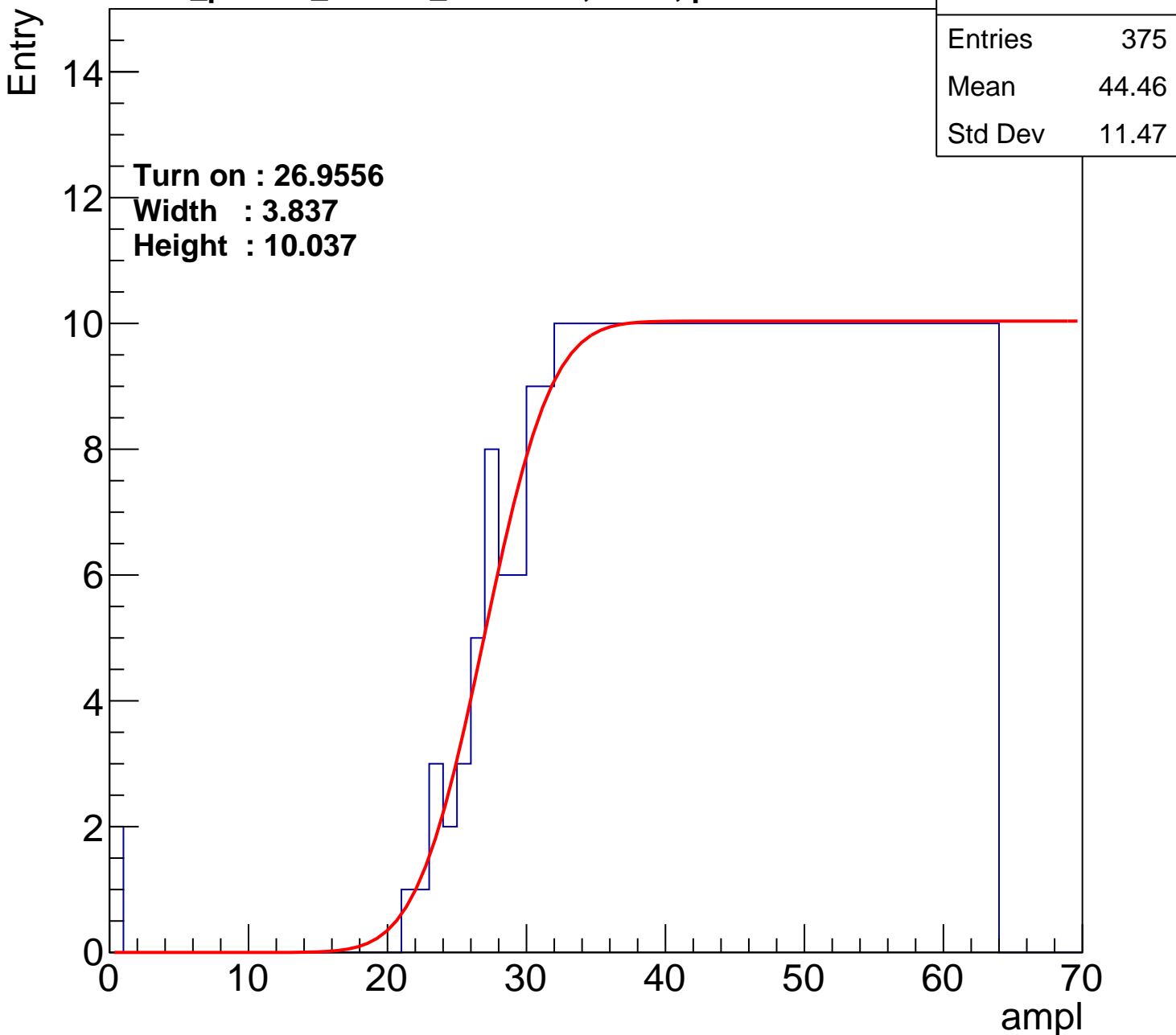
Mean	44.46
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Std Dev	11.47
---------	-------

**Turn on : 26.9556**

**Width : 3.837**

**Height : 10.037**





# B1L103S, U14-ch63

calib\_packv5\_042523\_0143.root, FC#7, port C2

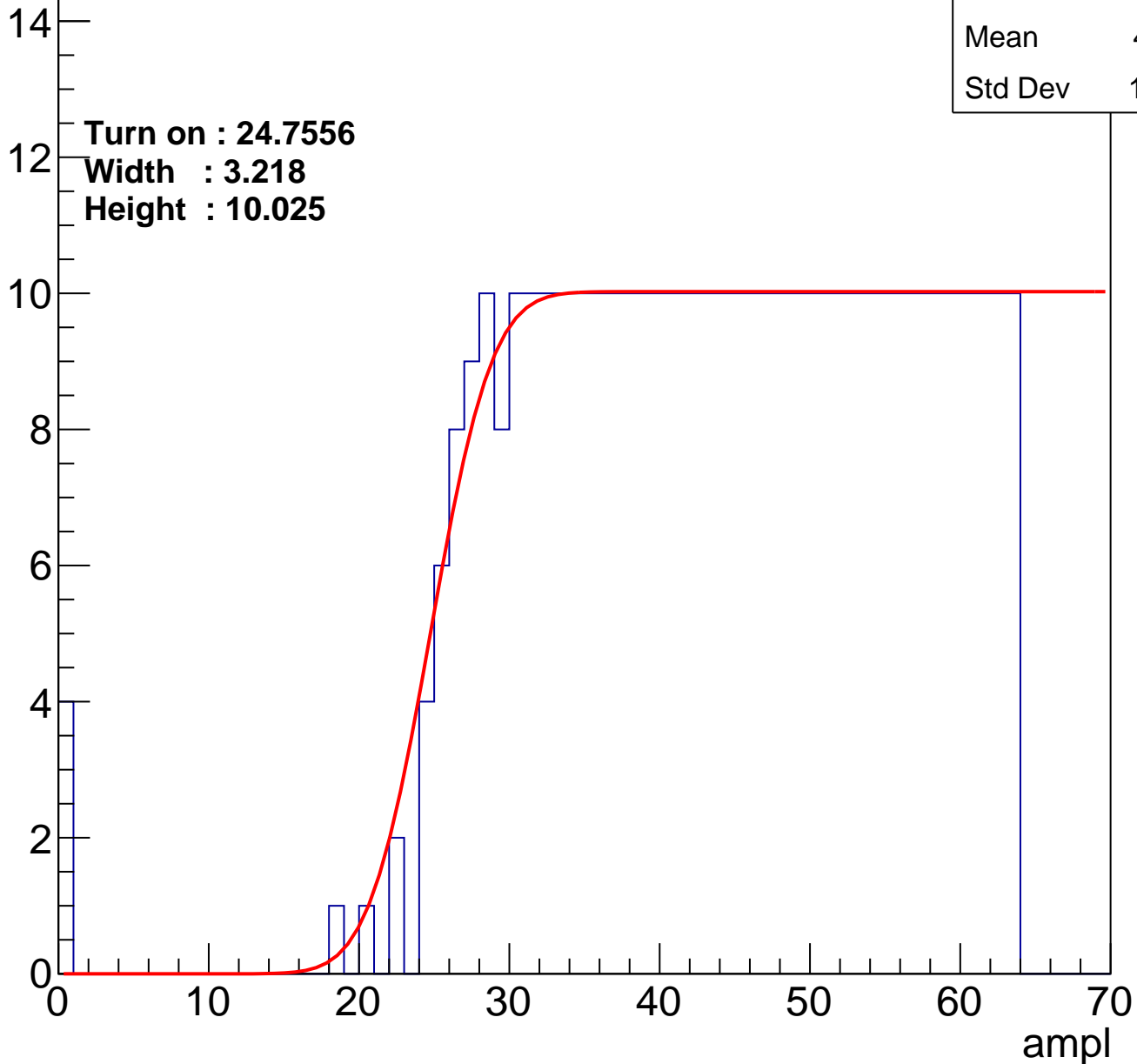
Entries	393
Mean	43.51
Std Dev	12.15

Turn on : 24.7556

Width : 3.218

Height : 10.025

Entry



# B1L103S, U14-ch64

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	369
Mean	44.77
Std Dev	11.29

Turn on : 27.4745

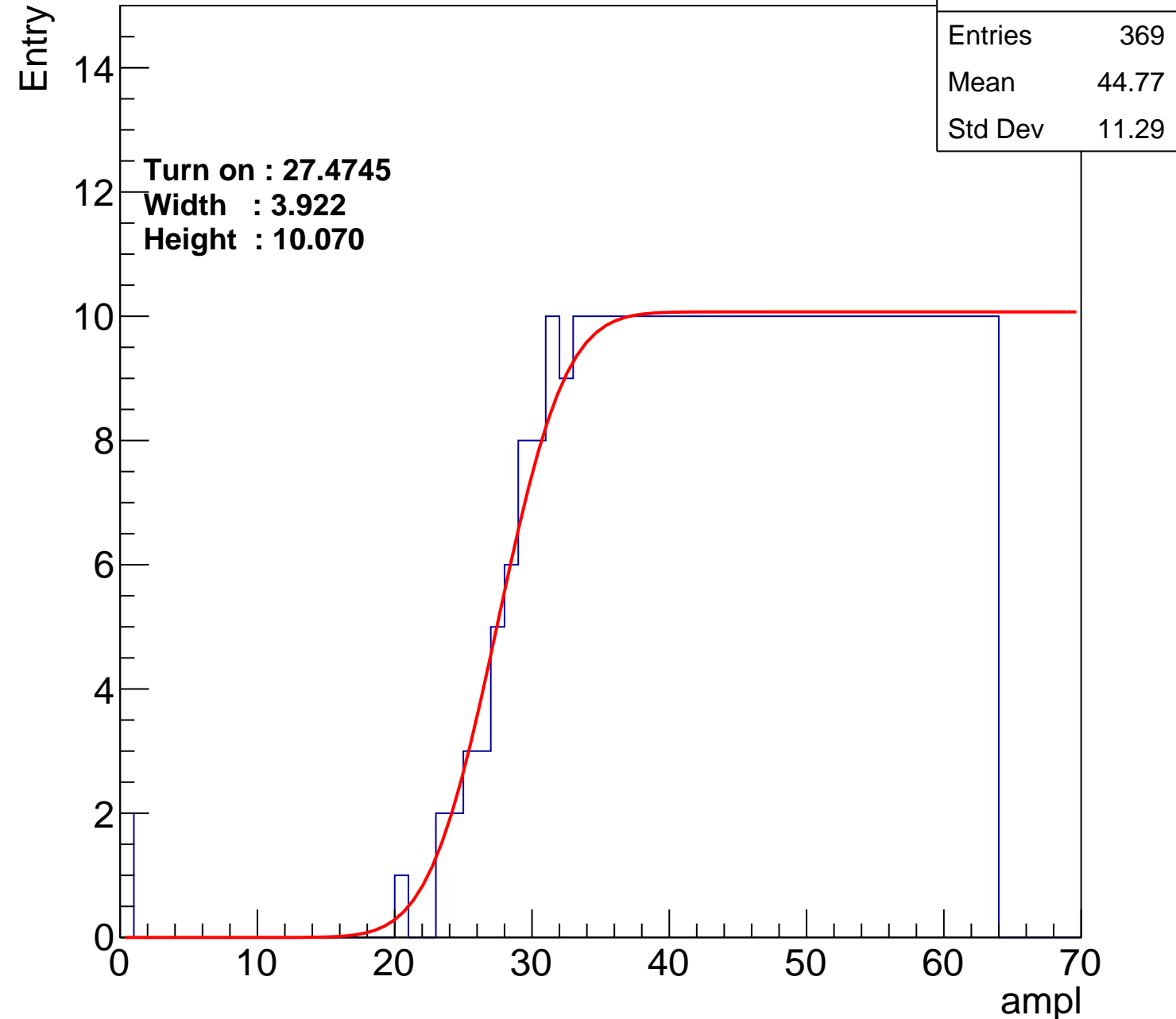
Width : 3.922

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch65

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	386
Mean	43.75
Std Dev	12.21

Turn on : 25.8558

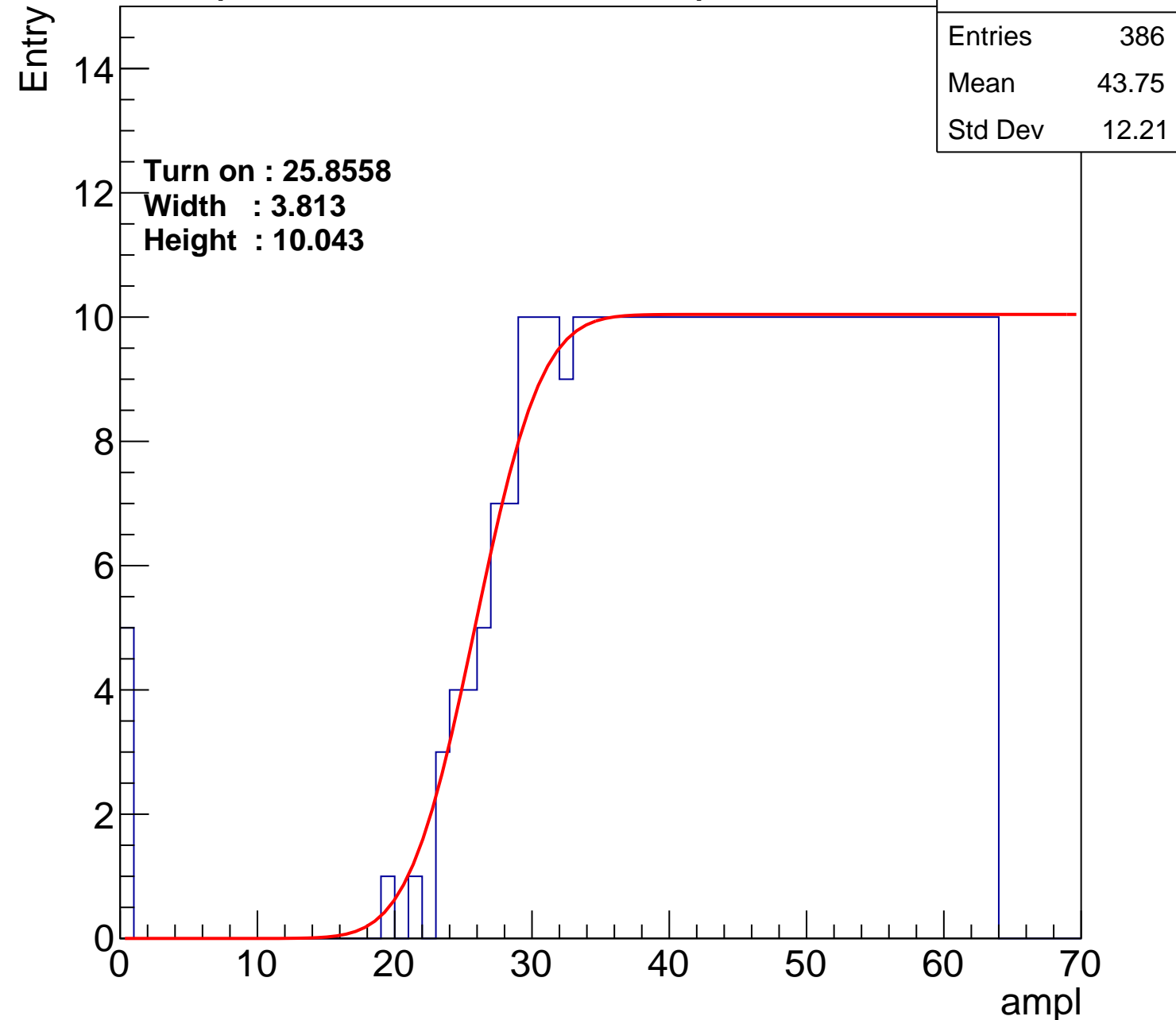
Width : 3.813

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch66

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	376
Mean	44.37
Std Dev	11.63

Turn on : 26.9905

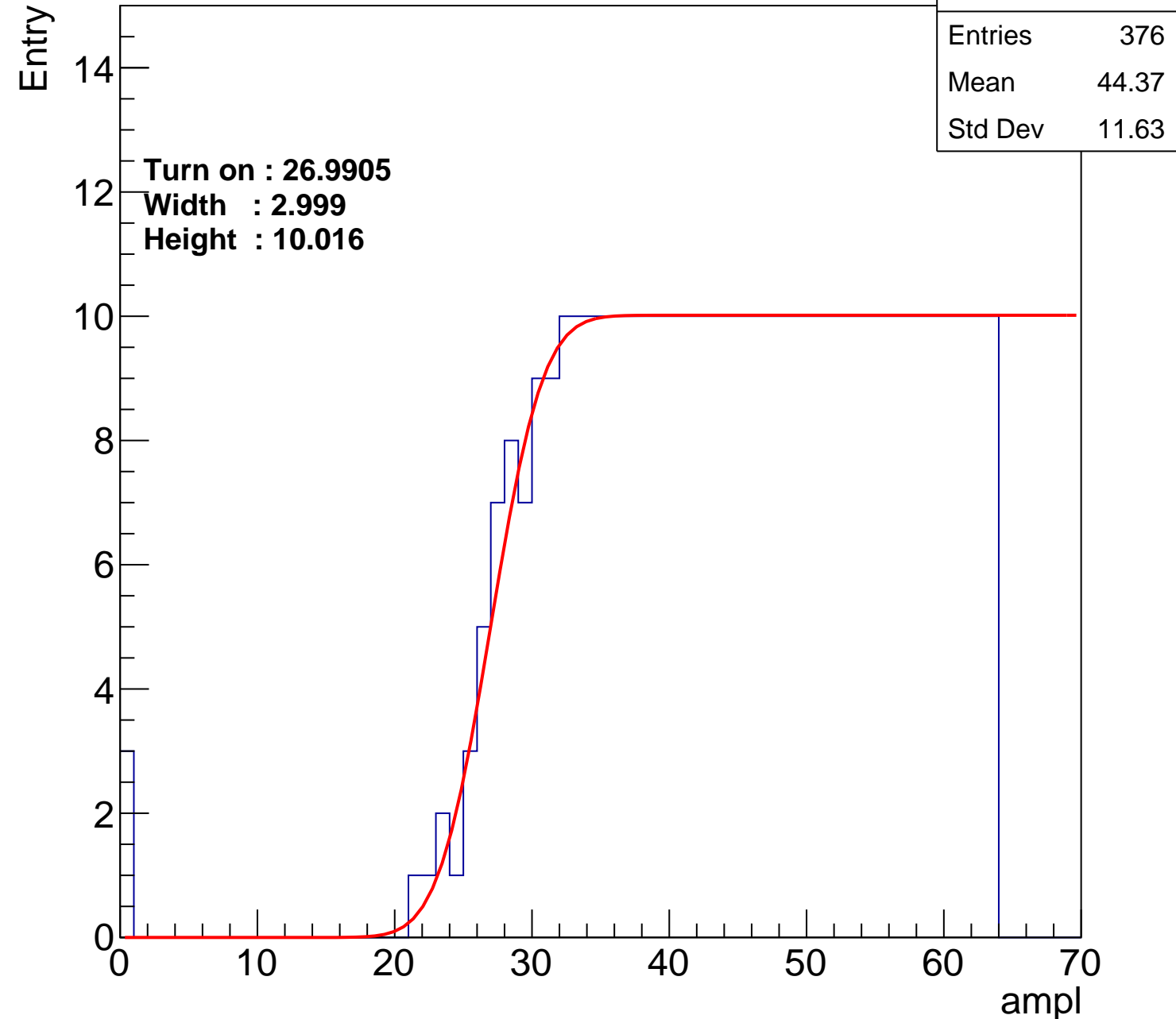
Width : 2.999

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch67

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	420
Mean	42.24
Std Dev	12.68

**Turn on : 22.7497**

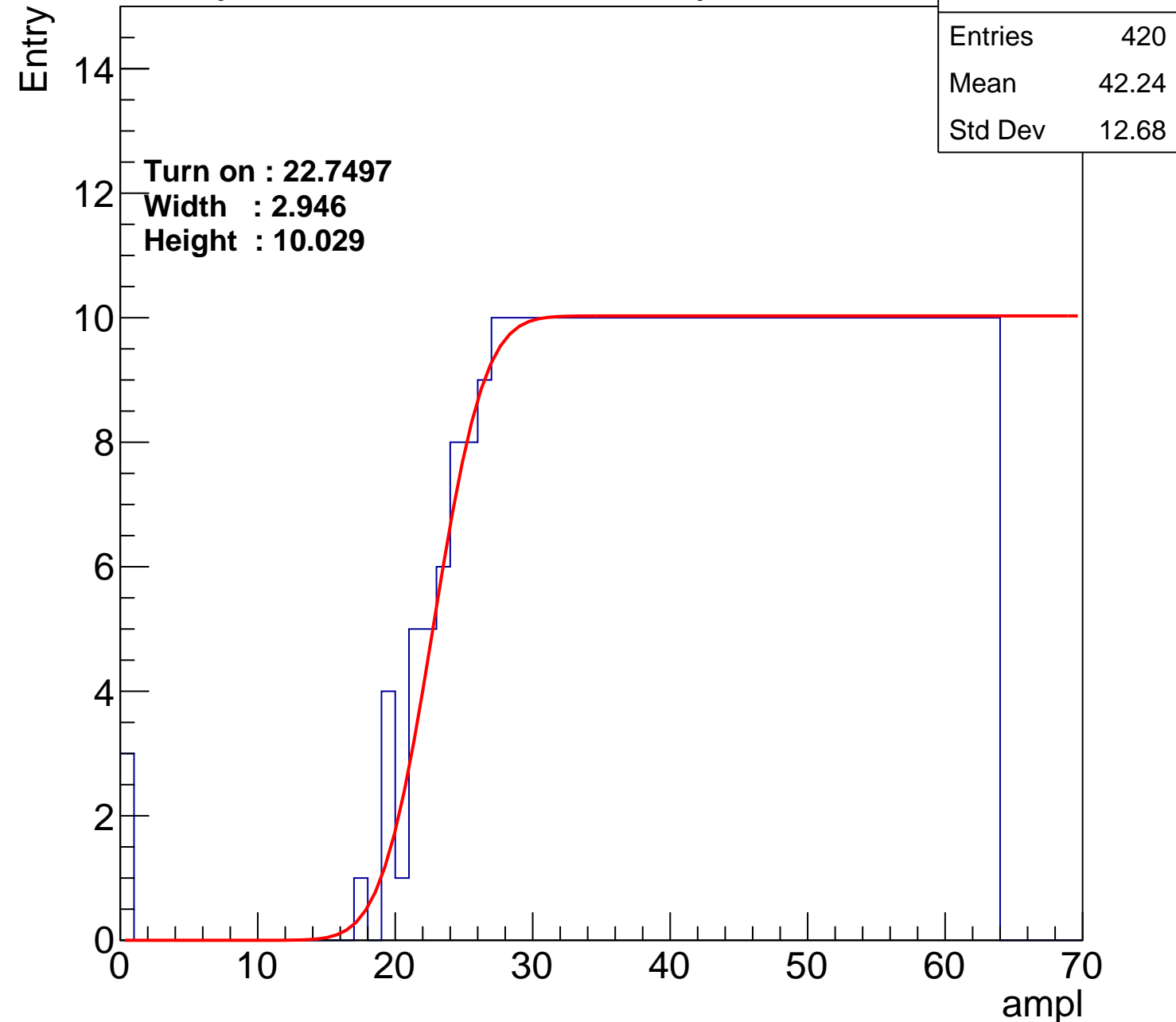
**Width : 2.946**

**Height : 10.029**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch68

calib\_packv5\_042523\_0143.root, FC#7, port C2

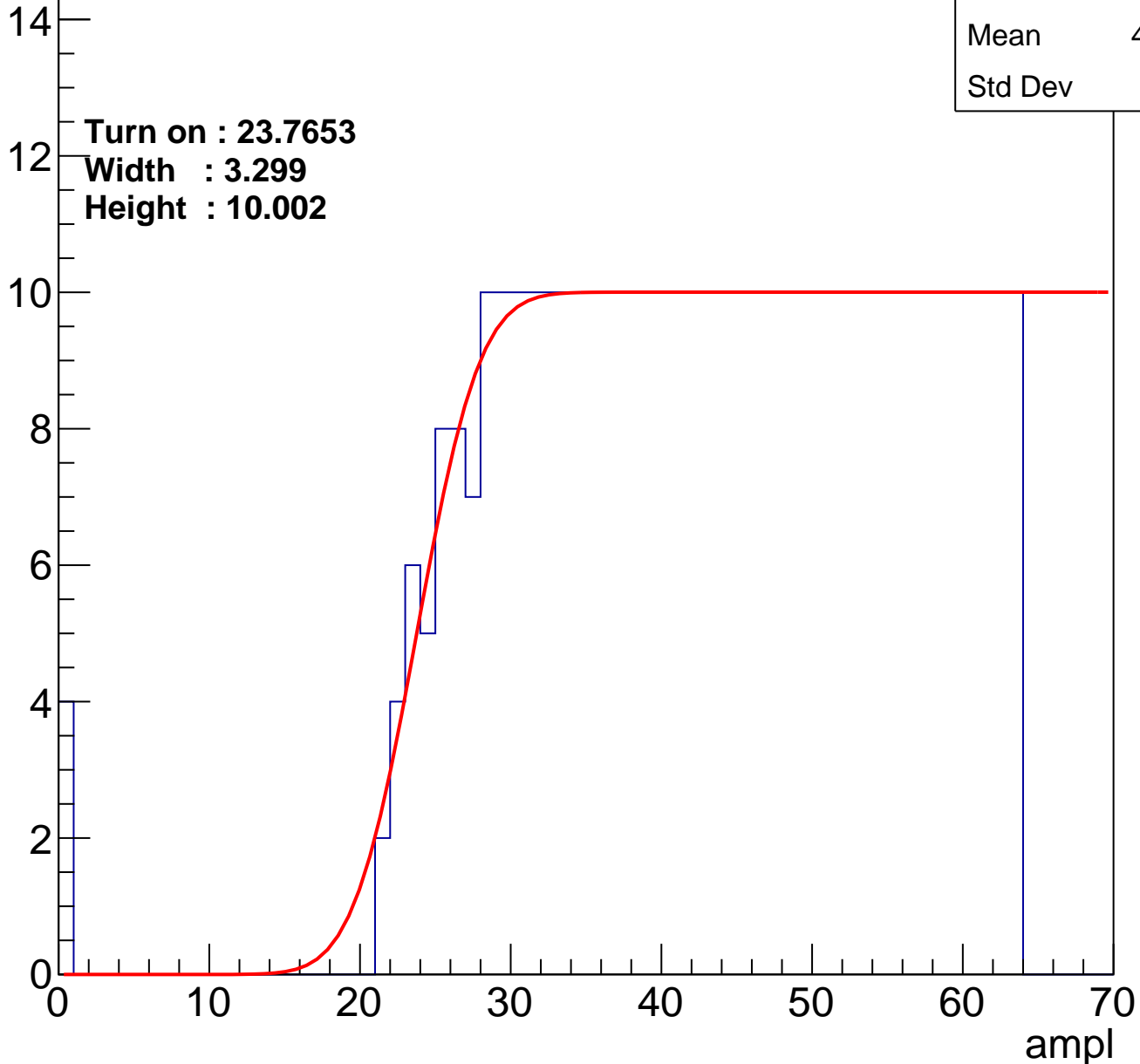
Entries	404
Mean	42.98
Std Dev	12.4

Turn on : 23.7653

Width : 3.299

Height : 10.002

Entry



# B1L103S, U14-ch69

calib\_packv5\_042523\_0143.root, FC#7, port C2

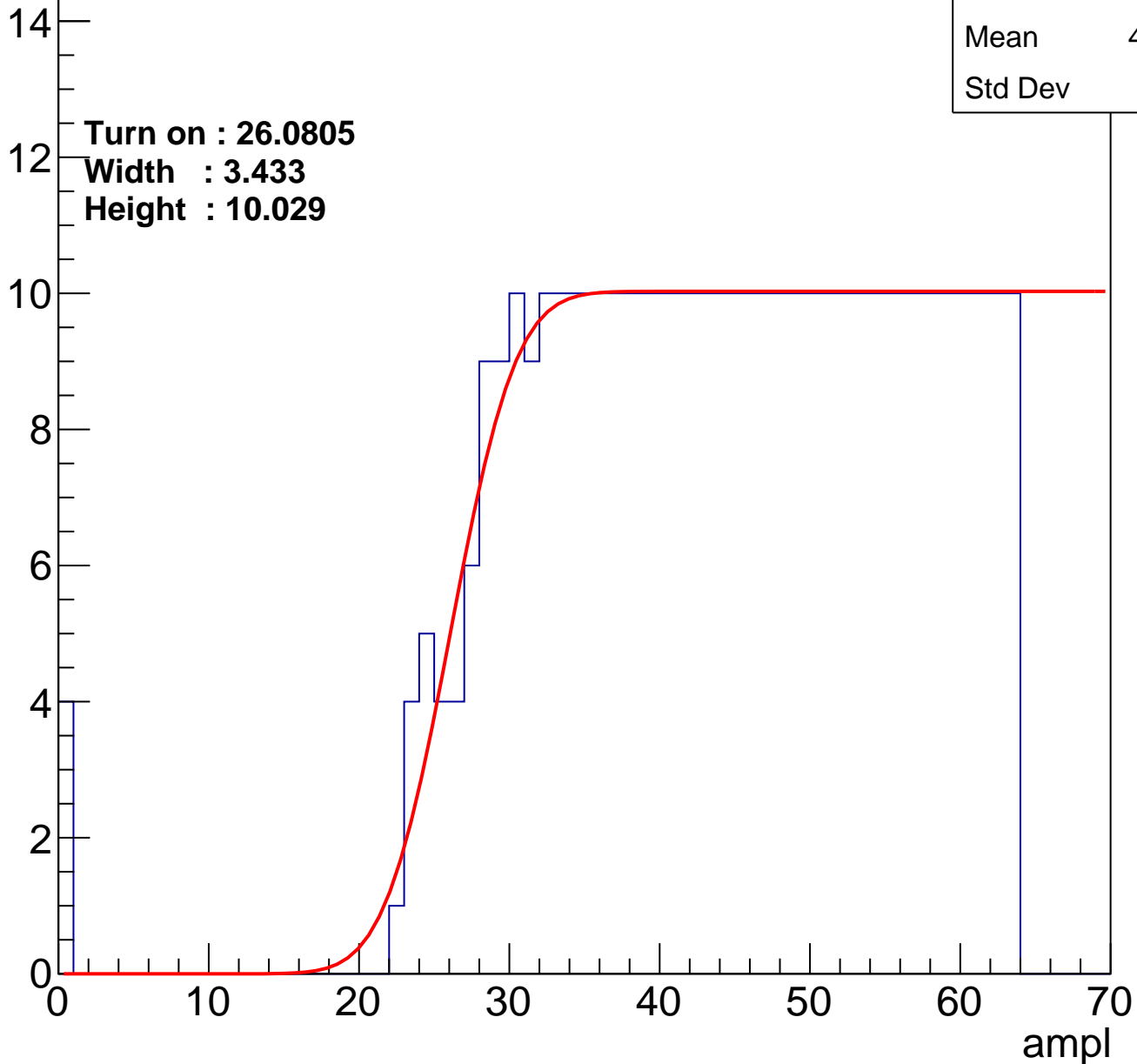
Entries	385
Mean	43.88
Std Dev	12

Turn on : 26.0805

Width : 3.433

Height : 10.029

Entry



# B1L103S, U14-ch70

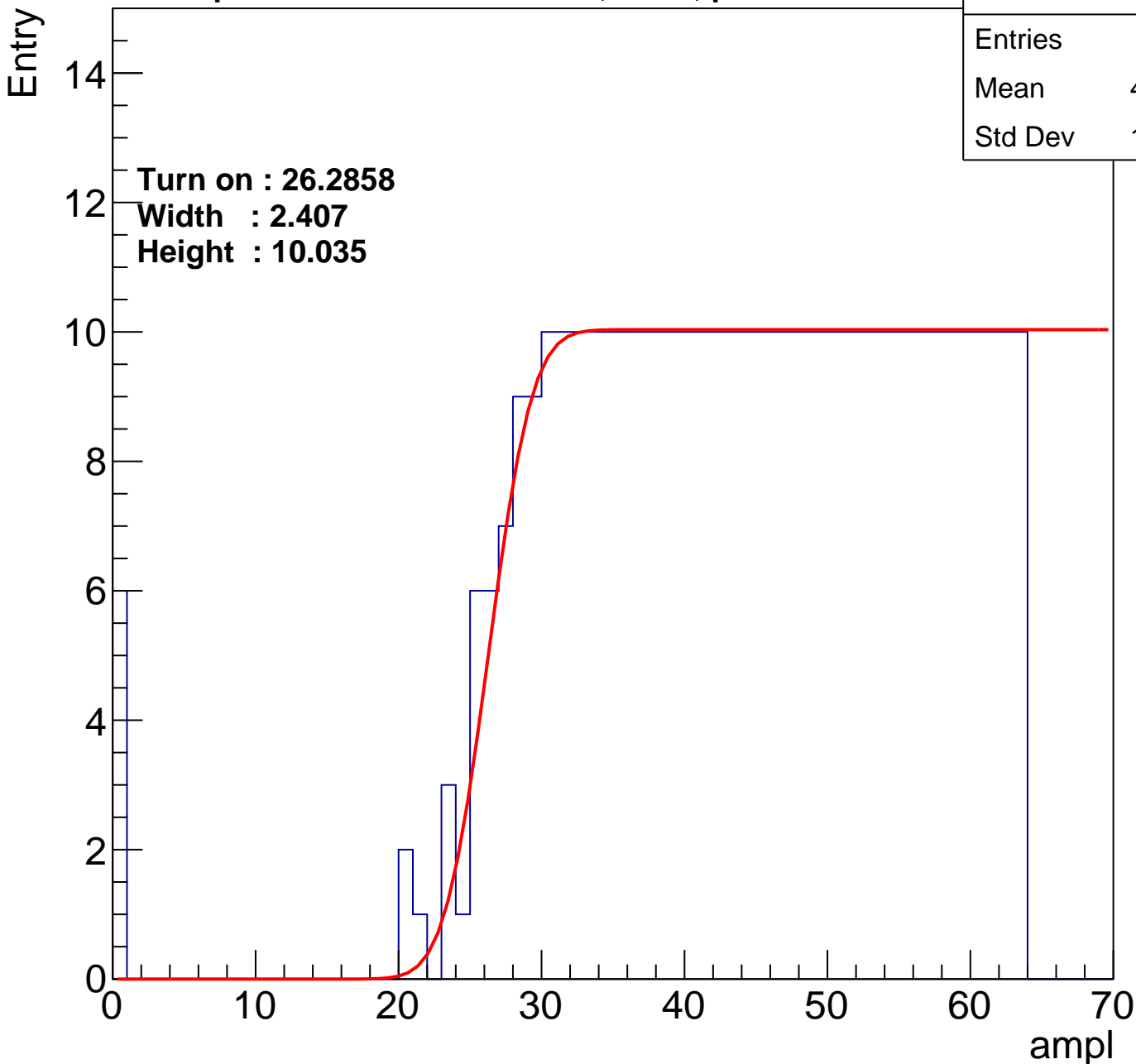
**calib\_packv5\_042523\_0143.root, FC#7, port C2**

Entries	390
Mean	43.52
Std Dev	12.43

**Turn on : 26.2858**

**Width : 2.407**

**Height : 10.035**





# B1L103S, U14-ch71

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	366
Mean	44.93
Std Dev	11.19

Turn on : 27.9875

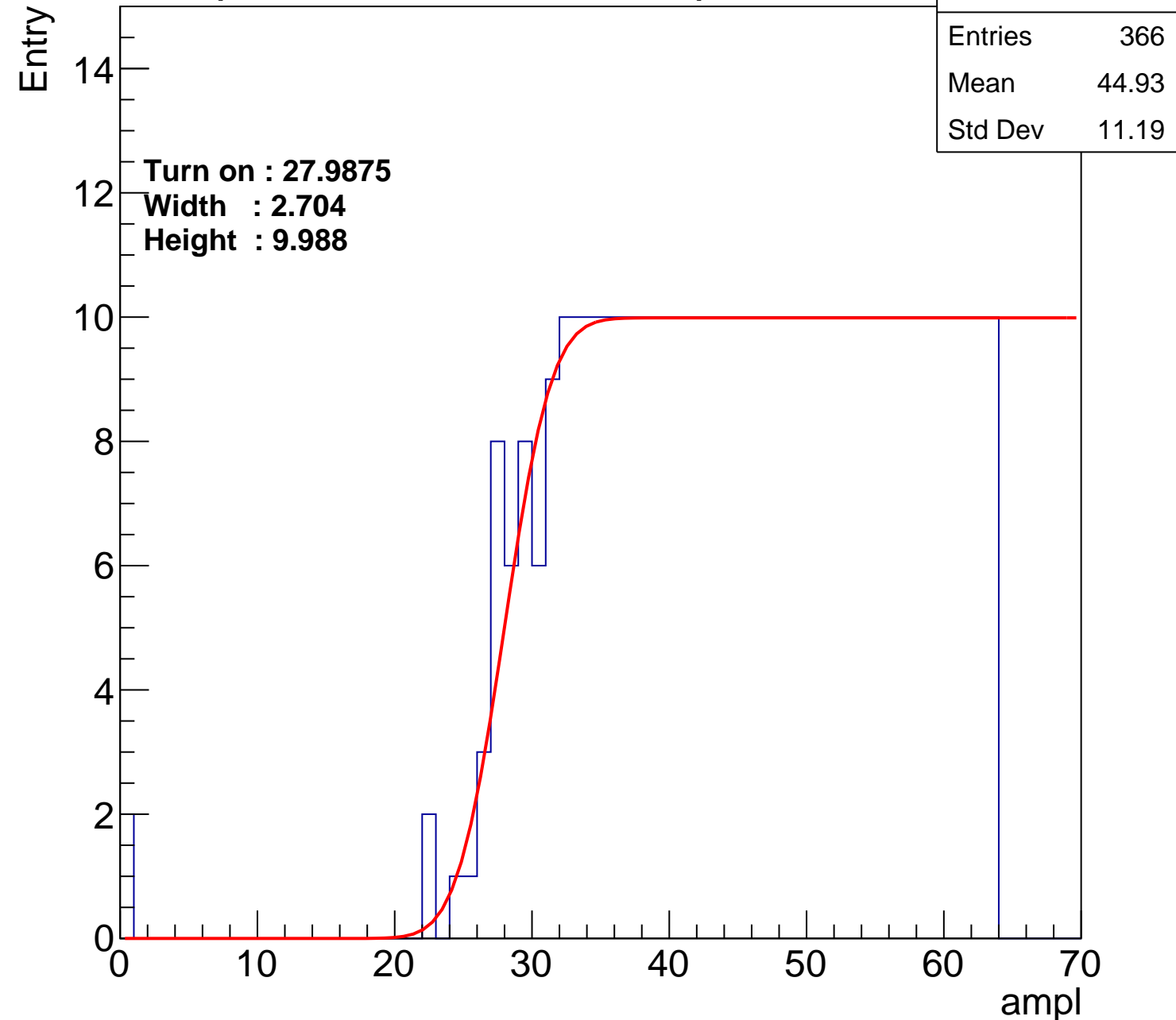
Width : 2.704

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch72

calib\_packv5\_042523\_0143.root, FC#7, port C2

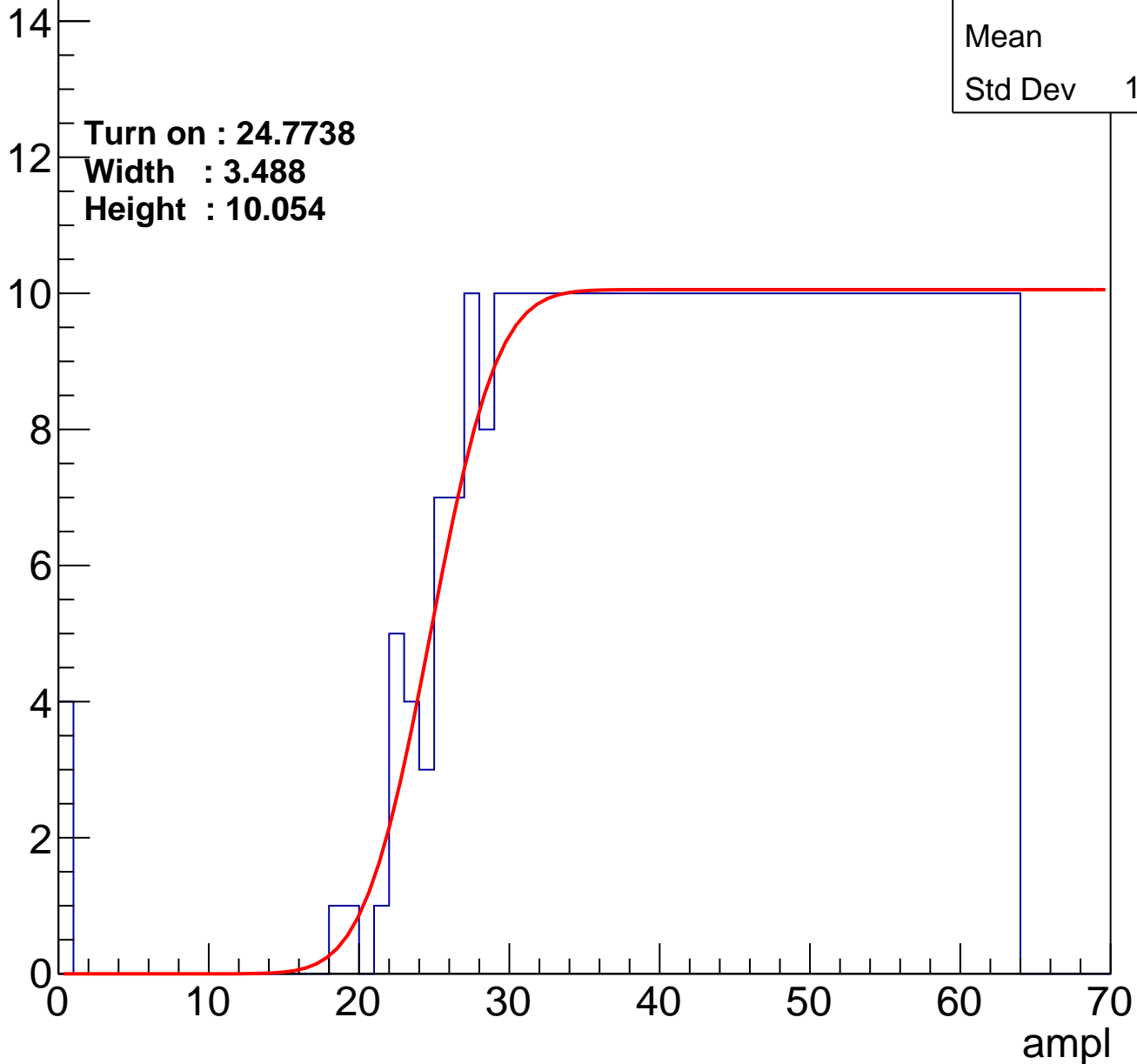
Entries	401
Mean	43.1
Std Dev	12.38

Turn on : 24.7738

Width : 3.488

Height : 10.054

Entry



# B1L103S, U14-ch73

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	387
Mean	43.89
Std Dev	11.75

Turn on : 25.9309

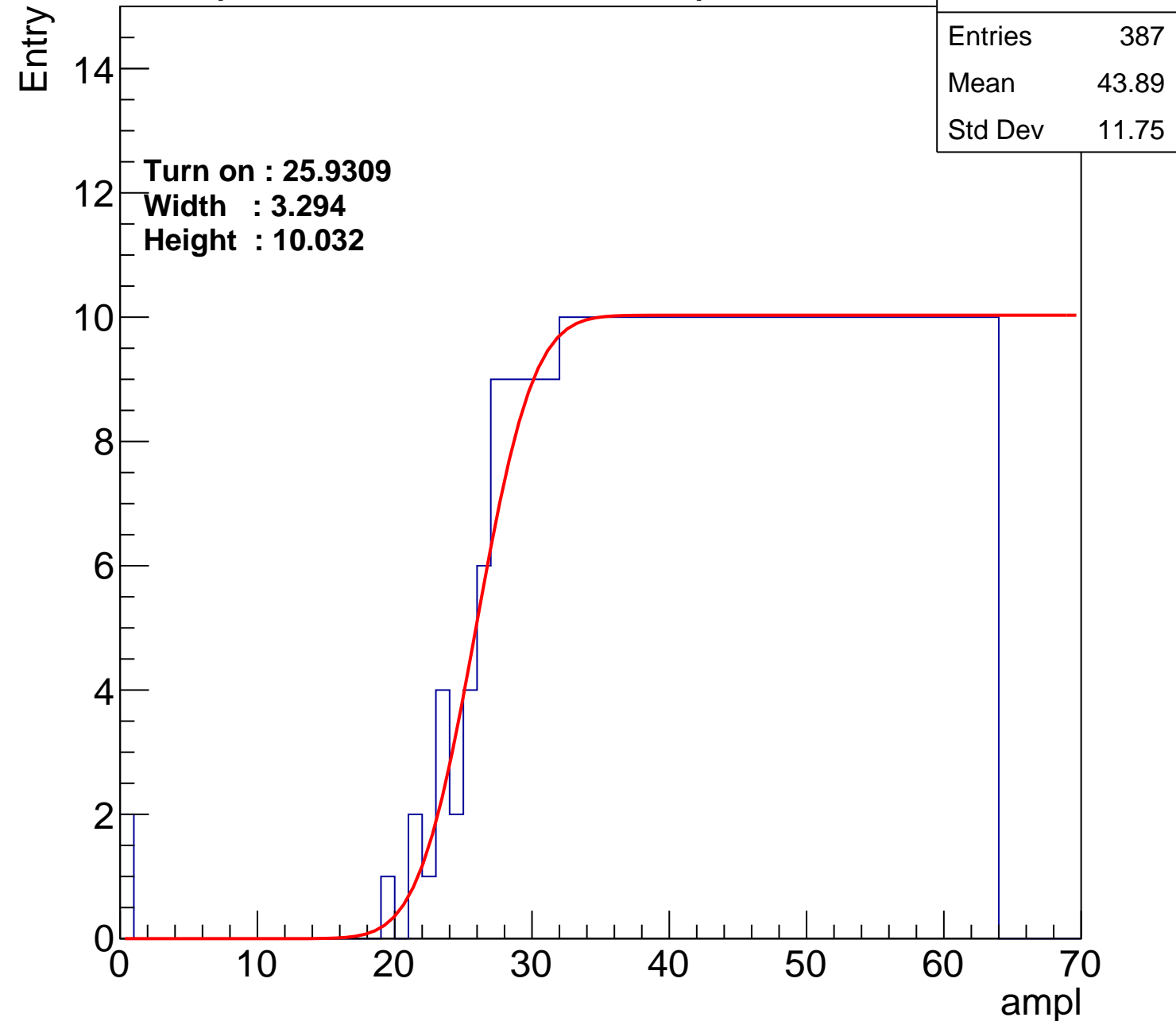
Width : 3.294

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch74

calib\_packv5\_042523\_0143.root, FC#7, port C2

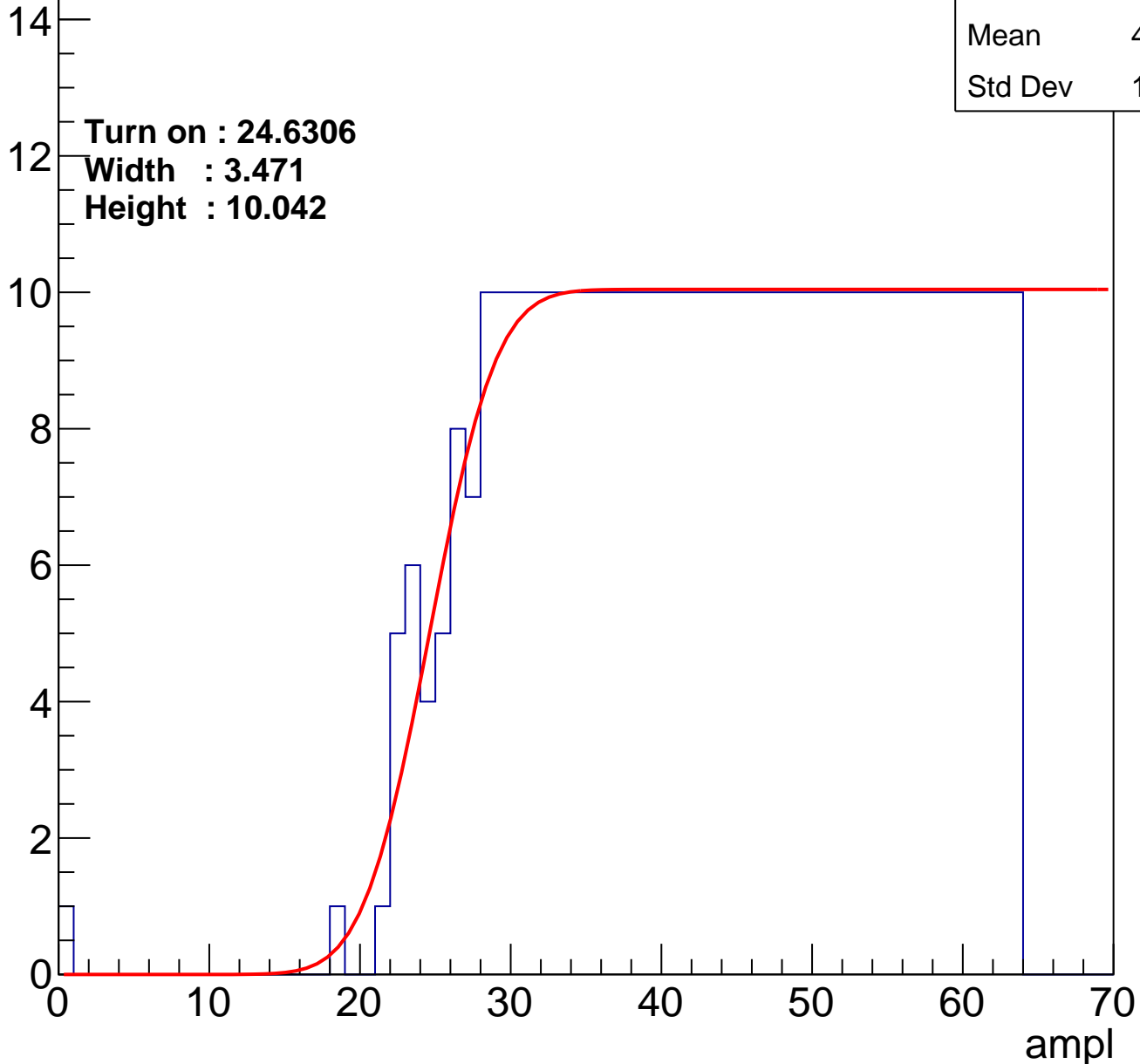
Entries	398
Mean	43.43
Std Dev	11.84

Turn on : 24.6306

Width : 3.471

Height : 10.042

Entry



# B1L103S, U14-ch75

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	366
Mean	44.9
Std Dev	11.24

Turn on : 27.5323

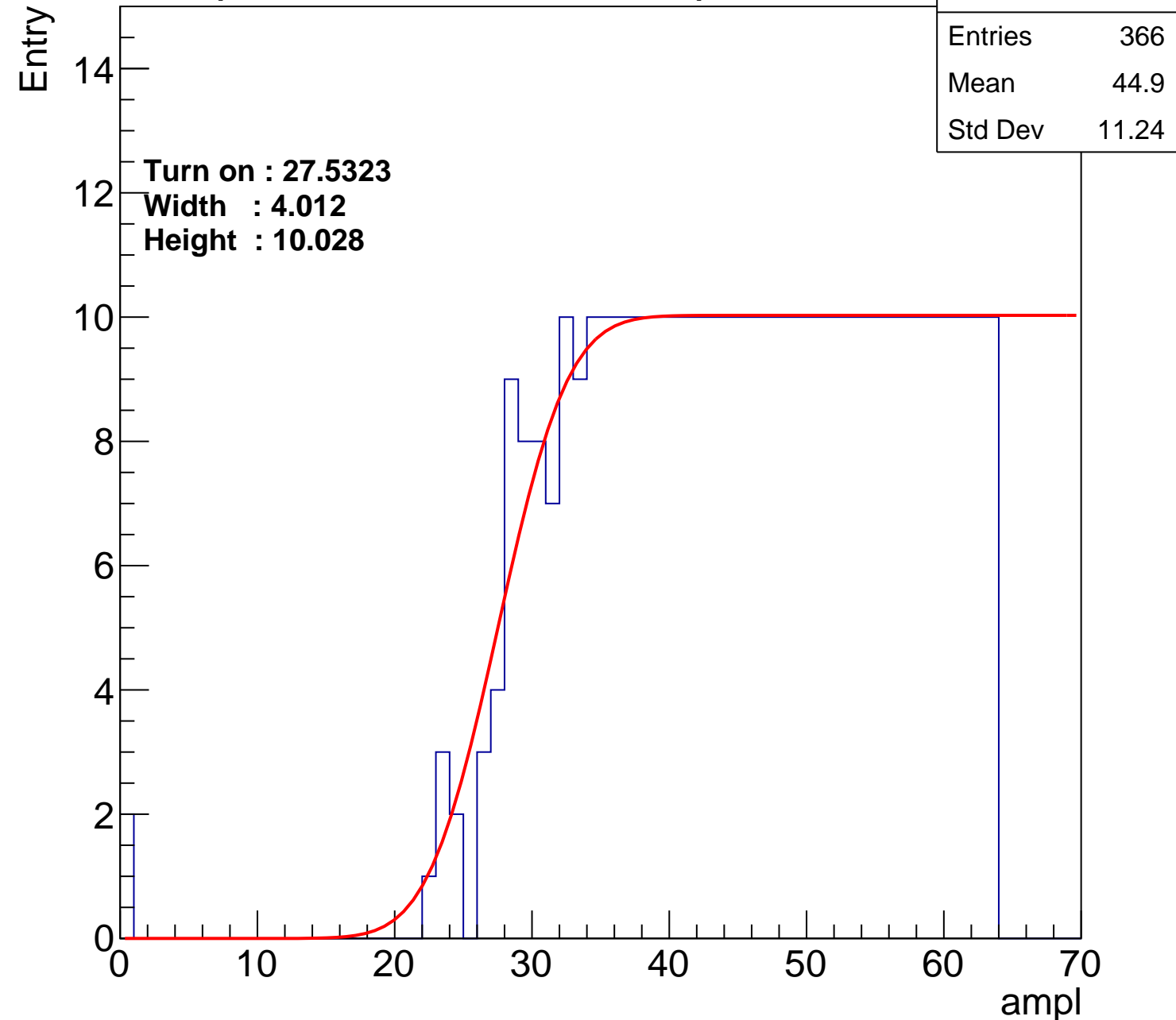
Width : 4.012

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch76

calib\_packv5\_042523\_0143.root, FC#7, port C2

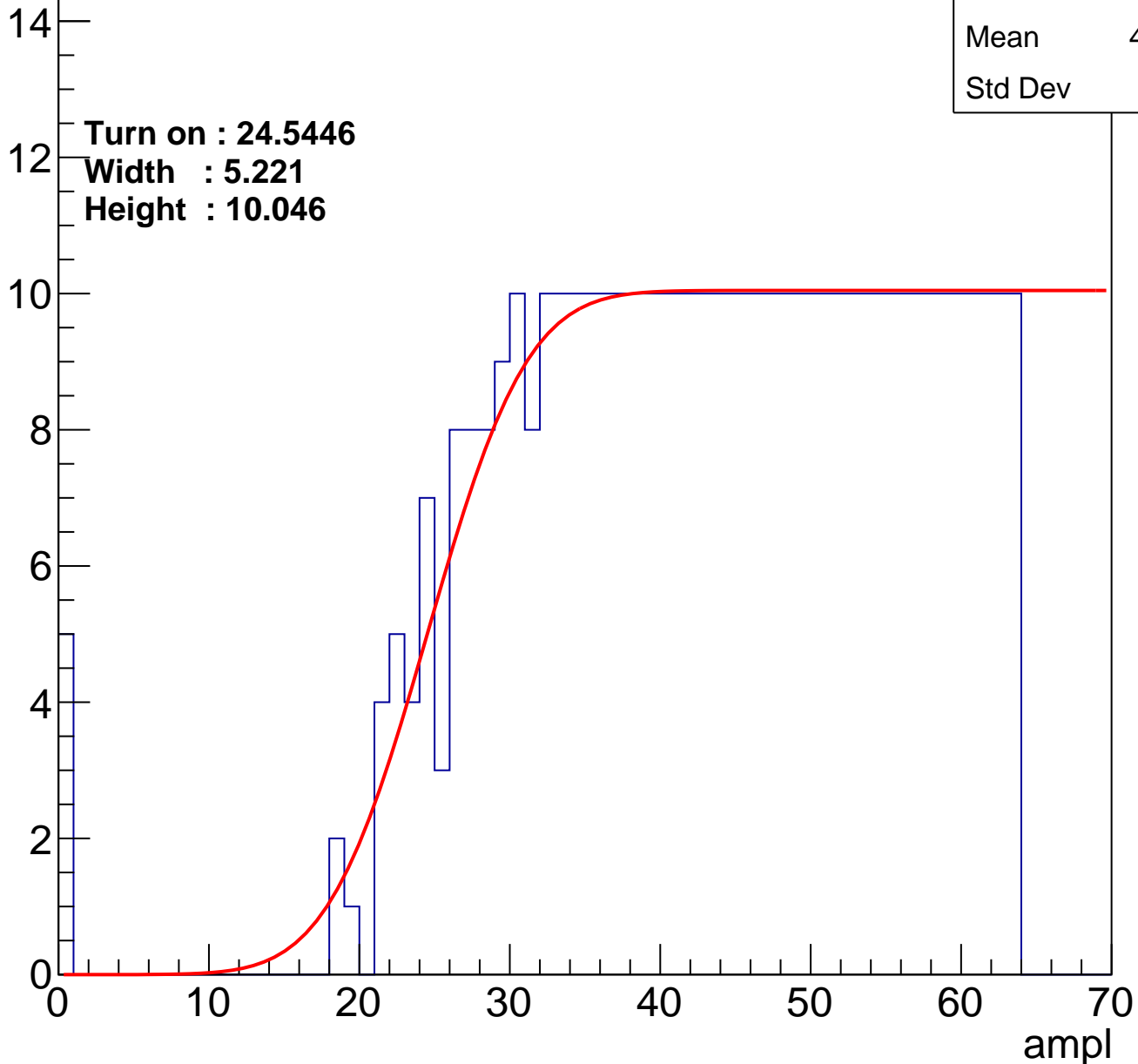
Entries	402
Mean	42.89
Std Dev	12.7

Turn on : 24.5446

Width : 5.221

Height : 10.046

Entry



# B1L103S, U14-ch77

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	371
Mean	44.66
Std Dev	11.36

Turn on : 27.4721

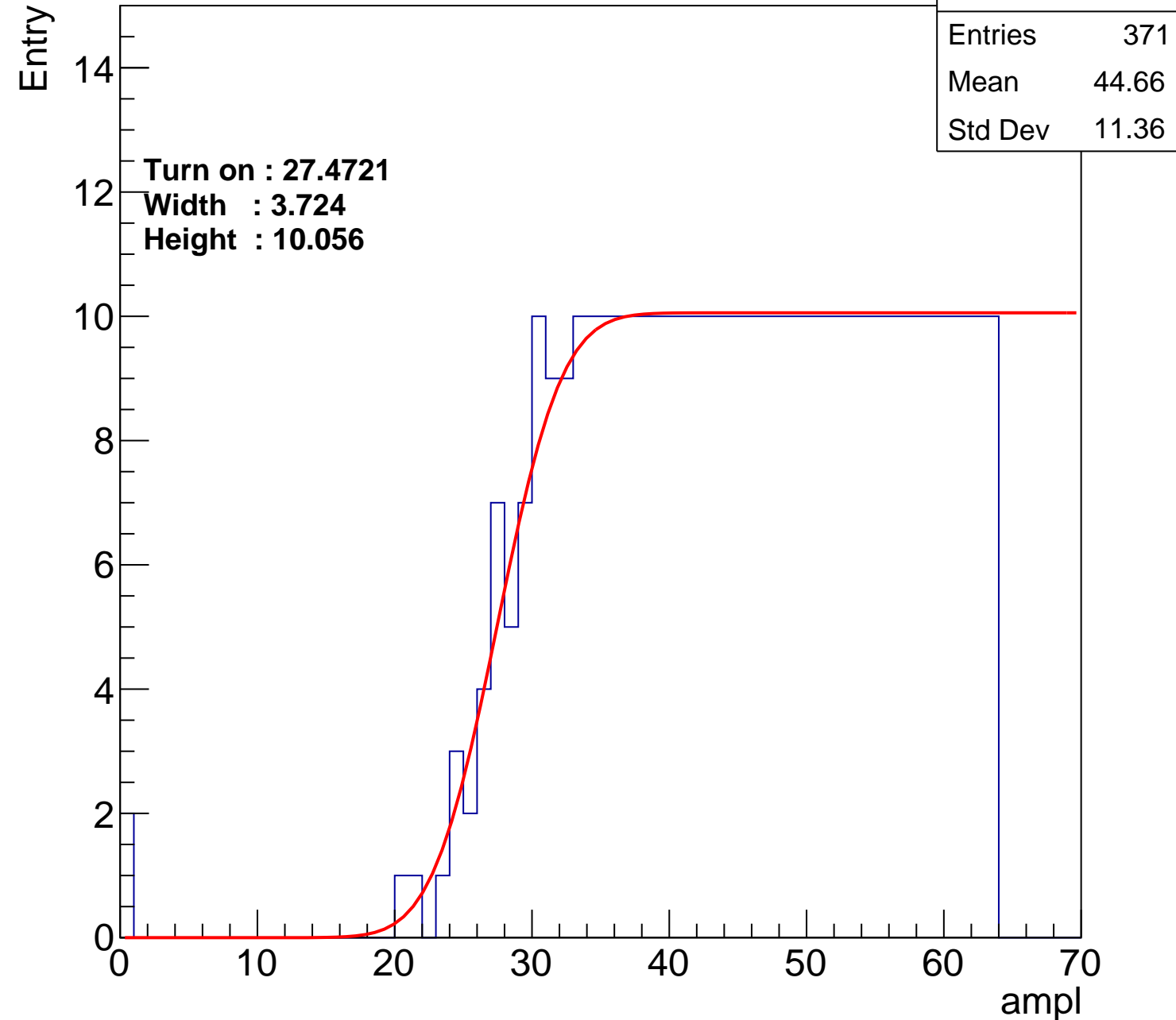
Width : 3.724

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch78

calib\_packv5\_042523\_0143.root, FC#7, port C2

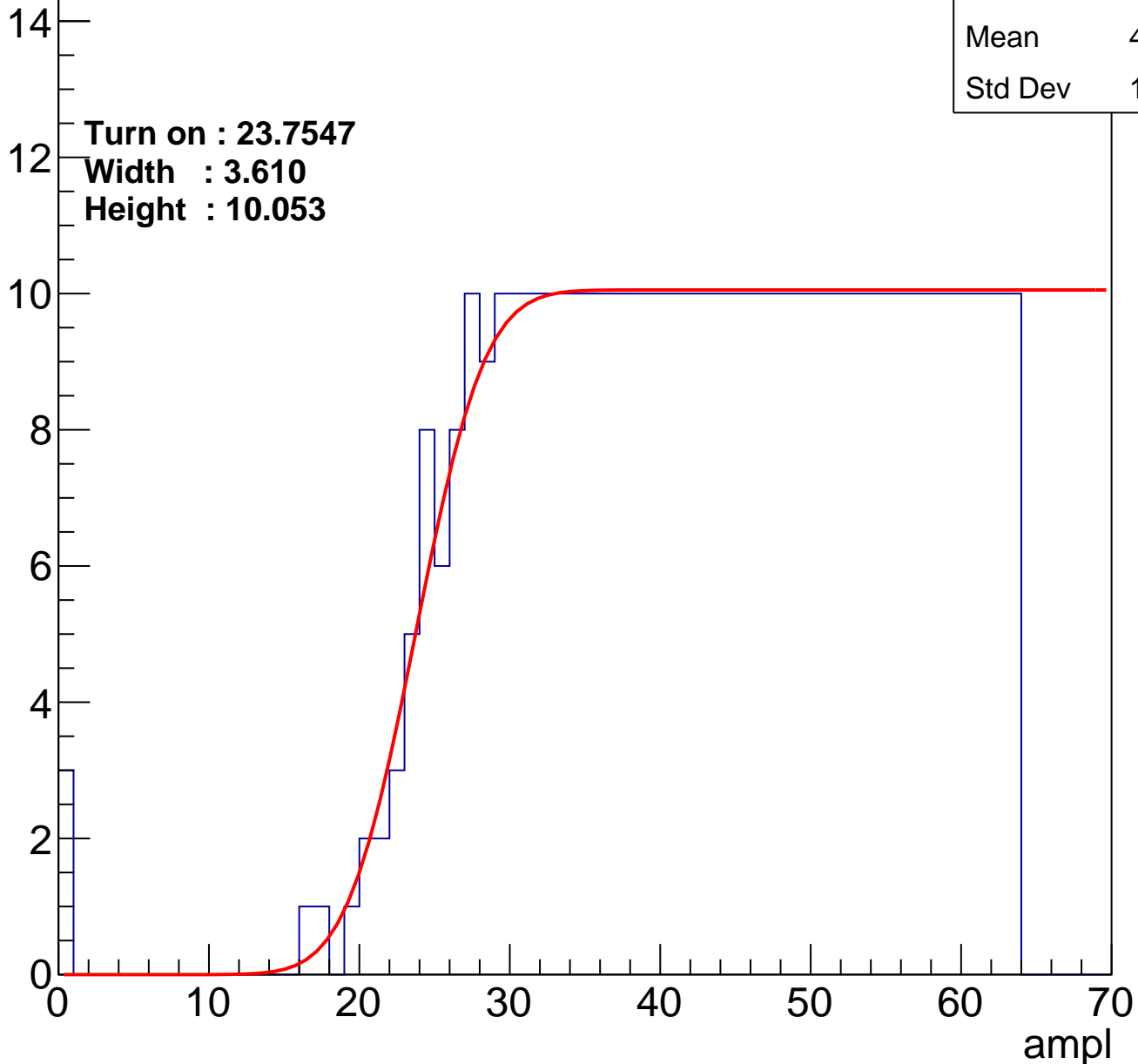
Entries	409
Mean	42.76
Std Dev	12.45

**Turn on : 23.7547**

**Width : 3.610**

**Height : 10.053**

Entry





# B1L103S, U14-ch79

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	373
Mean	44.37
Std Dev	11.94

Turn on : 27.8835

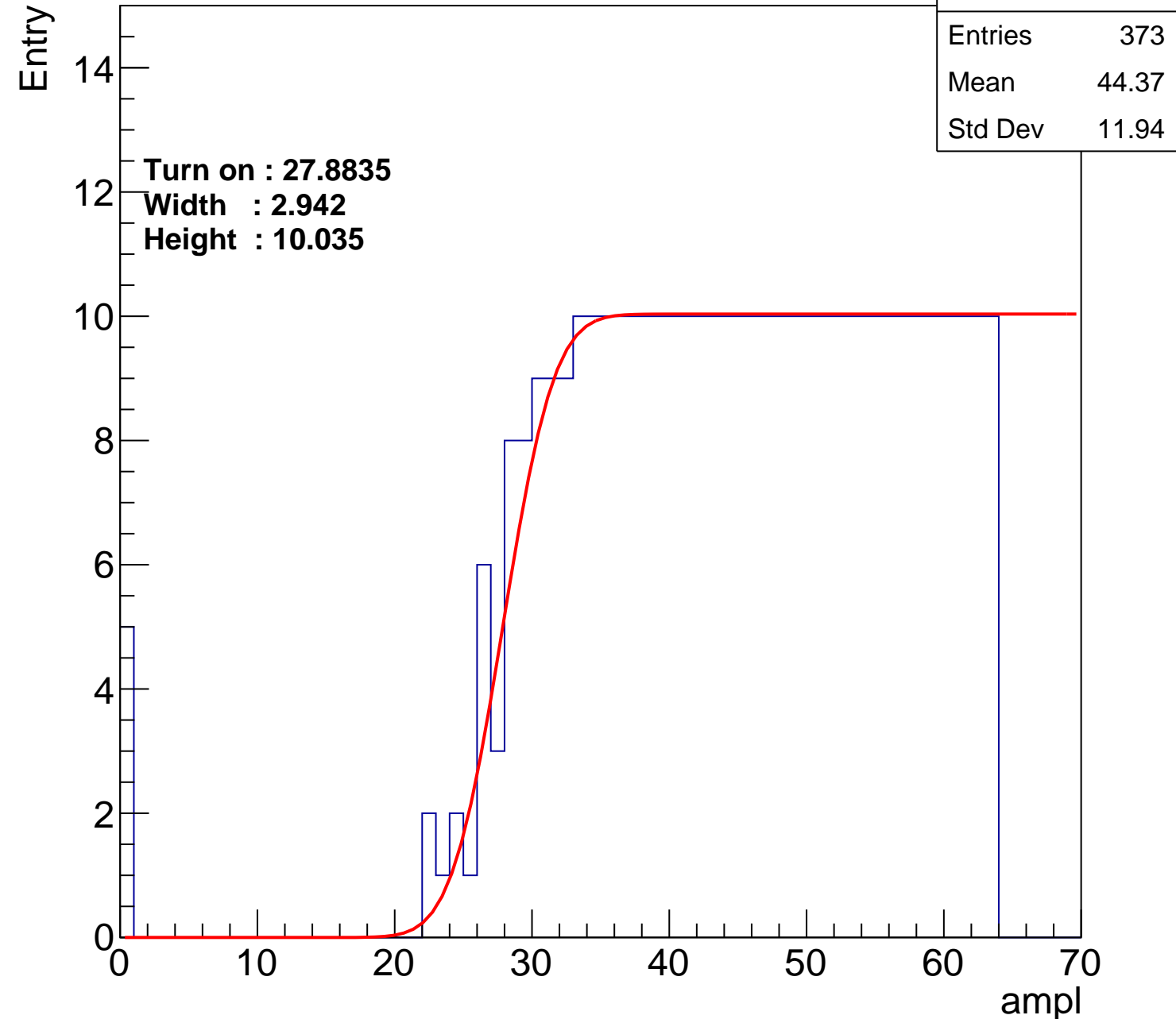
Width : 2.942

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch80

calib\_packv5\_042523\_0143.root, FC#7, port C2

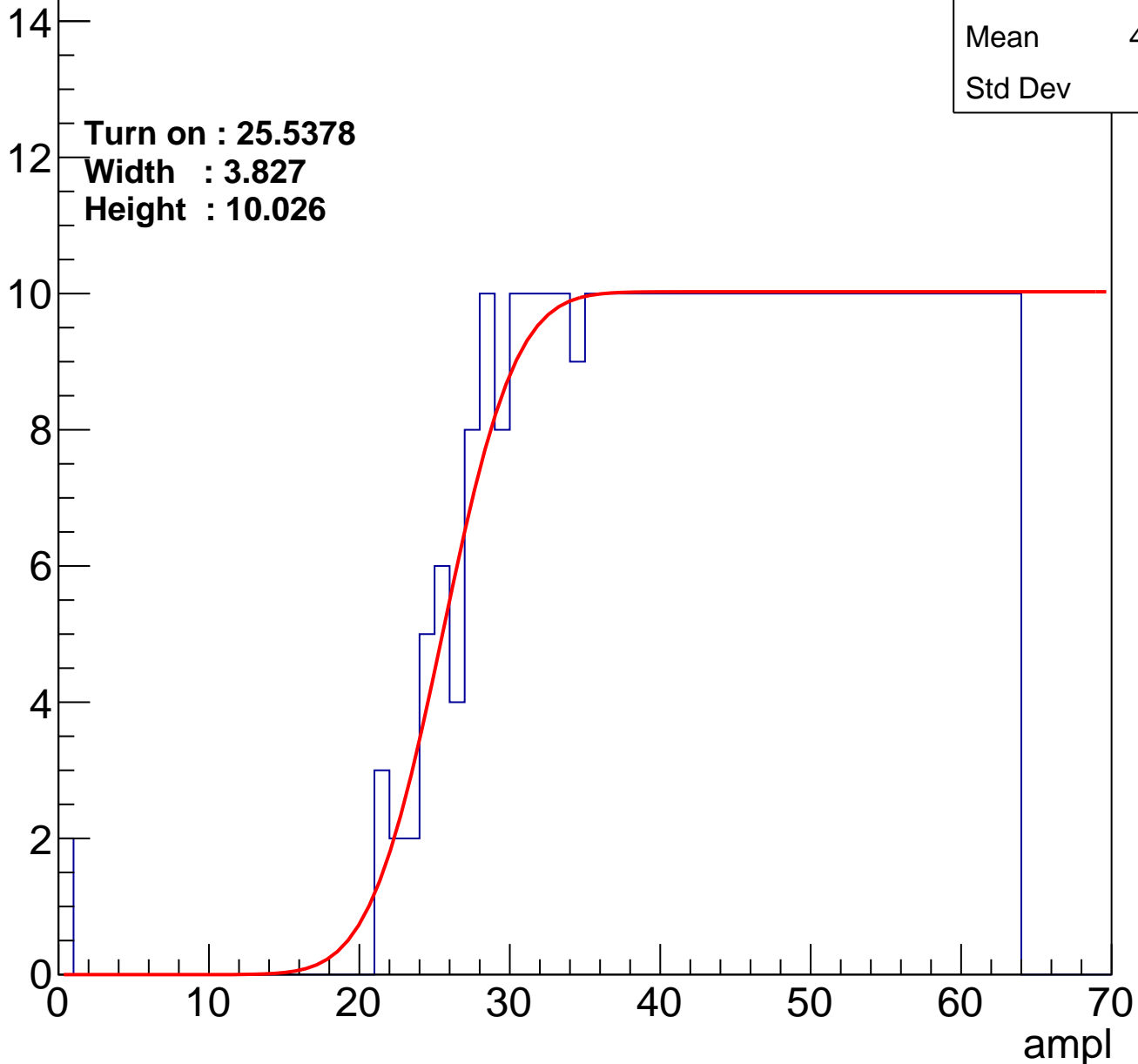
Entries	389
Mean	43.78
Std Dev	11.8

Turn on : 25.5378

Width : 3.827

Height : 10.026

Entry



# B1L103S, U14-ch81

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	377
Mean	44.22
Std Dev	11.88

Turn on : 26.7356

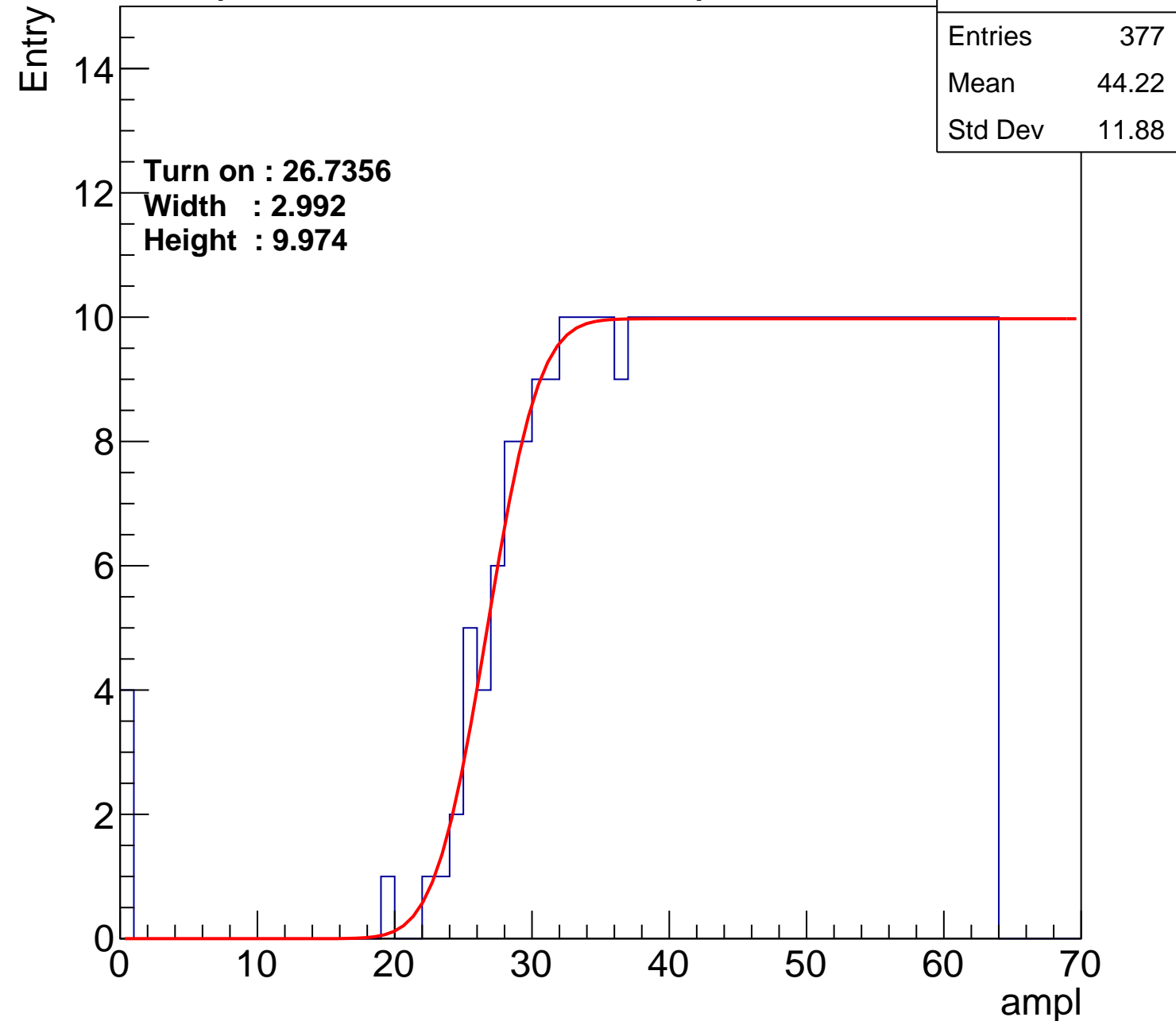
Width : 2.992

Height : 9.974

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch82

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	378
Mean	44.24
Std Dev	11.8

Turn on : 26.4732

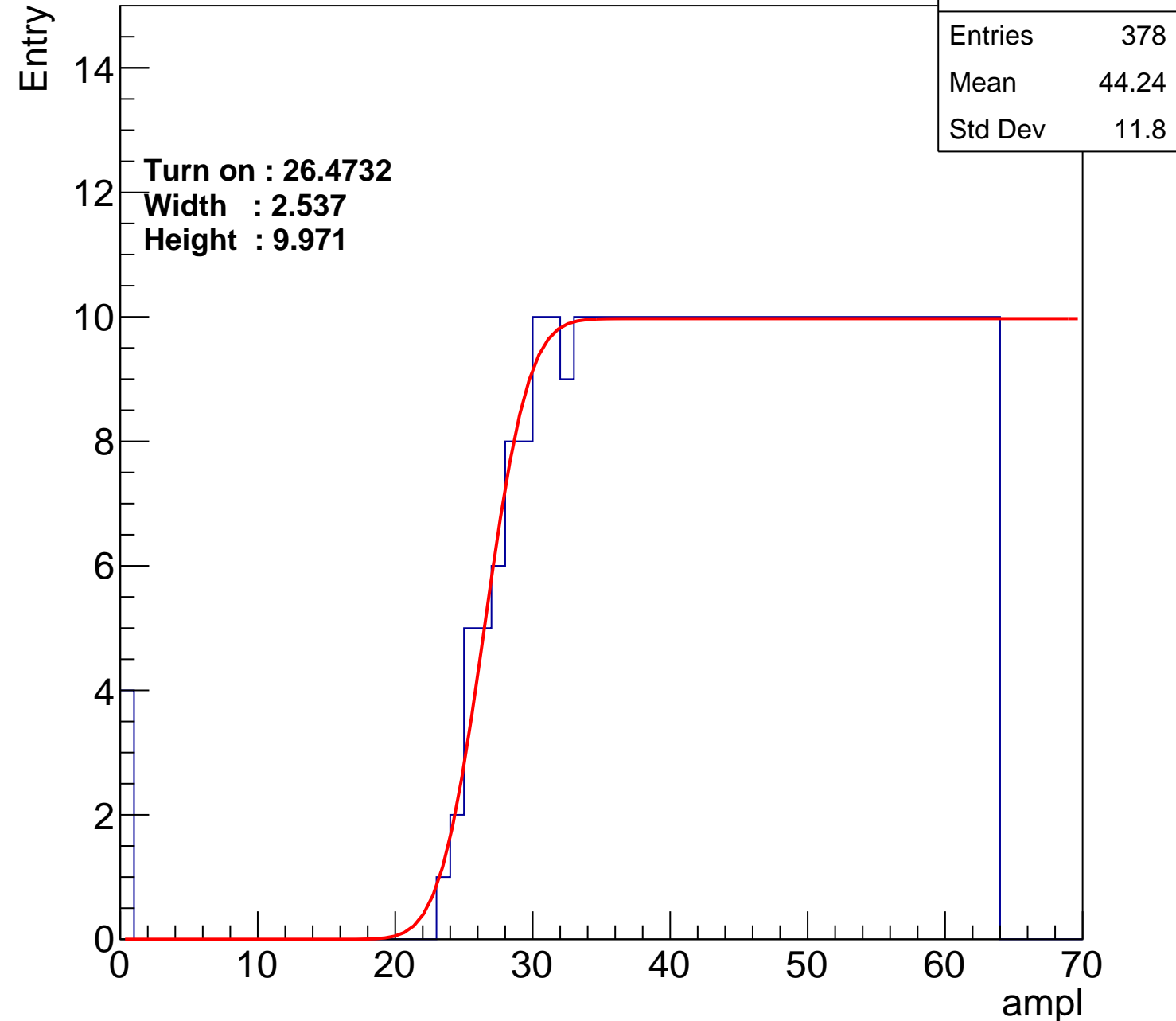
Width : 2.537

Height : 9.971

Entry

14  
12  
10  
8  
6  
4  
2  
0

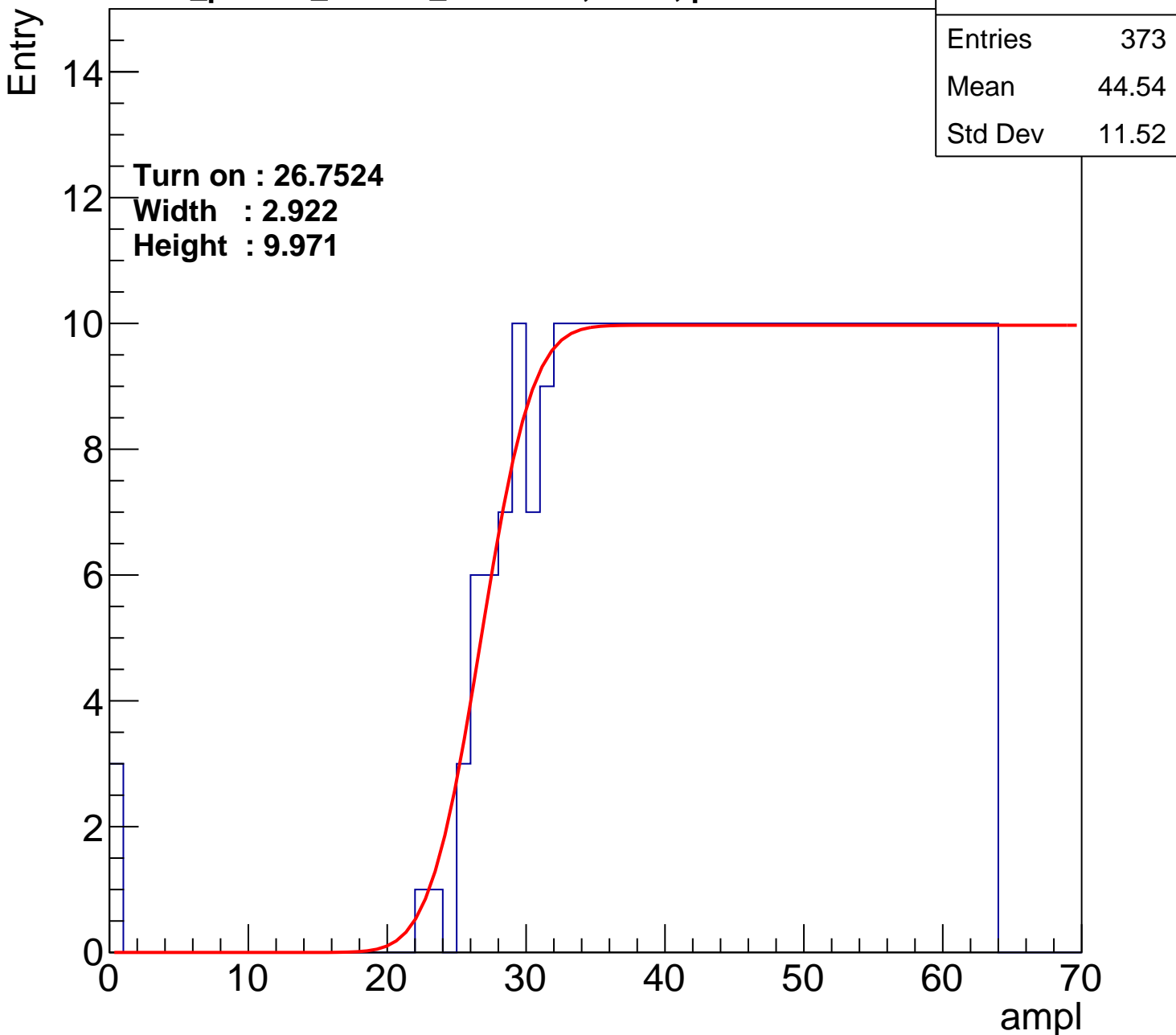
ampl



**calib\_packv5\_042523\_0143.root, FC#7, port C2**

**calib\_packv5\_042523\_0143.root, FC#7, port C2**

Turn on : 26.7524  
Width : 2.922  
Height : 9.971



# B1L103S, U14-ch84

calib\_packv5\_042523\_0143.root, FC#7, port C2

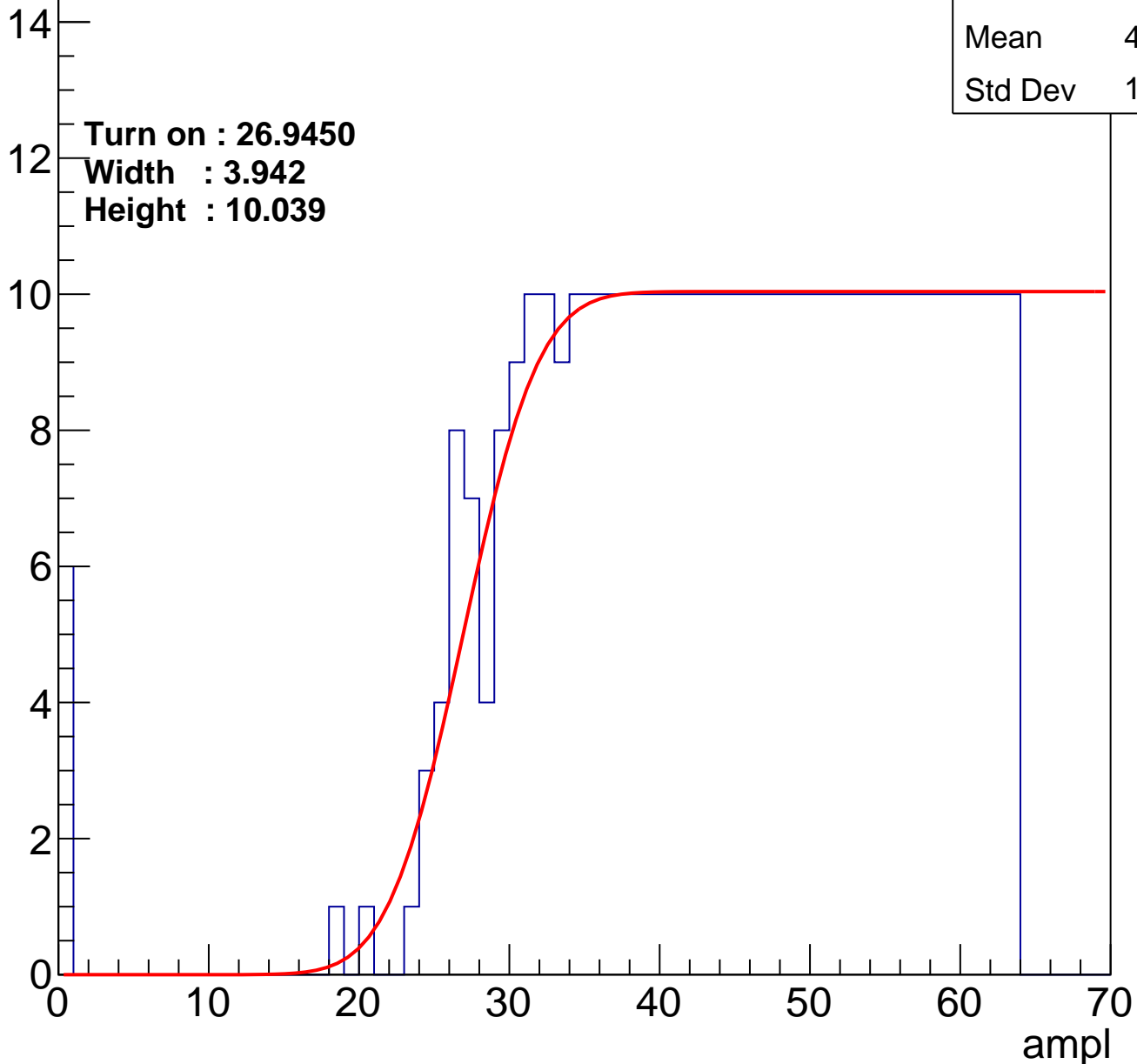
Entries	381
Mean	43.89
Std Dev	12.33

Turn on : 26.9450

Width : 3.942

Height : 10.039

Entry



# B1L103S, U14-ch85

calib\_packv5\_042523\_0143.root, FC#7, port C2

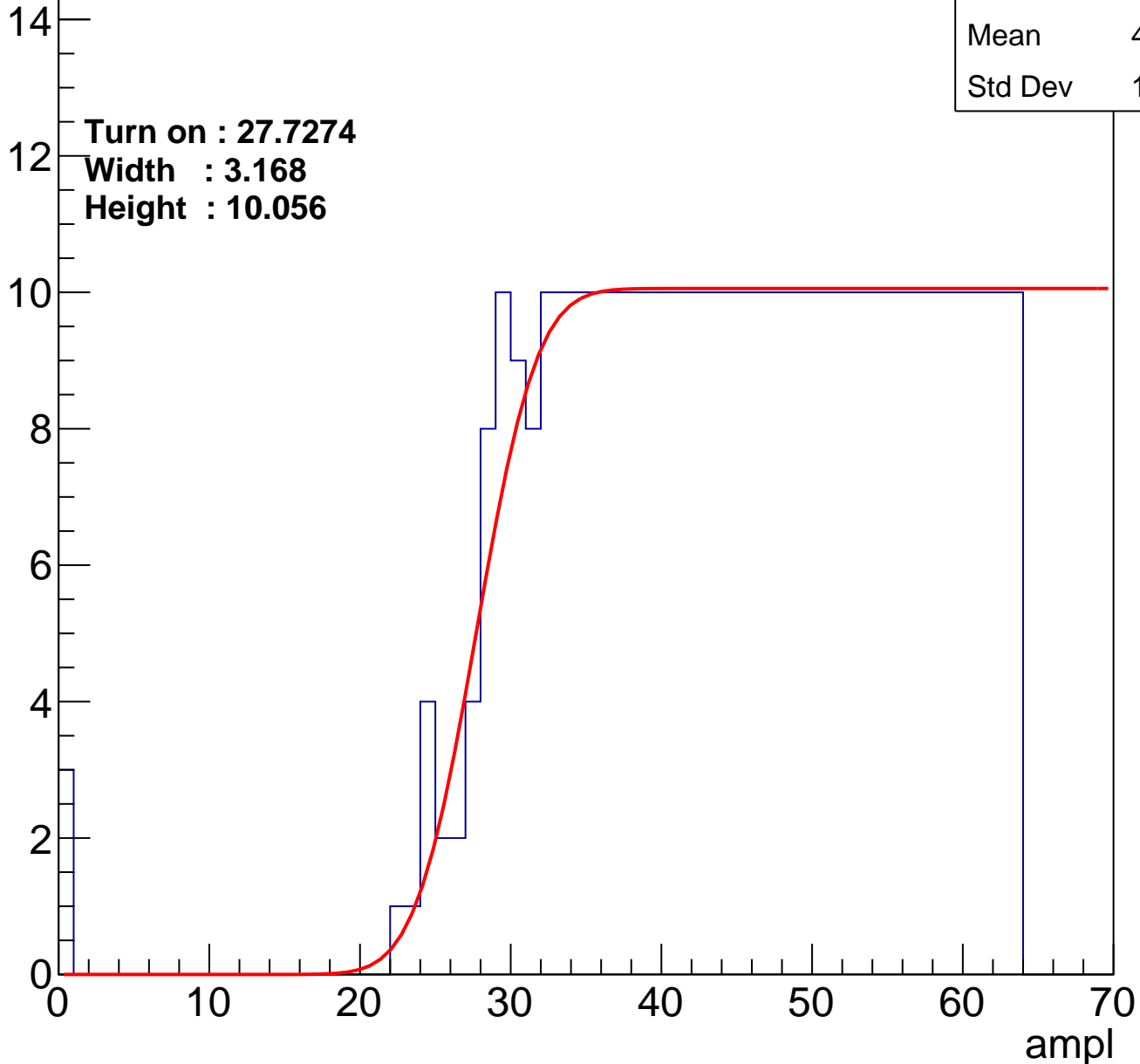
Entries	372
Mean	44.58
Std Dev	11.52

Turn on : 27.7274

Width : 3.168

Height : 10.056

Entry



# B1L103S, U14-ch86

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	397
Mean	43.43
Std Dev	11.95

Turn on : 24.5414

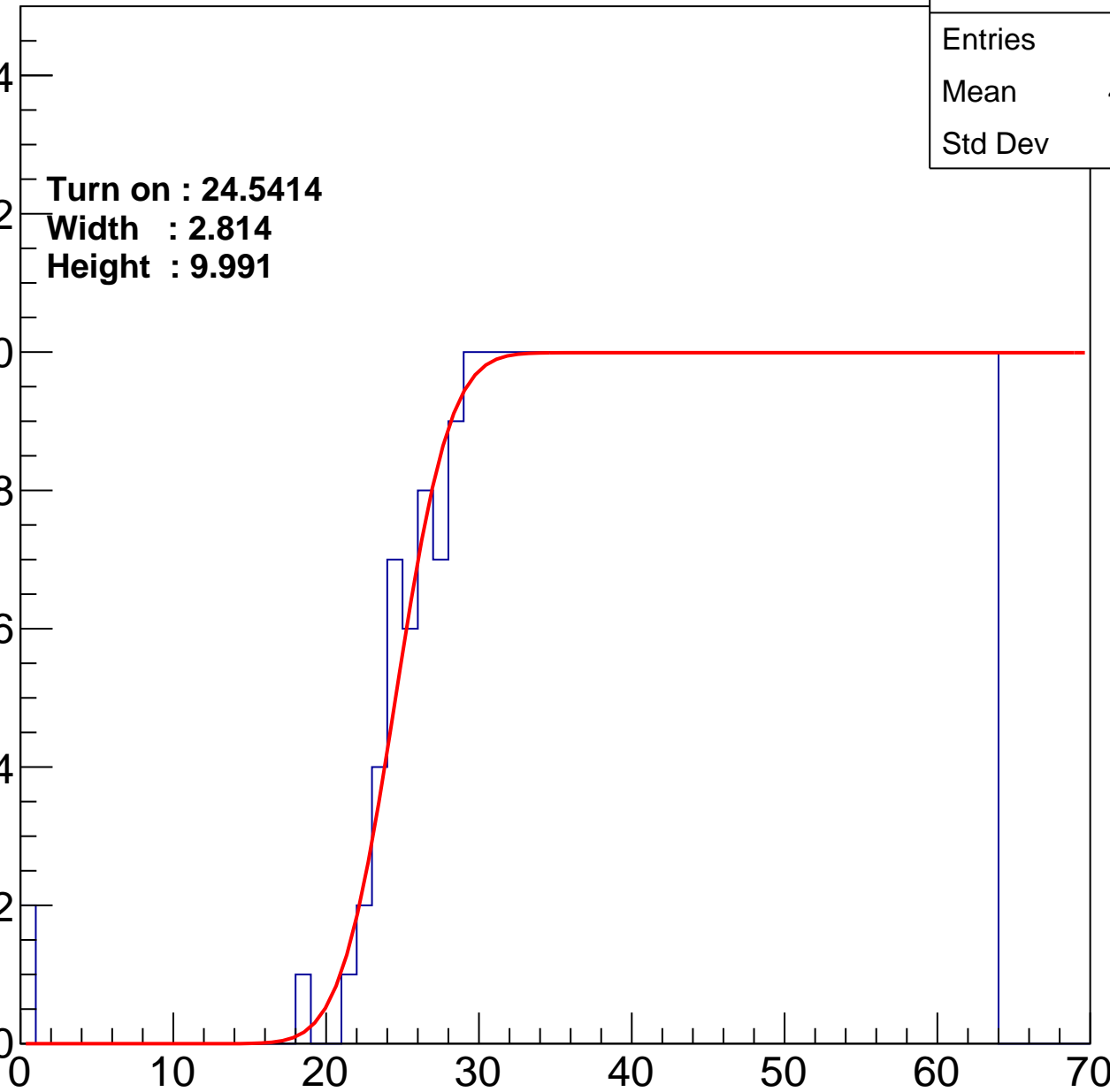
Width : 2.814

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U14-ch87

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	399
Mean	43.02
Std Dev	12.9

Turn on : 25.7416

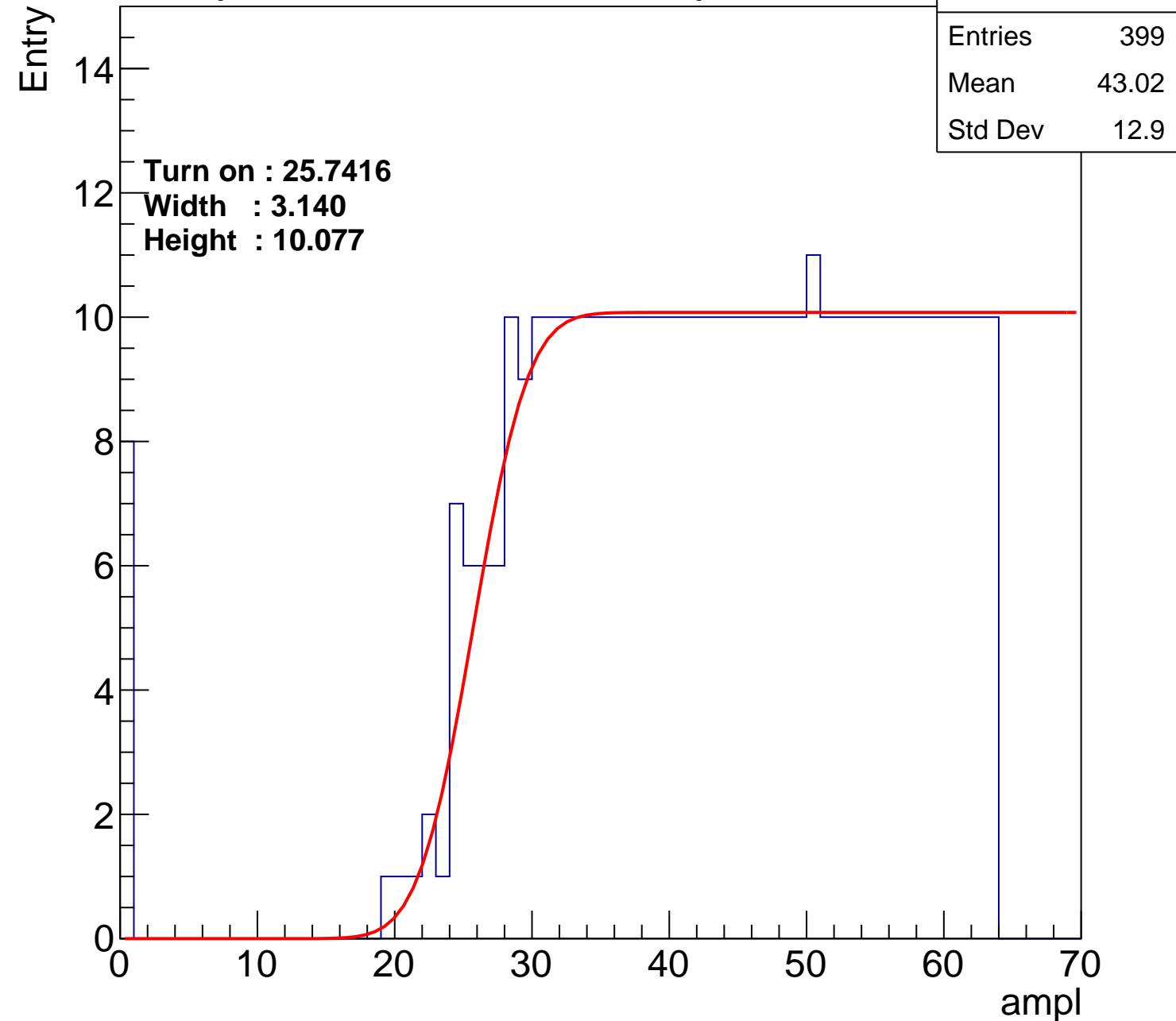
Width : 3.140

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch88

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	399
Mean	43.07
Std Dev	12.63

Turn on : 24.8112

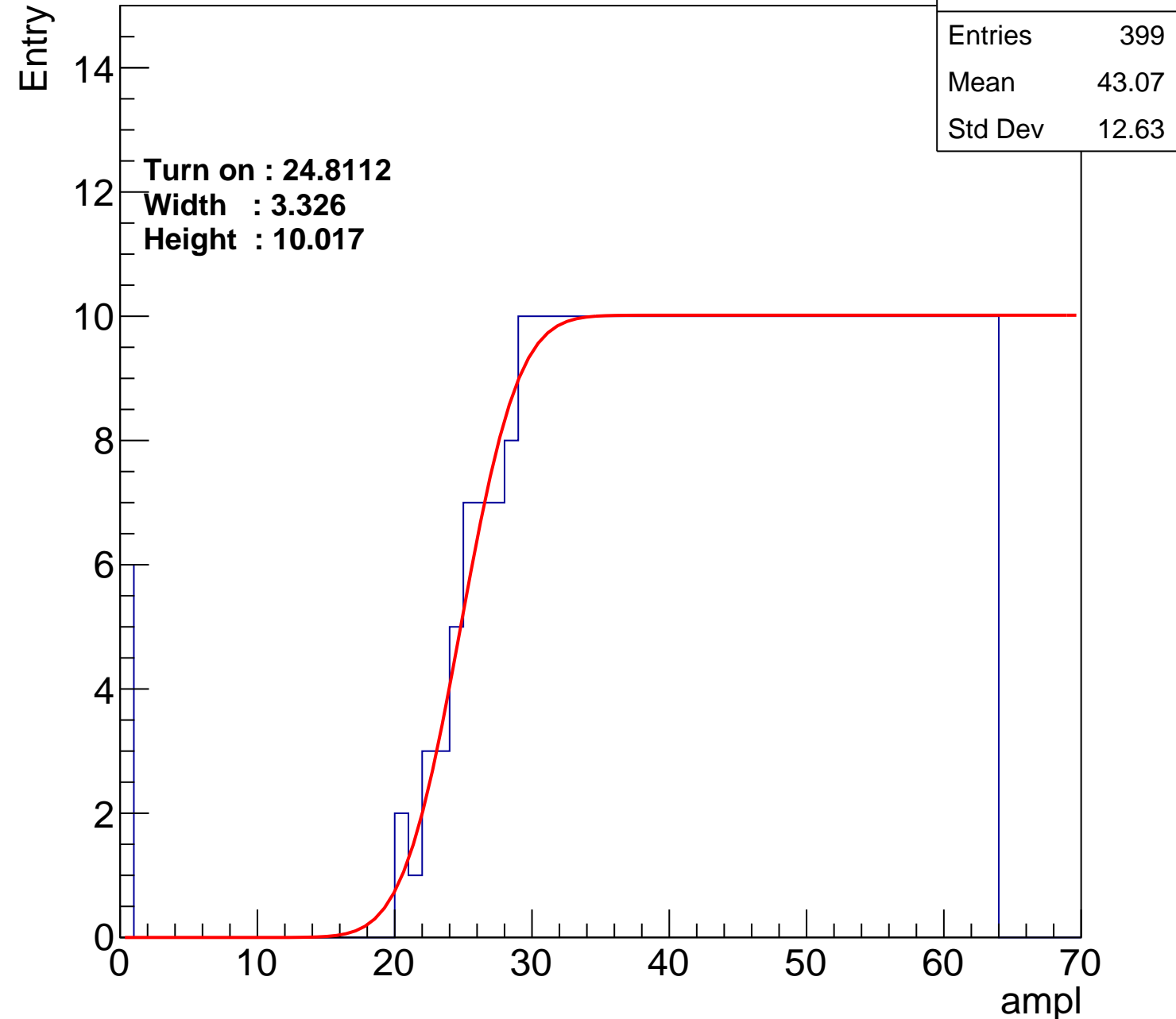
Width : 3.326

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch89

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	379
Mean	44.33
Std Dev	11.39

Turn on : 26.4387

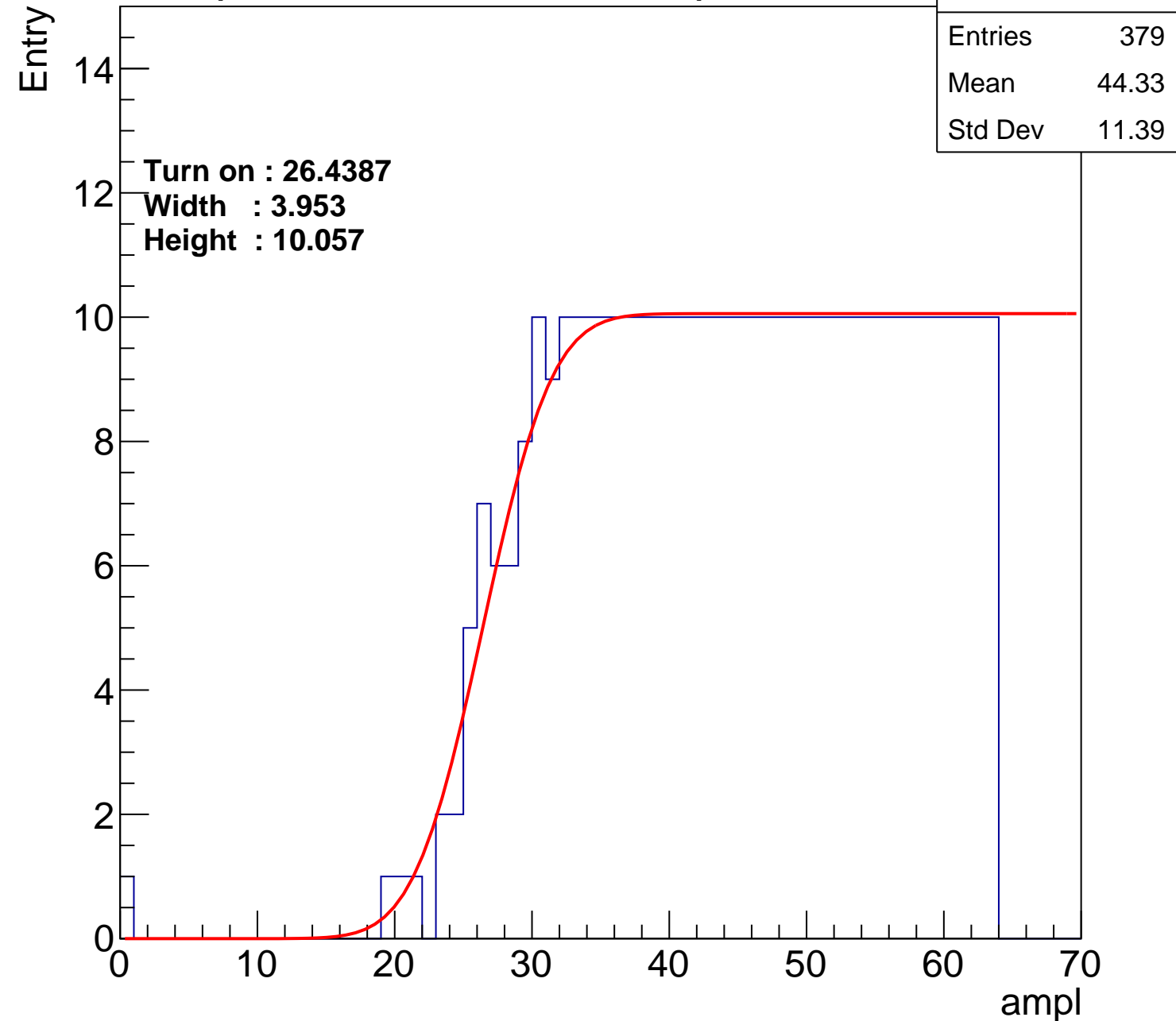
Width : 3.953

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch90

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	394
Mean	43.64
Std Dev	11.7

**Turn on : 24.3008**

**Width : 3.560**

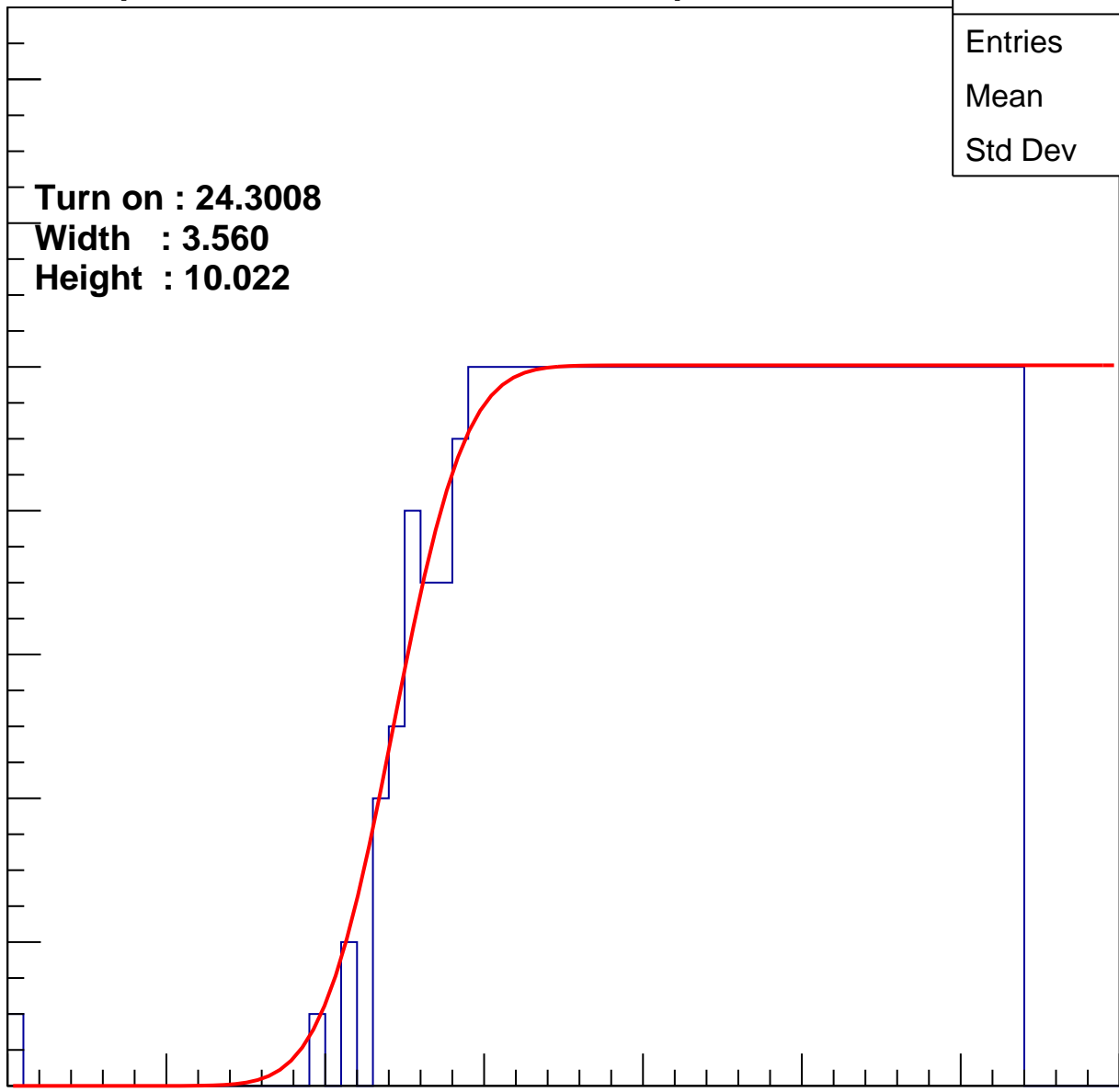
**Height : 10.022**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U14-ch91

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	377
Mean	44.25
Std Dev	11.84

Turn on : 26.9869

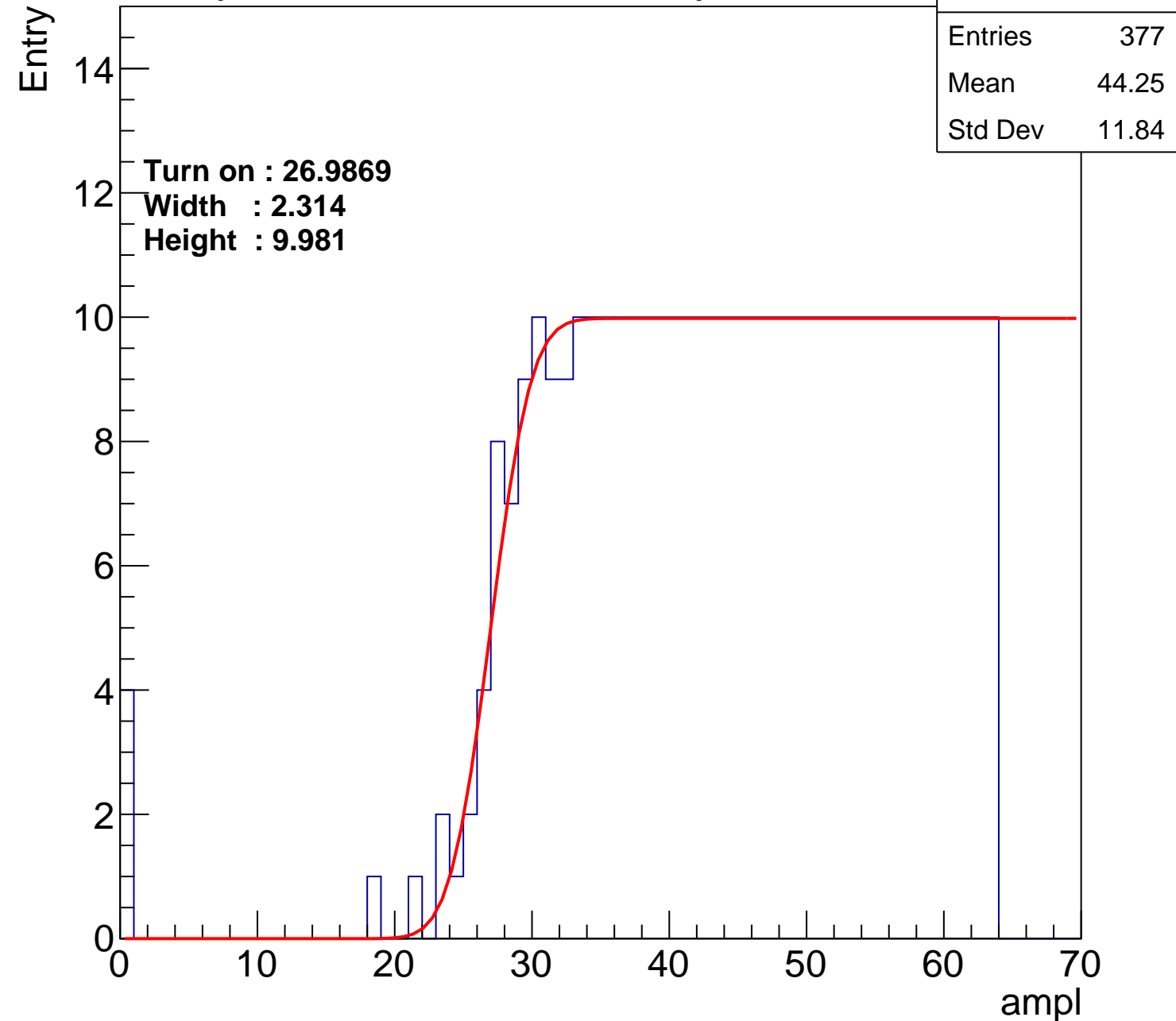
Width : 2.314

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch92

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	397
Mean	43.45
Std Dev	11.85

Turn on : 24.6414

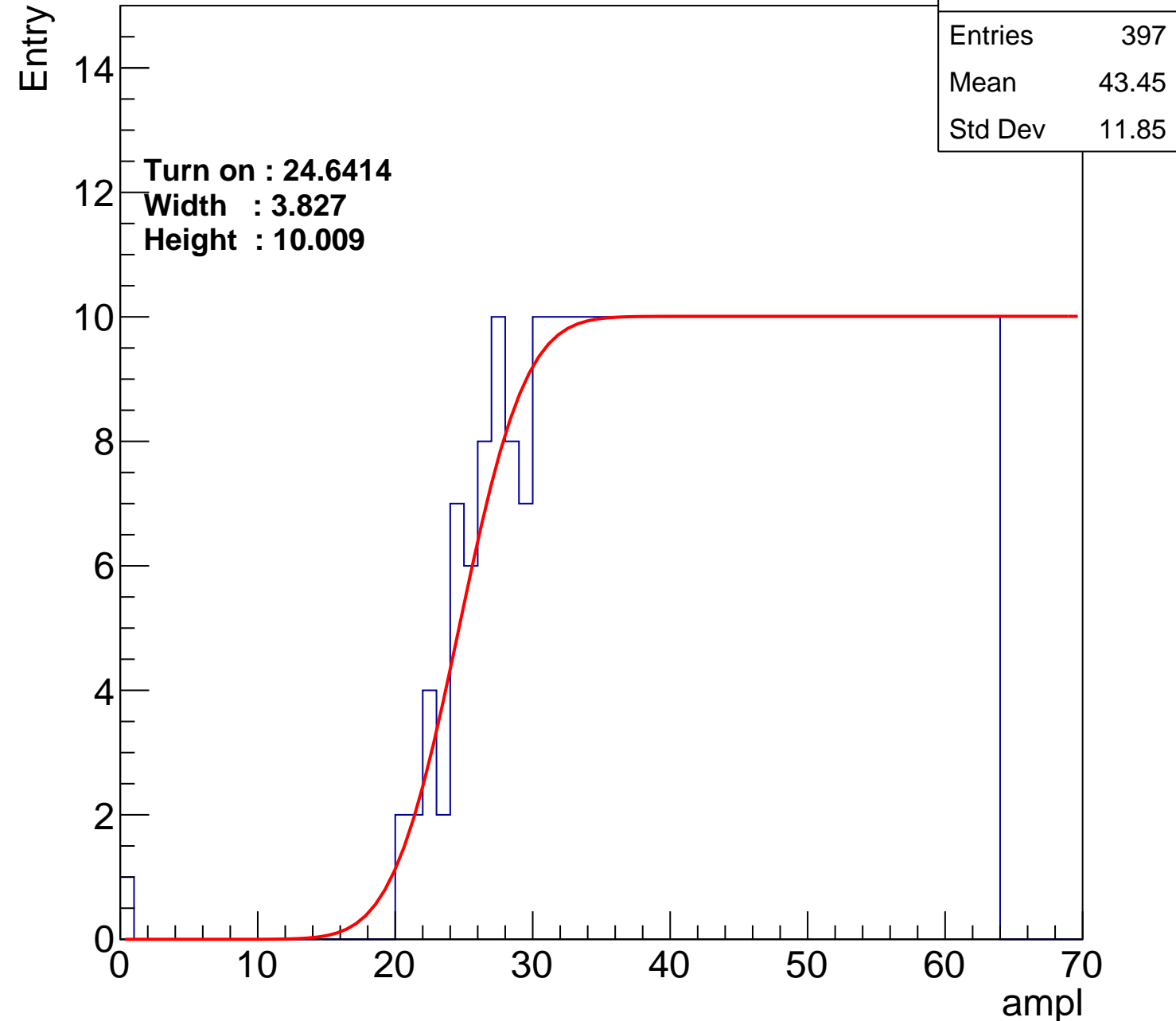
Width : 3.827

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch93

calib\_packv5\_042523\_0143.root, FC#7, port C2

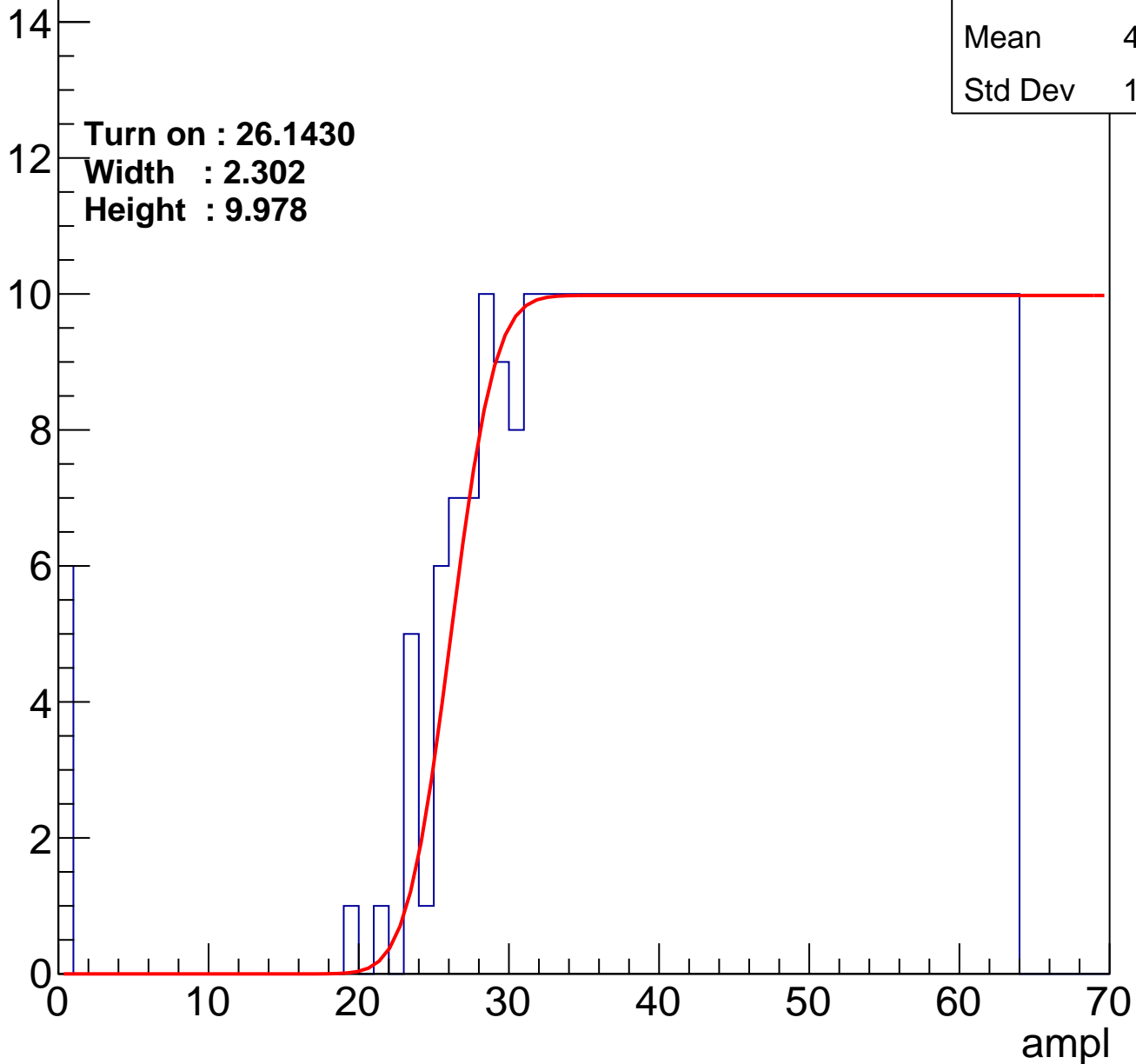
Entries	391
Mean	43.46
Std Dev	12.47

Turn on : 26.1430

Width : 2.302

Height : 9.978

Entry



# B1L103S, U14-ch94

calib\_packv5\_042523\_0143.root, FC#7, port C2

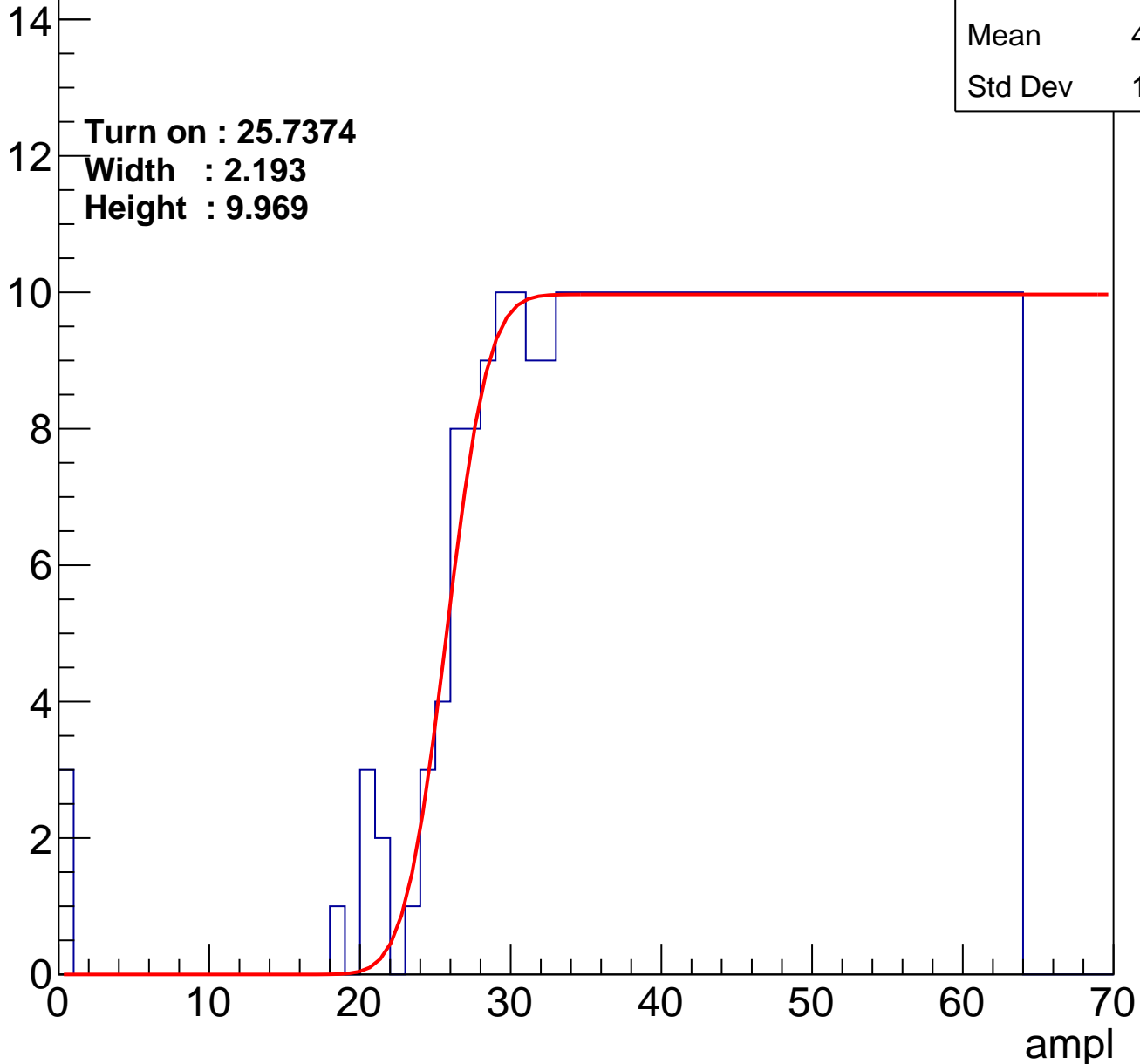
Entries	390
Mean	43.66
Std Dev	12.02

Turn on : 25.7374

Width : 2.193

Height : 9.969

Entry





# B1L103S, U14-ch95

calib\_packv5\_042523\_0143.root, FC#7, port C2

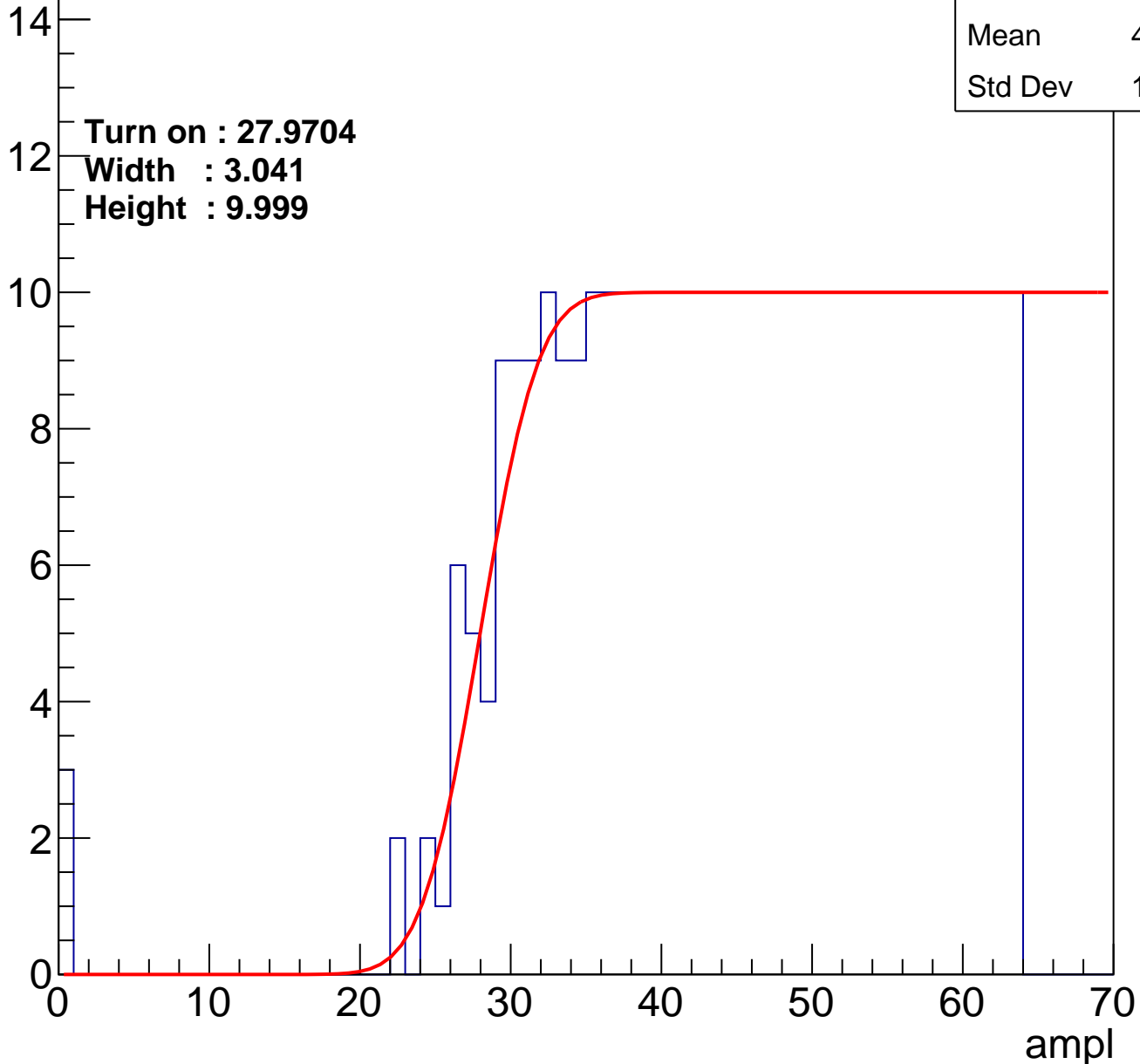
Entries	368
Mean	44.74
Std Dev	11.47

Turn on : 27.9704

Width : 3.041

Height : 9.999

Entry



# B1L103S, U14-ch96

calib\_packv5\_042523\_0143.root, FC#7, port C2

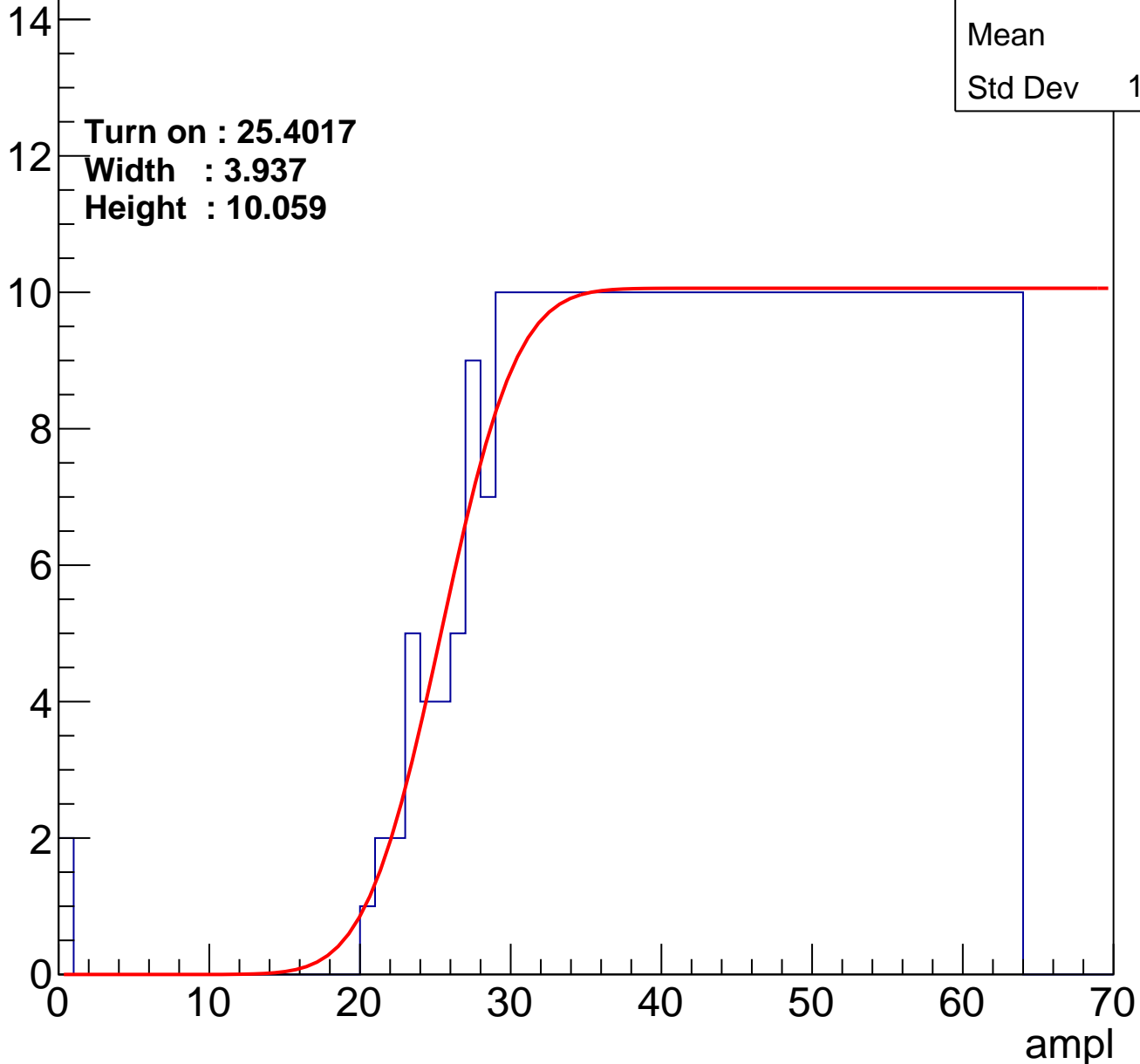
Entries	391
Mean	43.7
Std Dev	11.84

Turn on : 25.4017

Width : 3.937

Height : 10.059

Entry



# B1L103S, U14-ch97

calib\_packv5\_042523\_0143.root, FC#7, port C2

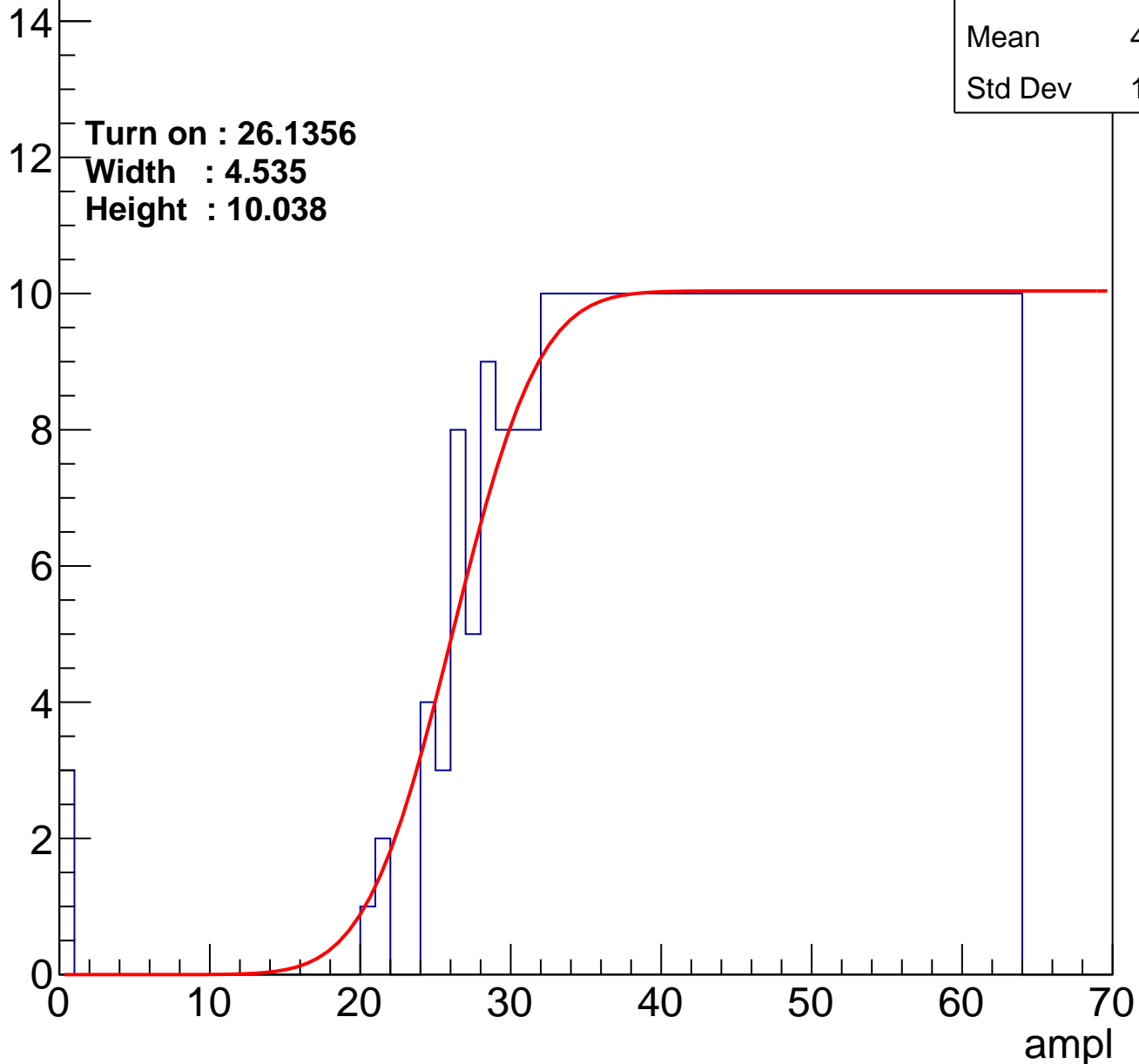
Entries	379
Mean	44.19
Std Dev	11.76

Turn on : 26.1356

Width : 4.535

Height : 10.038

Entry



# B1L103S, U14-ch98

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	381
Mean	44.05
Std Dev	11.86

Turn on : 26.3313

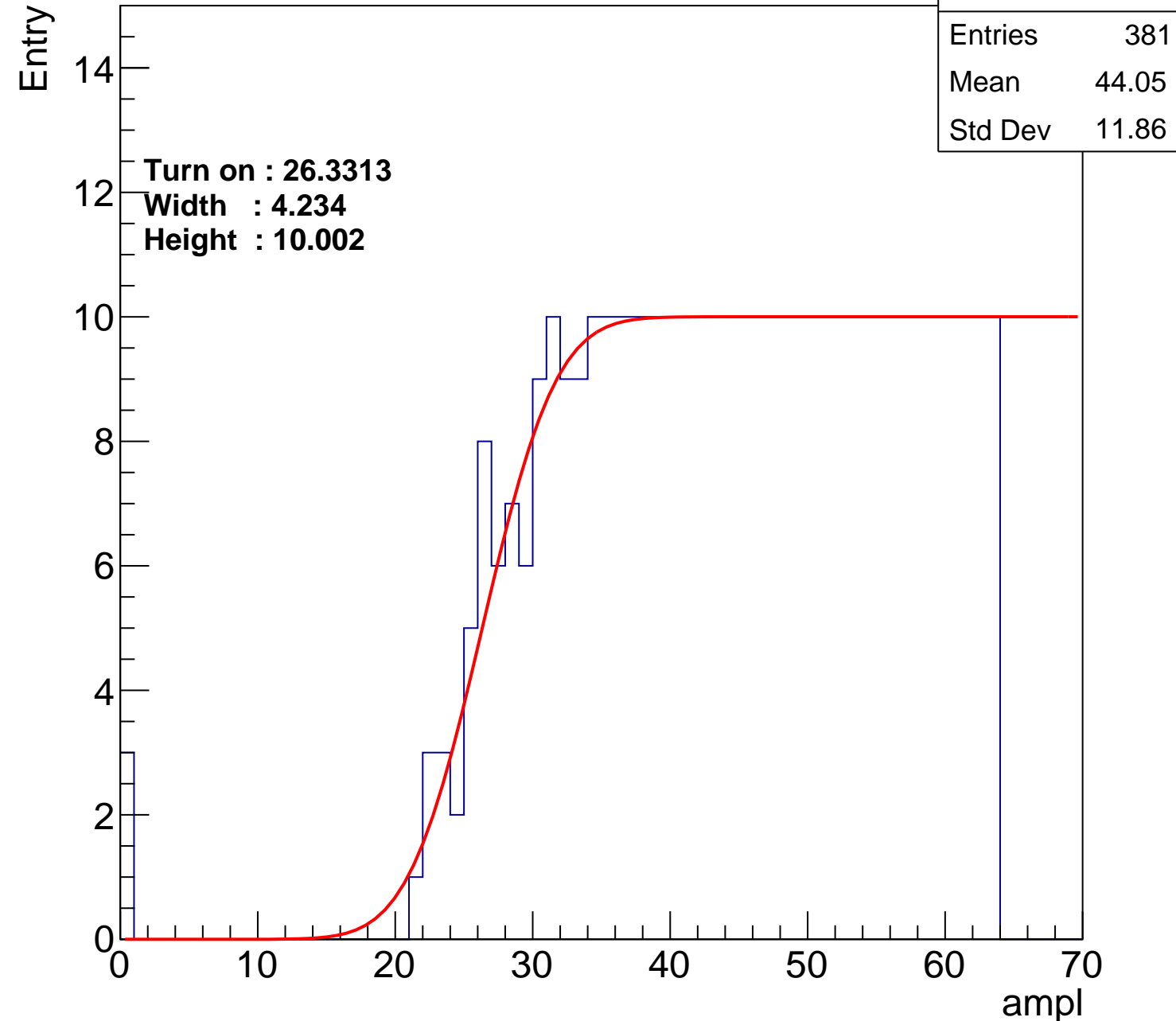
Width : 4.234

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch99

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	385
Mean	43.71
Std Dev	12.4

**Turn on : 26.9467**

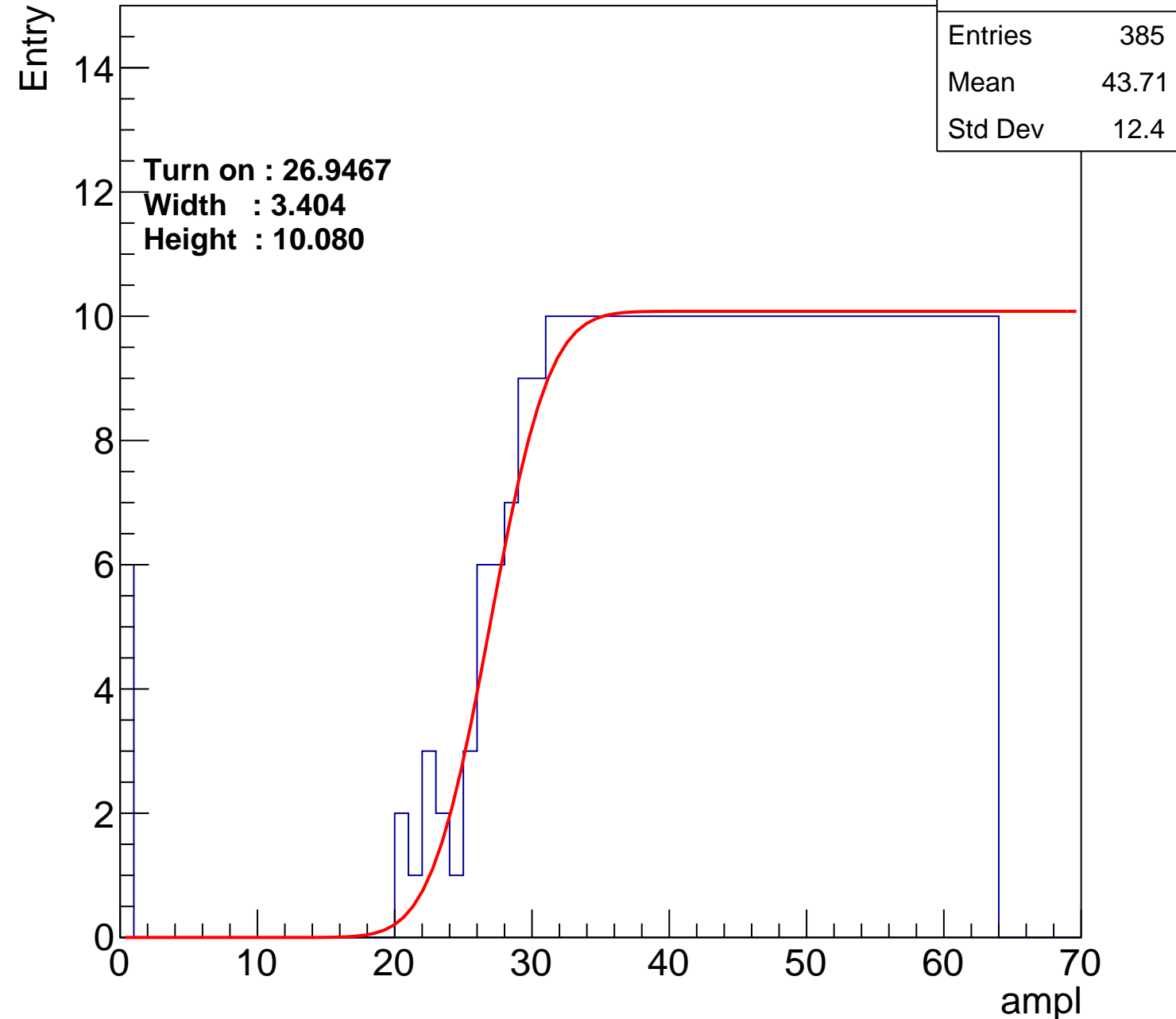
**Width : 3.404**

**Height : 10.080**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch100

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	375
Mean	44.36
Std Dev	11.7

Turn on : 27.8219

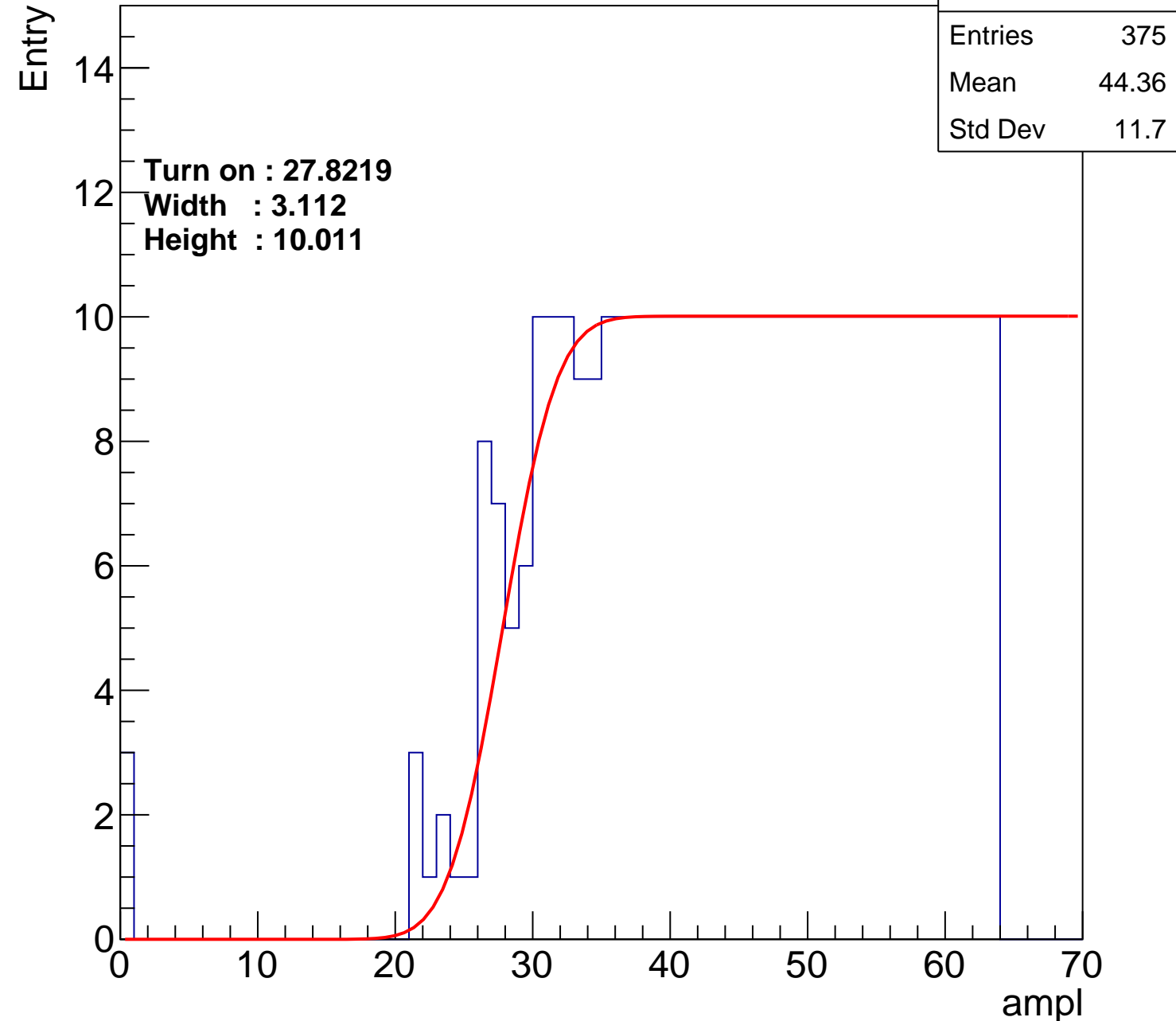
Width : 3.112

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch101

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	380
Mean	44.17
Std Dev	11.81

Turn on : 26.4353

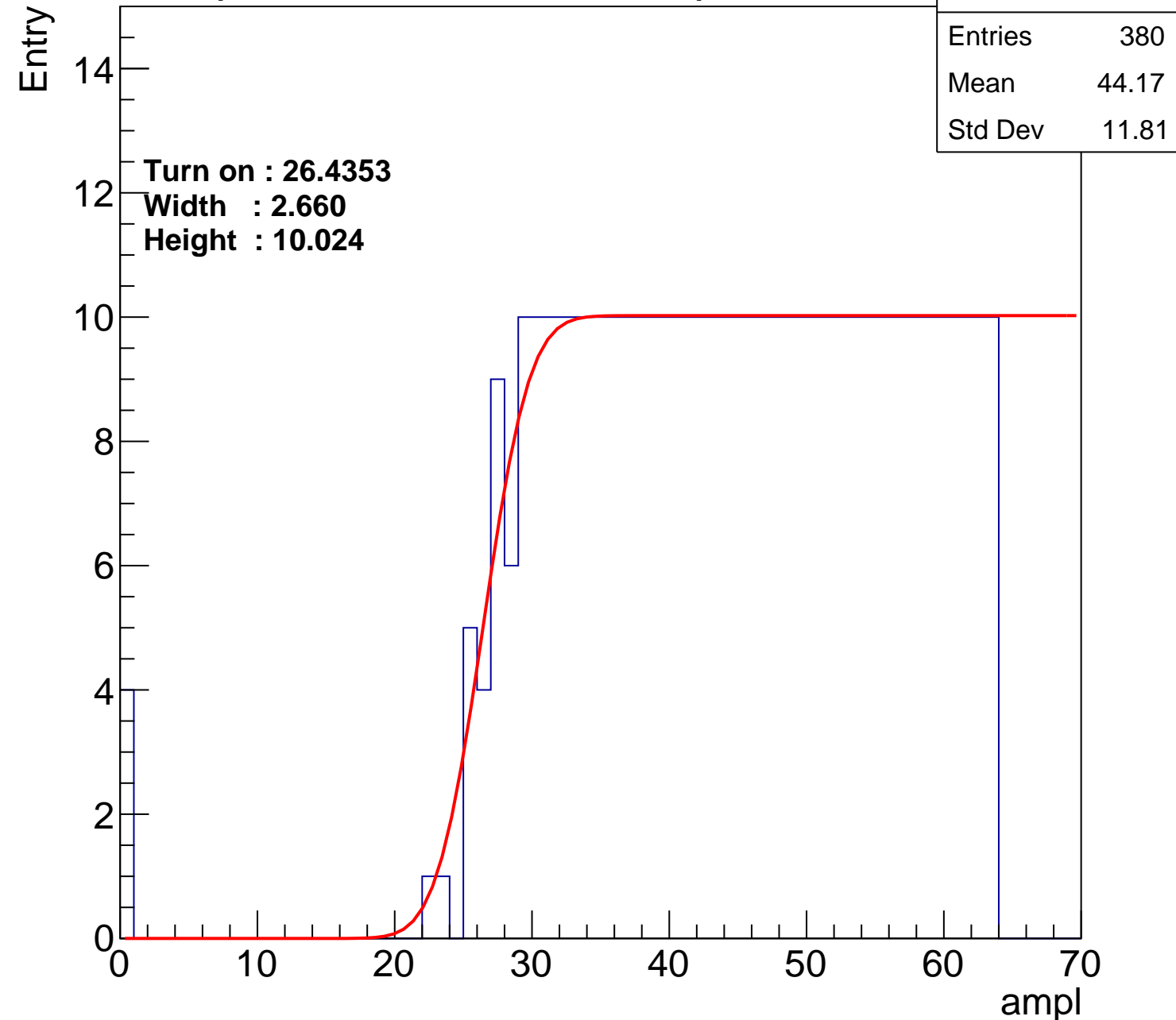
Width : 2.660

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch102

calib\_packv5\_042523\_0143.root, FC#7, port C2

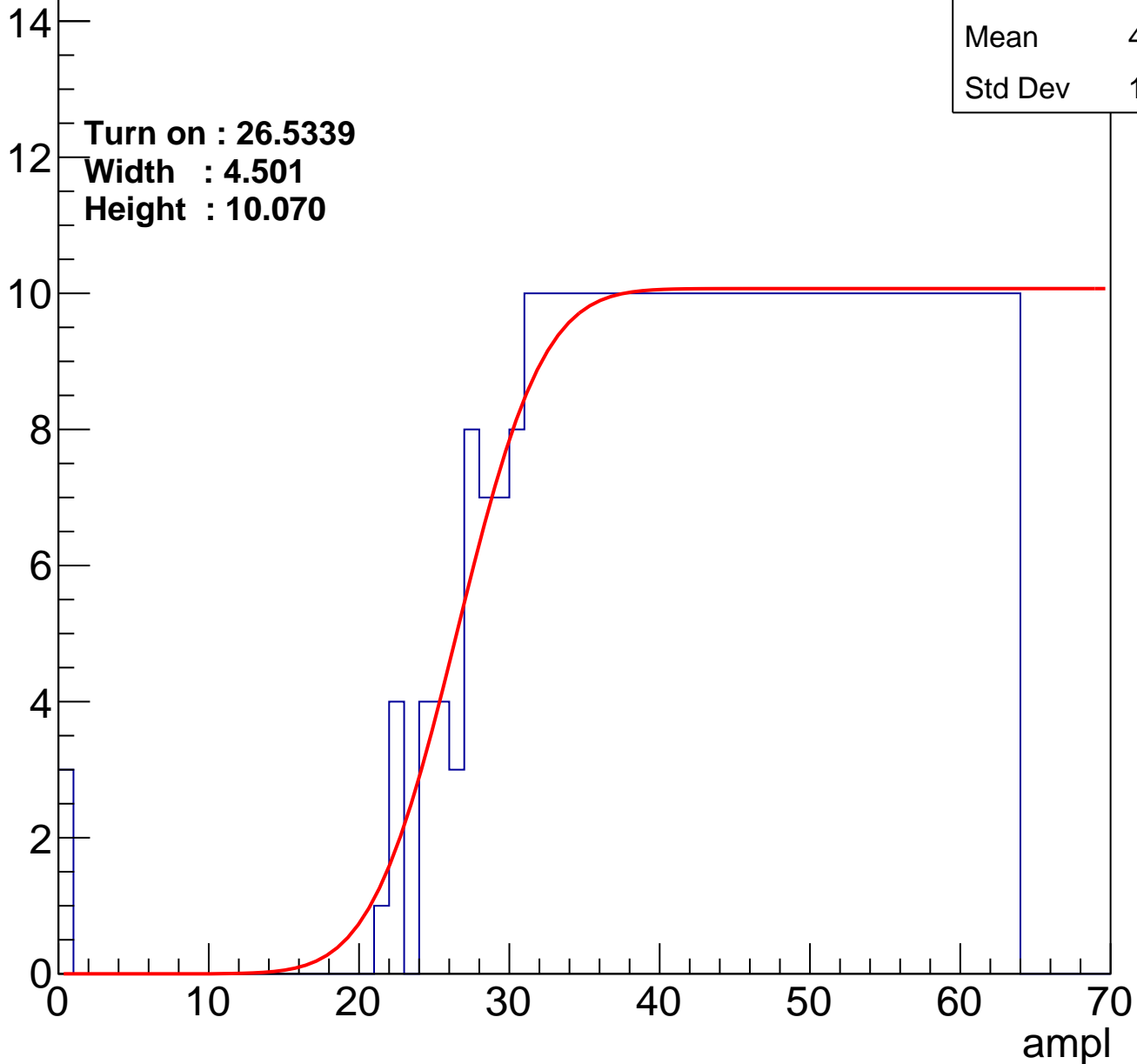
Entries	379
Mean	44.19
Std Dev	11.76

Turn on : 26.5339

Width : 4.501

Height : 10.070

Entry





# B1L103S, U14-ch103

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	395
Mean	43.37
Std Dev	12.27

Turn on : 25.0908

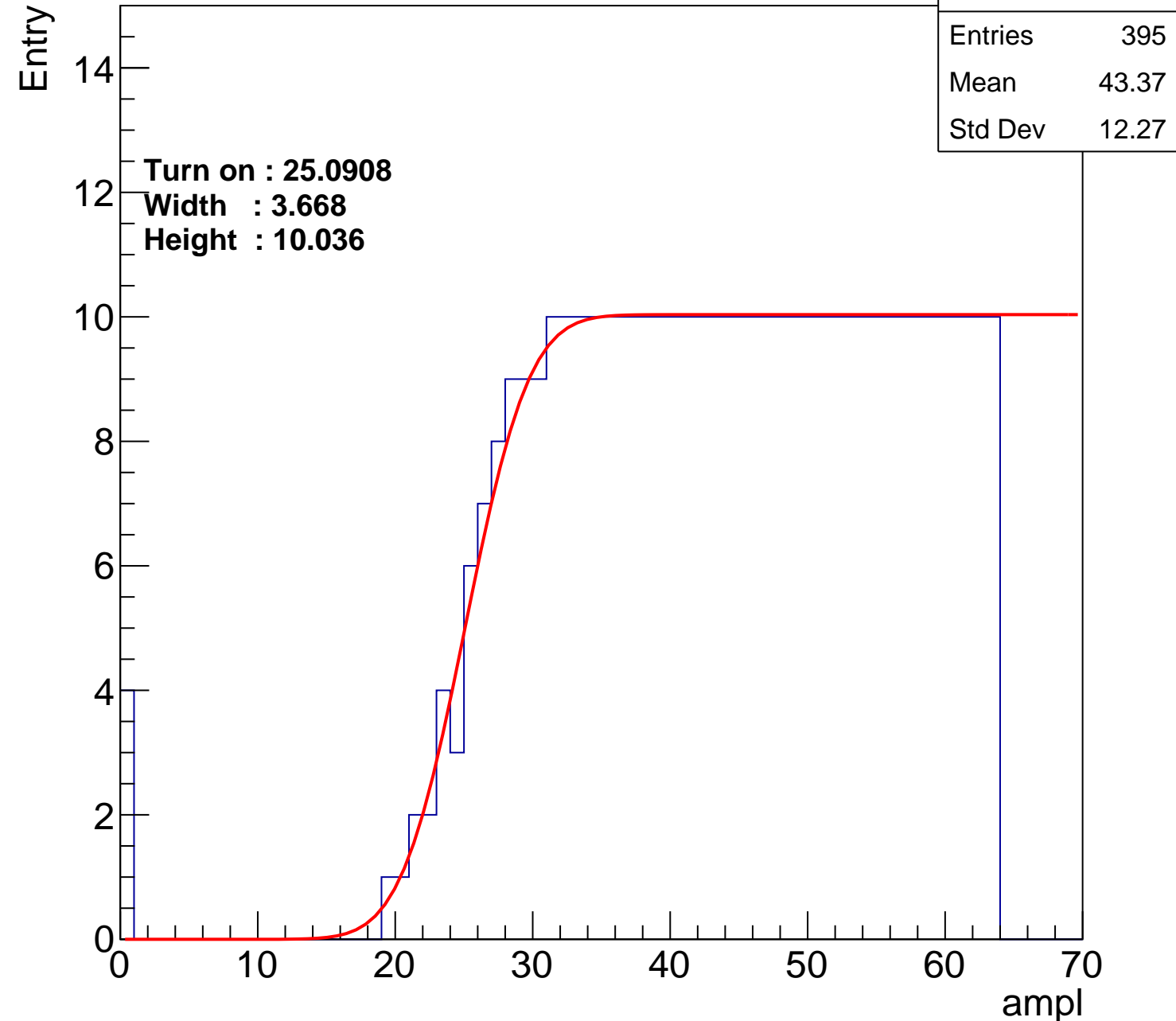
Width : 3.668

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch104

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	386
Mean	43.73
Std Dev	12.25

Turn on : 26.4056

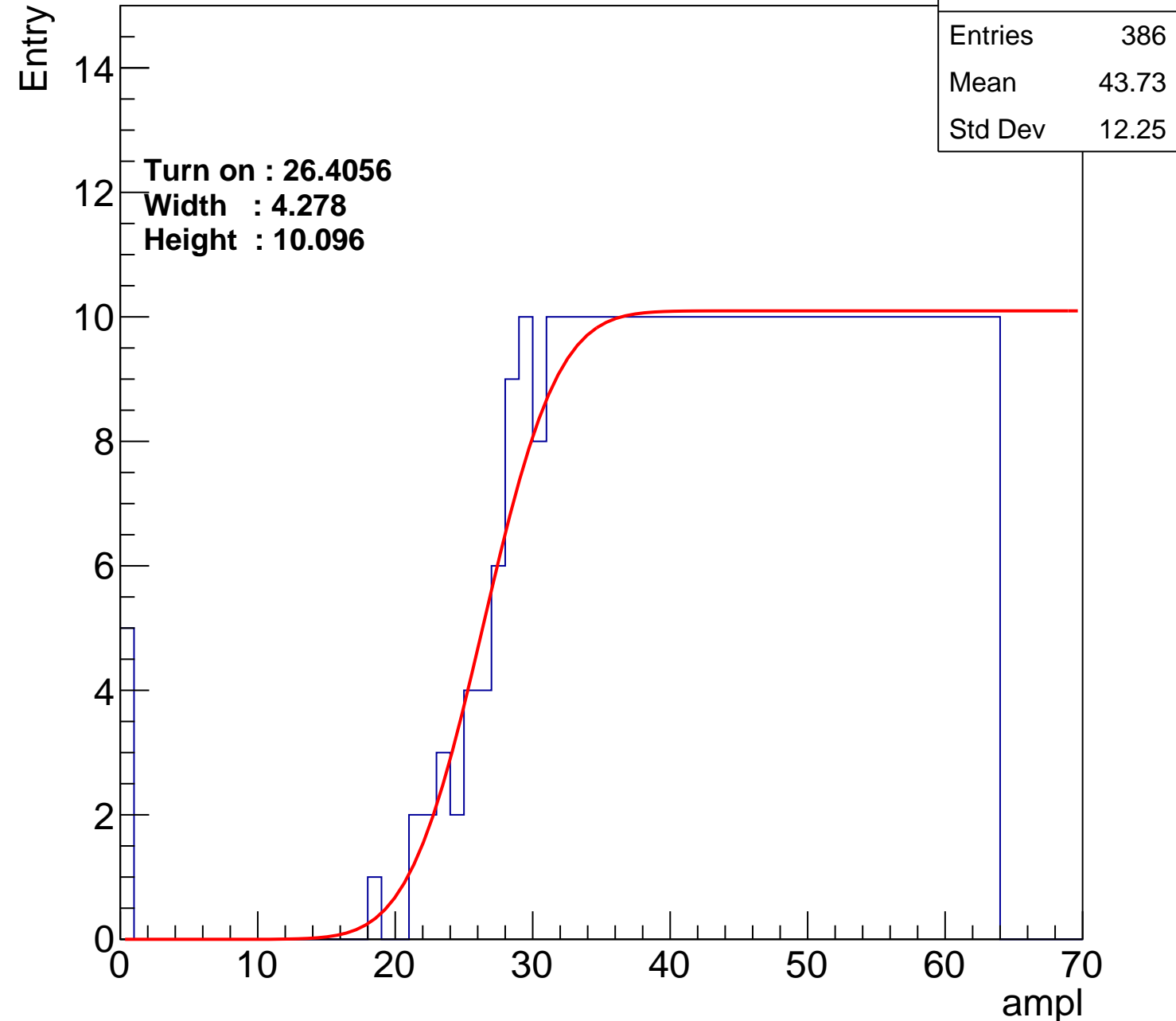
Width : 4.278

Height : 10.096

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch105

calib\_packv5\_042523\_0143.root, FC#7, port C2

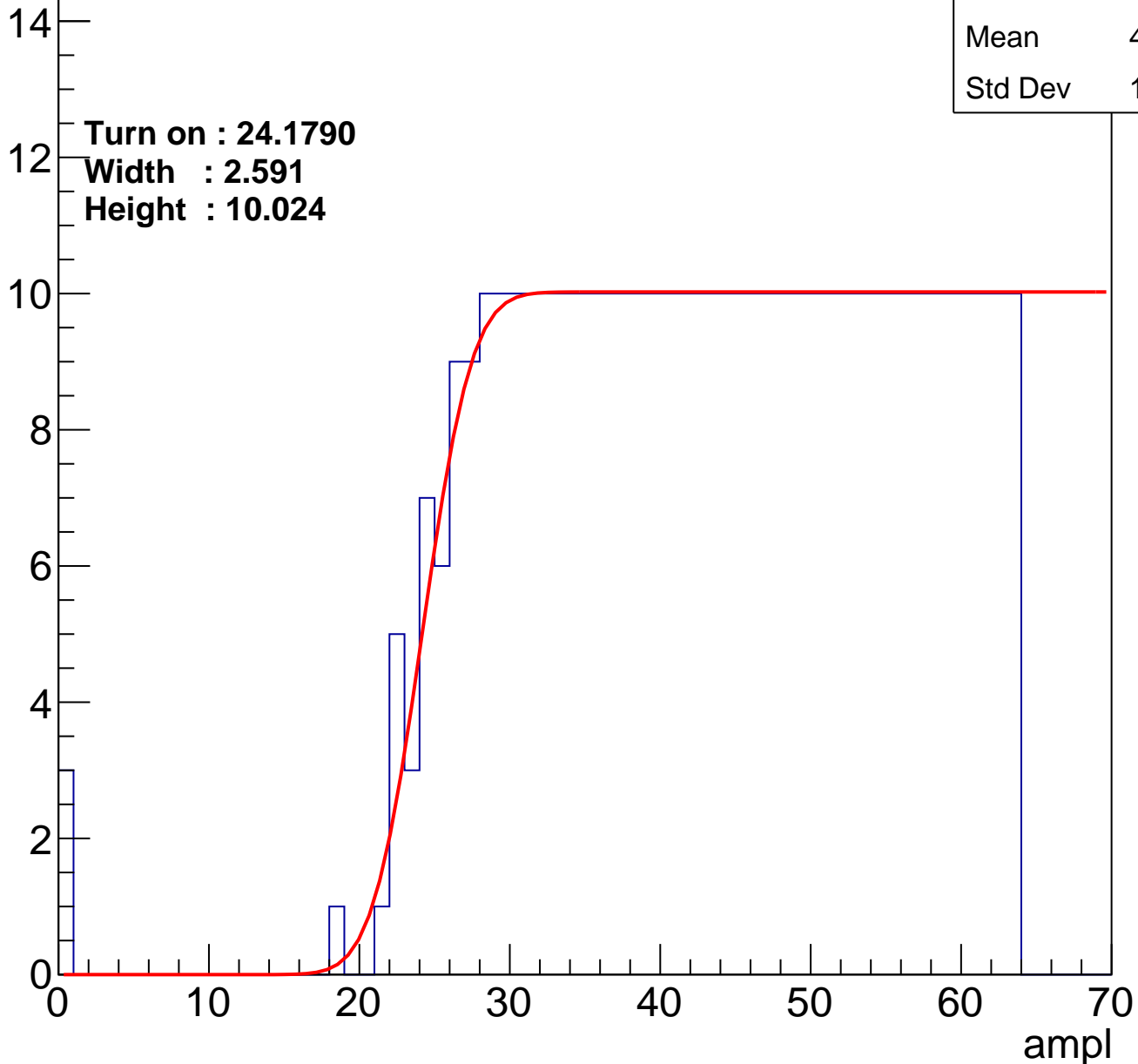
Entries	404
Mean	43.05
Std Dev	12.24

Turn on : 24.1790

Width : 2.591

Height : 10.024

Entry



# B1L103S, U14-ch106

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	386
Mean	43.87
Std Dev	11.89

**Turn on : 25.8303**

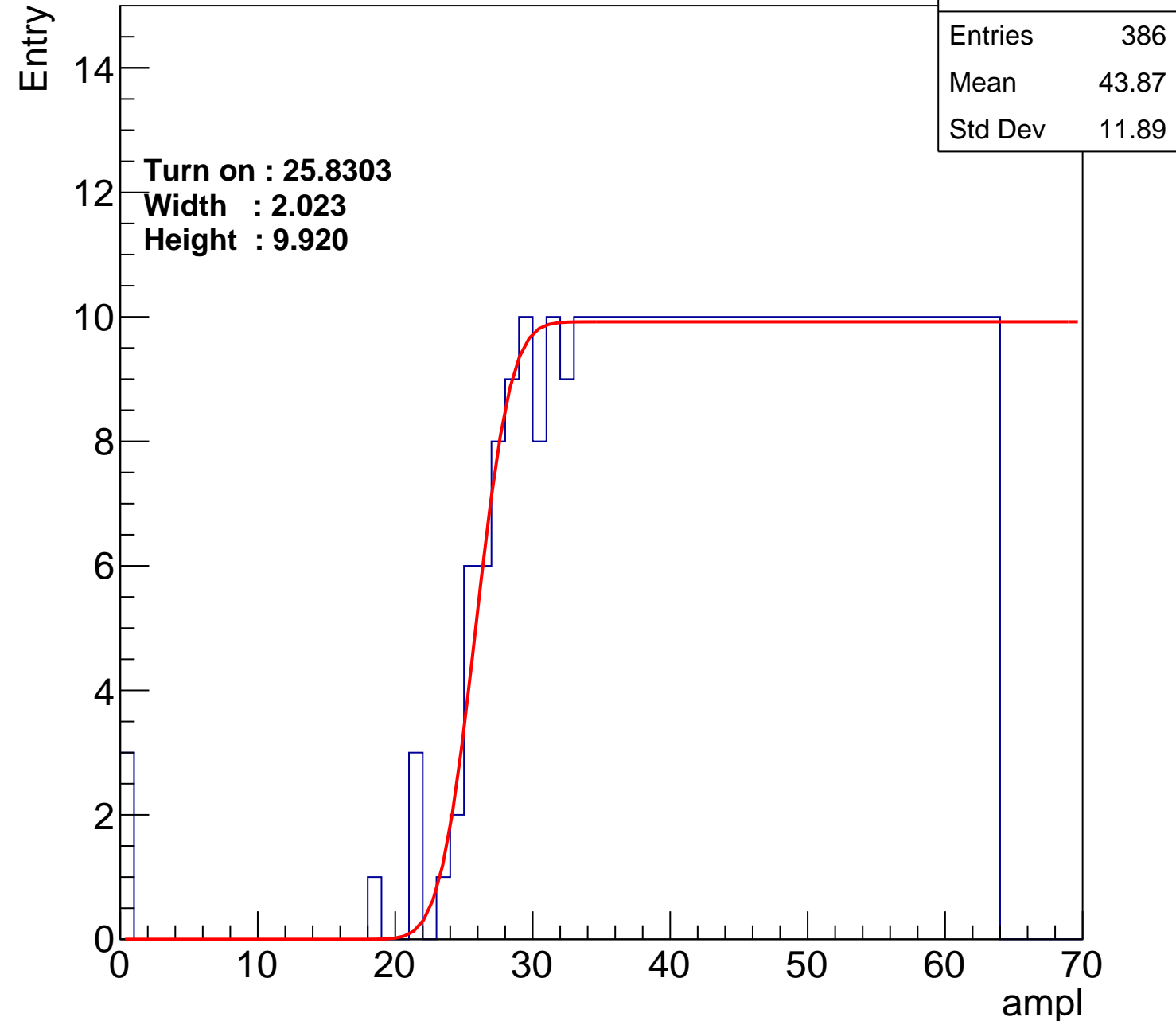
**Width : 2.023**

**Height : 9.920**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch107

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	381
Mean	43.81
Std Dev	12.52

Turn on : 27.9652

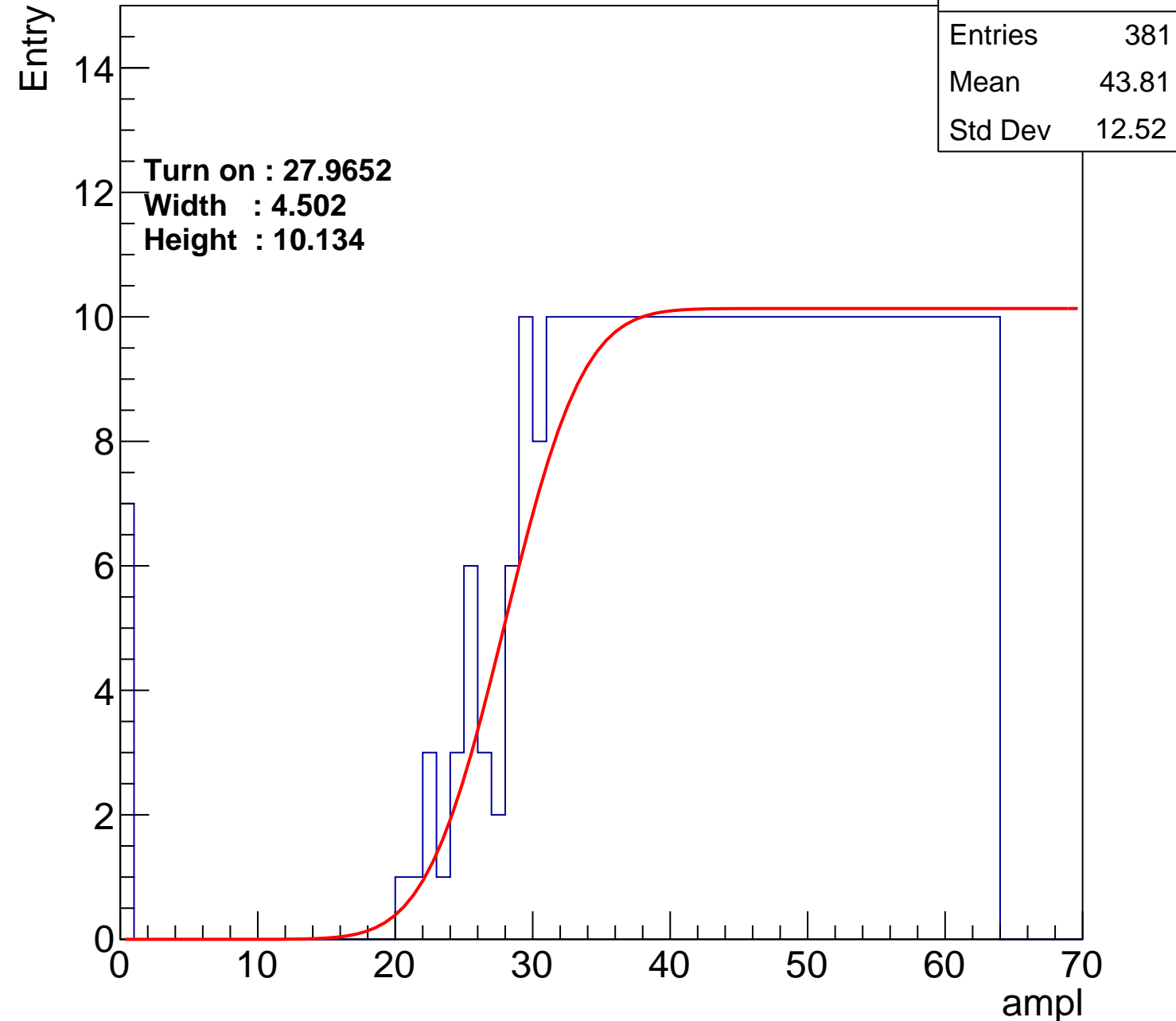
Width : 4.502

Height : 10.134

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch108

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	404
Mean	42.81
Std Dev	12.78

Turn on : 24.5465

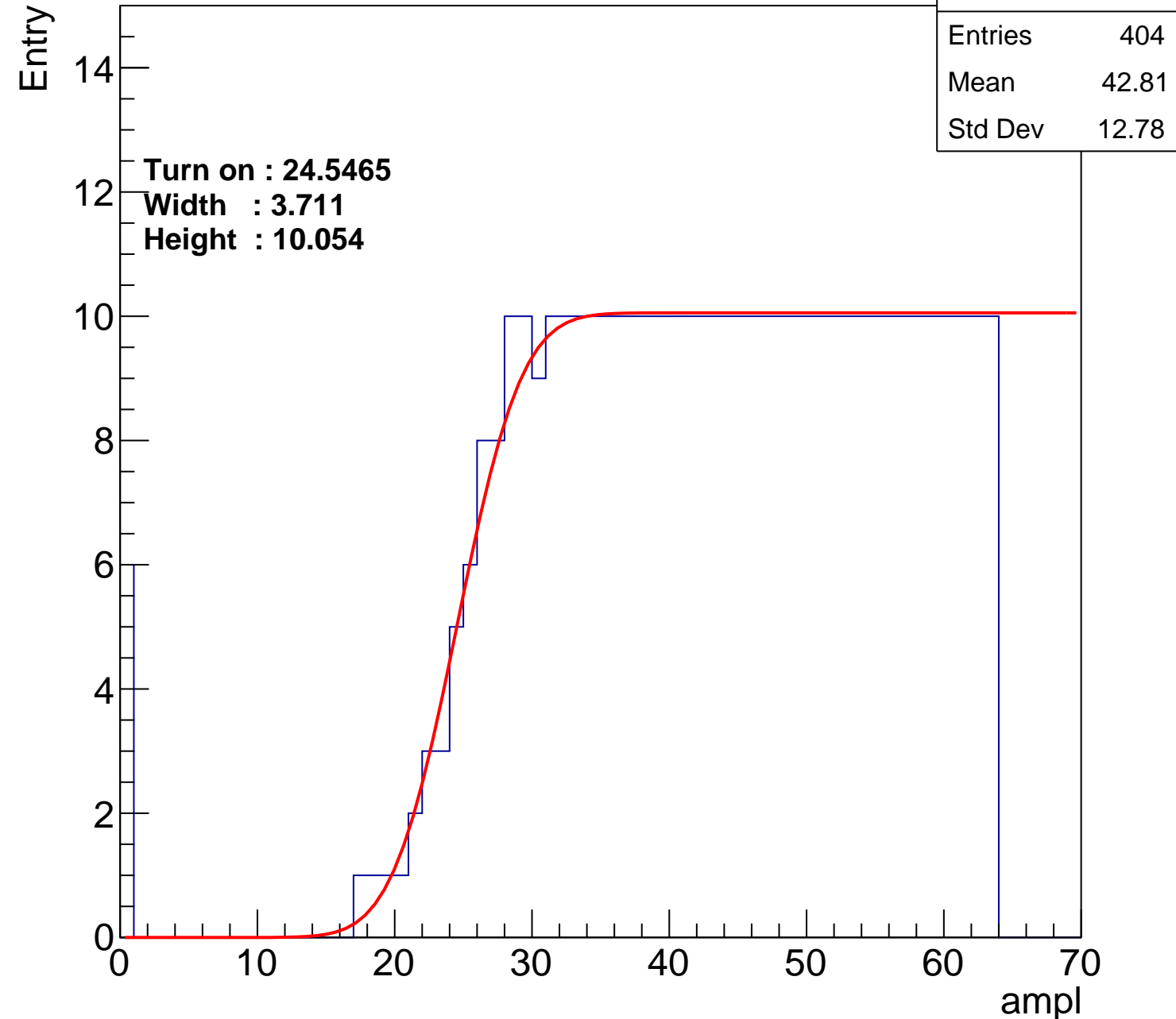
Width : 3.711

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch109

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	385
Mean	43.98
Std Dev	11.71

Turn on : 26.0142

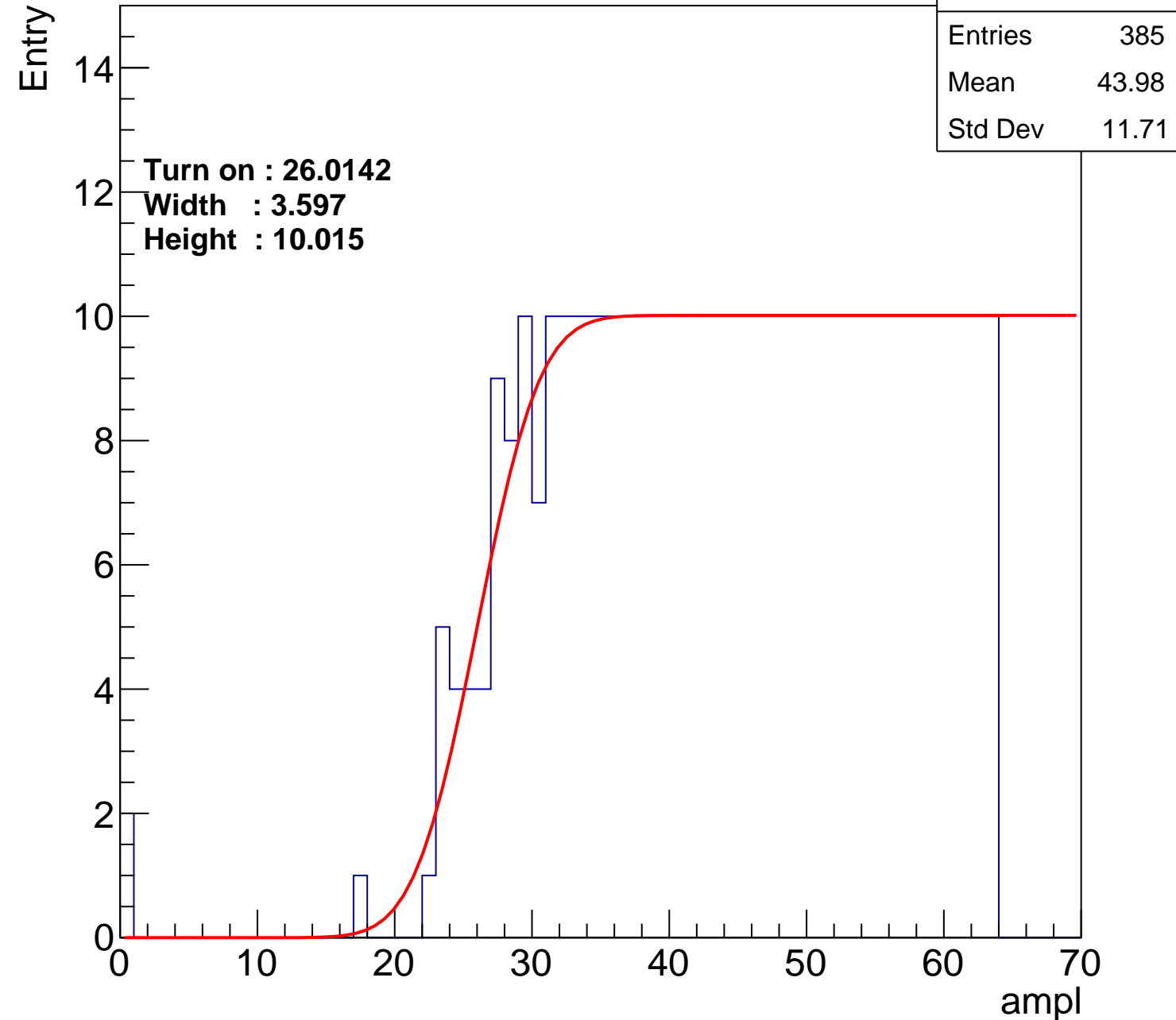
Width : 3.597

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch110

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	398
Mean	43.28
Std Dev	12.18

Turn on : 24.6073

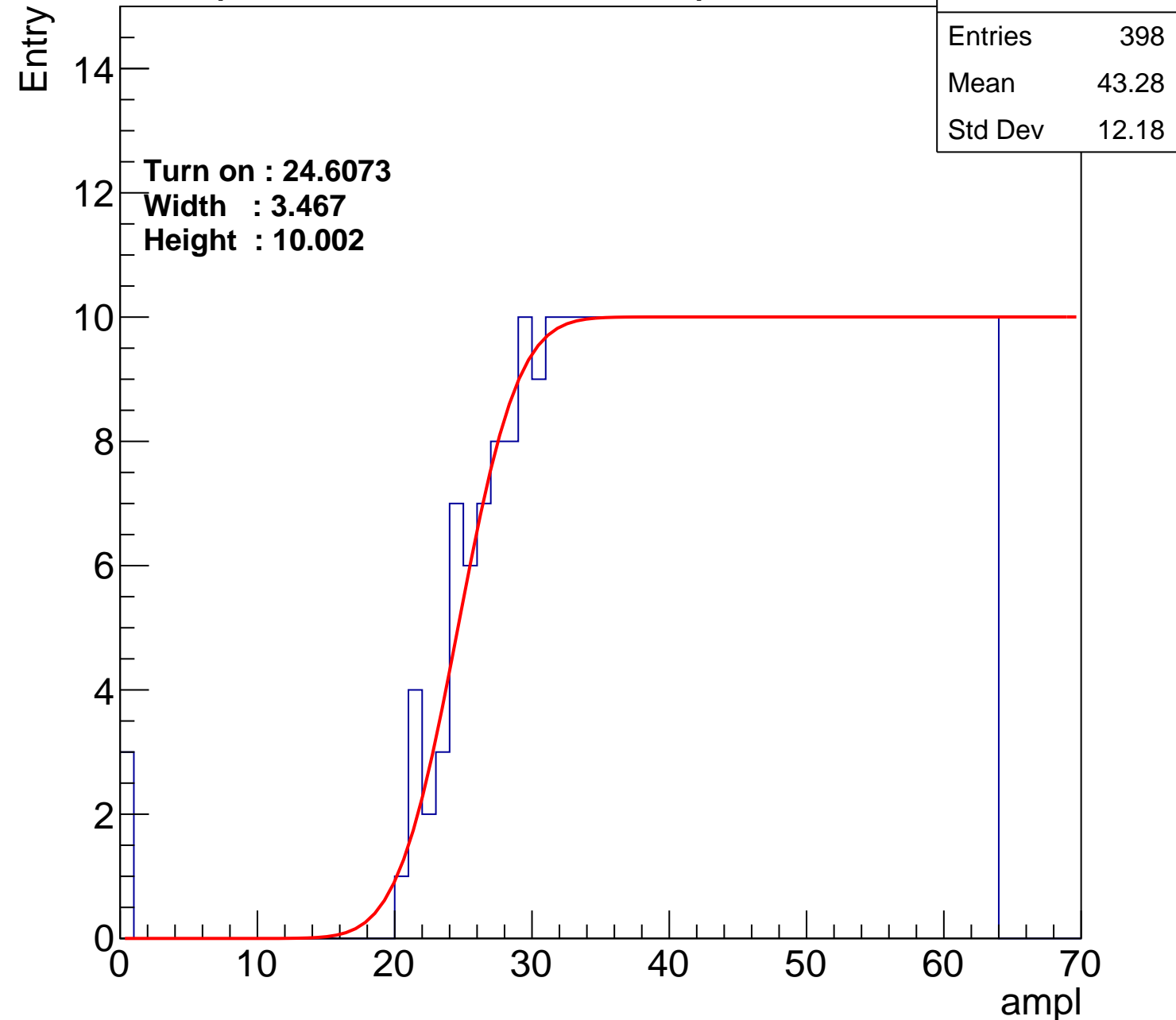
Width : 3.467

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U14-ch111

calib\_packv5\_042523\_0143.root, FC#7, port C2

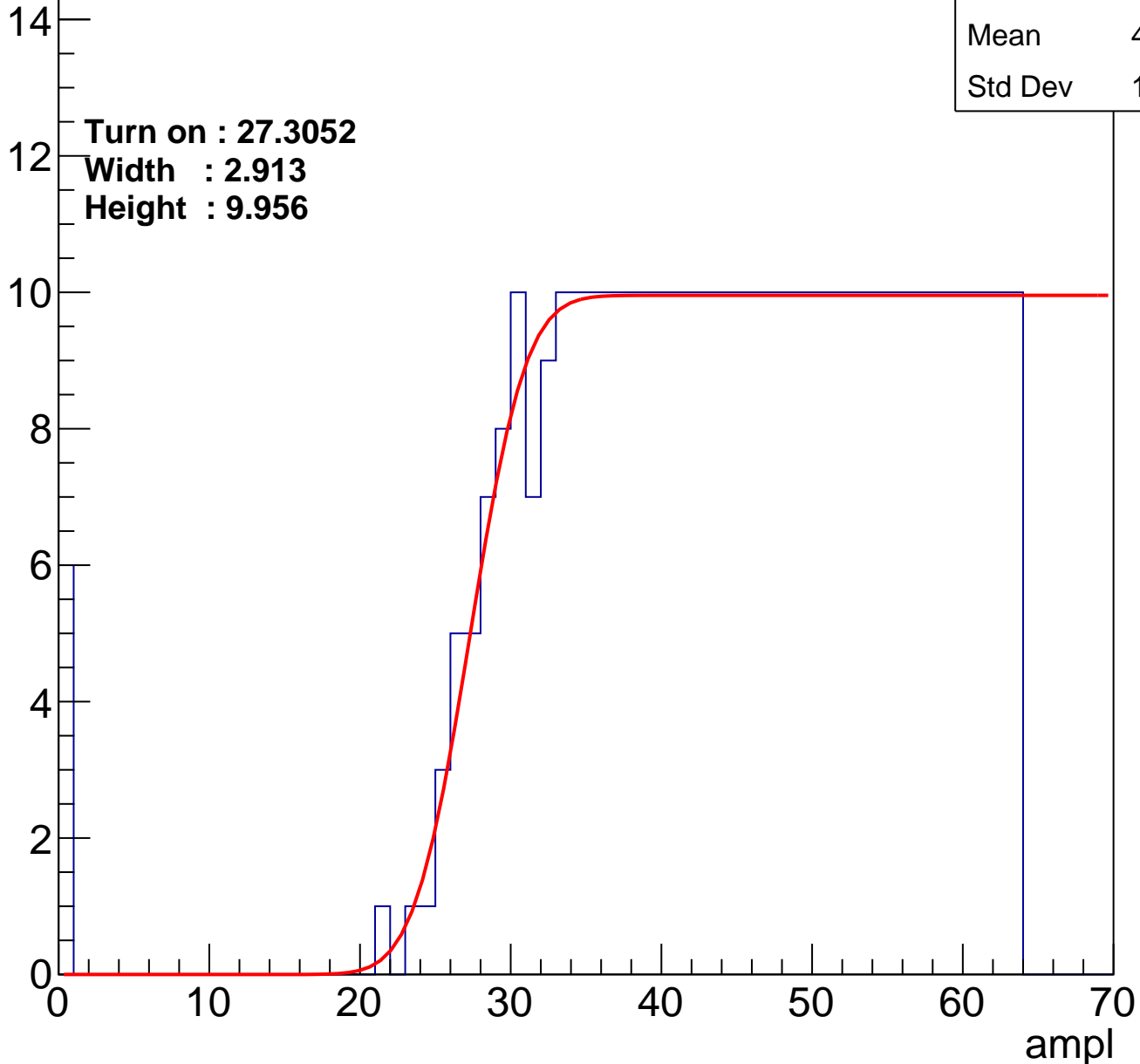
Entries	373
Mean	44.29
Std Dev	12.13

Turn on : 27.3052

Width : 2.913

Height : 9.956

Entry



# B1L103S, U14-ch112

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	385
Mean	43.93
Std Dev	11.87

**Turn on : 26.7847**

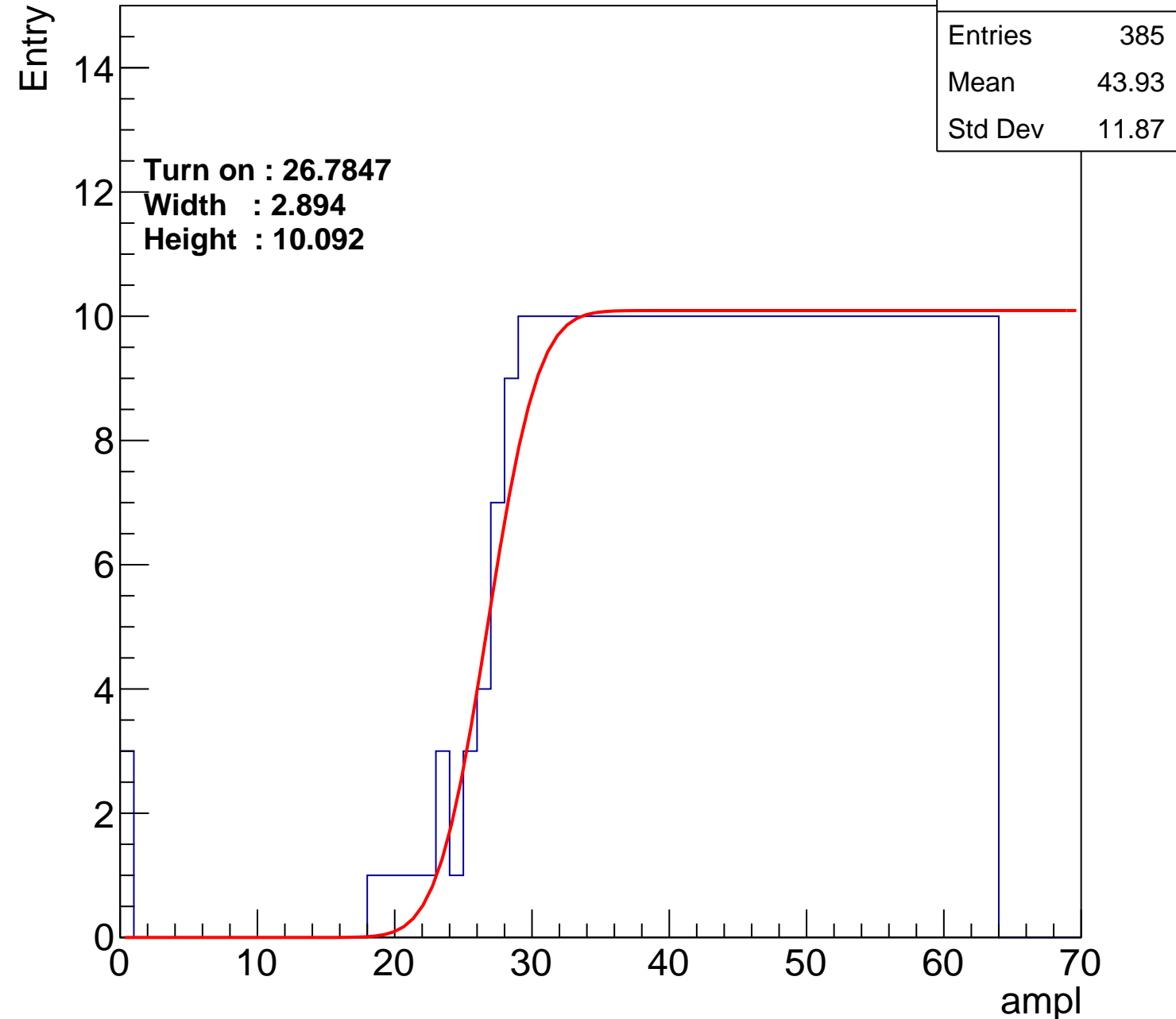
**Width : 2.894**

**Height : 10.092**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch113

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	405
Mean	42.85
Std Dev	12.55

Turn on : 24.3827

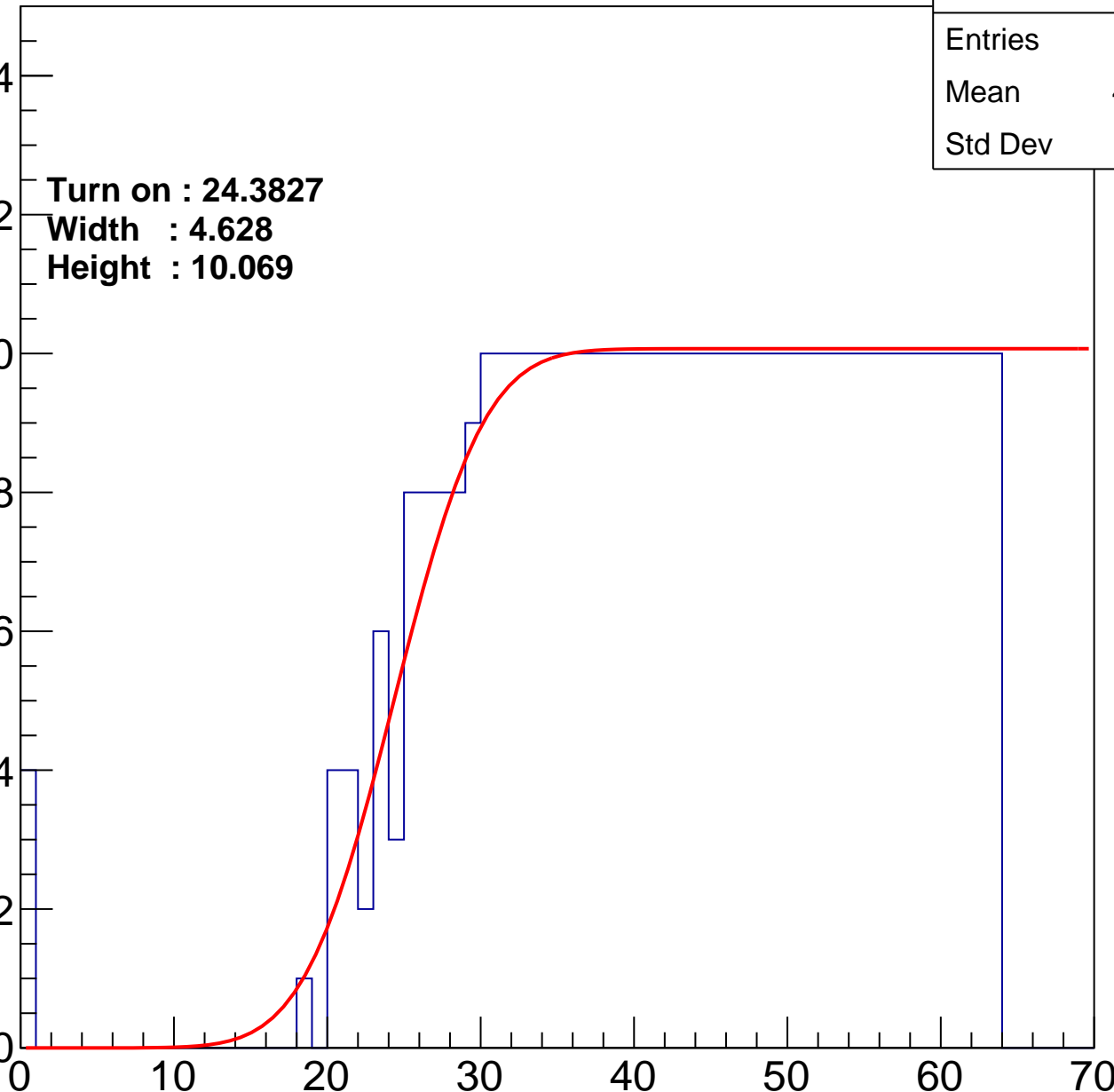
Width : 4.628

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch114

calib\_packv5\_042523\_0143.root, FC#7, port C2

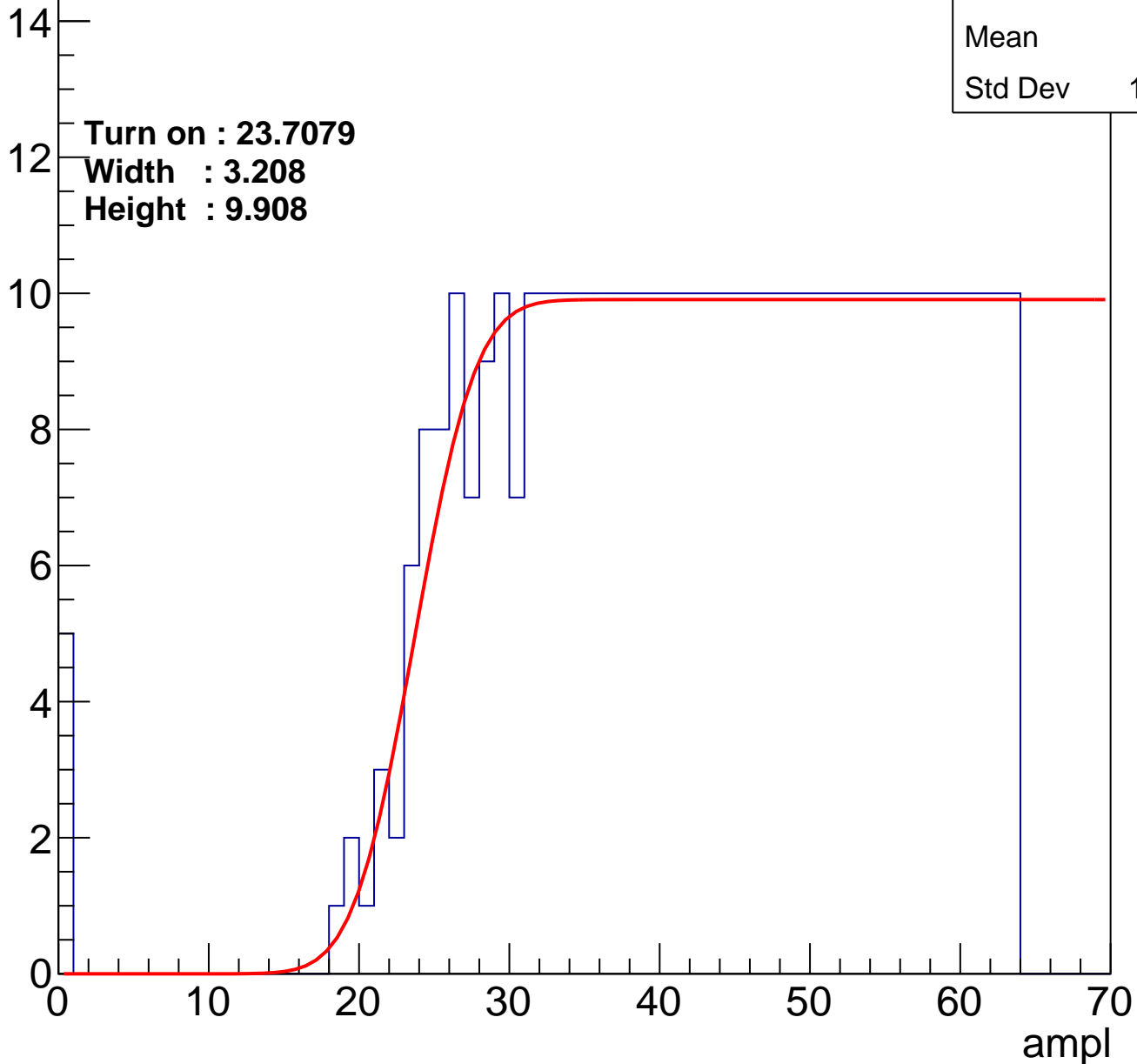
Entries	409
Mean	42.6
Std Dev	12.78

Turn on : 23.7079

Width : 3.208

Height : 9.908

Entry



# B1L103S, U14-ch115

calib\_packv5\_042523\_0143.root, FC#7, port C2

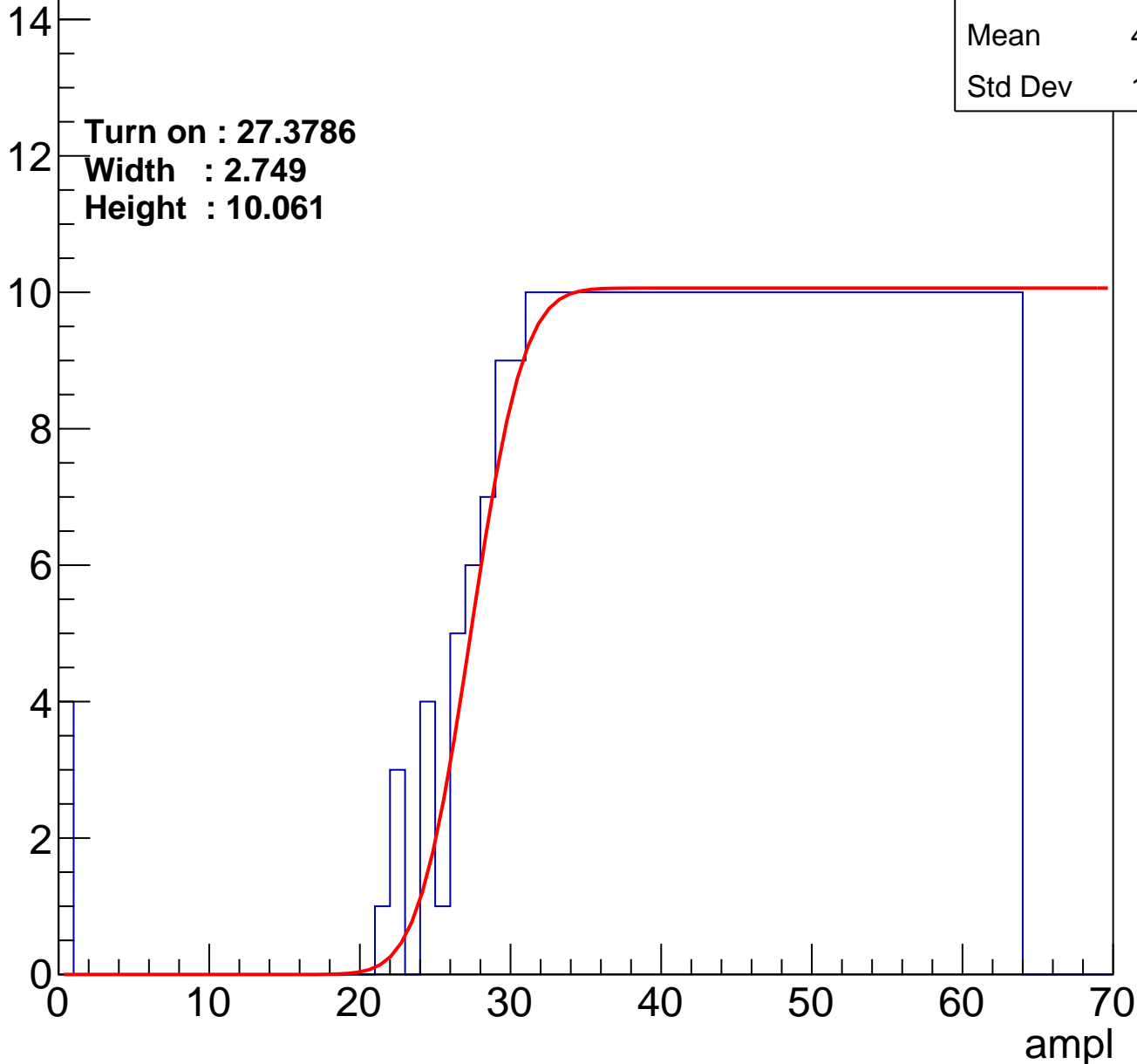
Entries	379
Mean	44.16
Std Dev	11.88

Turn on : 27.3786

Width : 2.749

Height : 10.061

Entry



# B1L103S, U14-ch116

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	386
Mean	43.82
Std Dev	11.98

Turn on : 25.8249

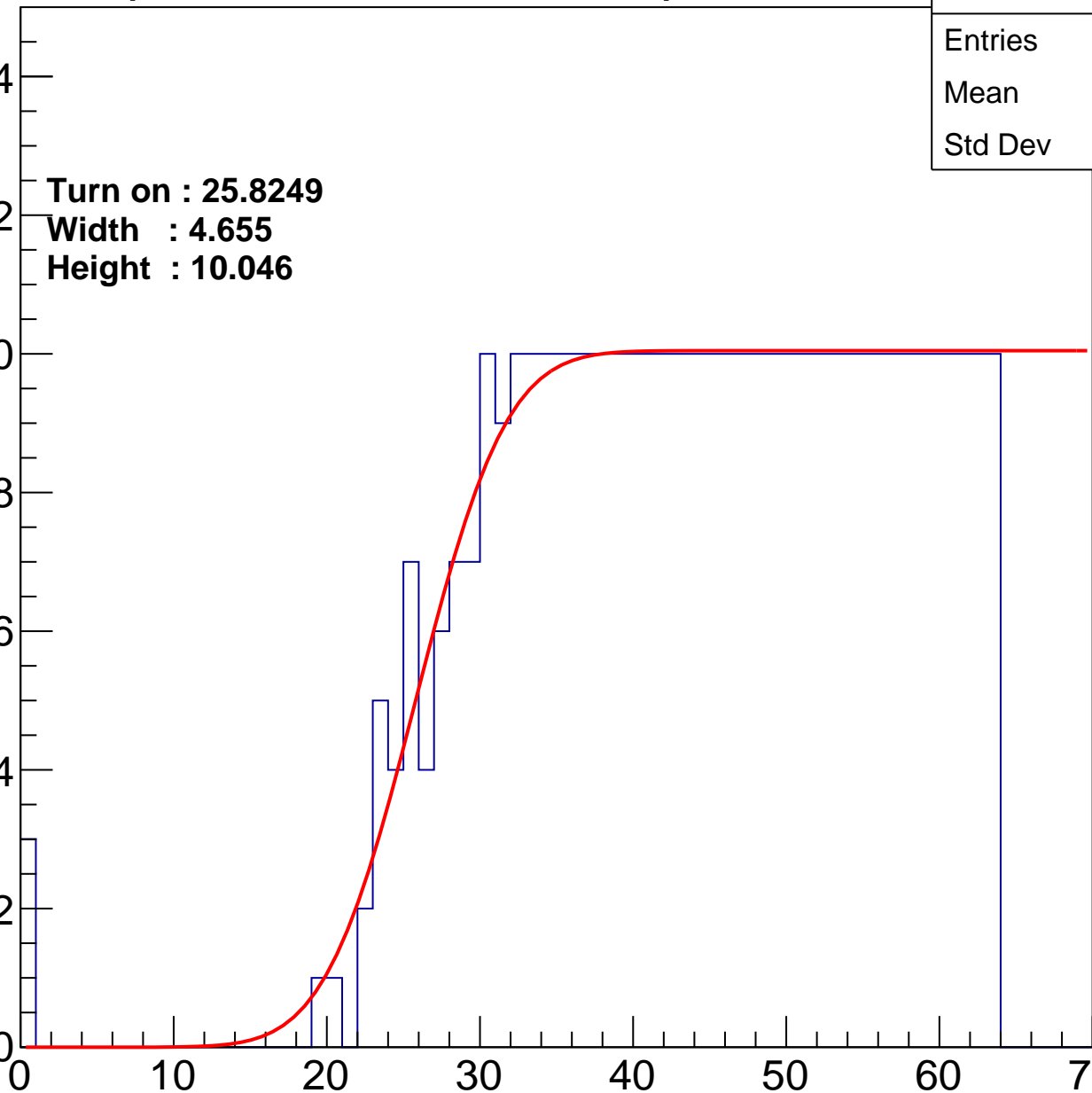
Width : 4.655

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch117

calib\_packv5\_042523\_0143.root, FC#7, port C2

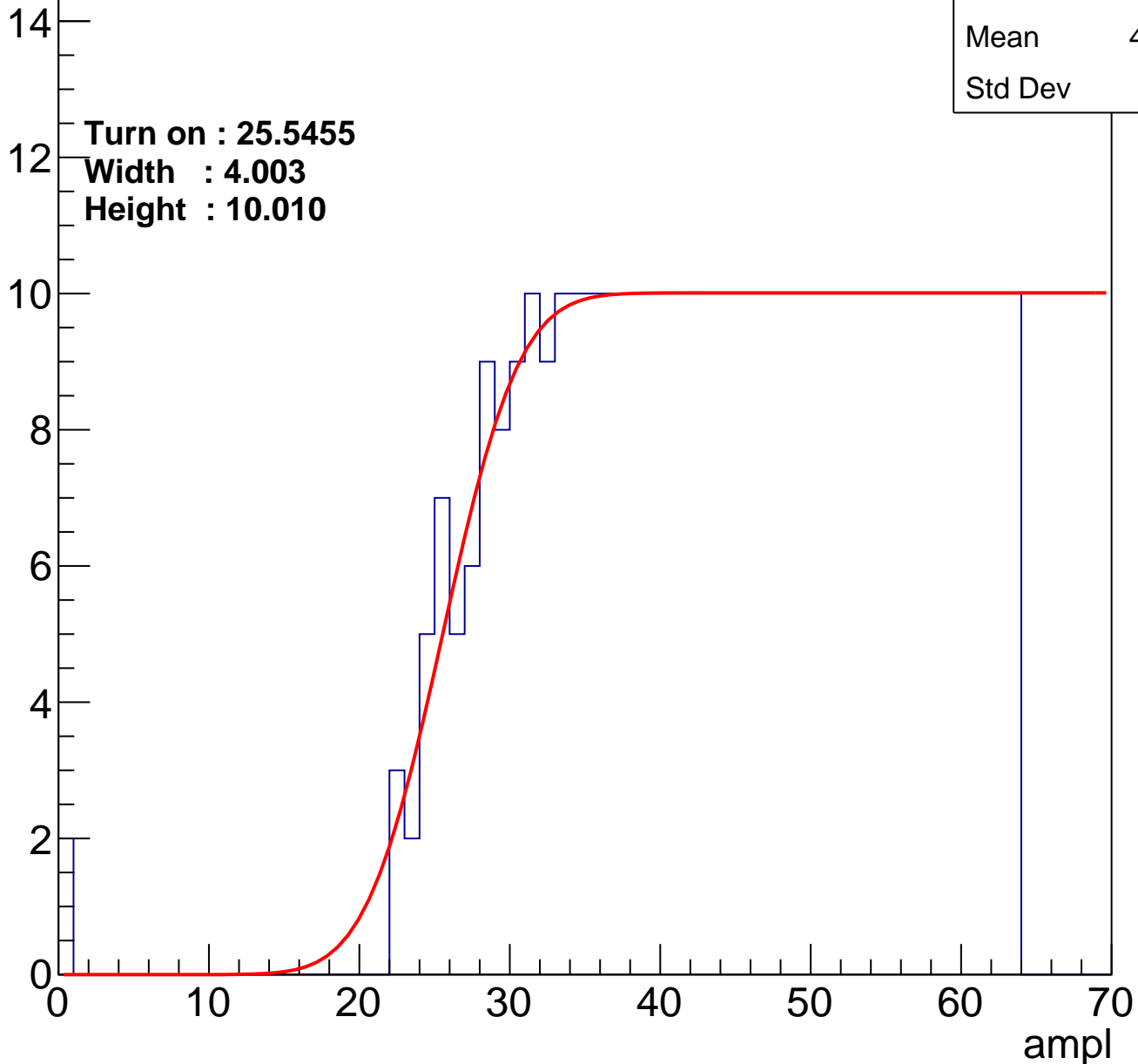
Entries	385
Mean	43.98
Std Dev	11.7

Turn on : 25.5455

Width : 4.003

Height : 10.010

Entry



# B1L103S, U14-ch118

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	401
Mean	42.99
Std Dev	12.61

Turn on : 25.1577

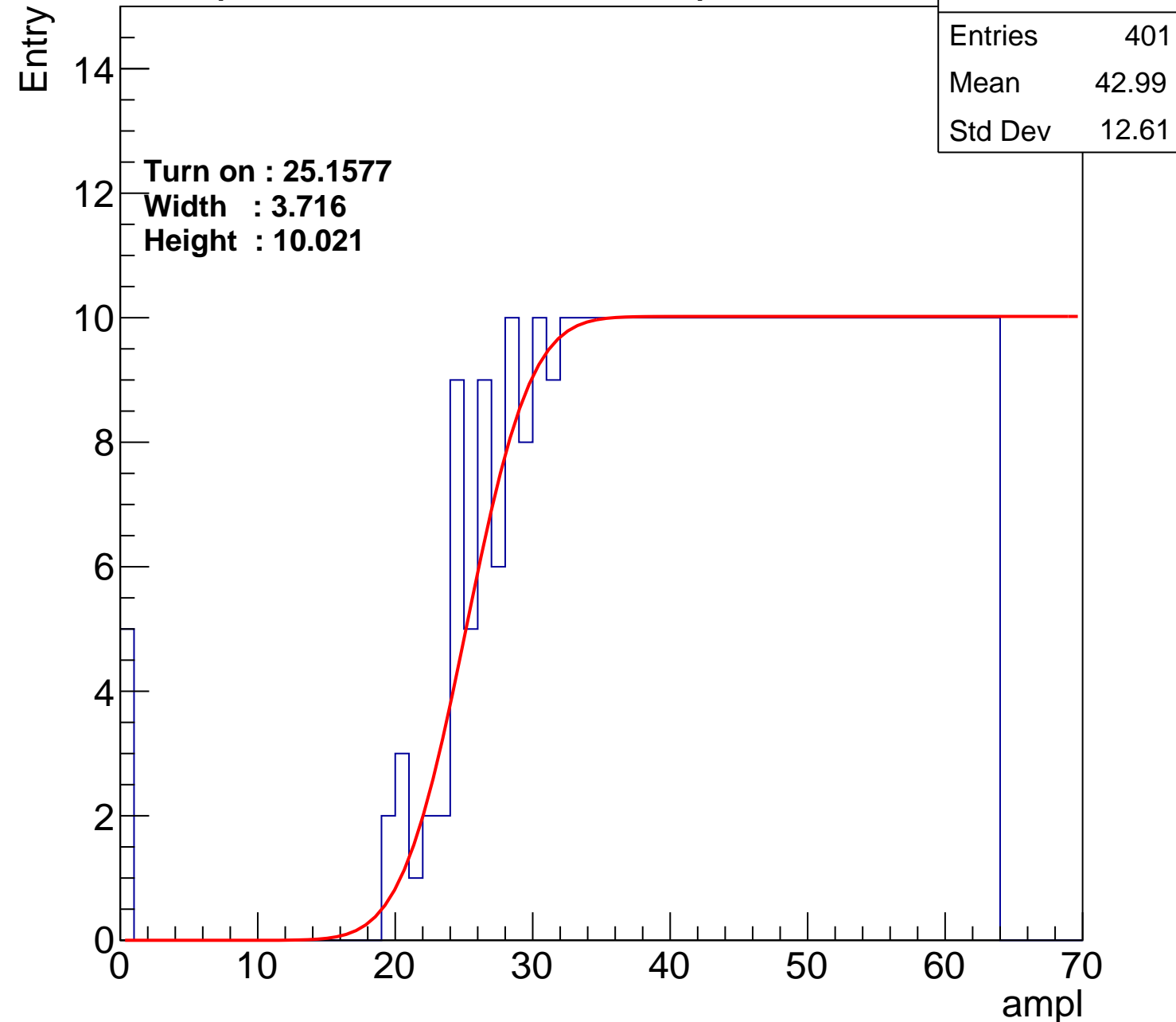
Width : 3.716

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L103S, U14-ch119

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.3339

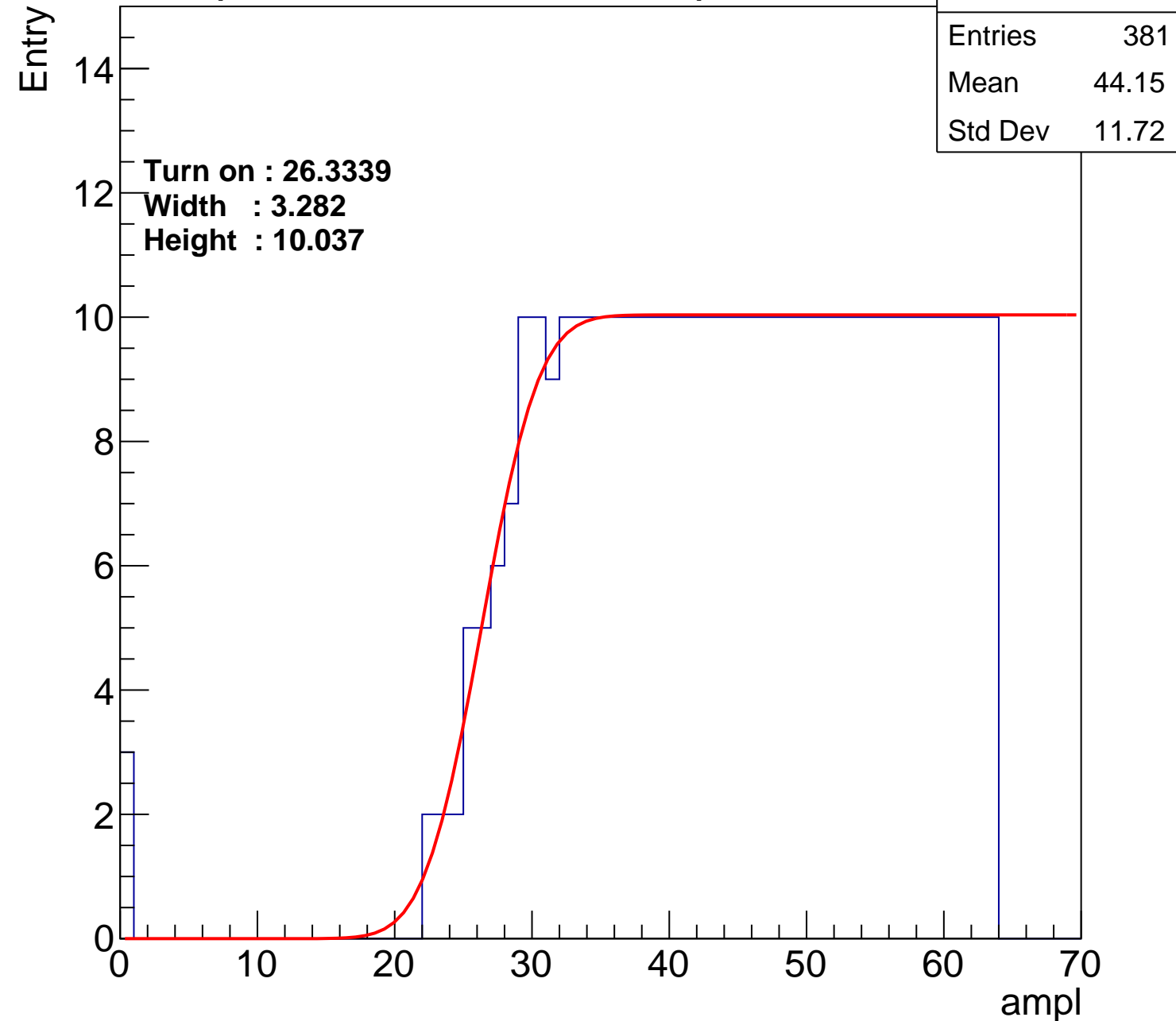
Width : 3.282

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch120

calib\_packv5\_042523\_0143.root, FC#7, port C2

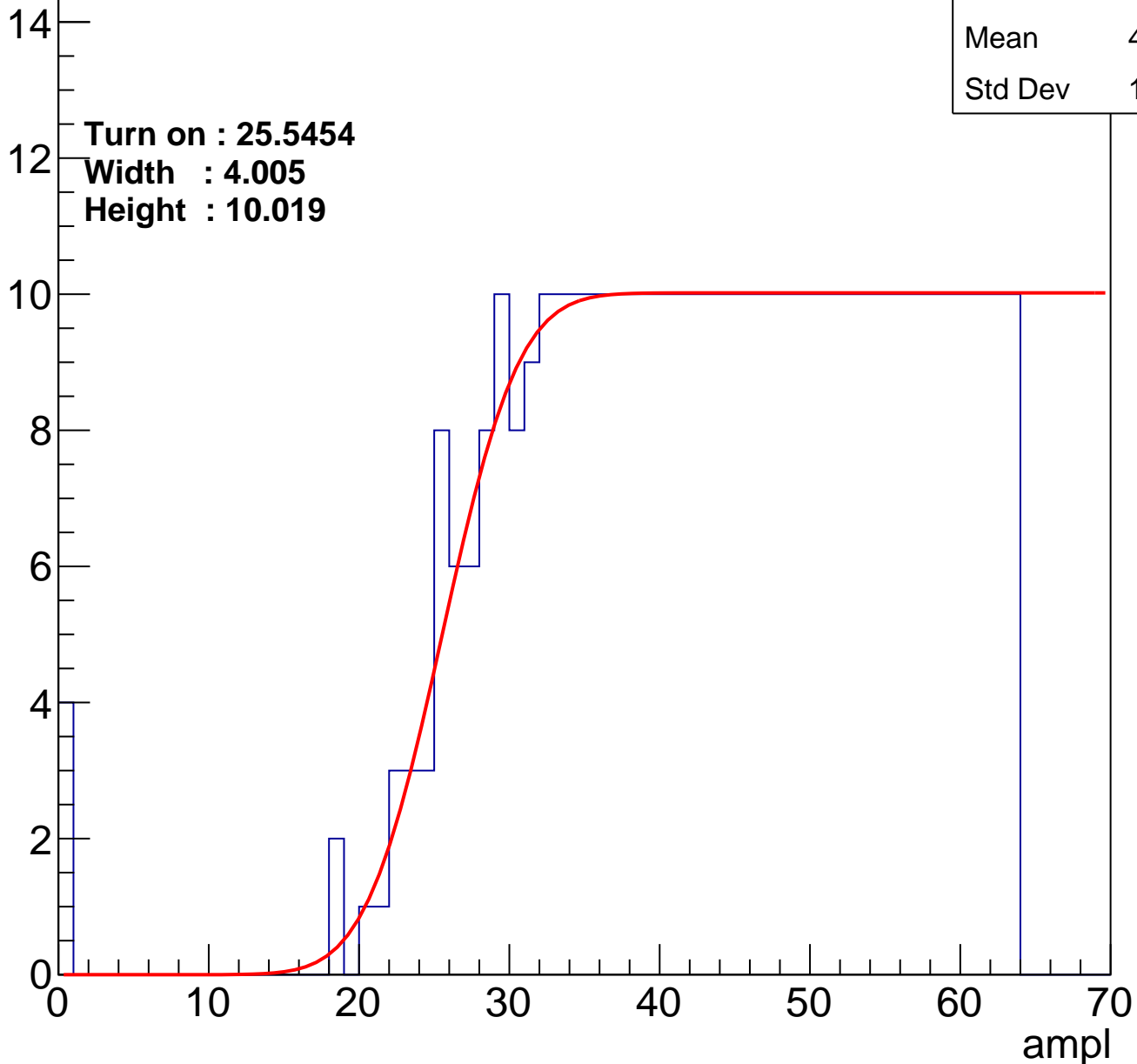
Entries	392
Mean	43.46
Std Dev	12.29

Turn on : 25.5454

Width : 4.005

Height : 10.019

Entry



# B1L103S, U14-ch121

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	392
Mean	43.45
Std Dev	12.36

**Turn on : 25.6144**

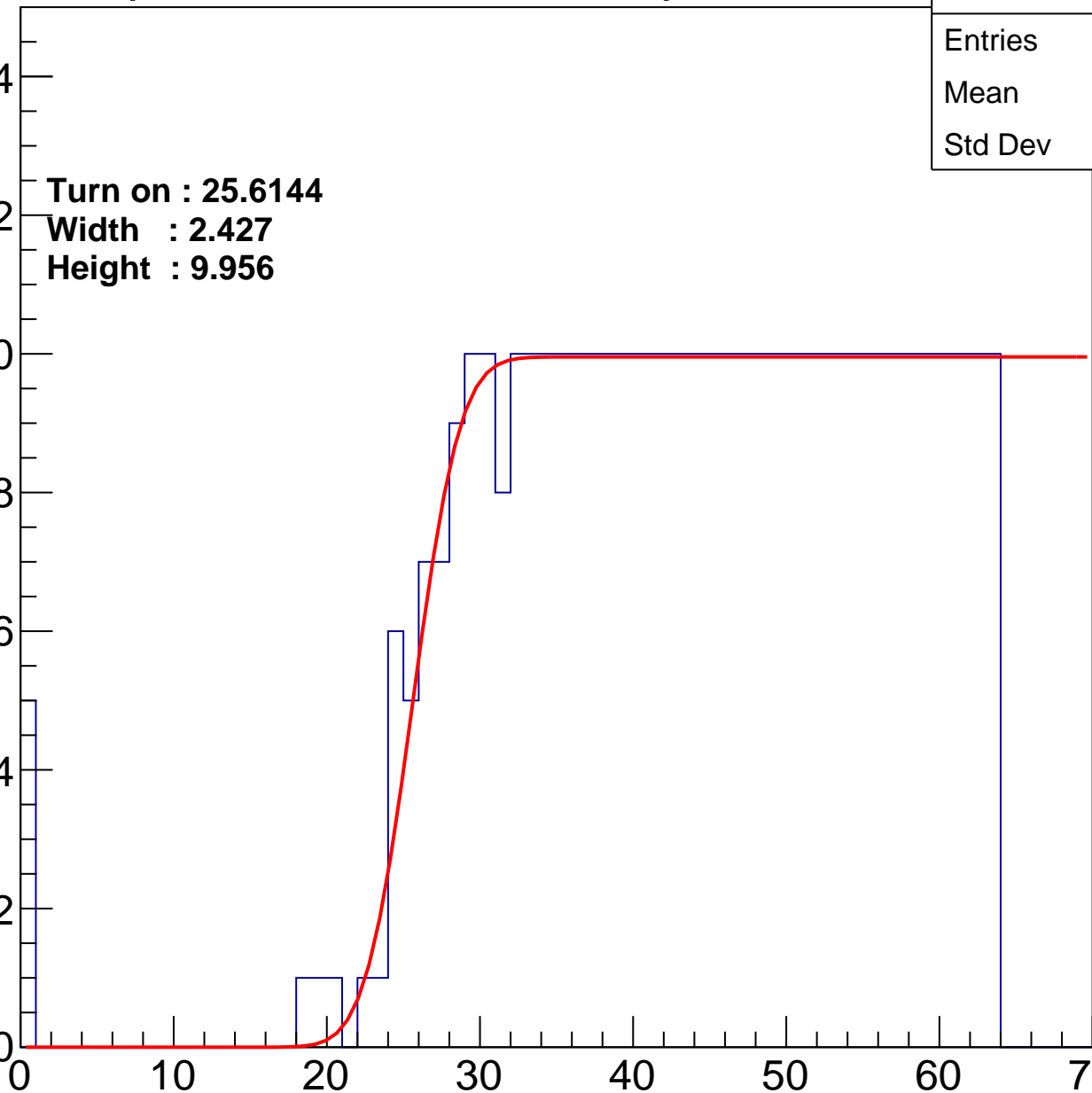
**Width : 2.427**

**Height : 9.956**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch122

calib\_packv5\_042523\_0143.root, FC#7, port C2

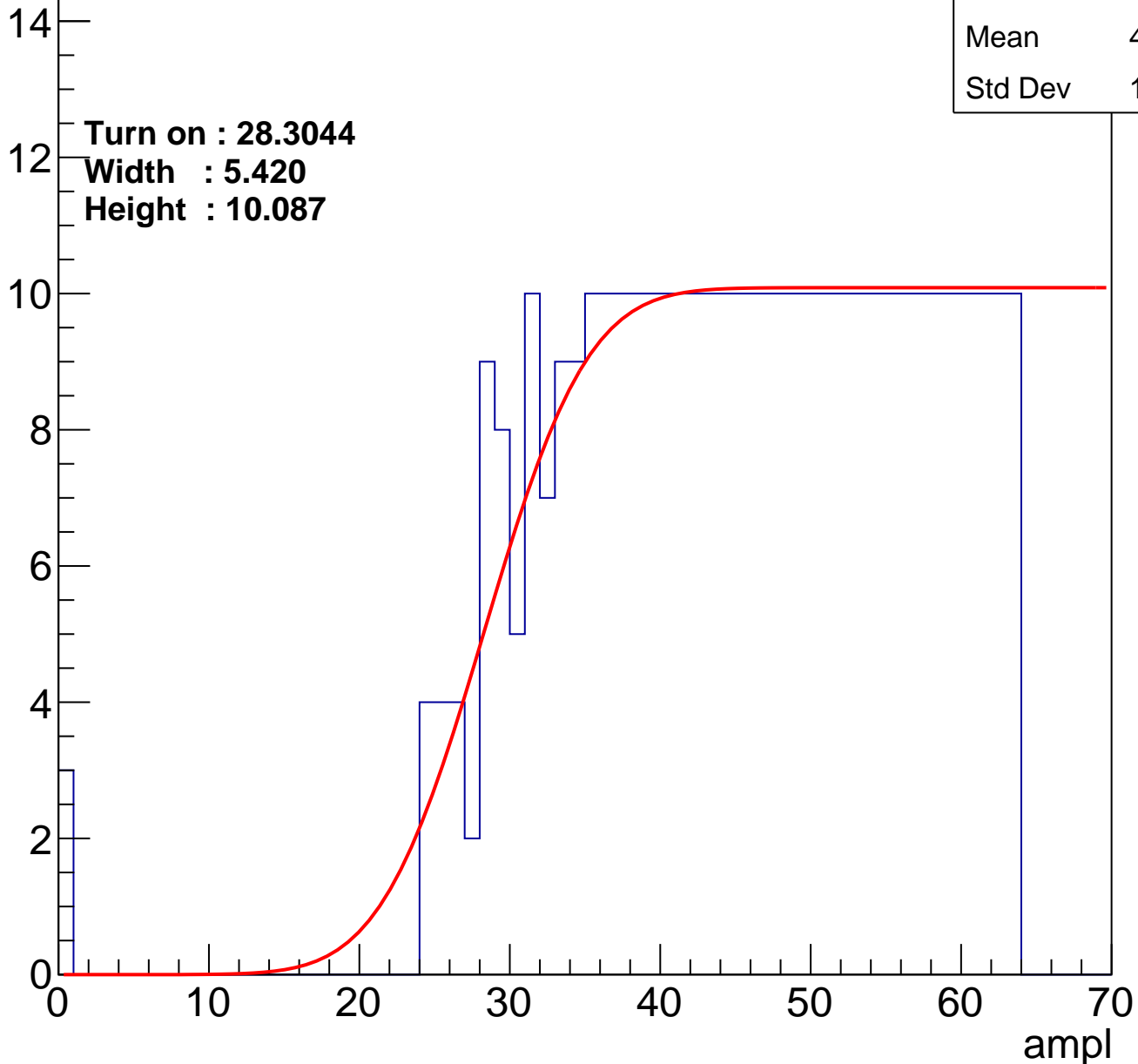
Entries	364
Mean	44.88
Std Dev	11.46

Turn on : 28.3044

Width : 5.420

Height : 10.087

Entry



# B1L103S, U14-ch123

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	391
Mean	43.71
Std Dev	11.81

Turn on : 25.0644

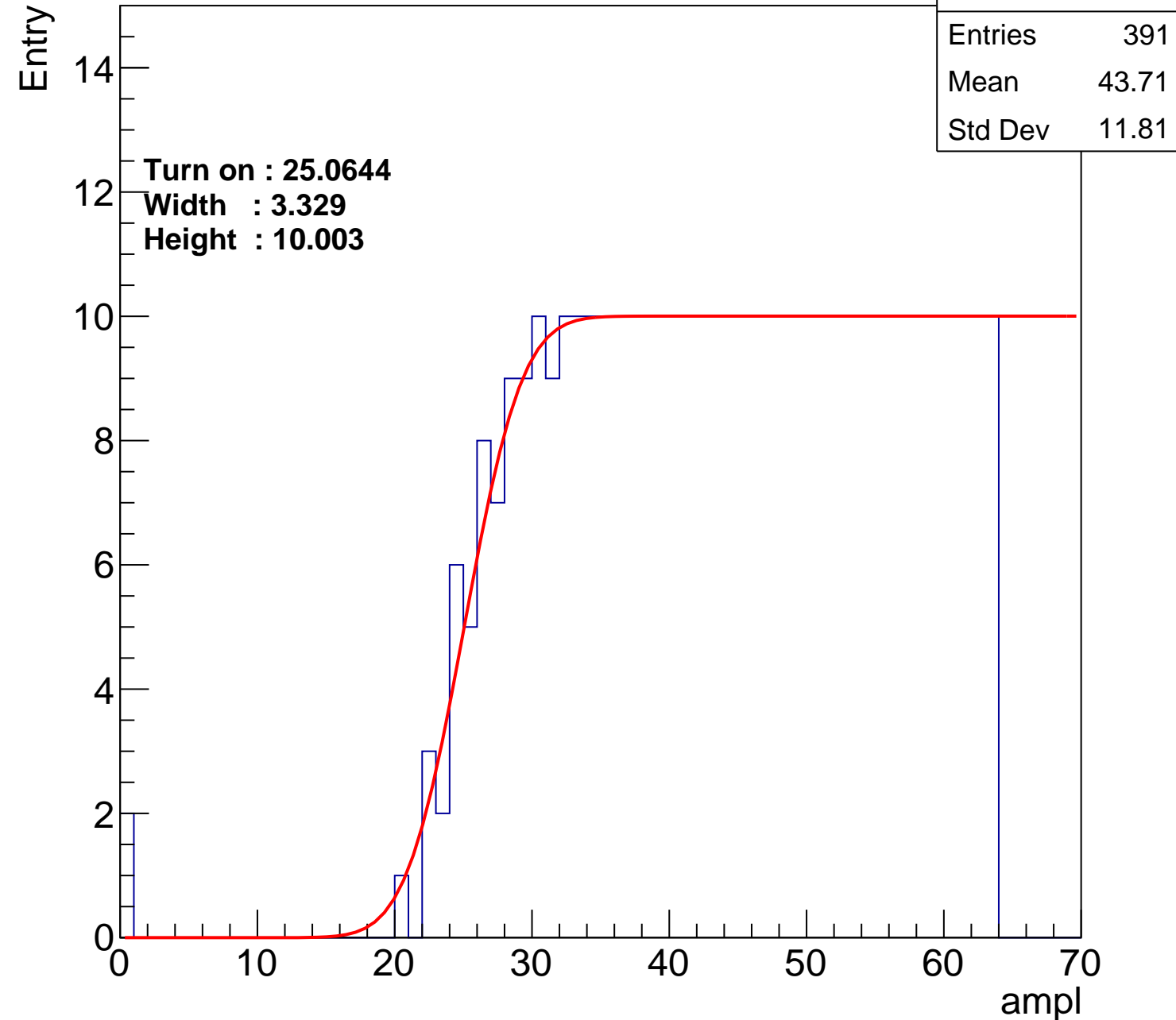
Width : 3.329

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch124

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	400
Mean	43.16
Std Dev	12.28

Turn on : 24.8590

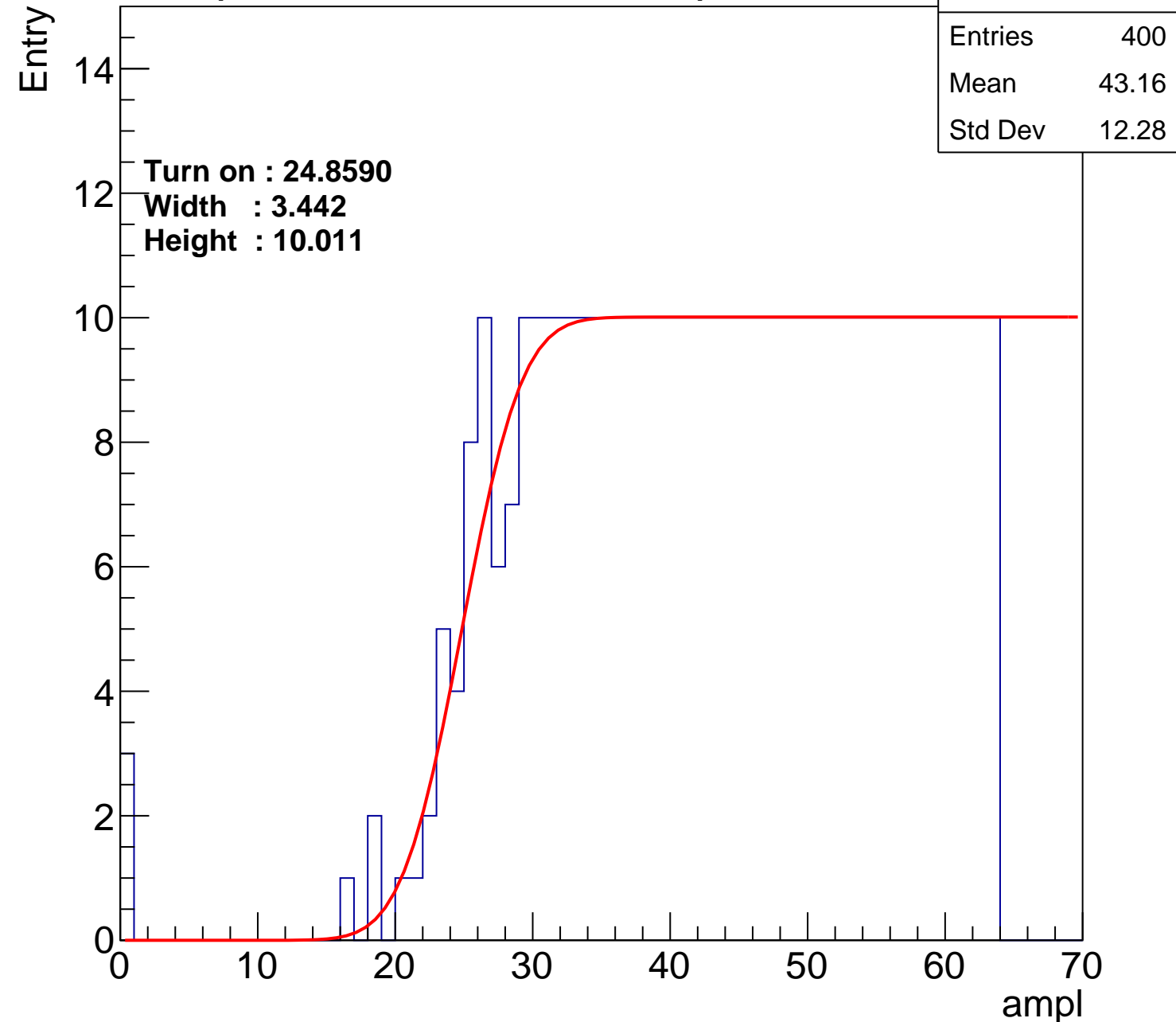
Width : 3.442

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch125

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	402
Mean	43.06
Std Dev	12.39

Turn on : 25.1939

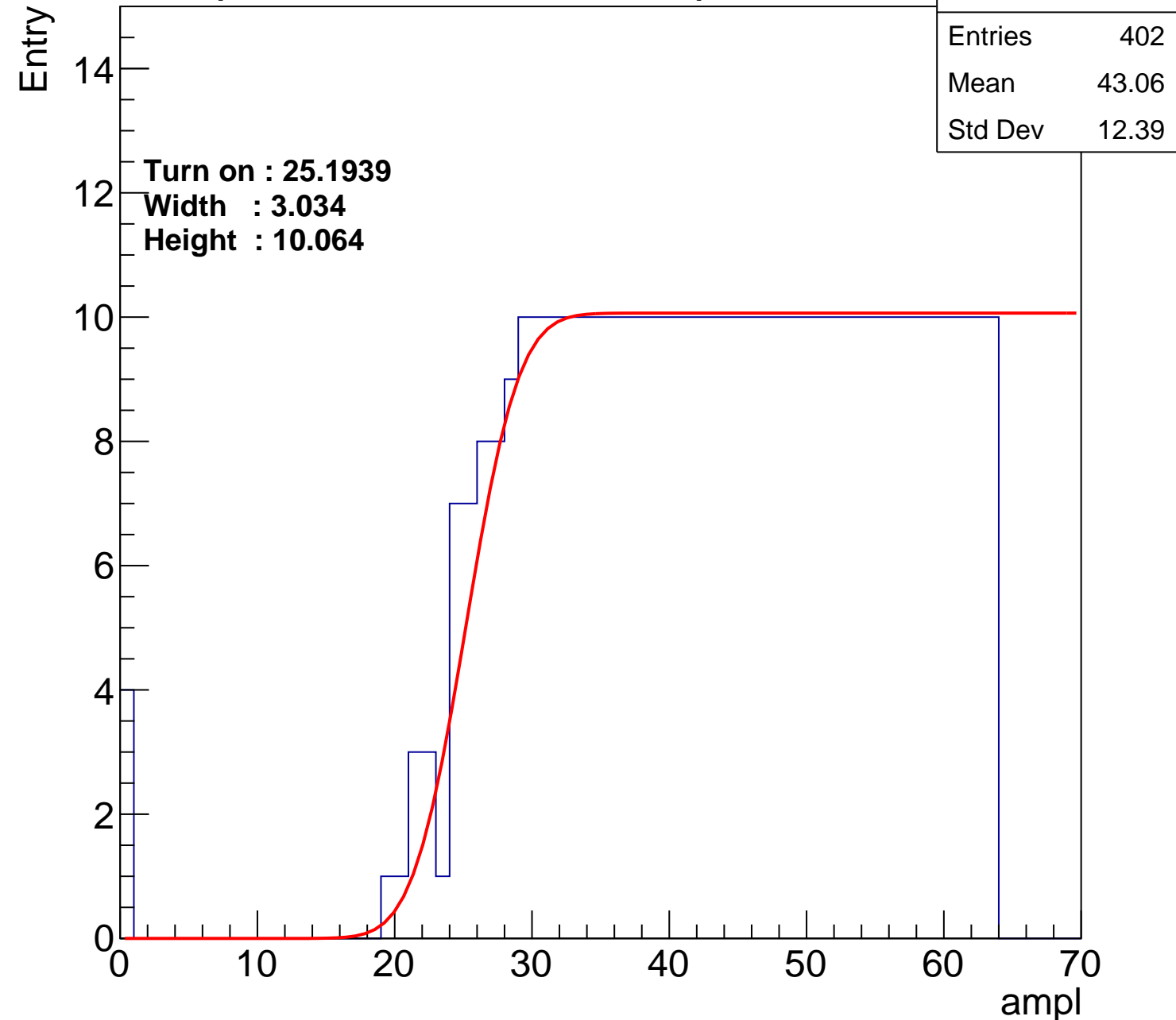
Width : 3.034

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch126

calib\_packv5\_042523\_0143.root, FC#7, port C2

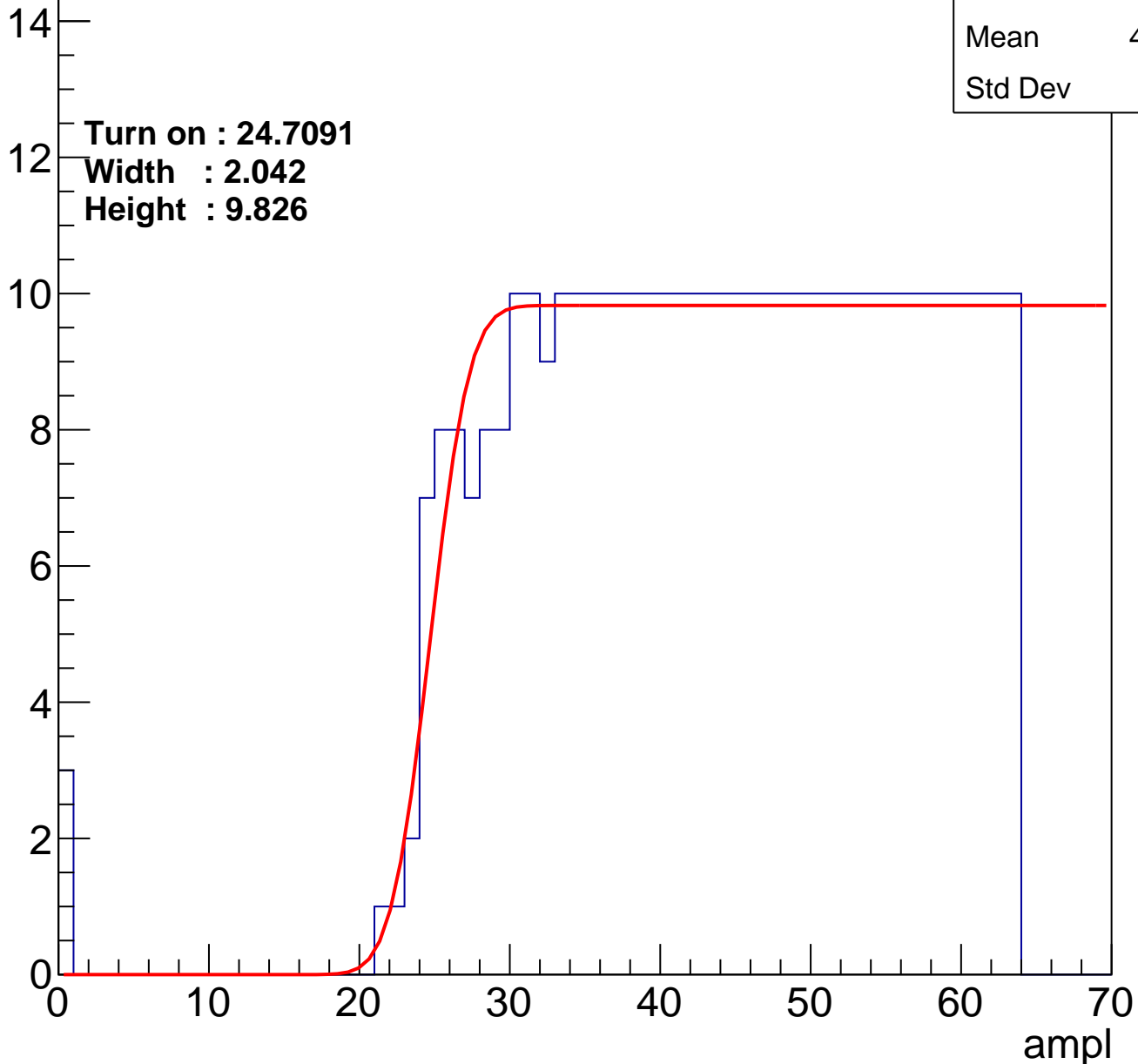
Entries	392
Mean	43.59
Std Dev	12

Turn on : 24.7091

Width : 2.042

Height : 9.826

Entry





# B1L103S, U14-ch127

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	376
Mean	44.4
Std Dev	11.6

Turn on : 27.1621

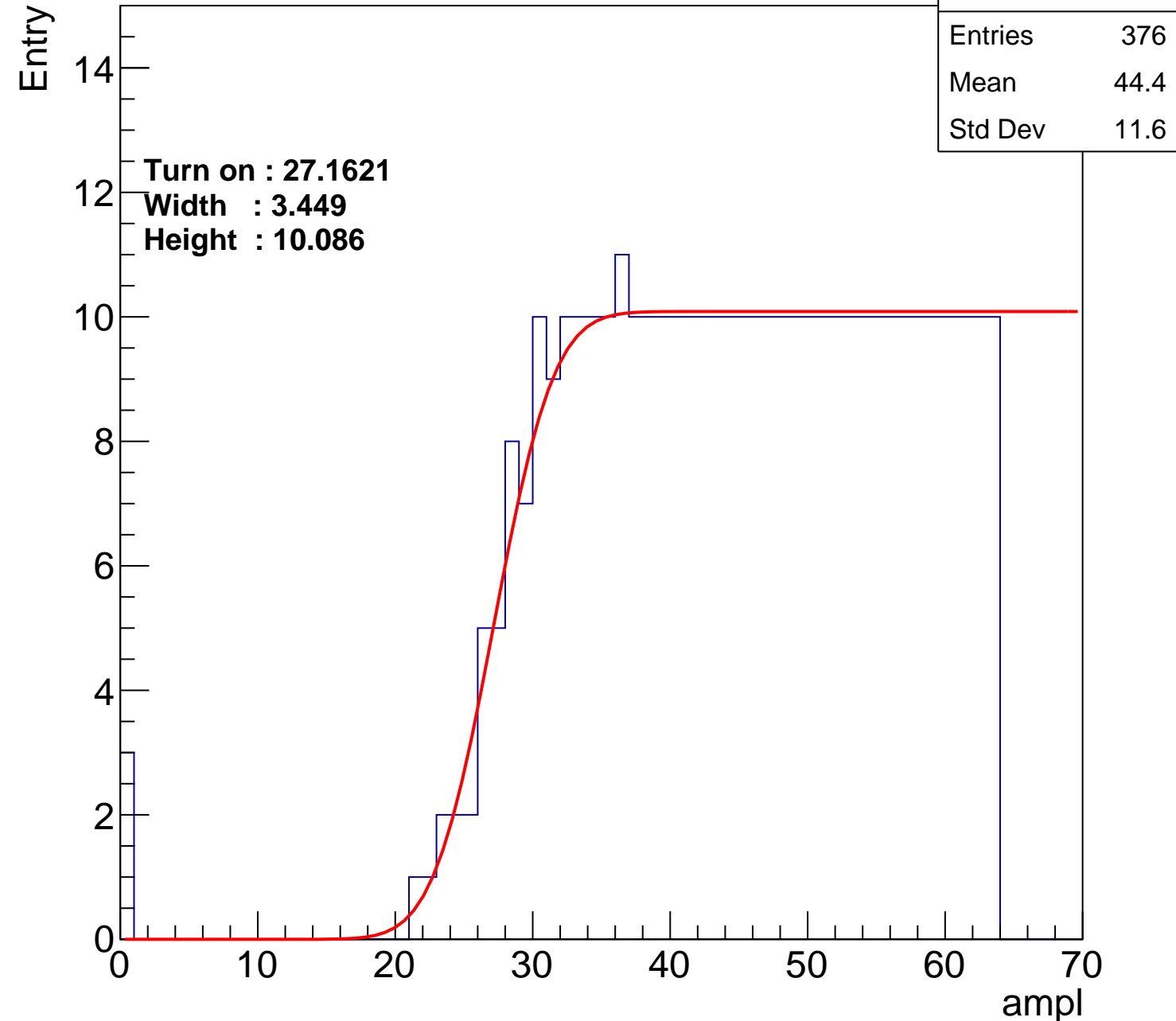
Width : 3.449

Height : 10.086

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U14-ch127

calib\_packv5\_042523\_0143.root, FC#7, port C2

Entries	376
Mean	44.4
Std Dev	11.6

Turn on : 27.1621

Width : 3.449

Height : 10.086

Entry

