

B1L102S, U26-ch0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44
Std Dev	11.97

Turn on : 26.5213

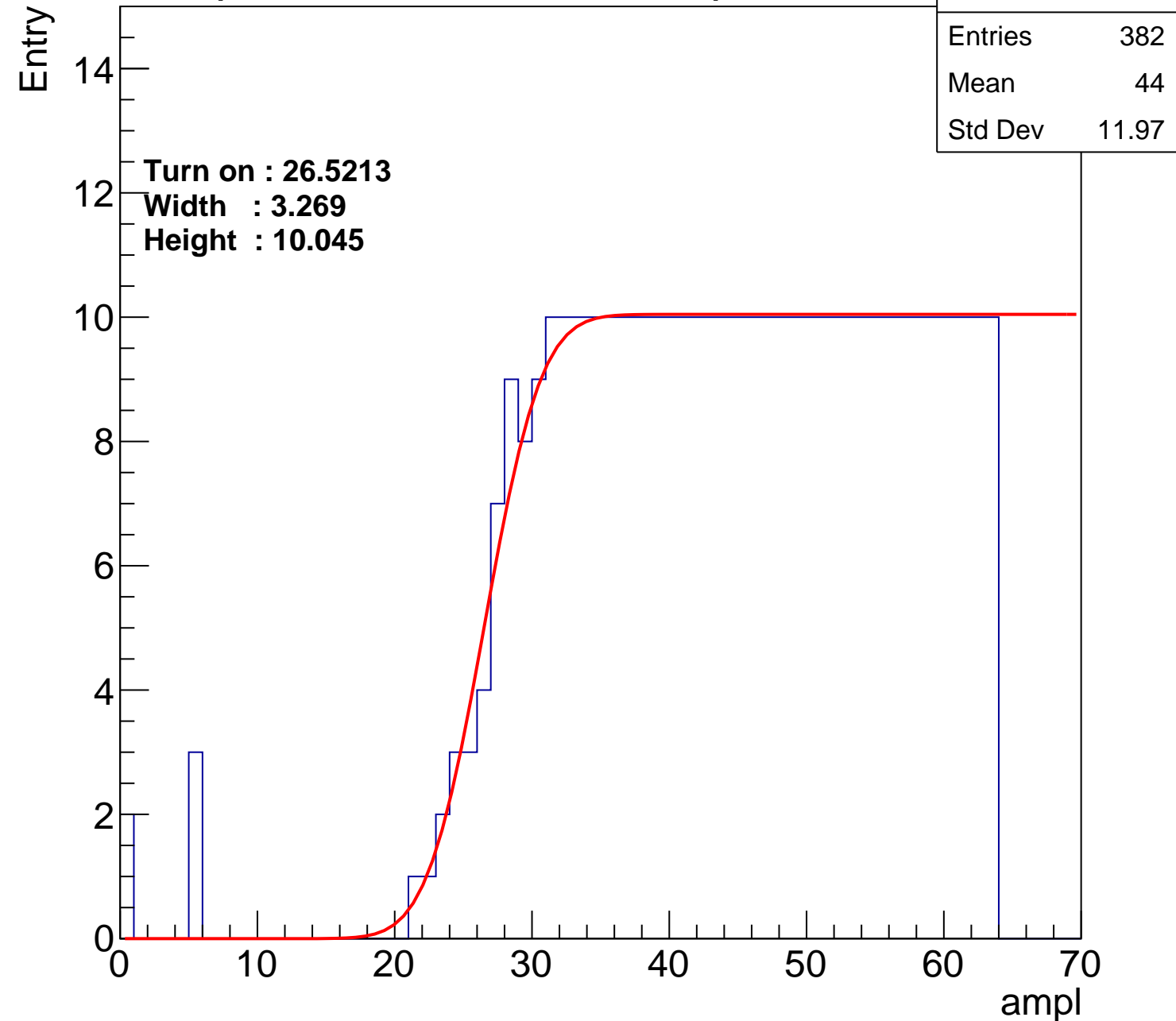
Width : 3.269

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.71
Std Dev	11.46

Turn on : 27.5123

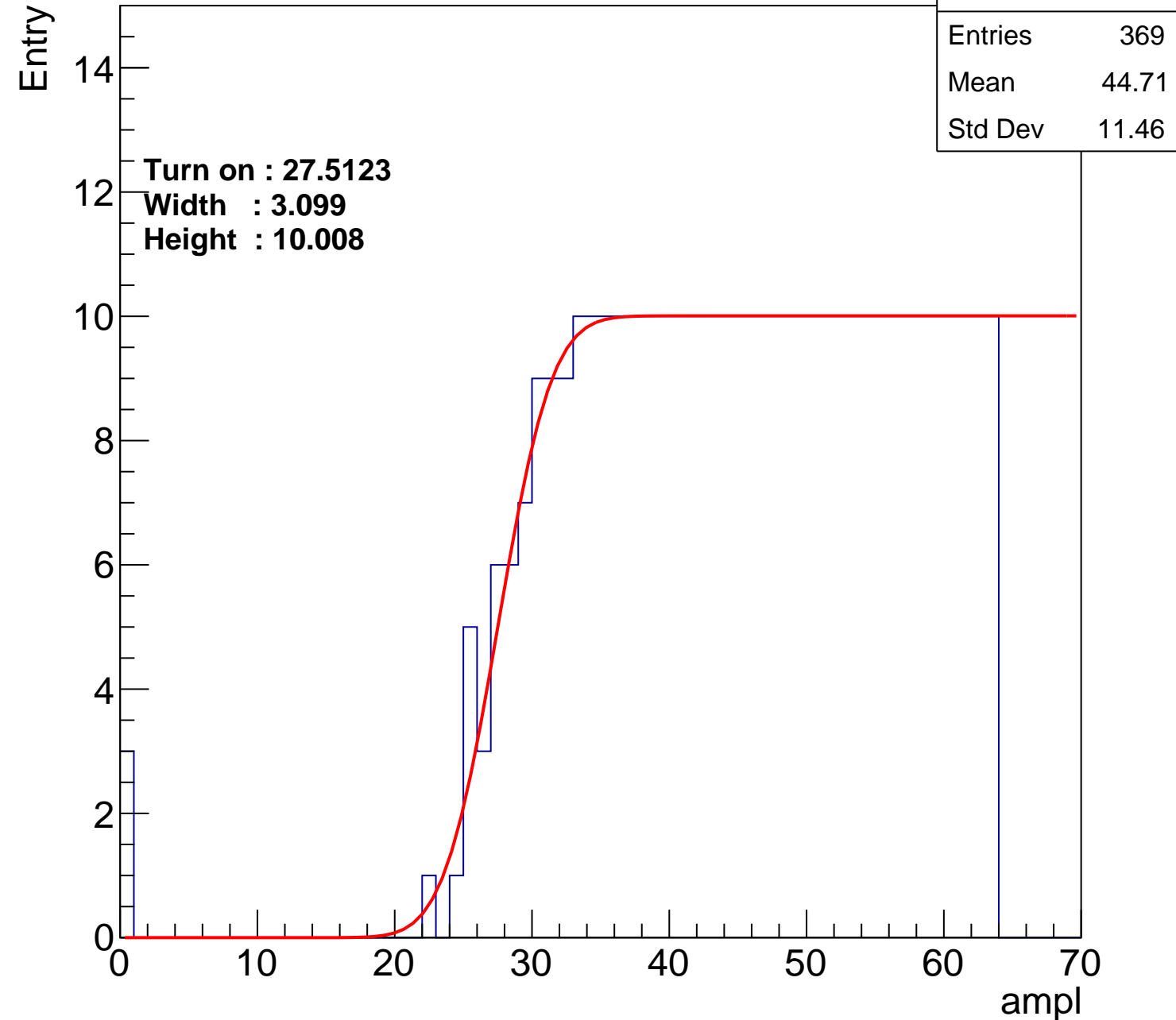
Width : 3.099

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 26.2580

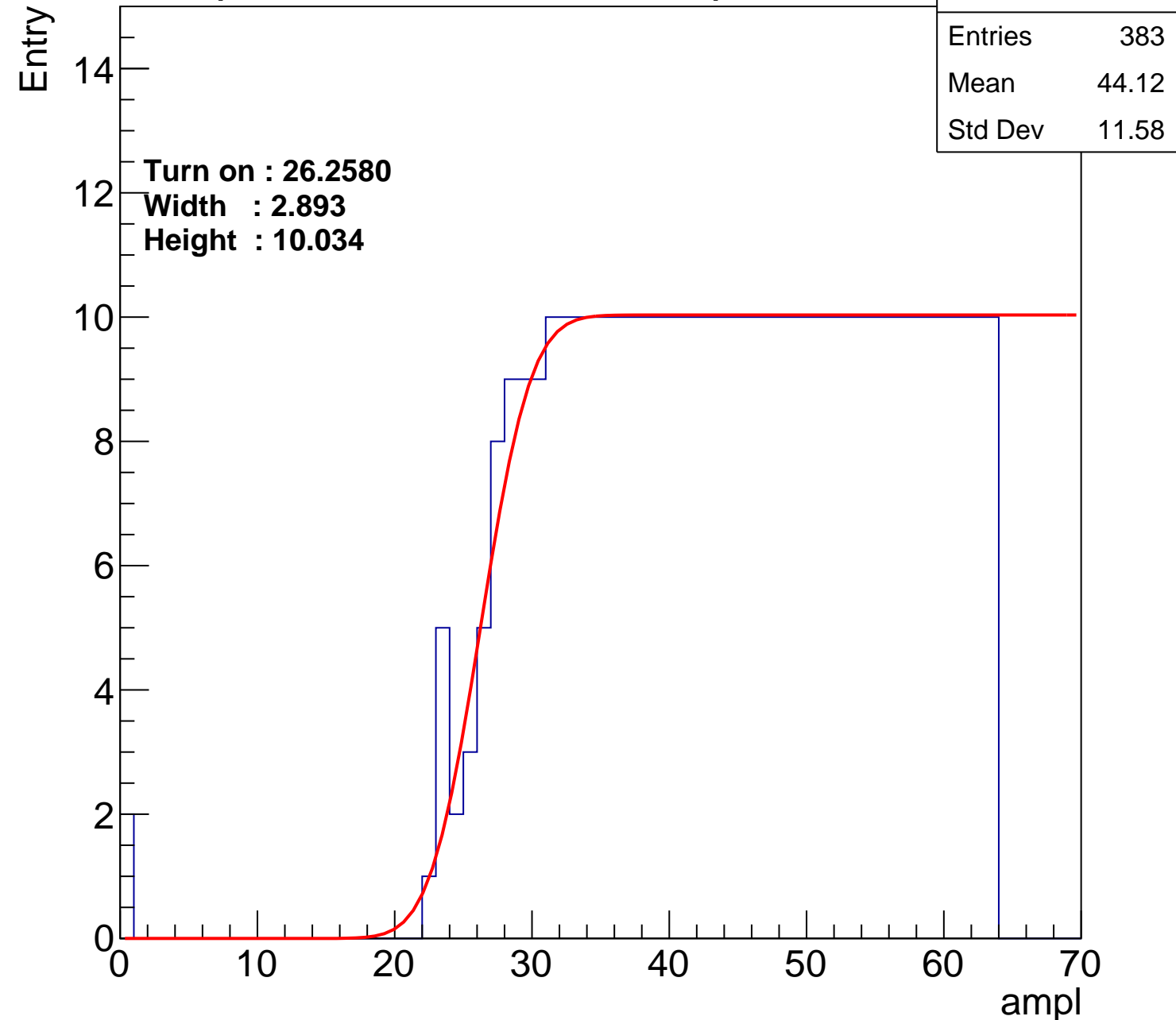
Width : 2.893

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.87
Std Dev	11.03

Turn on : 26.9243

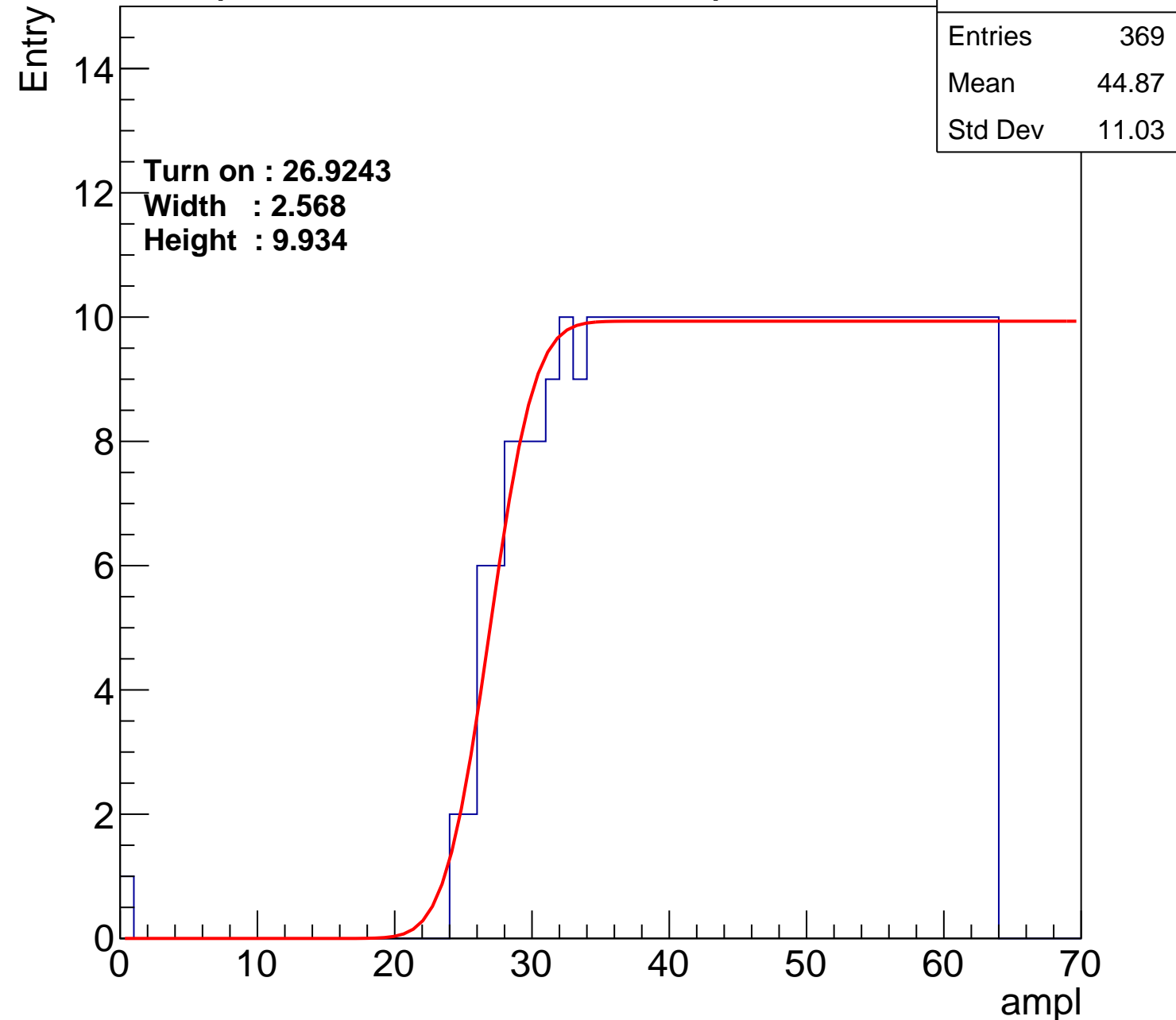
Width : 2.568

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.85
Std Dev	11.42

Turn on : 27.9581

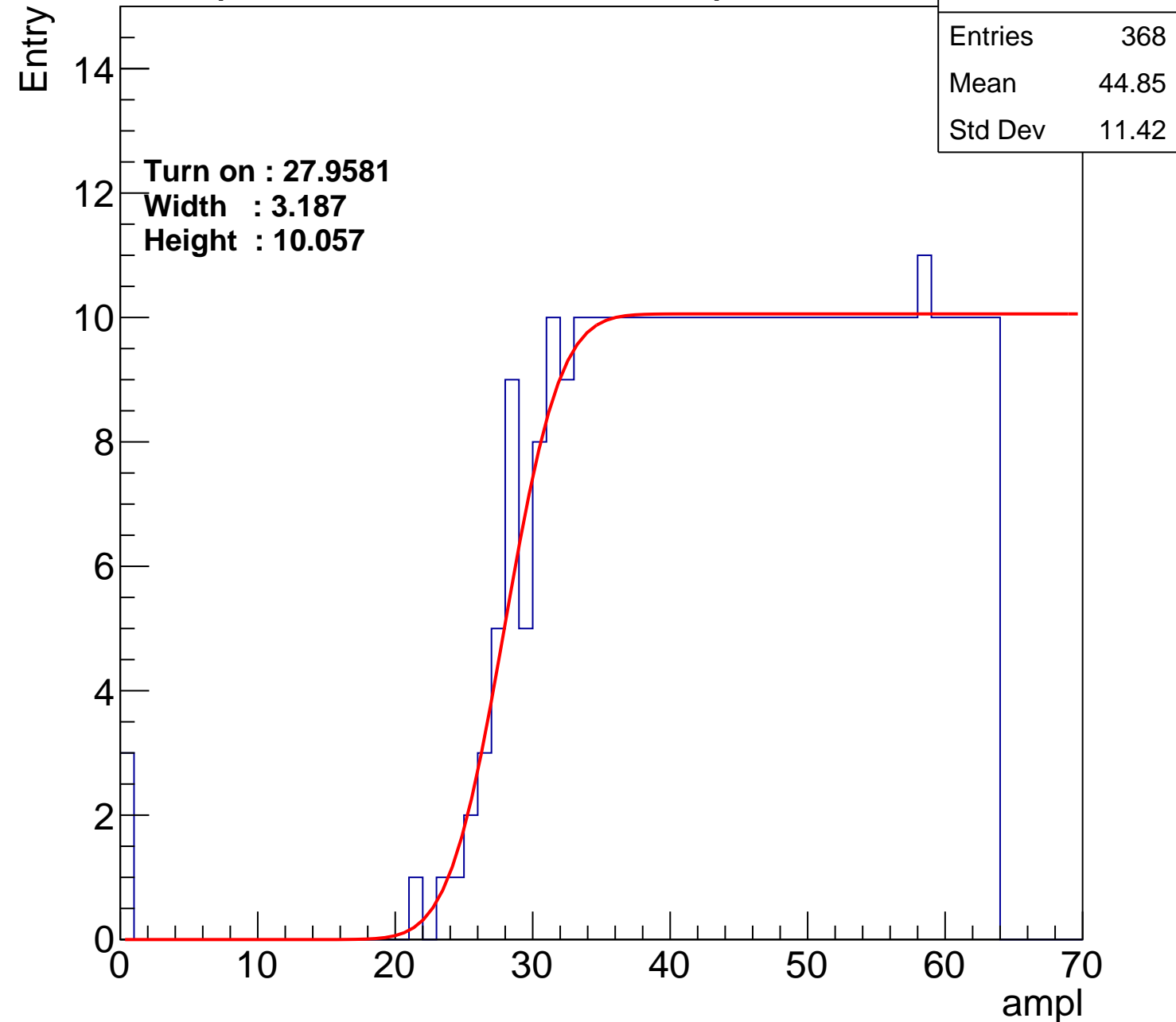
Width : 3.187

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 26.4945

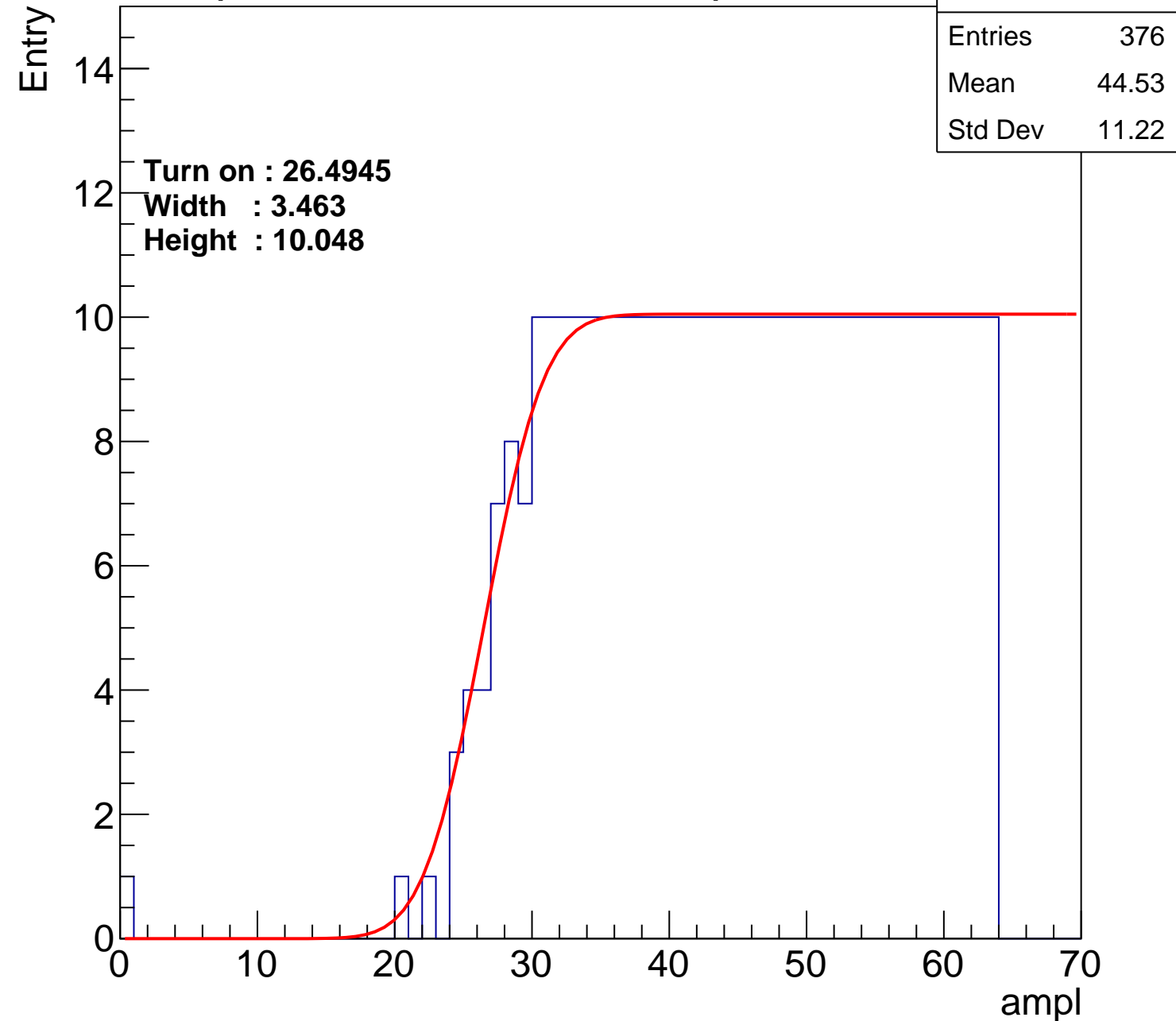
Width : 3.463

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	361
Mean	45.17
Std Dev	11.07

Turn on : 28.3598

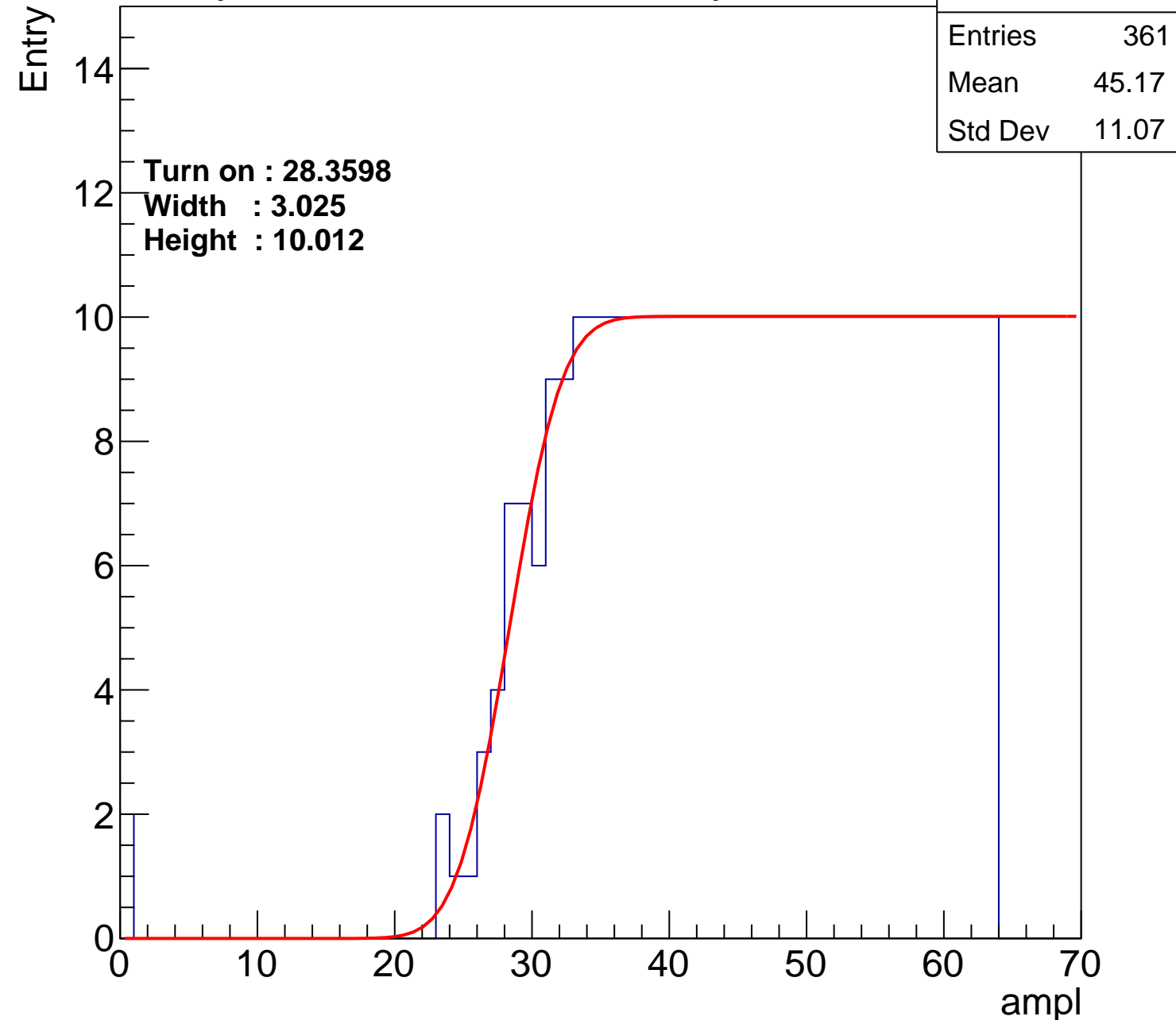
Width : 3.025

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch7

calib_packv5_042523_0143.root, FC#11, port A2

Entries	359
Mean	45.4
Std Dev	10.72

Turn on : 28.7454

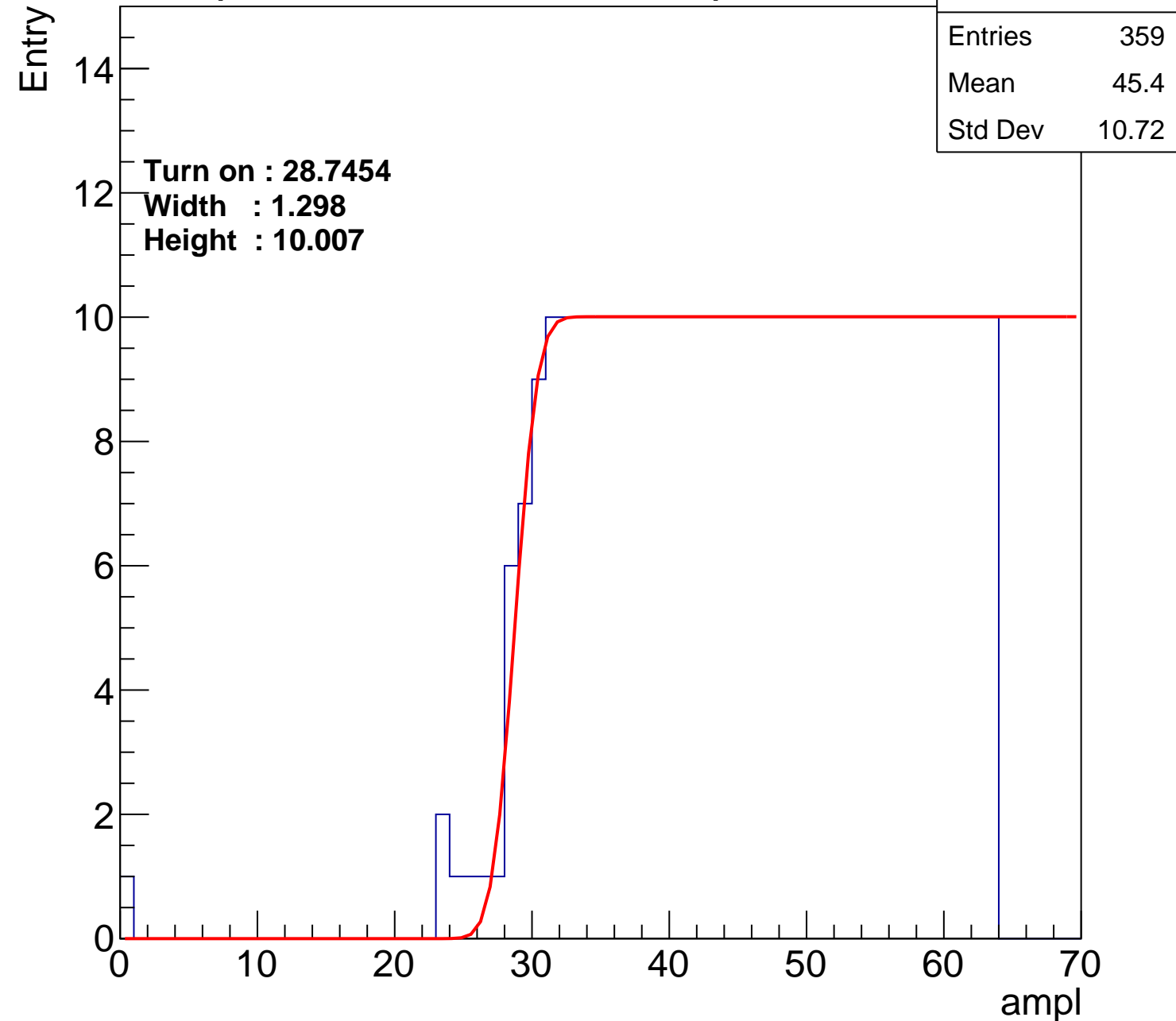
Width : 1.298

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch8

calib_packv5_042523_0143.root, FC#11, port A2

Entries	360
Mean	45.29
Std Dev	10.93

Turn on : 28.5791

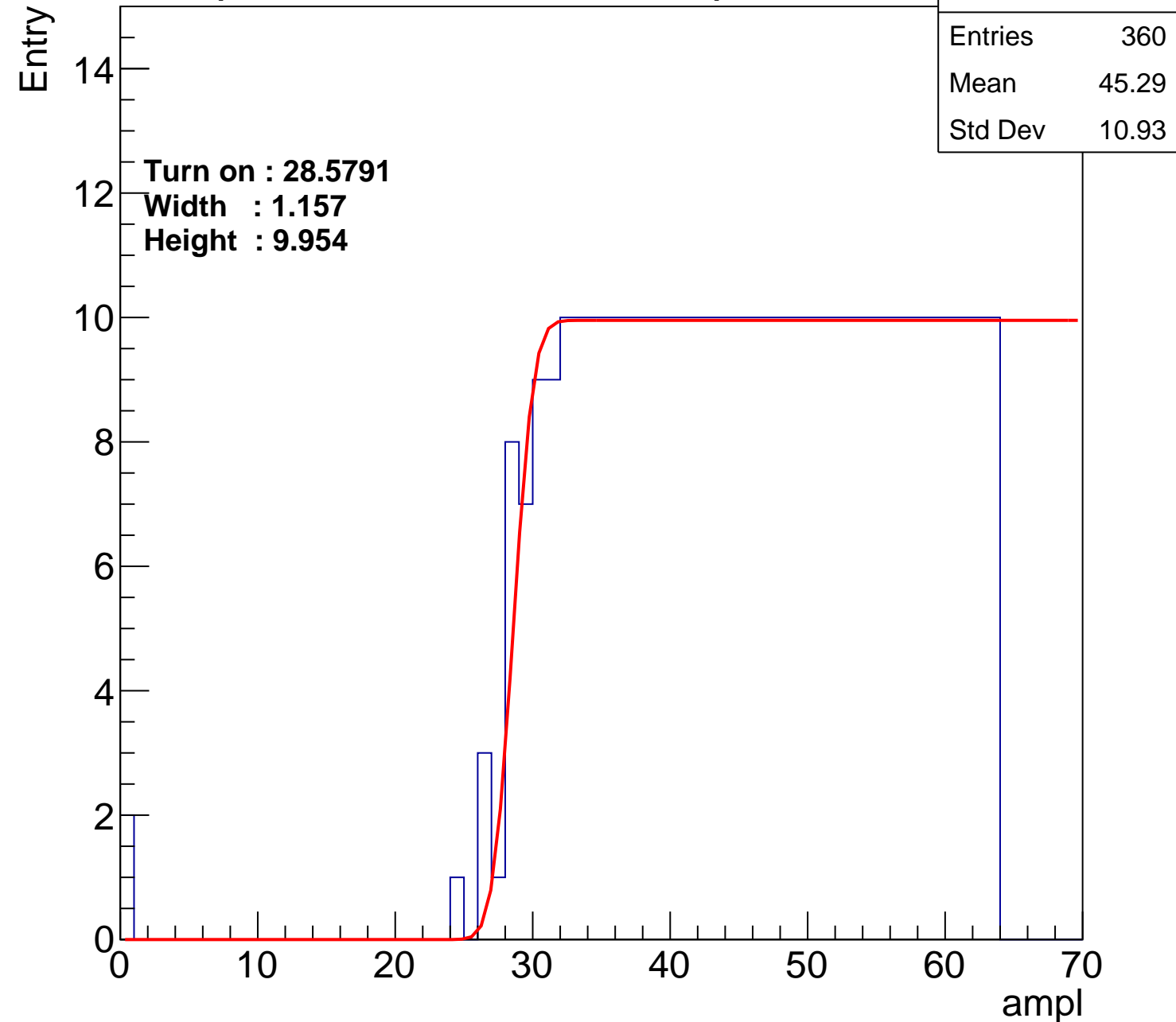
Width : 1.157

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch9

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.33
Std Dev	11.33

Turn on : 26.2079

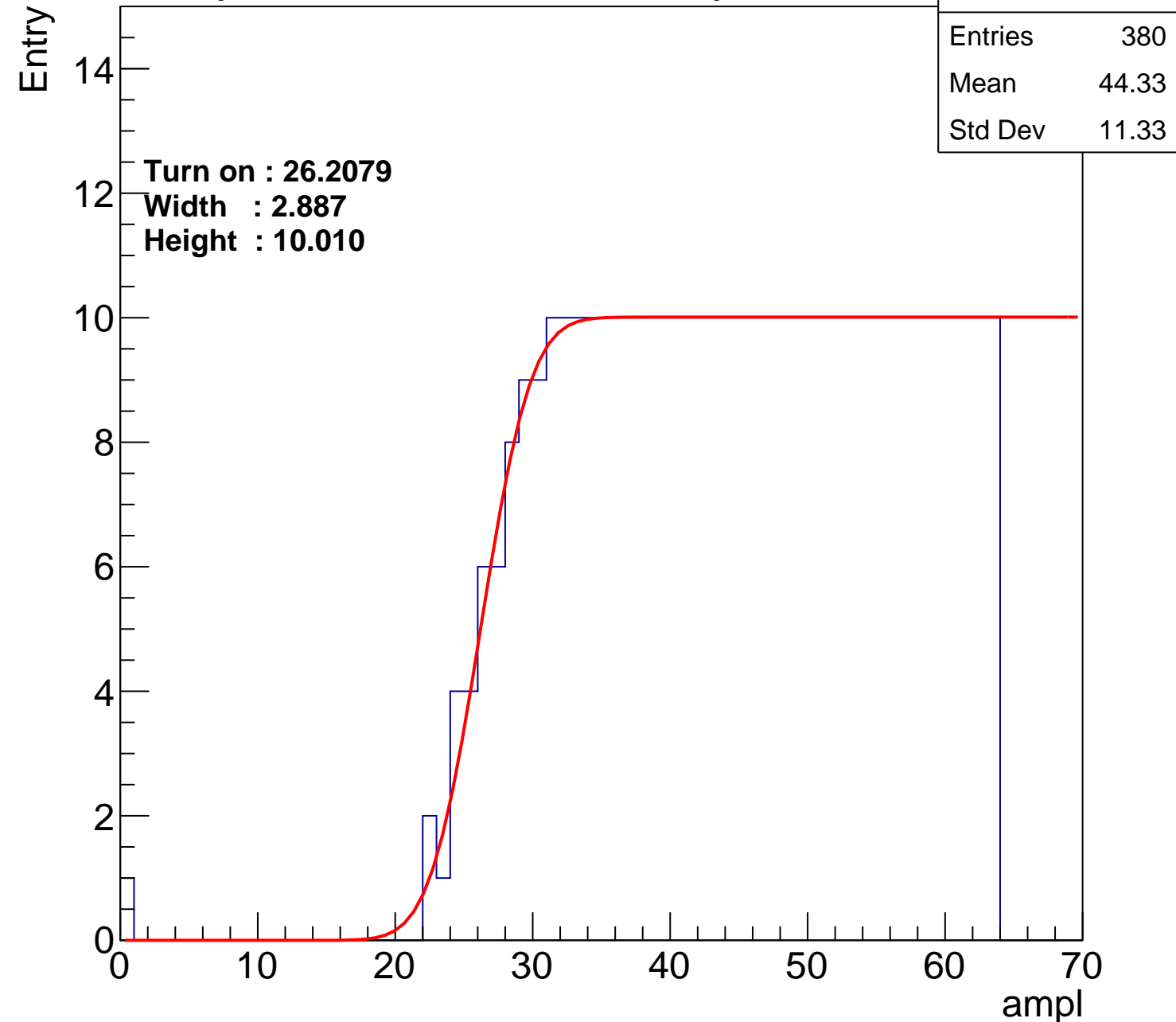
Width : 2.887

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch10

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	43.25
Std Dev	11.92

Turn on : 24.1282

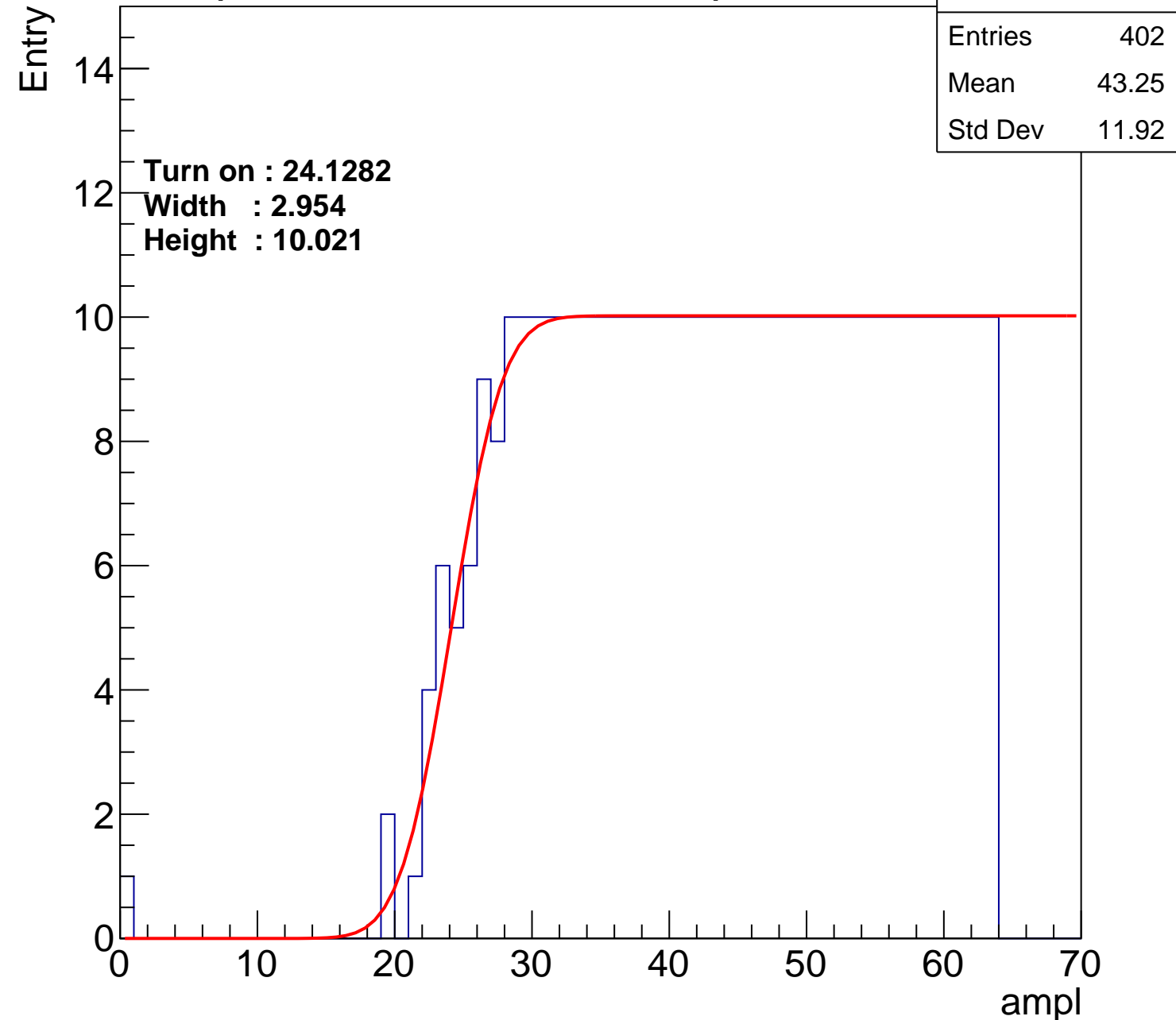
Width : 2.954

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch11

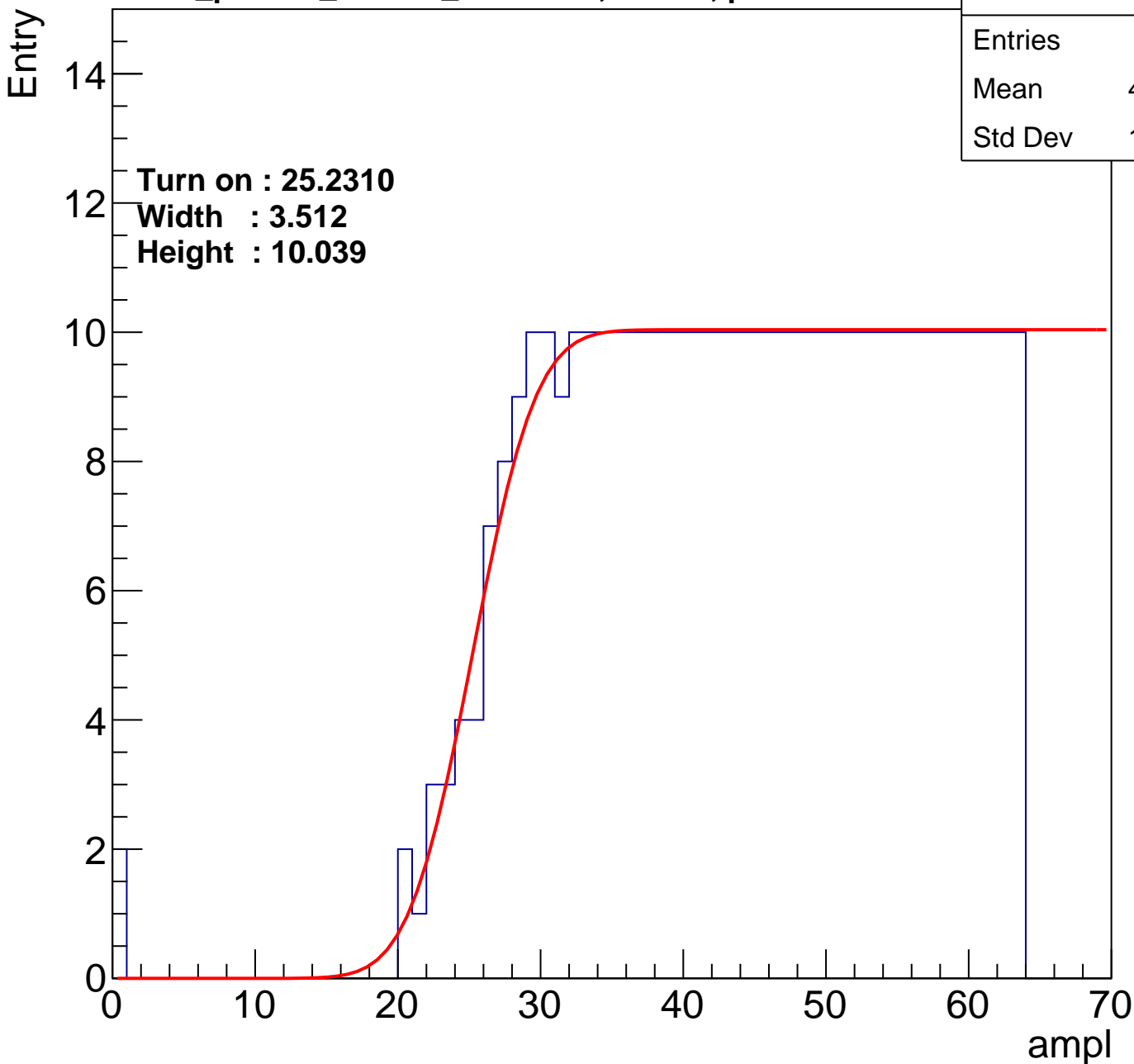
calib_packv5_042523_0143.root, FC#11, port A2

Turn on : 25.2310

Width : 3.512

Height : 10.039

Entries	392
Mean	43.65
Std Dev	11.86



B1L102S, U26-ch12

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.71
Std Dev	11.09

Turn on : 27.0167

Width : 2.696

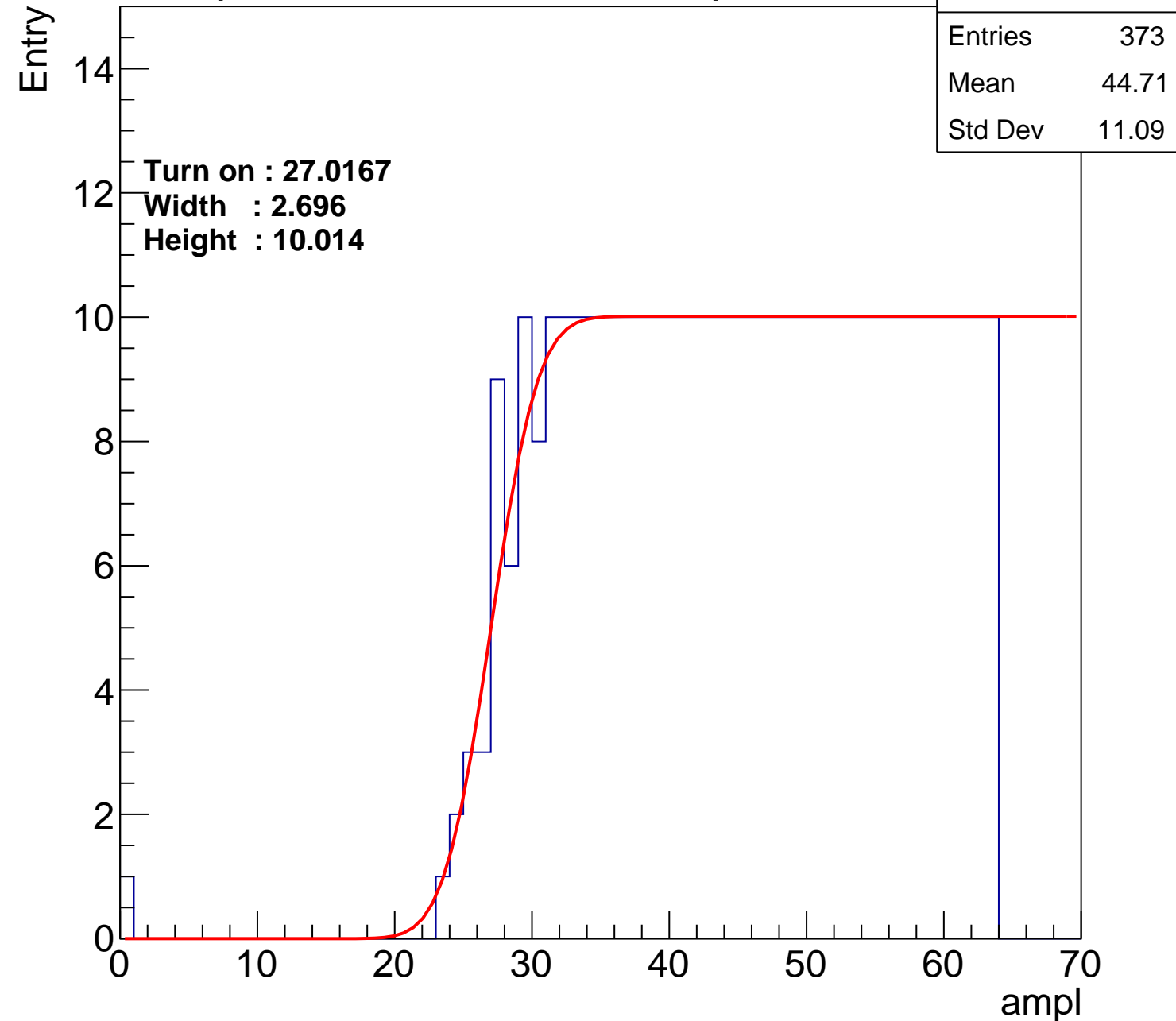
Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch13

calib_packv5_042523_0143.root, FC#11, port A2

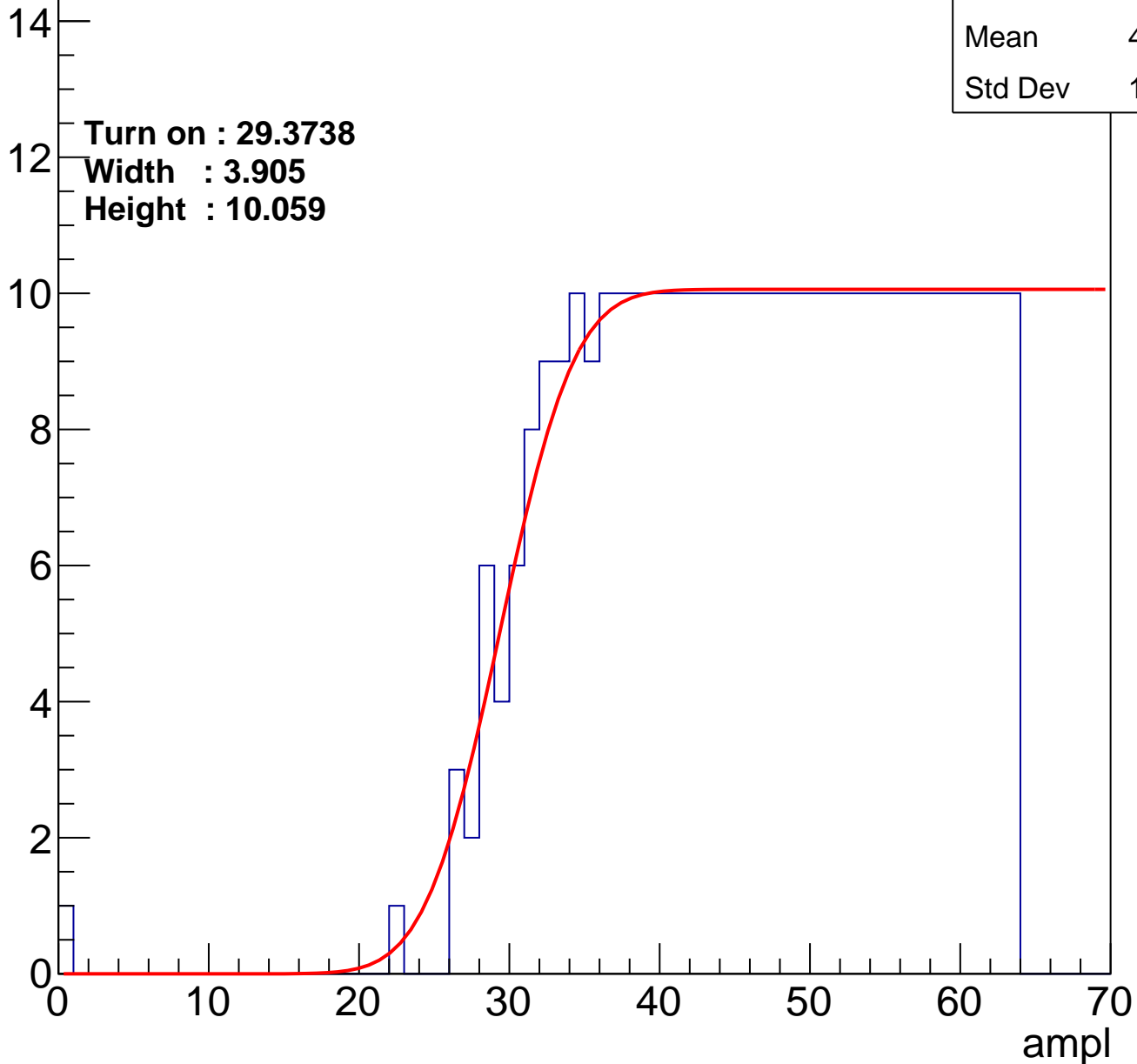
Entries	348
Mean	45.88
Std Dev	10.53

Turn on : 29.3738

Width : 3.905

Height : 10.059

Entry



B1L102S, U26-ch14

calib_packv5_042523_0143.root, FC#11, port A2

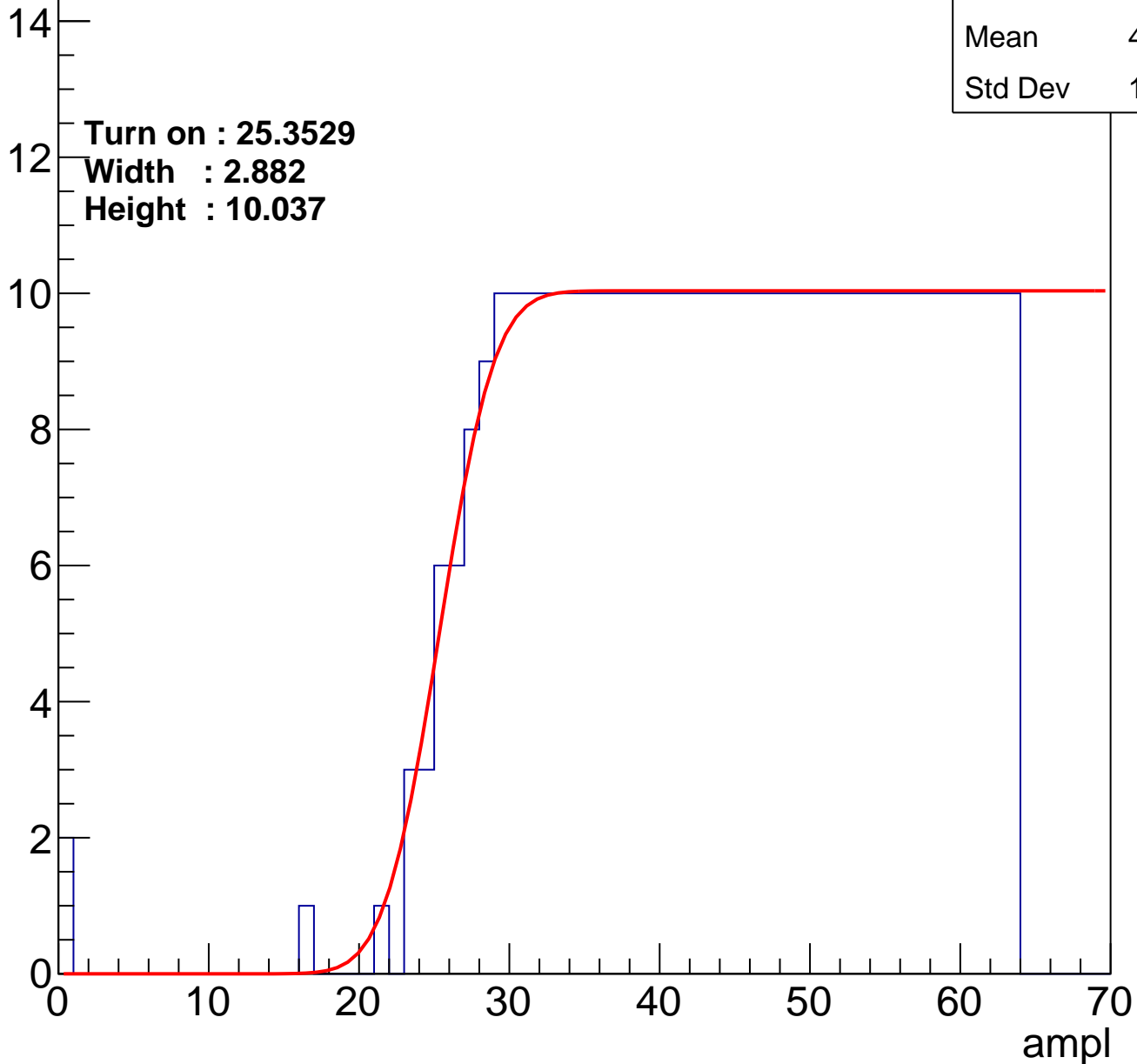
Entries	389
Mean	43.84
Std Dev	11.73

Turn on : 25.3529

Width : 2.882

Height : 10.037

Entry



B1L102S, U26-ch15

calib_packv5_042523_0143.root, FC#11, port A2

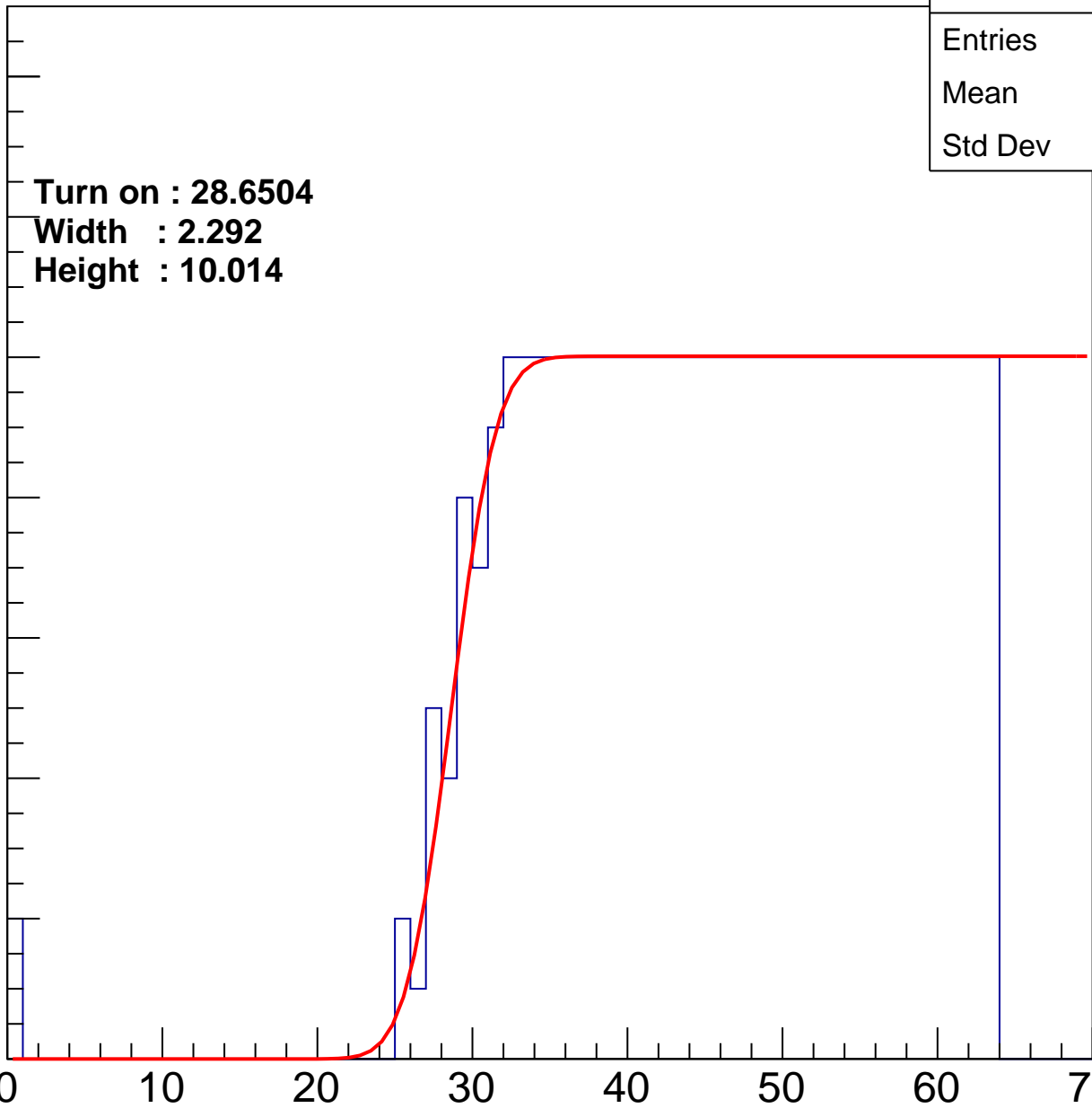
Entry

14
12
10
8
6
4
2
0

Turn on : 28.6504
Width : 2.292
Height : 10.014

Entries	358
Mean	45.37
Std Dev	10.91

ampl



B1L102S, U26-ch16

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	45.09
Std Dev	10.89

Turn on : 27.7375

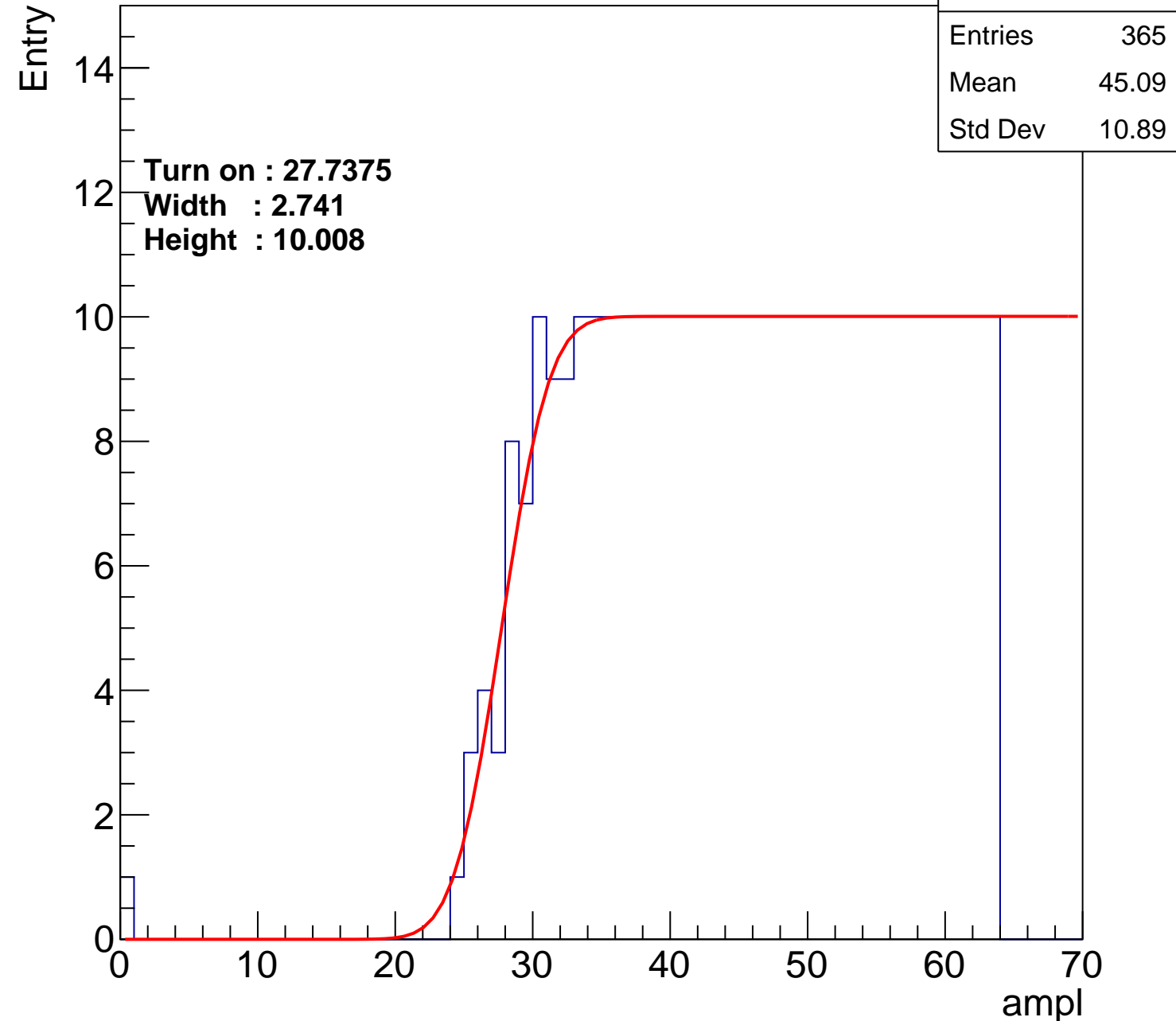
Width : 2.741

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch17

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 25.5829

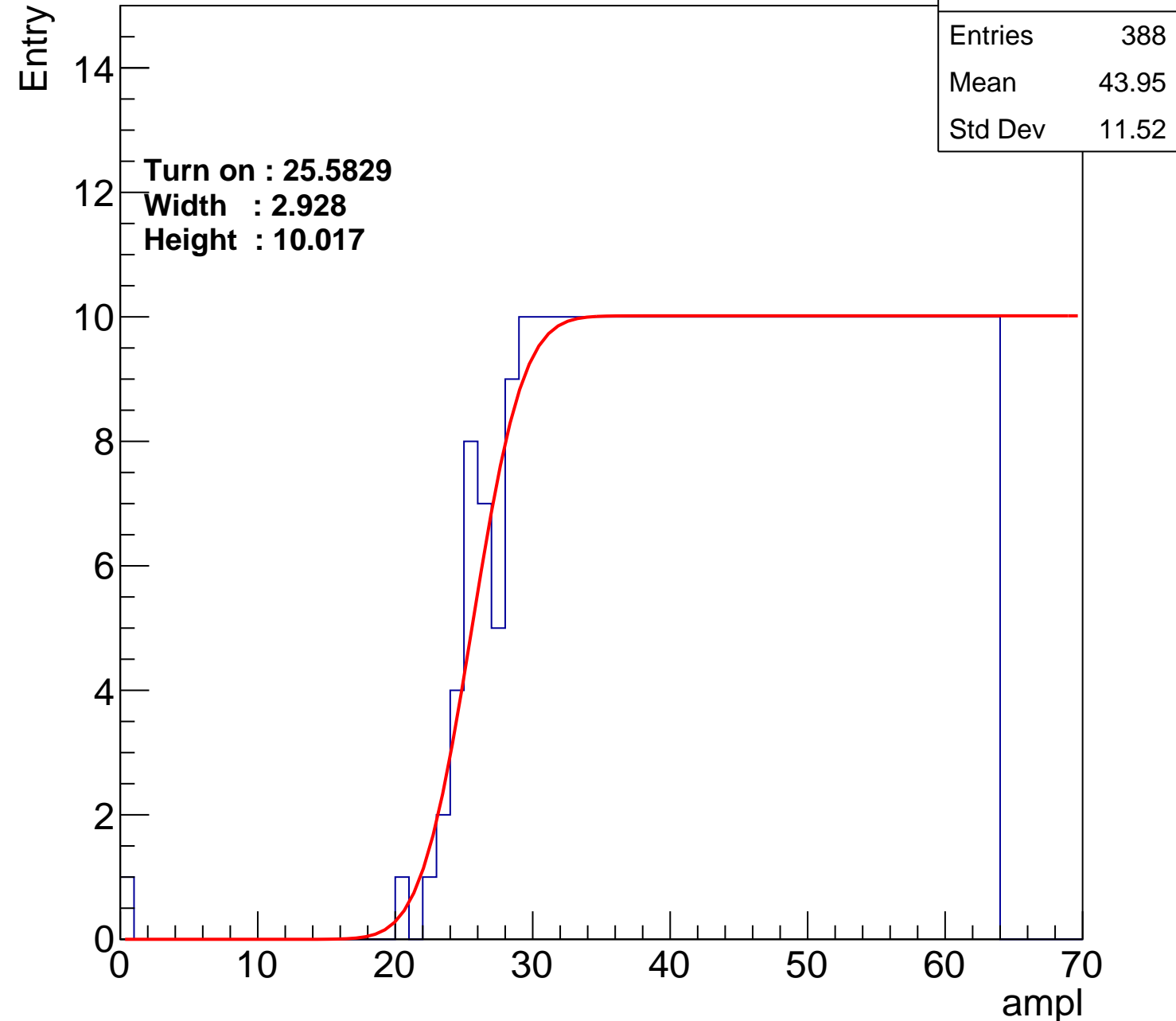
Width : 2.928

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch18

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 26.8312

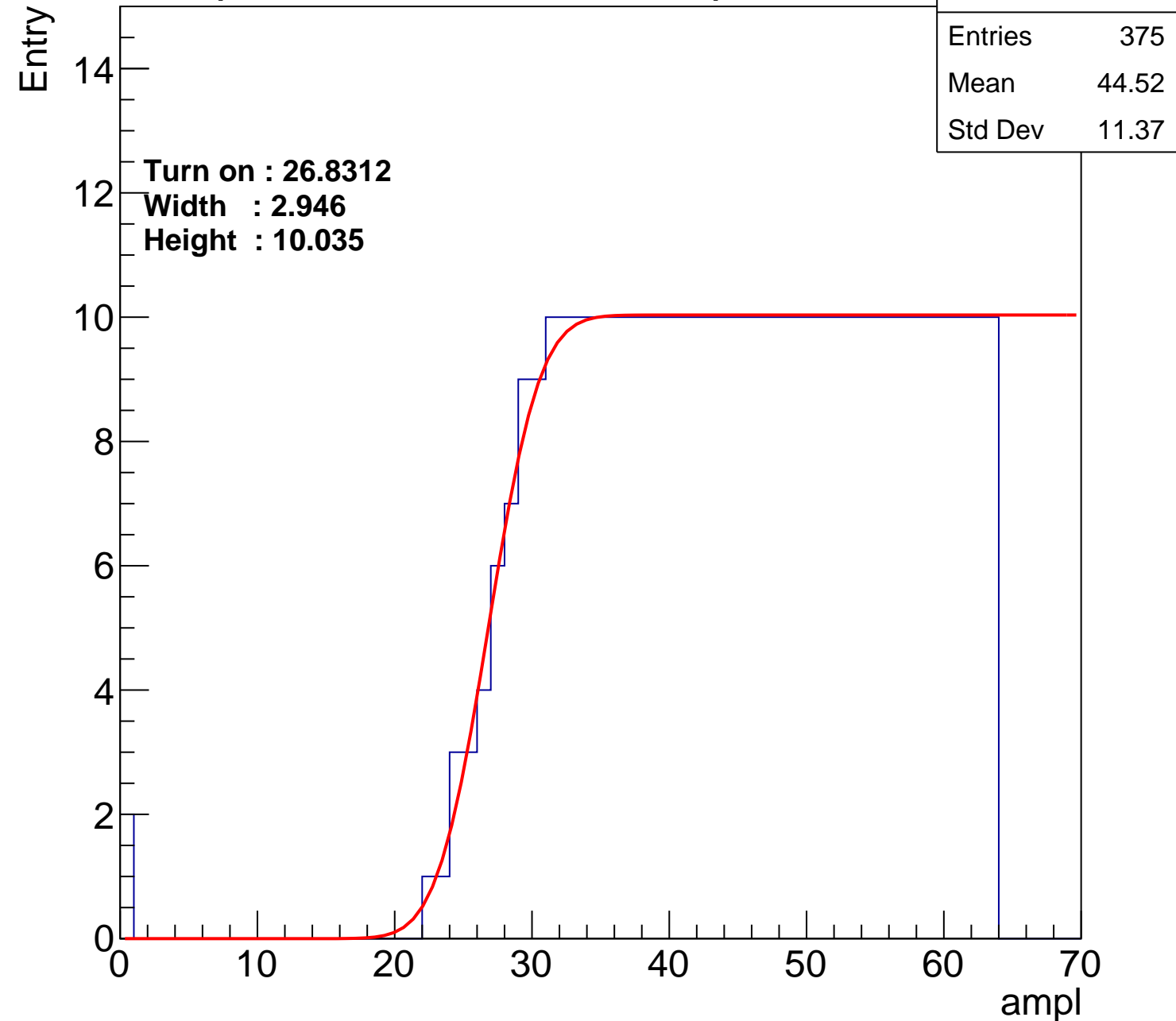
Width : 2.946

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch19

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.83
Std Dev	11.61

Turn on : 25.2260

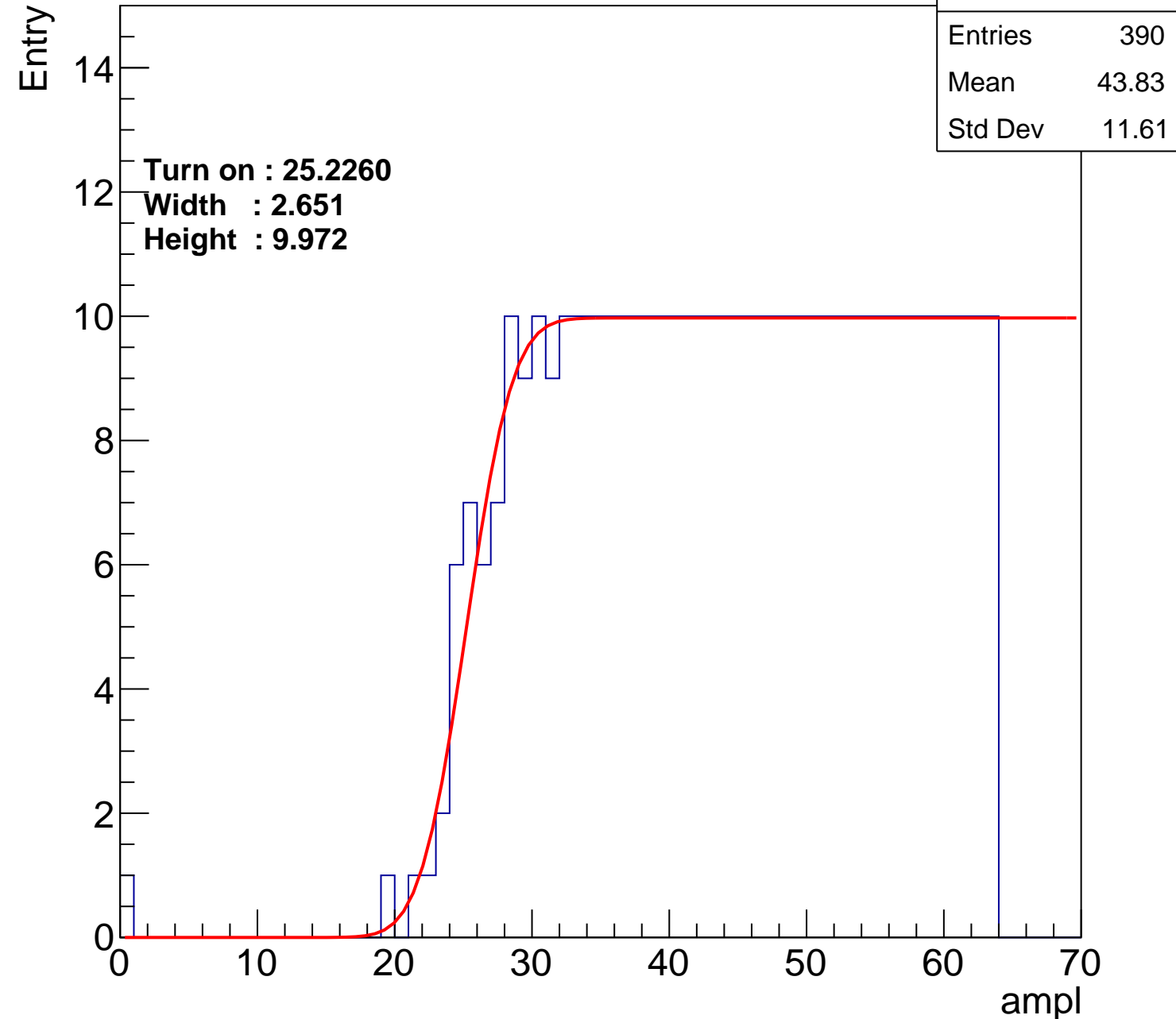
Width : 2.651

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch20

calib_packv5_042523_0143.root, FC#11, port A2

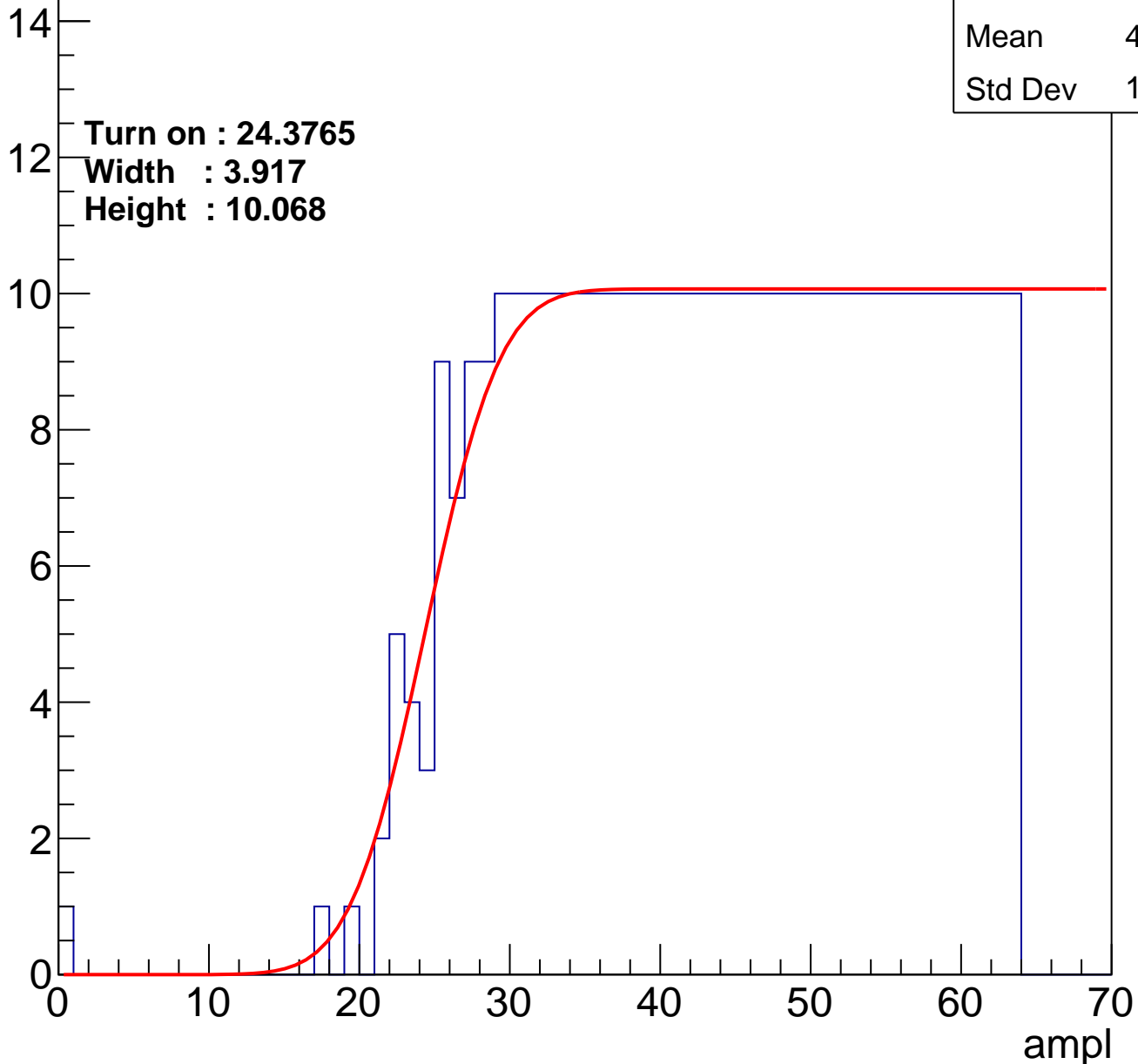
Entries	401
Mean	43.28
Std Dev	11.93

Turn on : 24.3765

Width : 3.917

Height : 10.068

Entry



B1L102S, U26-ch21

calib_packv5_042523_0143.root, FC#11, port A2

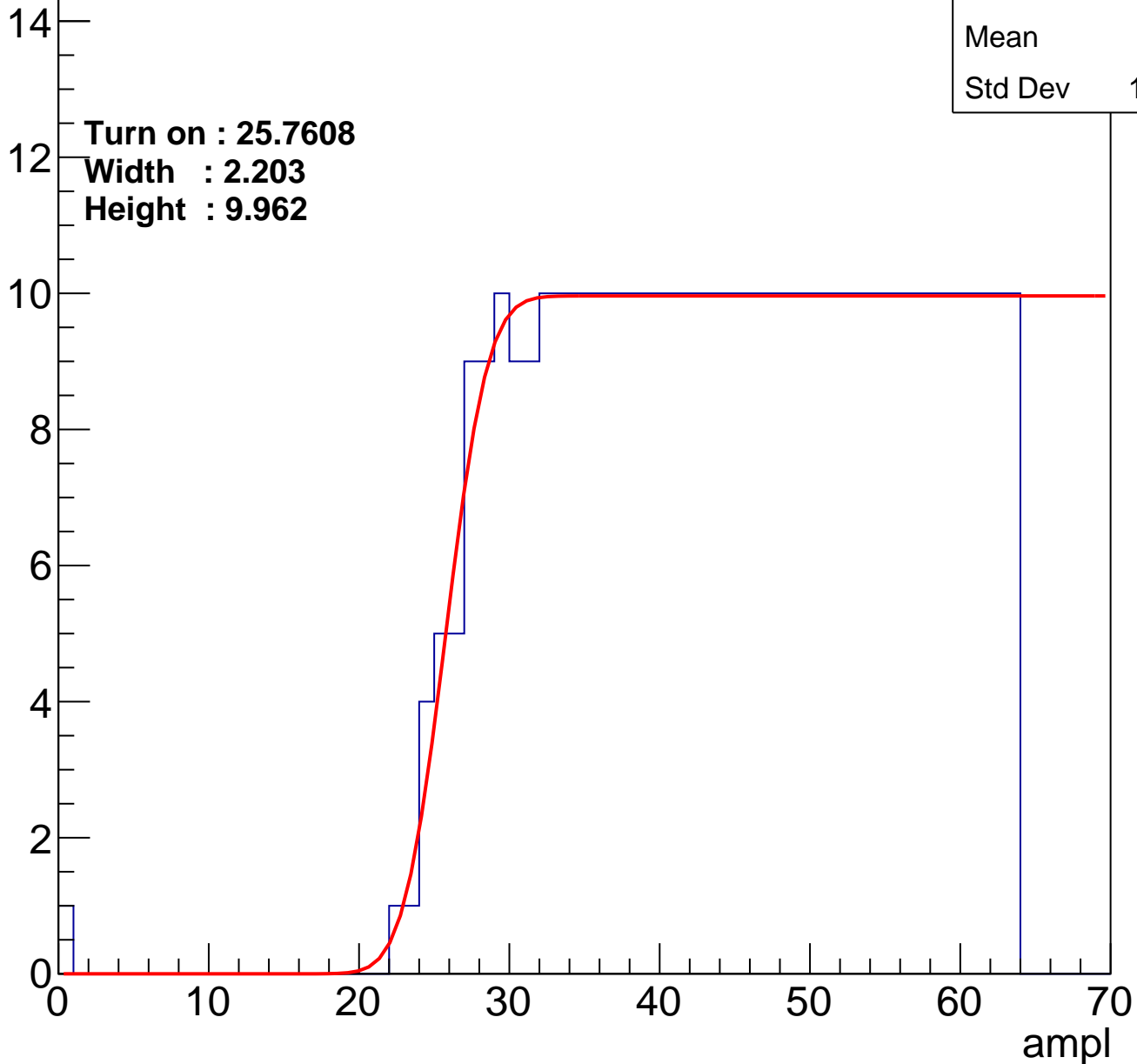
Entries	383
Mean	44.2
Std Dev	11.37

Turn on : 25.7608

Width : 2.203

Height : 9.962

Entry



B1L102S, U26-ch22

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.74
Std Dev	11.94

Turn on : 26.1832

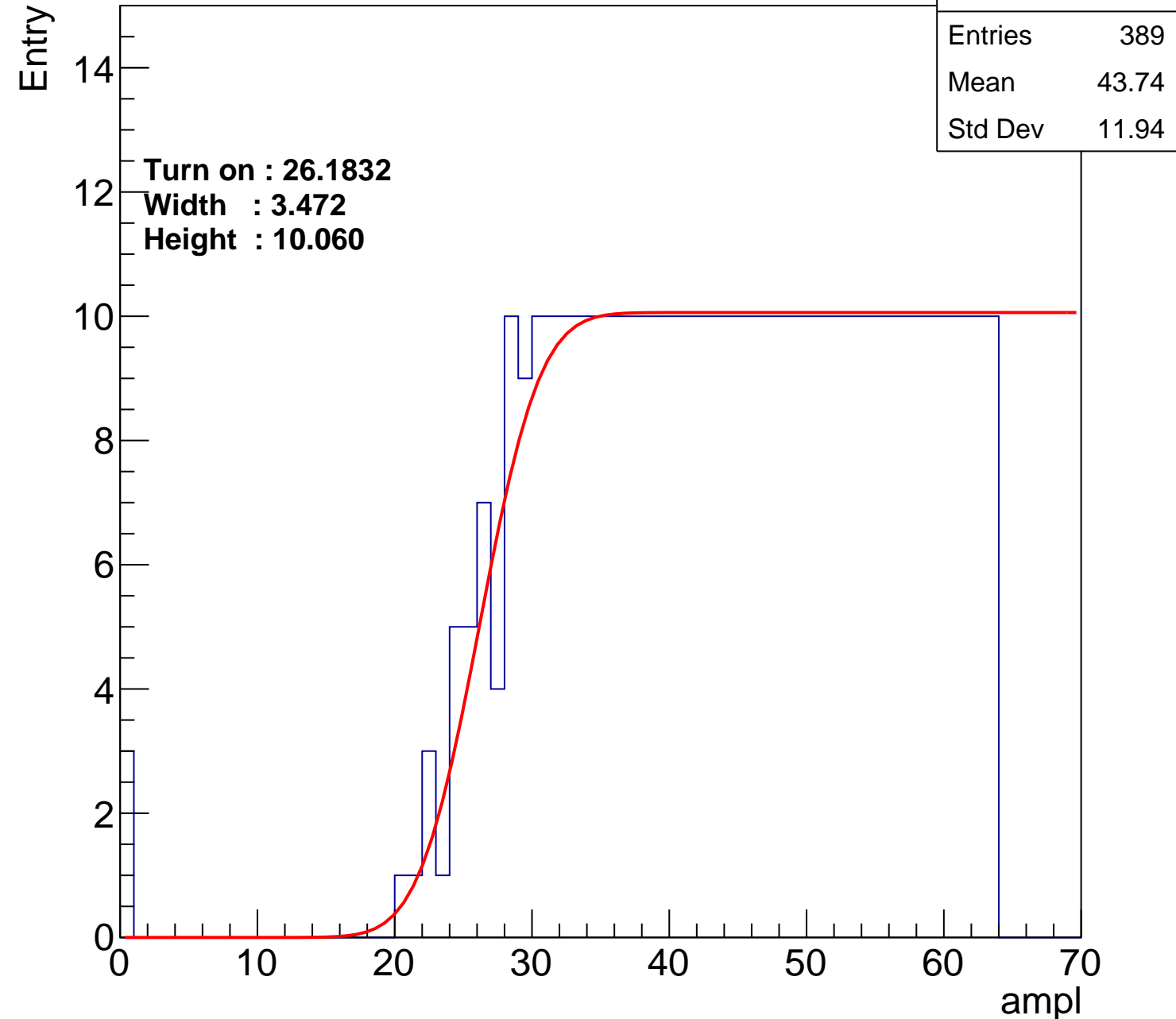
Width : 3.472

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch23

calib_packv5_042523_0143.root, FC#11, port A2

Entries	364
Mean	45.15
Std Dev	10.85

Turn on : 27.9591

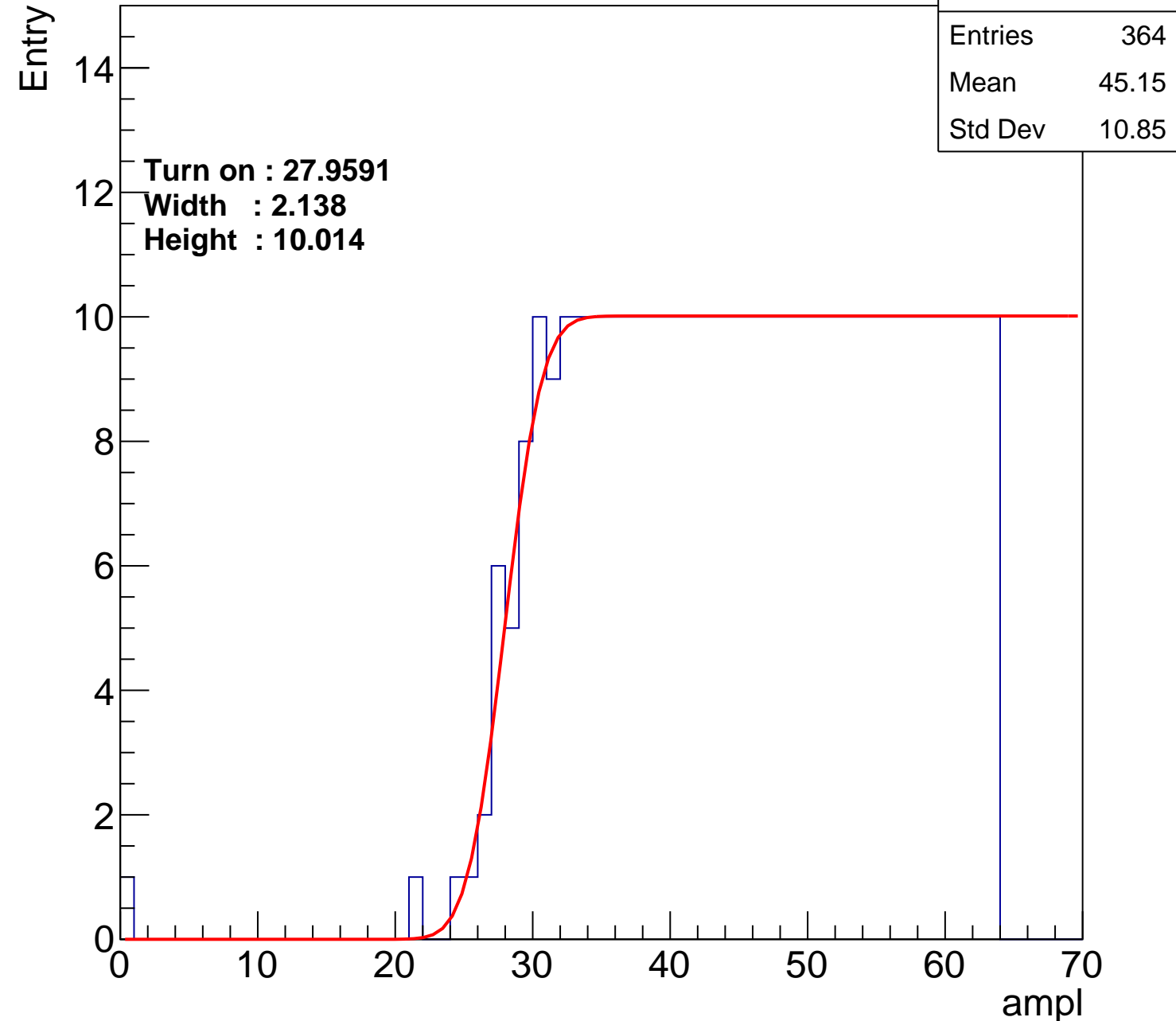
Width : 2.138

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch24

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.4790

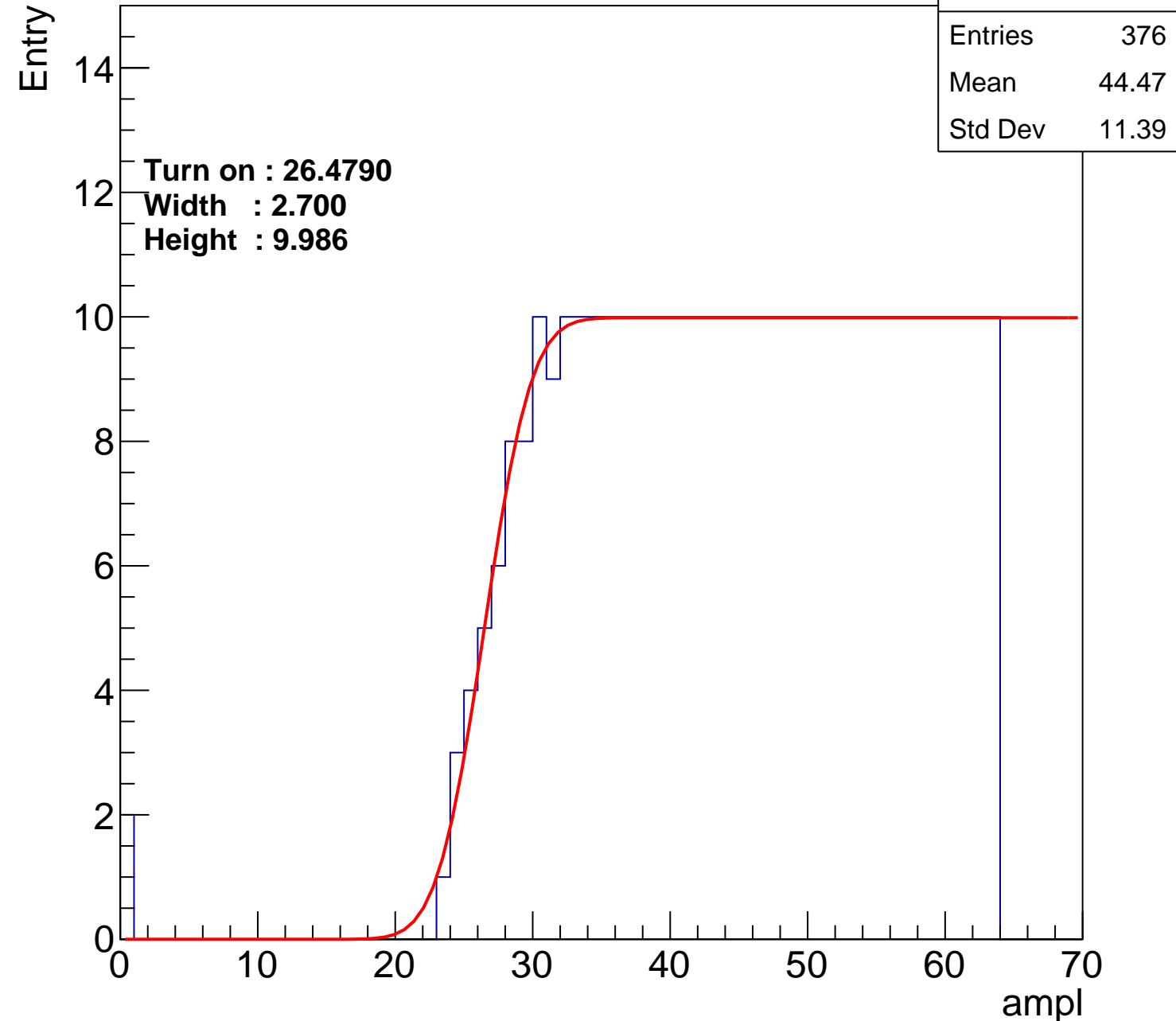
Width : 2.700

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch25

calib_packv5_042523_0143.root, FC#11, port A2

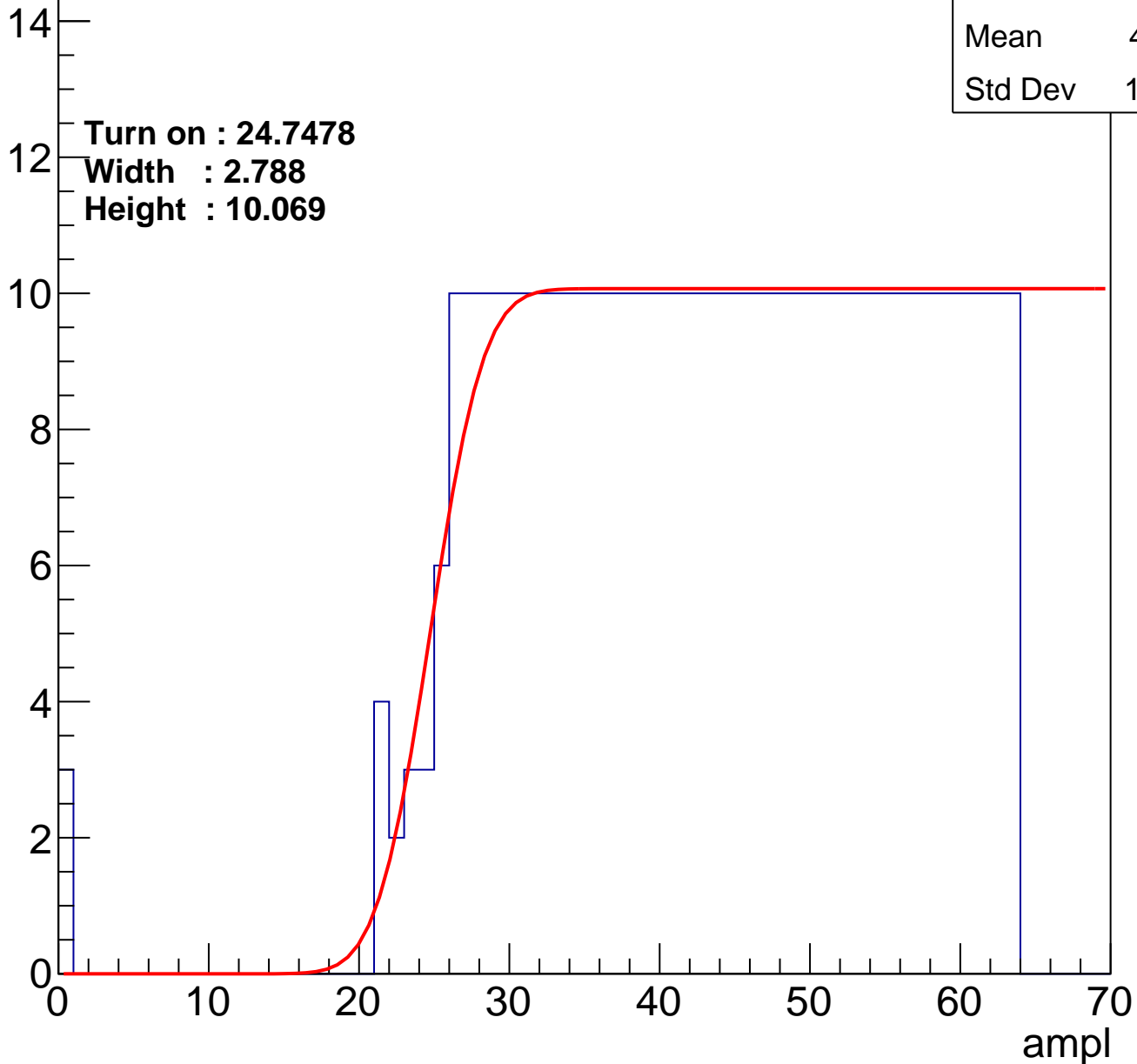
Entries	401
Mean	43.21
Std Dev	12.14

Turn on : 24.7478

Width : 2.788

Height : 10.069

Entry



B1L102S, U26-ch26

calib_packv5_042523_0143.root, FC#11, port A2

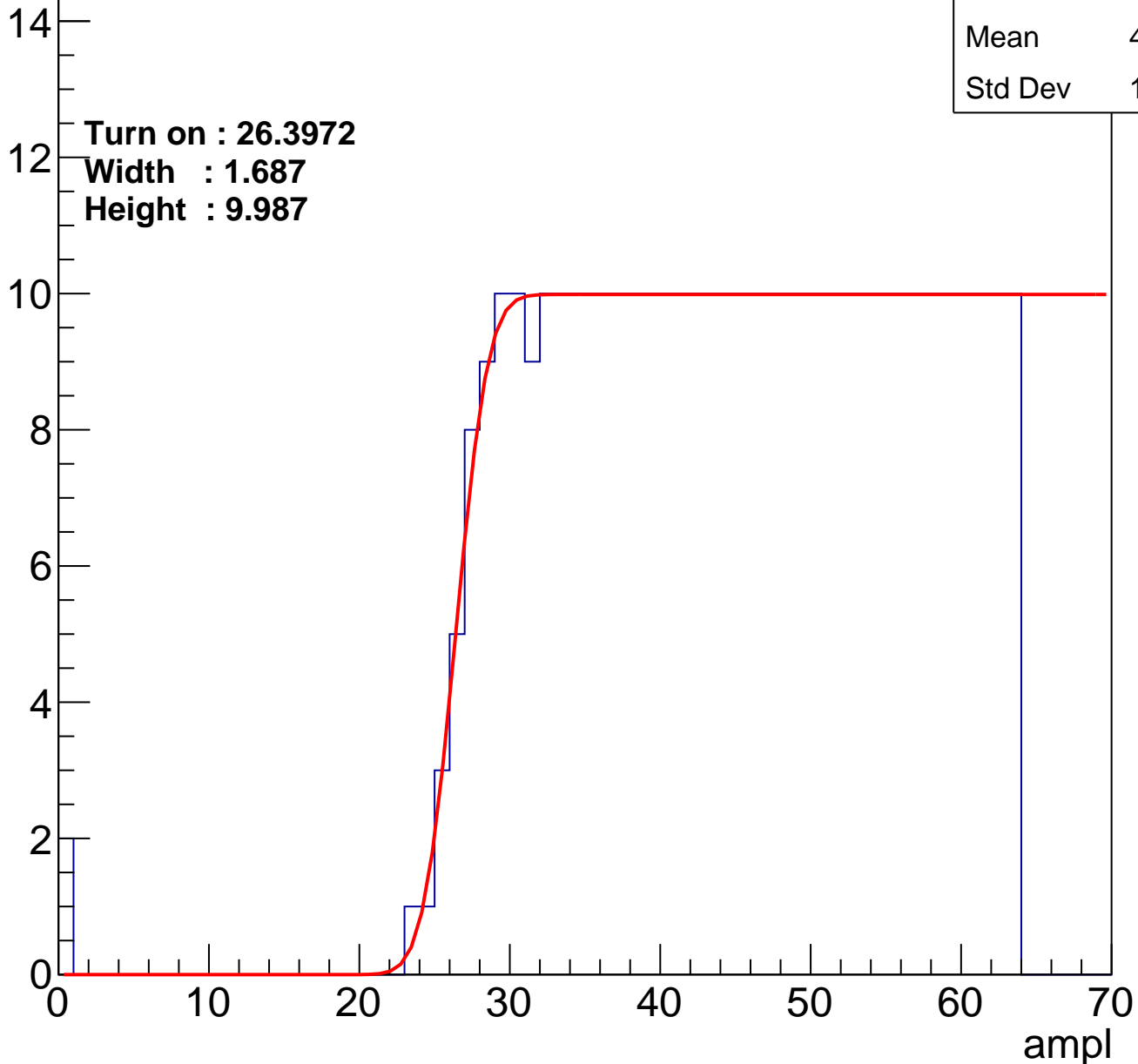
Entries	378
Mean	44.42
Std Dev	11.37

Turn on : 26.3972

Width : 1.687

Height : 9.987

Entry



B1L102S, U26-ch27

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.72
Std Dev	11.13

Turn on : 27.0973

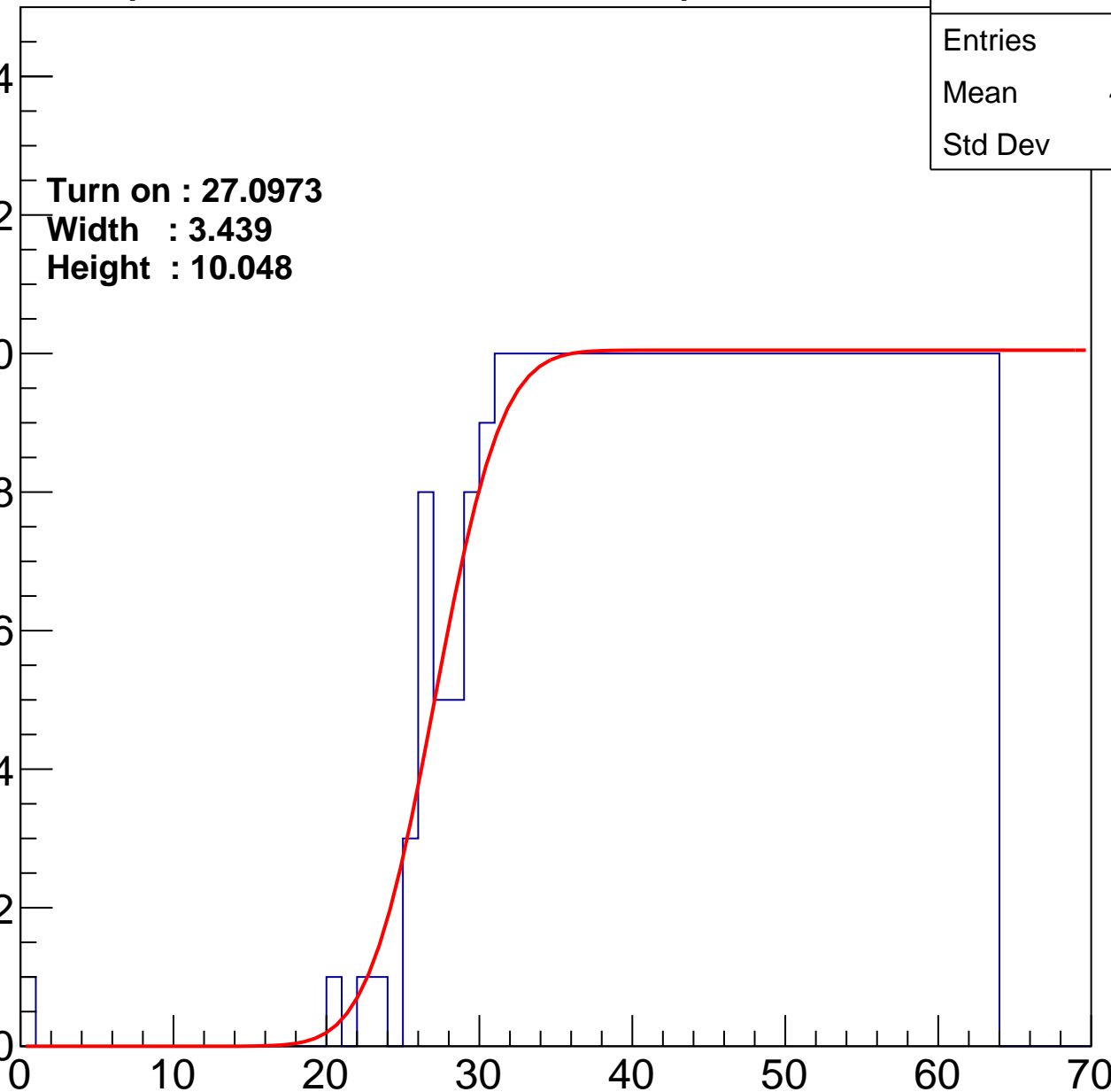
Width : 3.439

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch28

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.47
Std Dev	11.39

Turn on : 26.4801

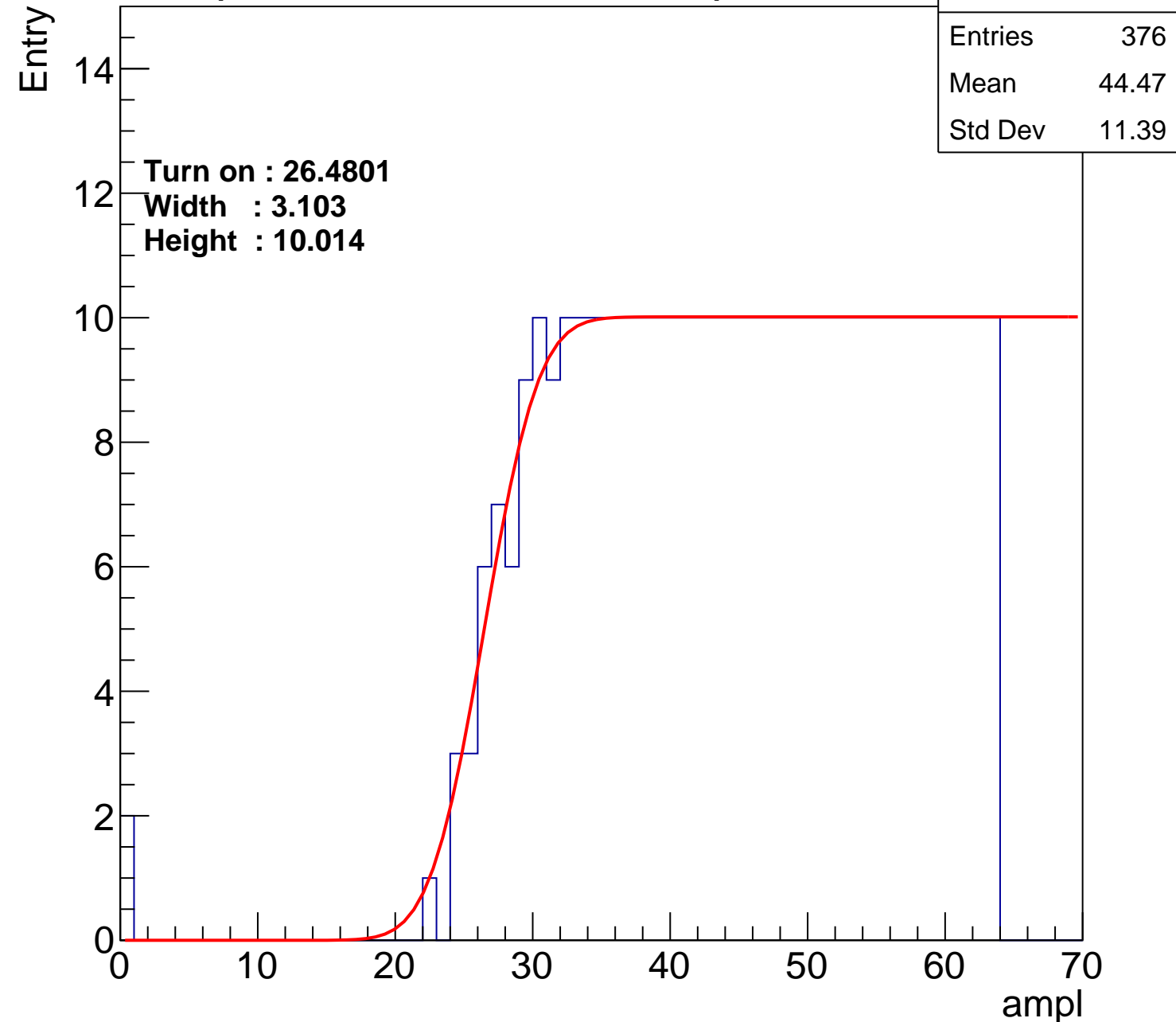
Width : 3.103

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch29

calib_packv5_042523_0143.root, FC#11, port A2

Entries	359
Mean	45.29
Std Dev	10.98

Turn on : 28.6406

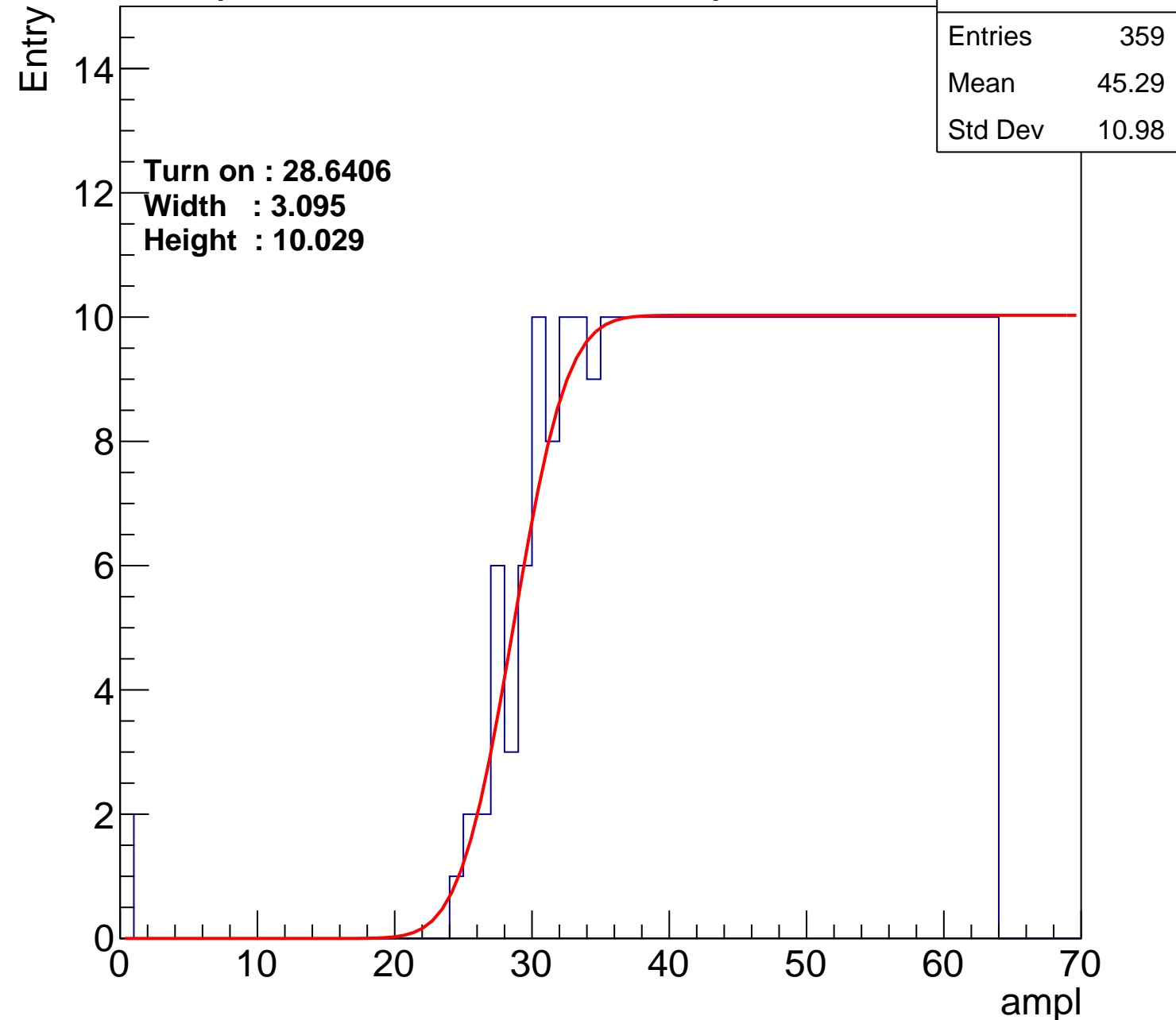
Width : 3.095

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch30

calib_packv5_042523_0143.root, FC#11, port A2

Entries	358
Mean	45.26
Std Dev	11.19

Turn on : 29.0683

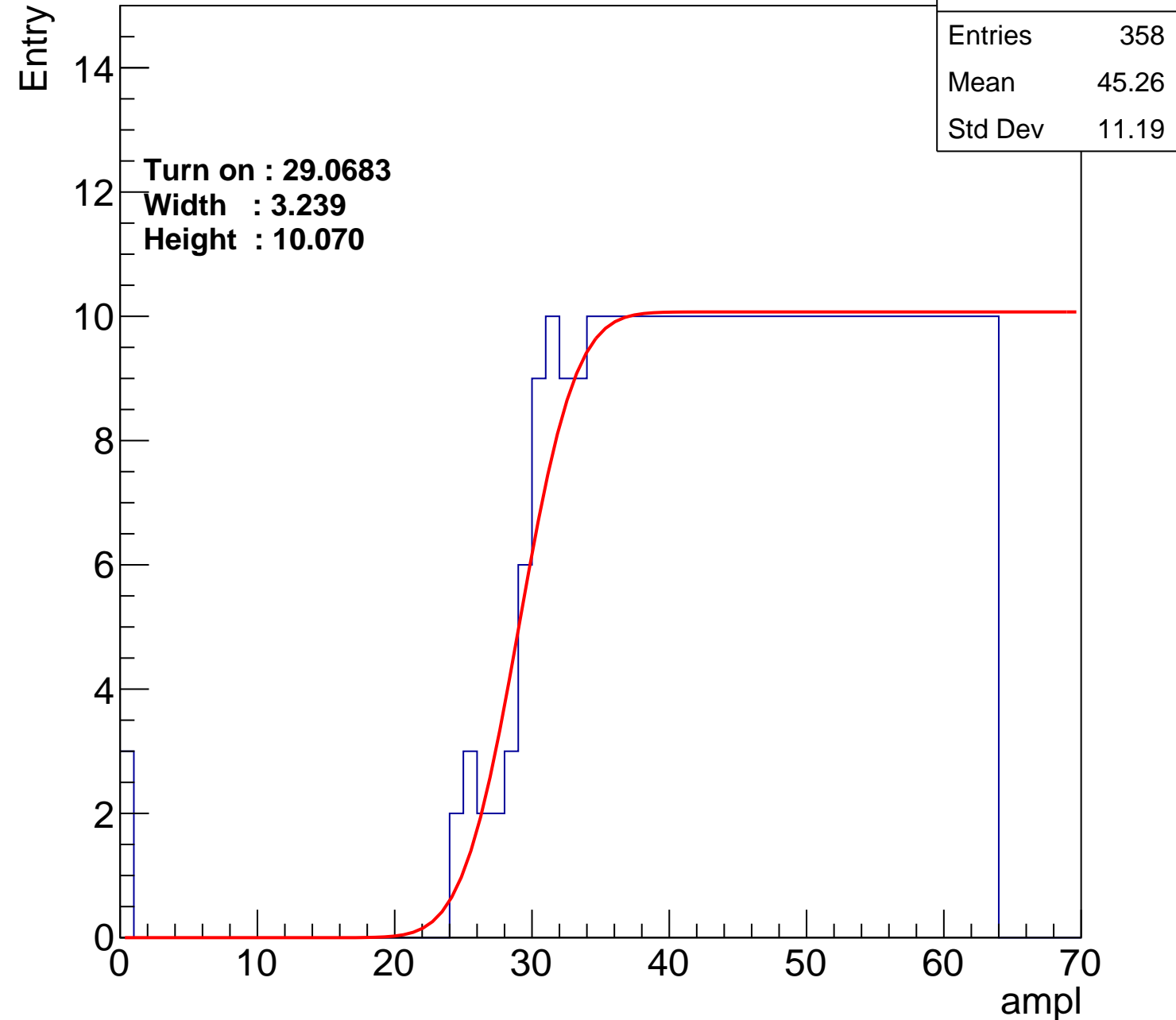
Width : 3.239

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch31

calib_packv5_042523_0143.root, FC#11, port A2

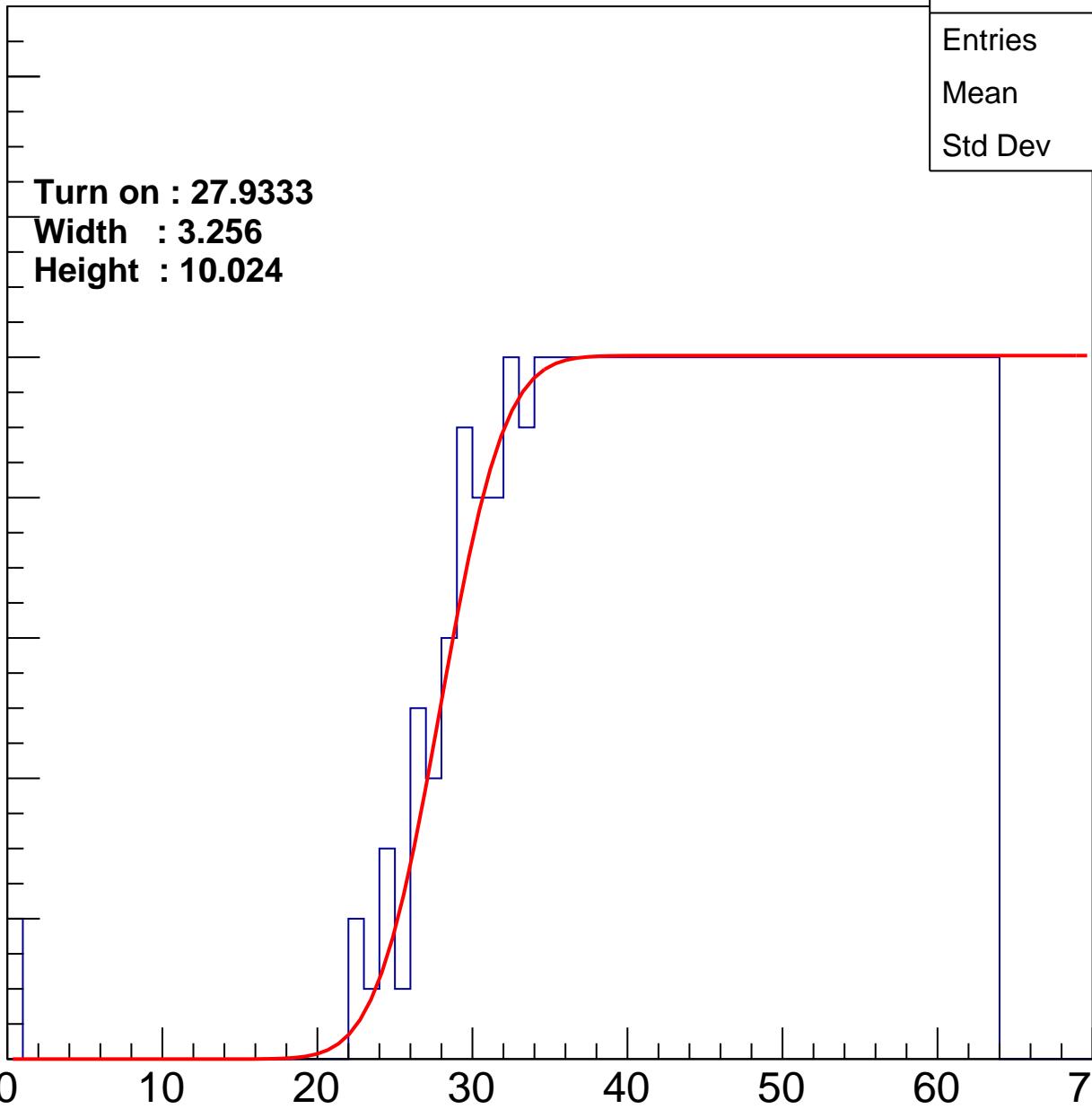
Entry

14
12
10
8
6
4
2
0

Turn on : 27.9333
Width : 3.256
Height : 10.024

Entries	368
Mean	44.8
Std Dev	11.29

ampl



B1L102S, U26-ch32

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	44.01
Std Dev	11.47

Turn on : 25.4561

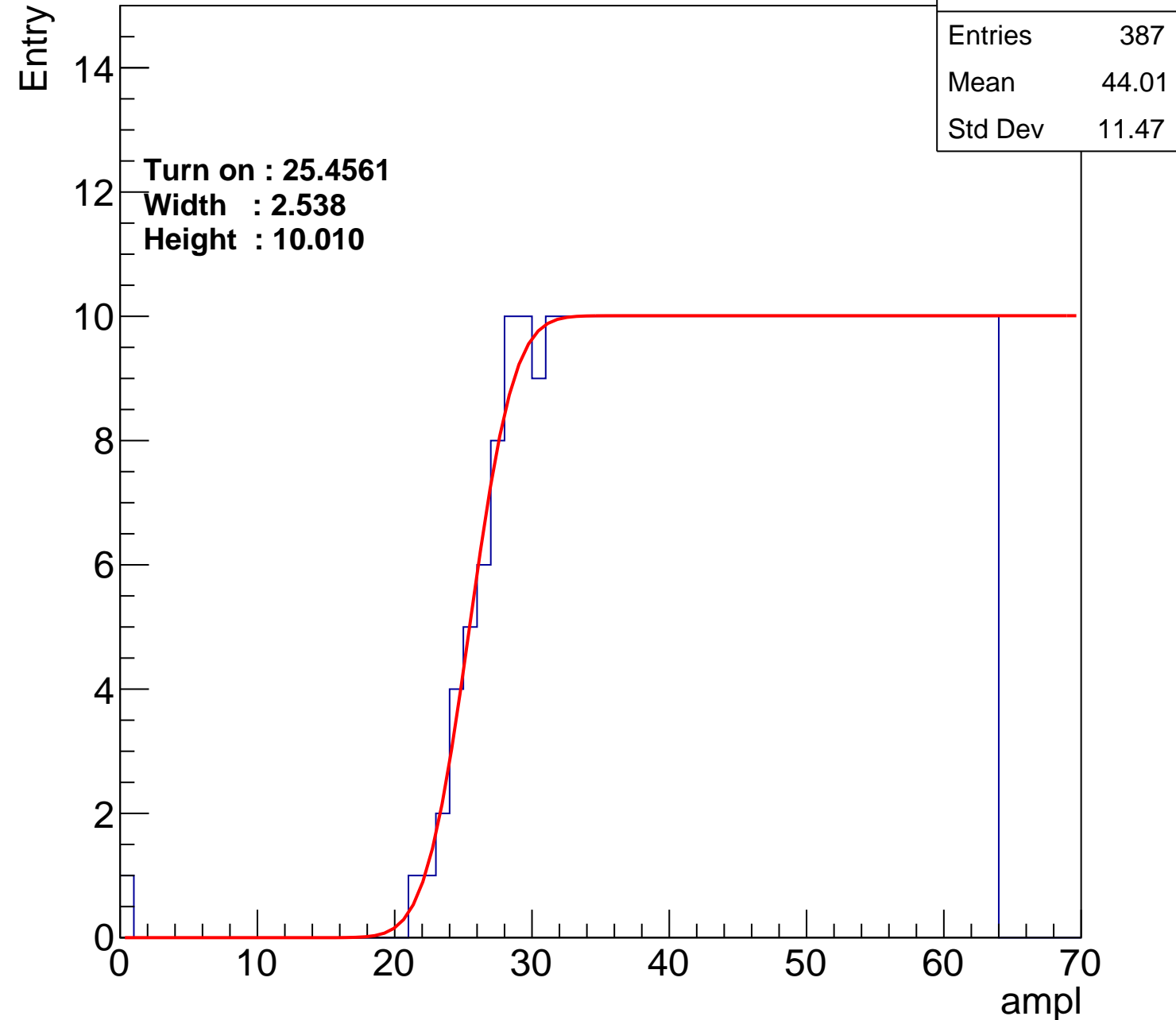
Width : 2.538

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch33

calib_packv5_042523_0143.root, FC#11, port A2

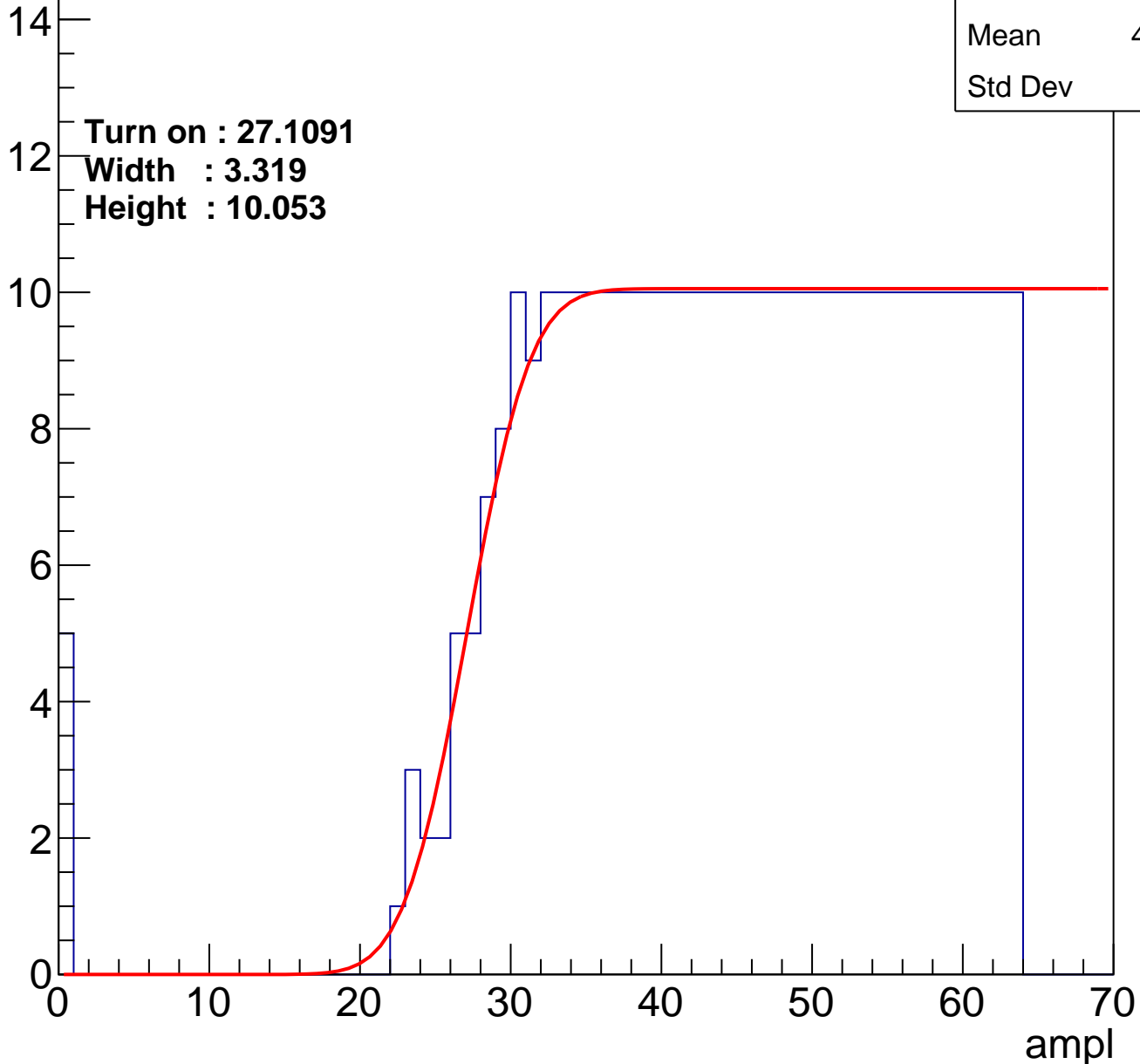
Entries	377
Mean	44.19
Std Dev	12

Turn on : 27.1091

Width : 3.319

Height : 10.053

Entry



B1L102S, U26-ch34

calib_packv5_042523_0143.root, FC#11, port A2

Entries	363
Mean	44.94
Std Dev	11.53

Turn on : 28.9421

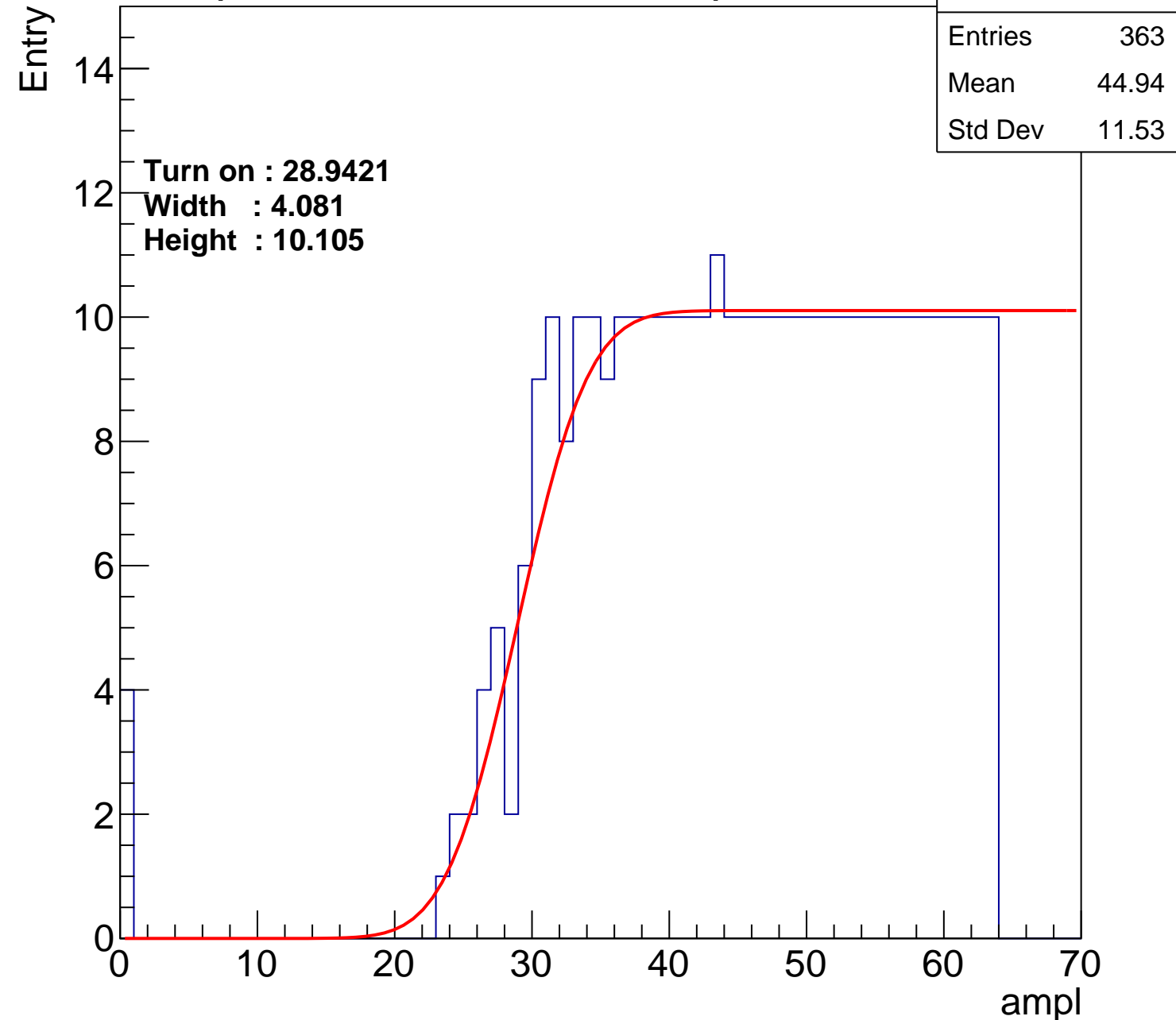
Width : 4.081

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch35

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 26.9248

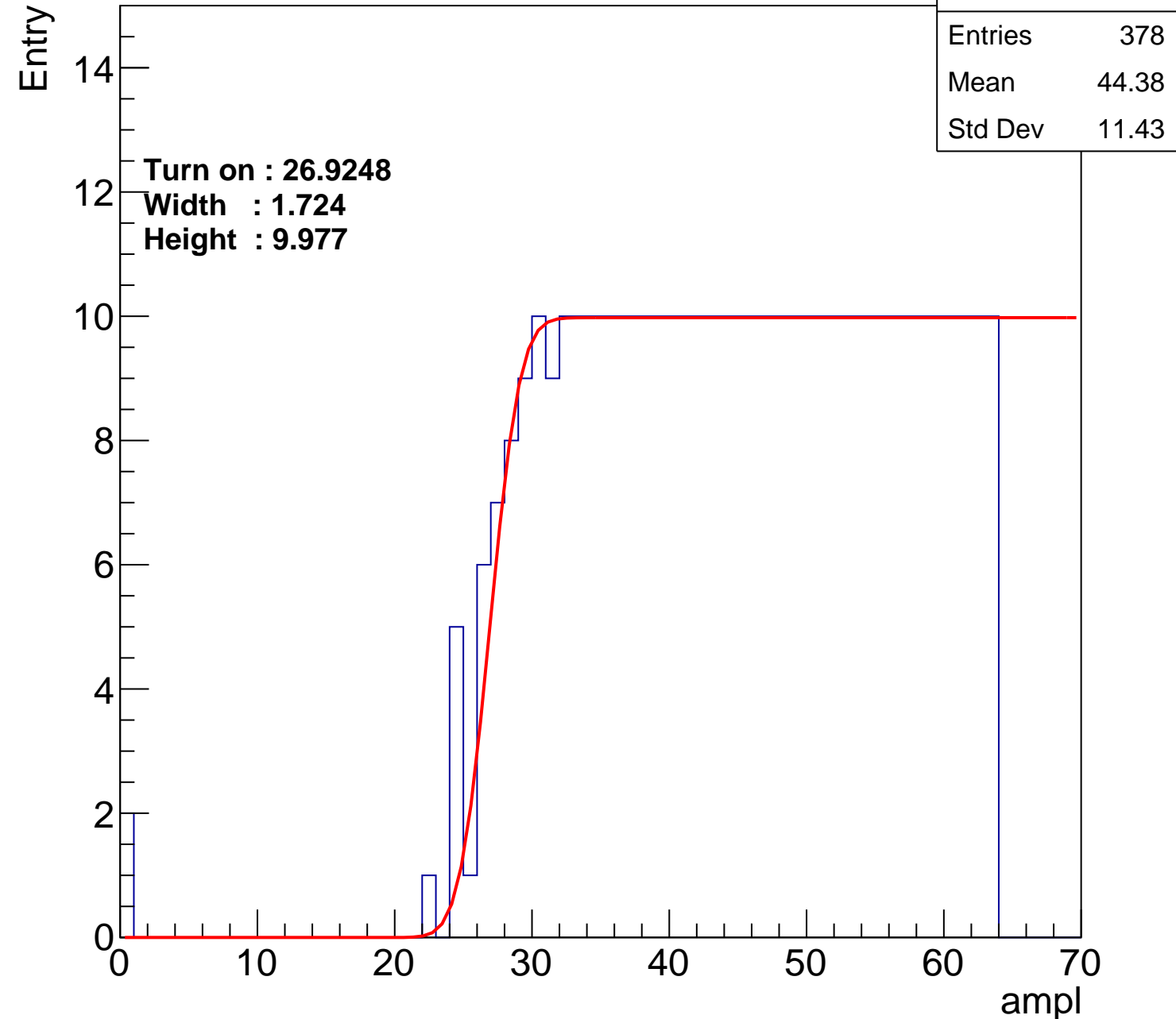
Width : 1.724

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch36

calib_packv5_042523_0143.root, FC#11, port A2

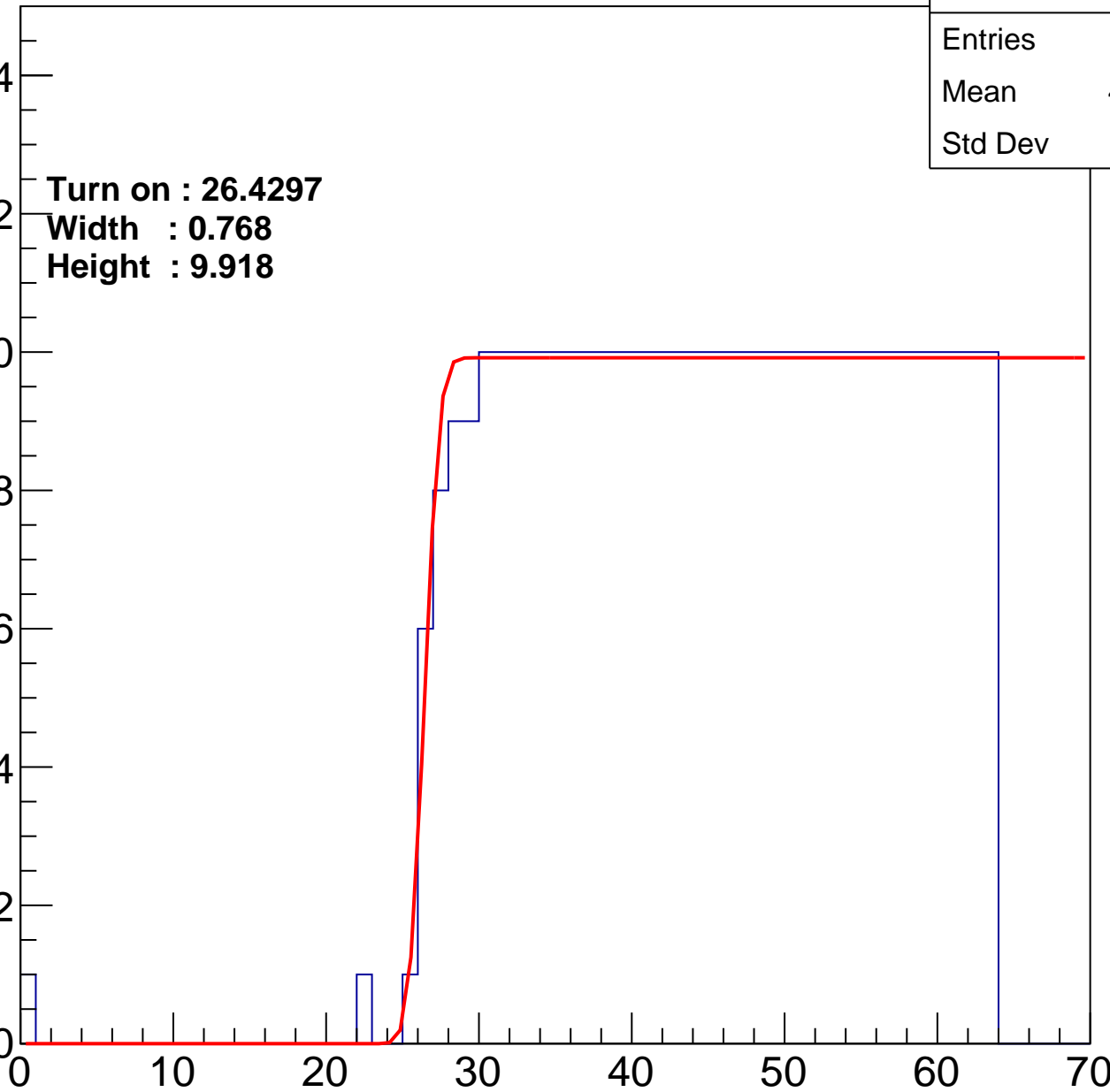
Entry

14
12
10
8
6
4
2
0

Turn on : 26.4297
Width : 0.768
Height : 9.918

Entries	375
Mean	44.65
Std Dev	11.08

ampl



B1L102S, U26-ch37

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.47
Std Dev	11.27

Turn on : 26.7744

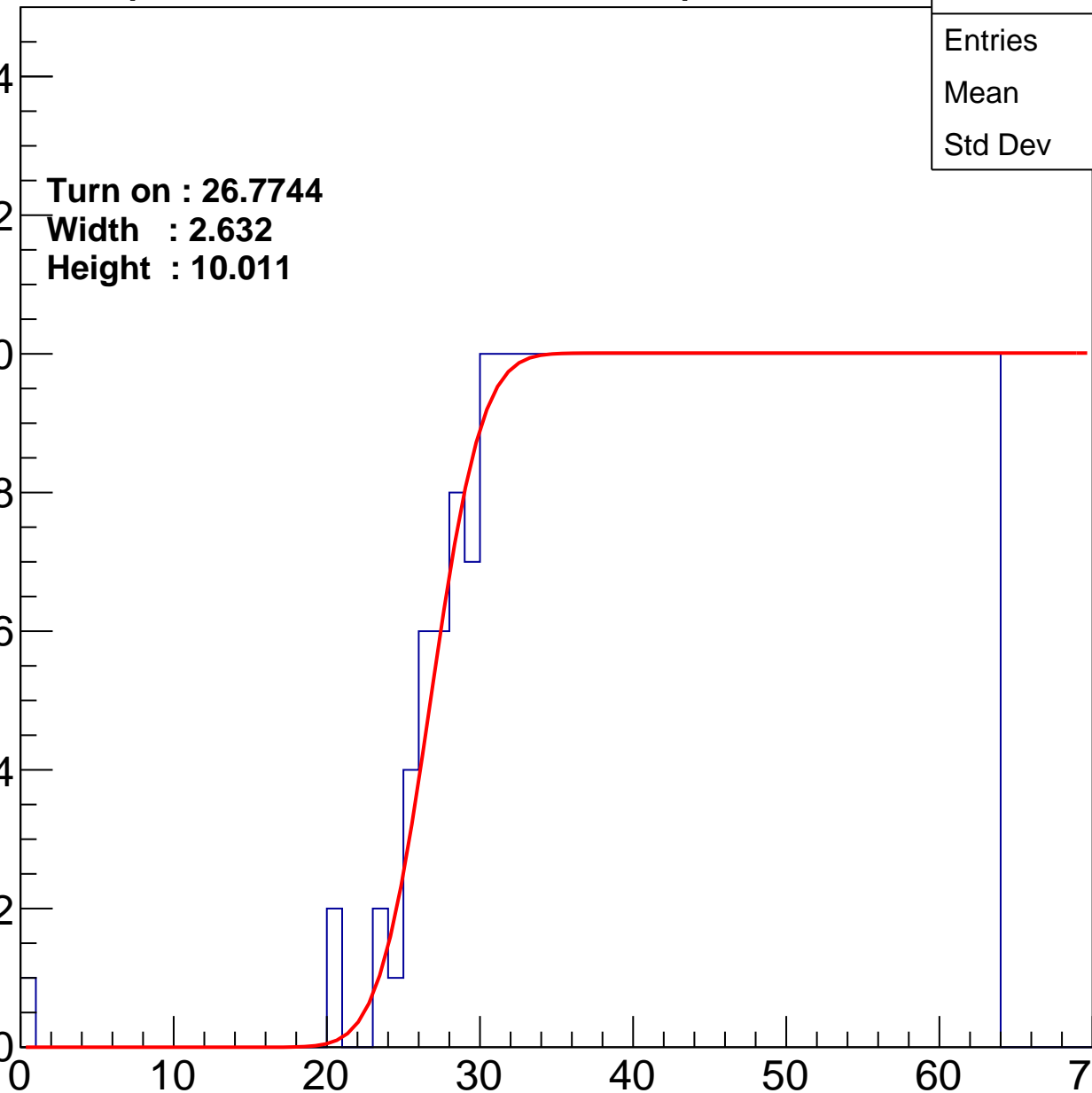
Width : 2.632

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch38

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.18
Std Dev	11.59

Turn on : 26.5742

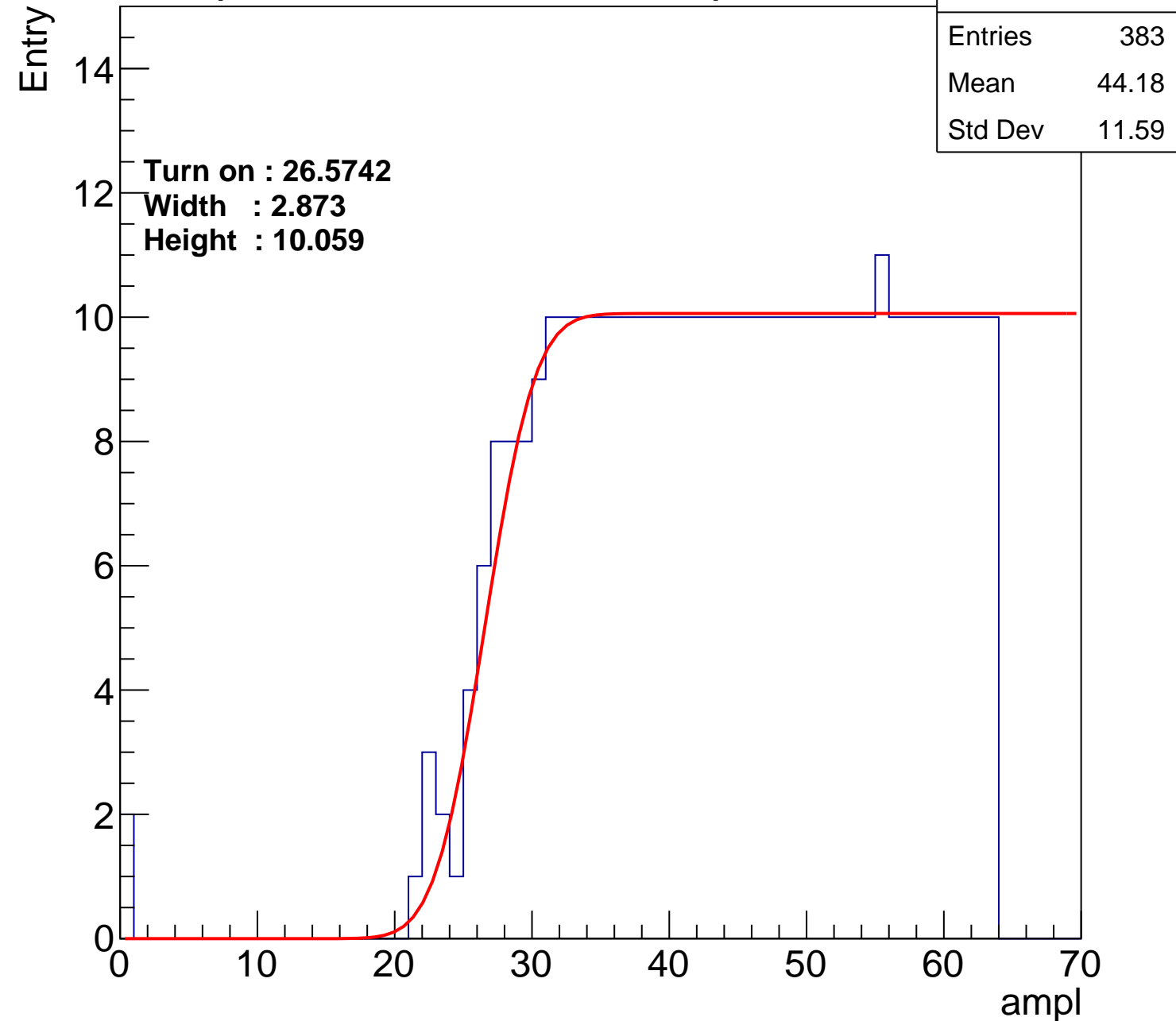
Width : 2.873

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch39

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.02
Std Dev	11.75

Turn on : 26.1411

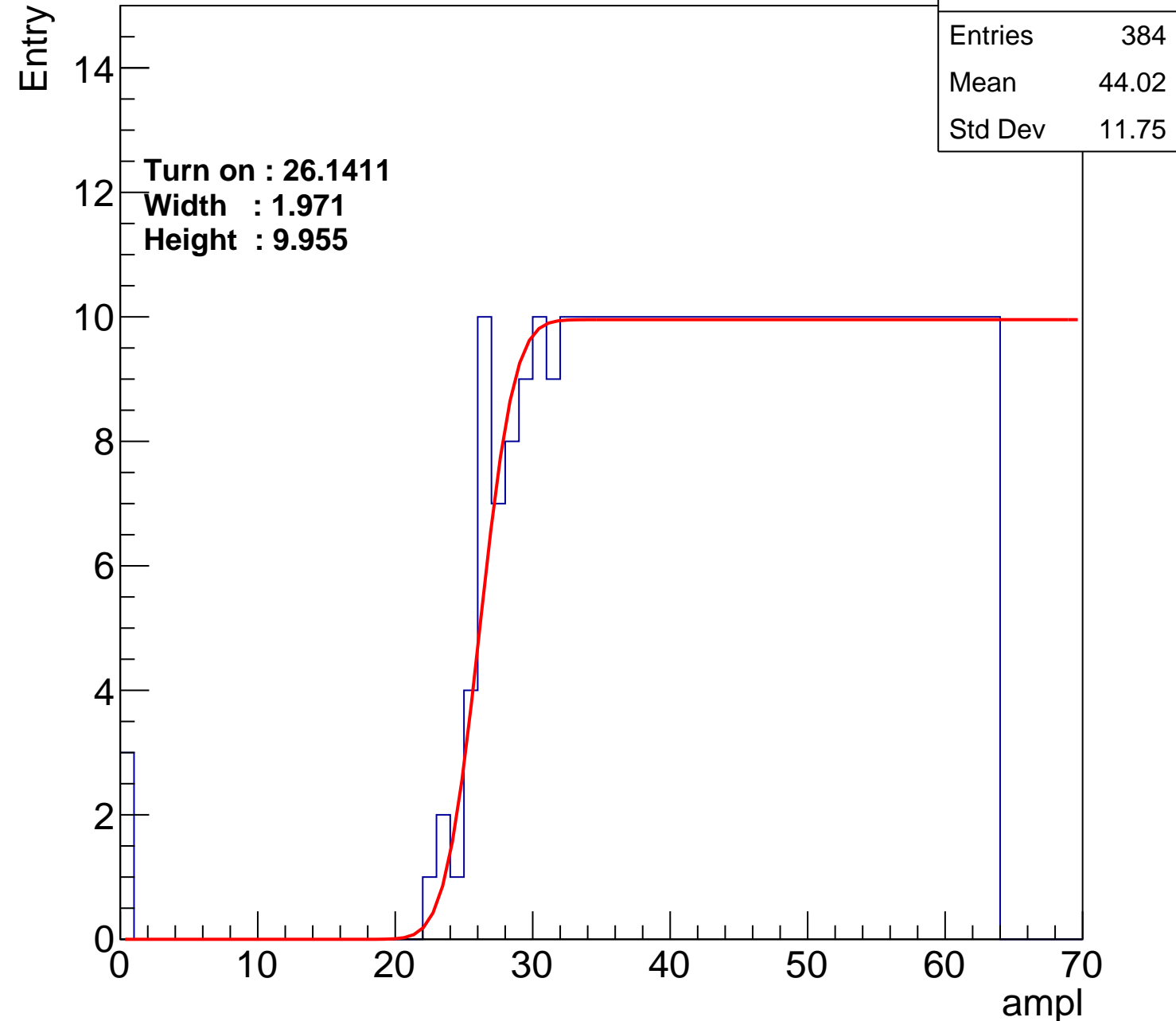
Width : 1.971

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch40

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.99
Std Dev	11.67

Turn on : 25.8650

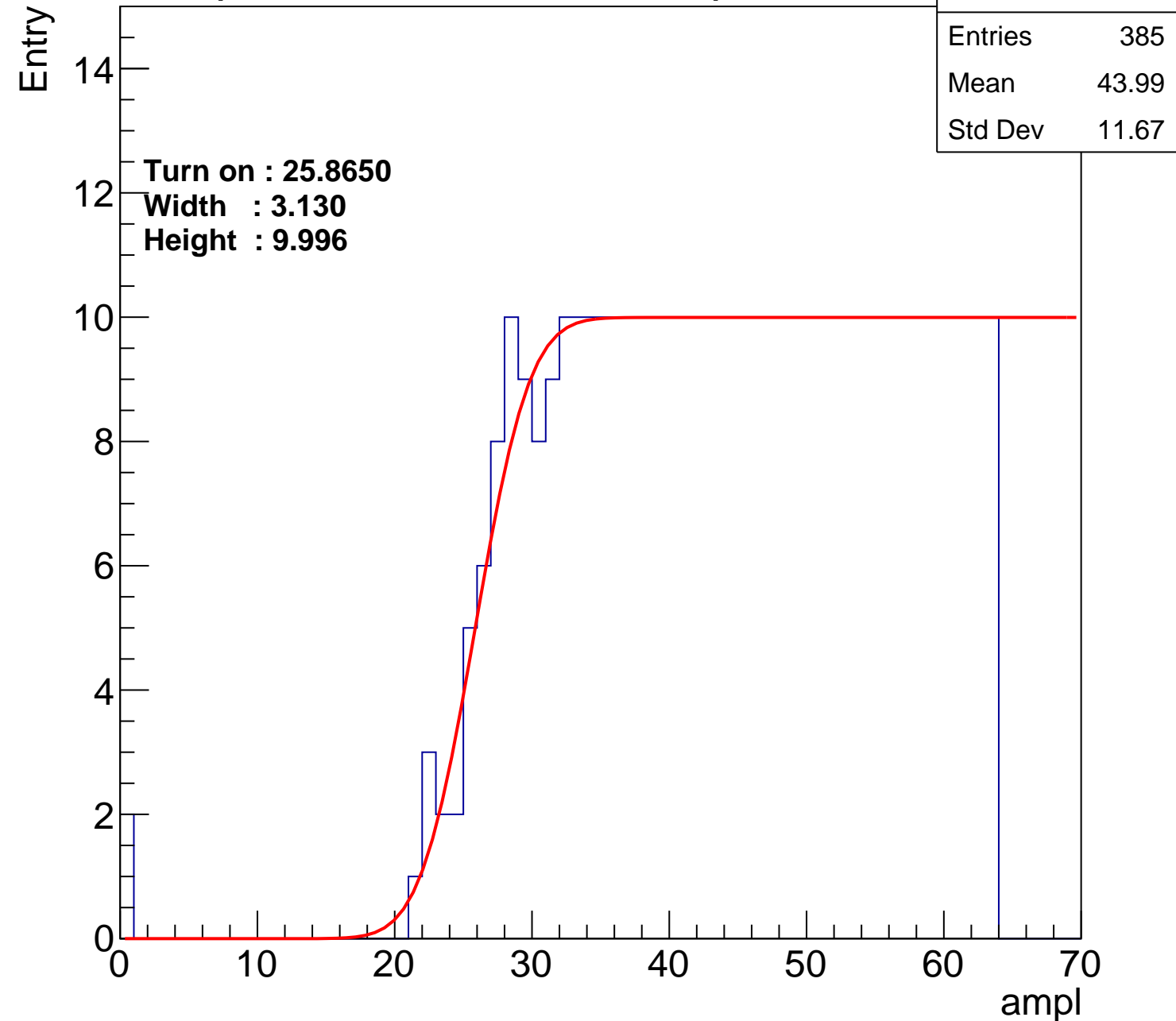
Width : 3.130

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch41

calib_packv5_042523_0143.root, FC#11, port A2

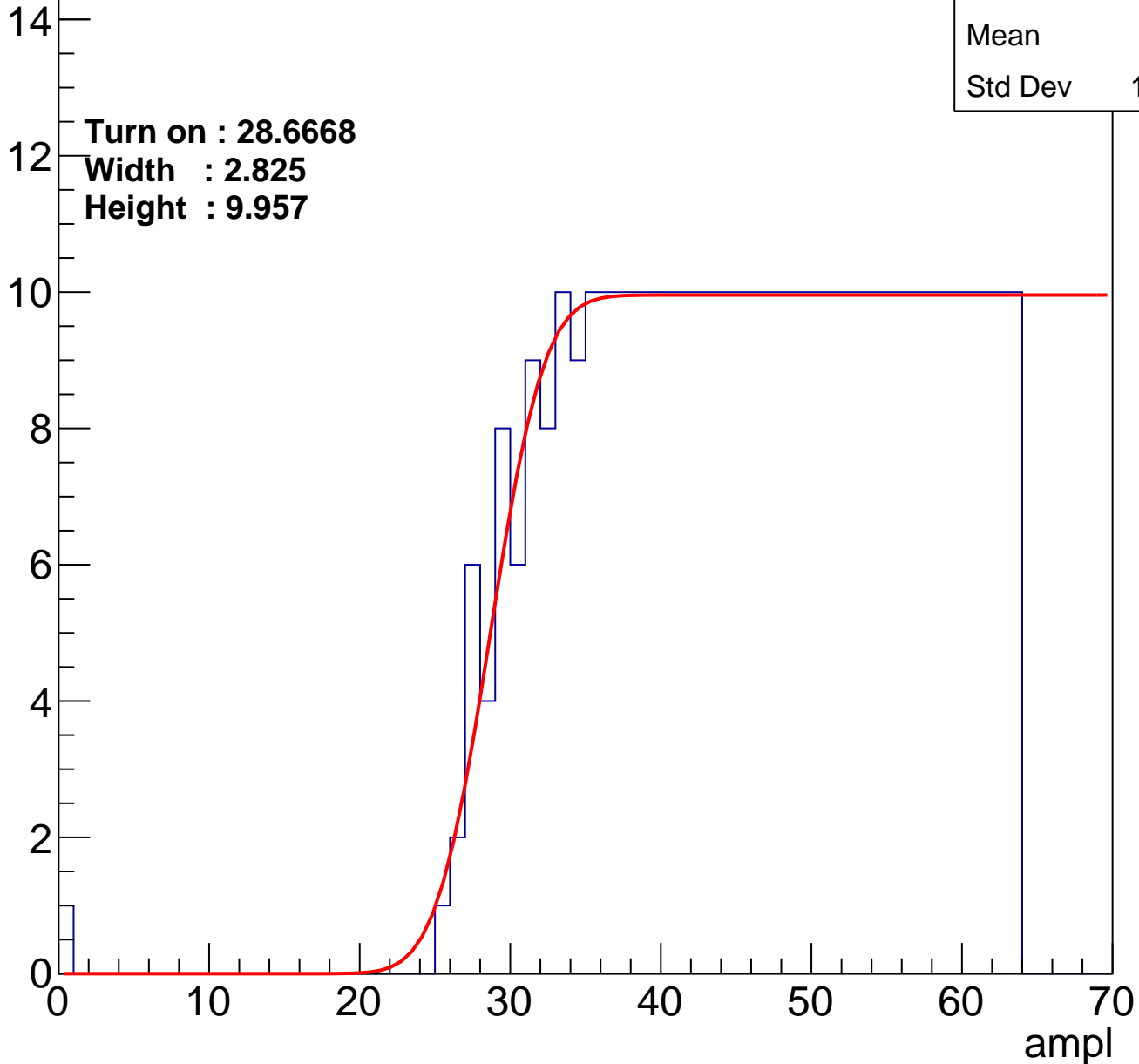
Entries	354
Mean	45.6
Std Dev	10.64

Turn on : 28.6668

Width : 2.825

Height : 9.957

Entry



B1L102S, U26-ch42

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.41
Std Dev	12.09

Turn on : 24.8045

Width : 2.426

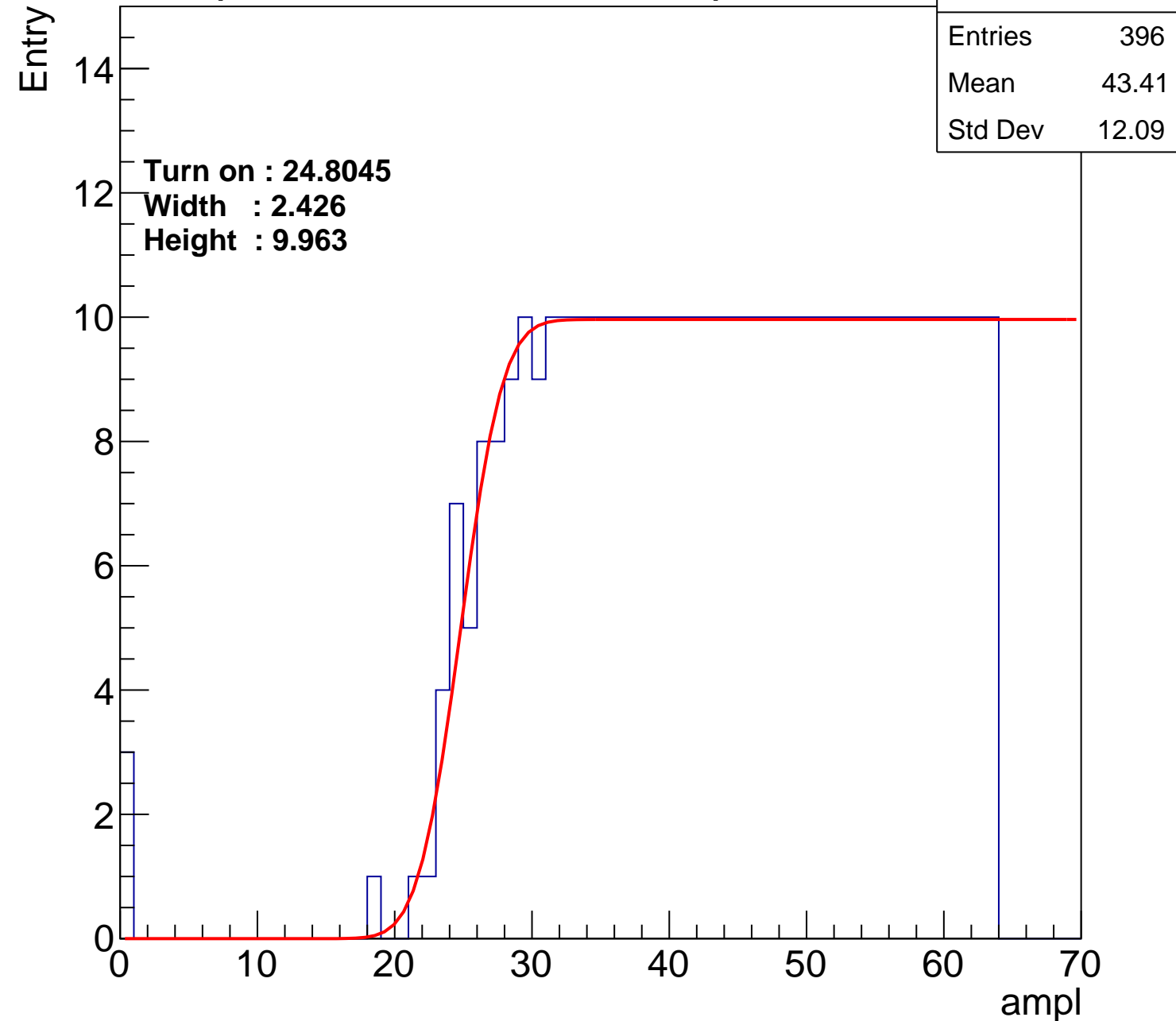
Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch43

calib_packv5_042523_0143.root, FC#11, port A2

Entries	364
Mean	45.08
Std Dev	11.05

Turn on : 27.7068

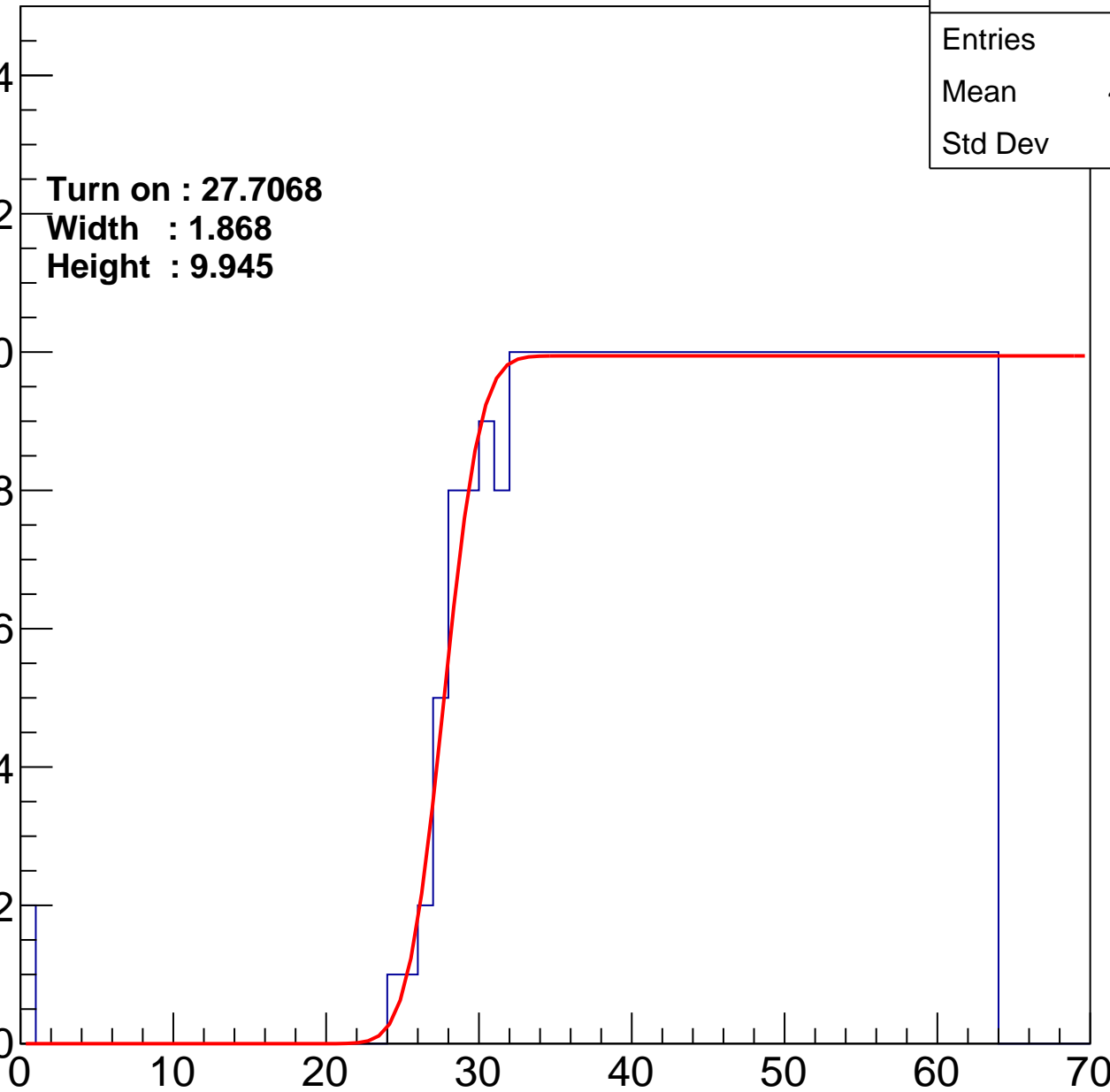
Width : 1.868

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch44

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.7
Std Dev	11.95

Turn on : 25.4536

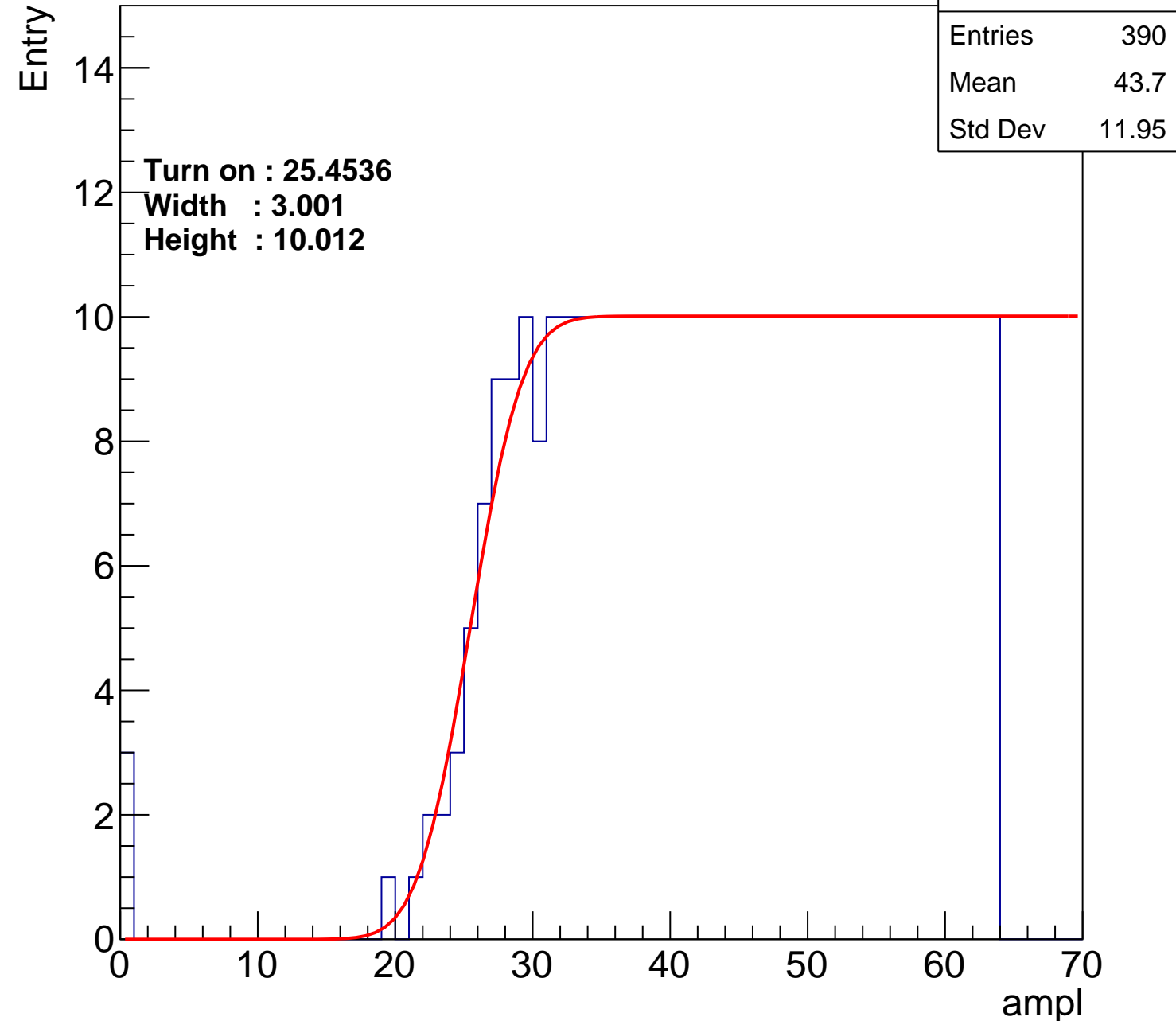
Width : 3.001

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch45

calib_packv5_042523_0143.root, FC#11, port A2

Entries	364
Mean	44.99
Std Dev	11.29

Turn on : 27.8739

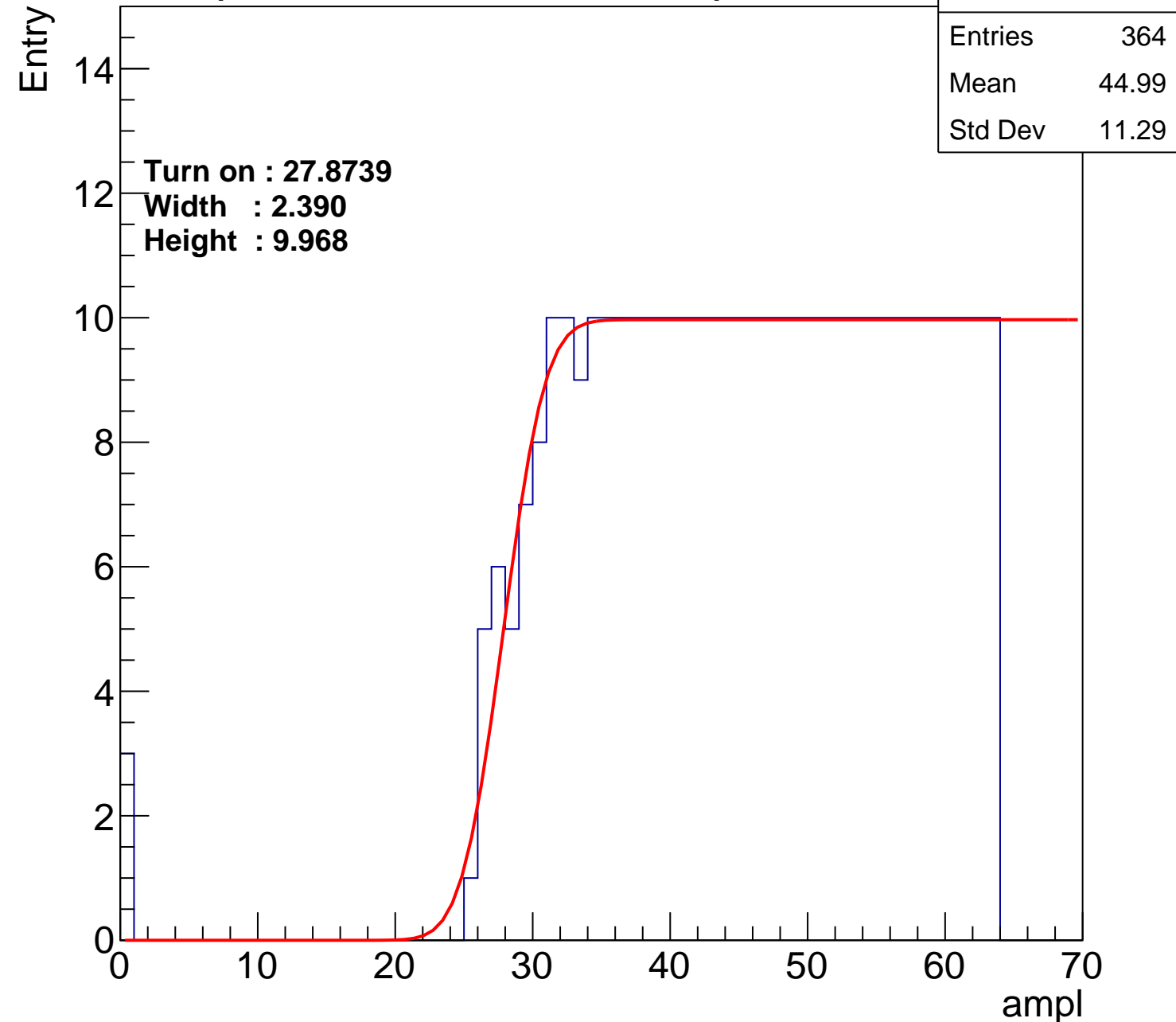
Width : 2.390

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch46

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.64
Std Dev	11.93

Turn on : 25.0889

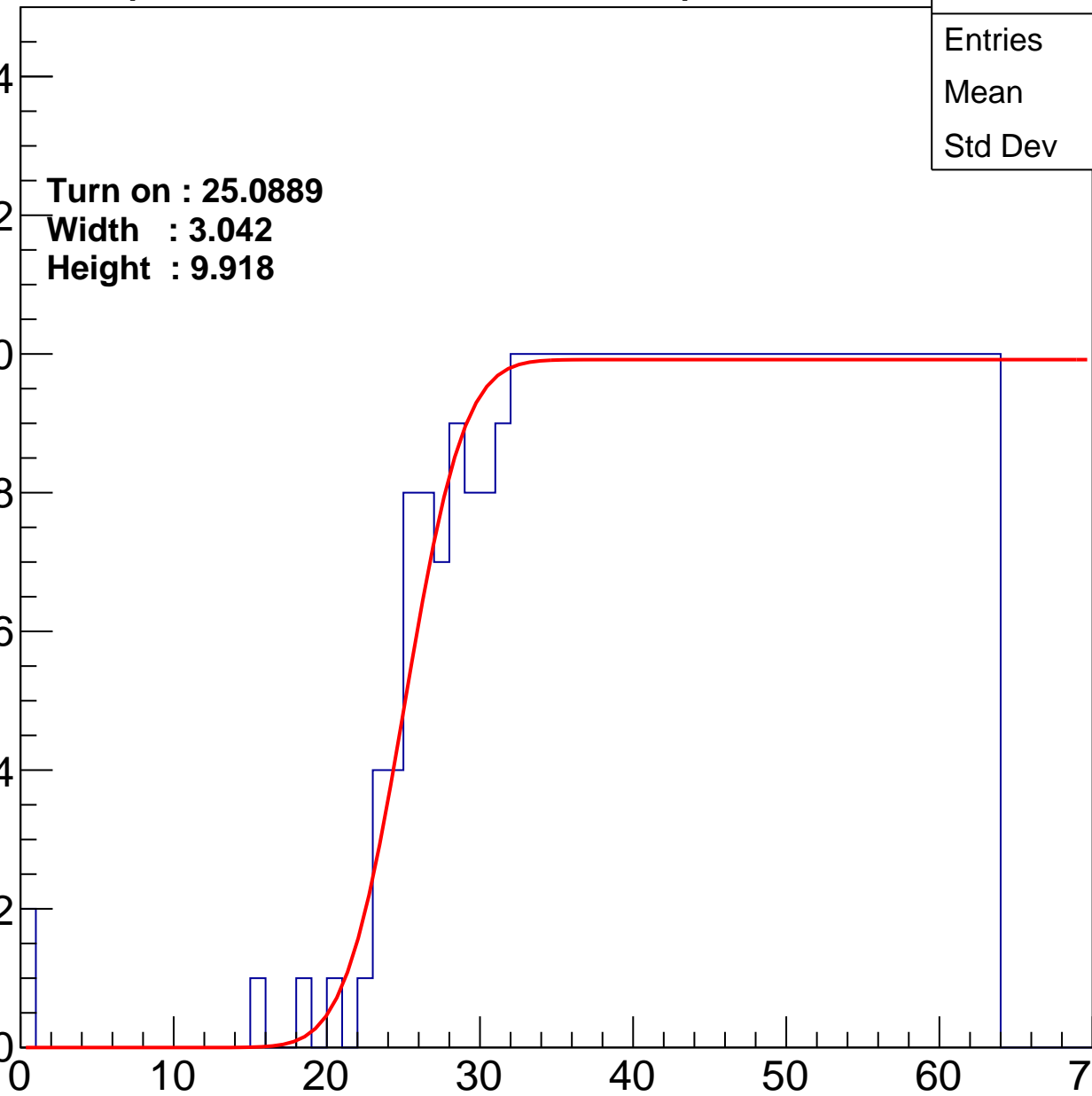
Width : 3.042

Height : 9.918

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch47

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 26.8641

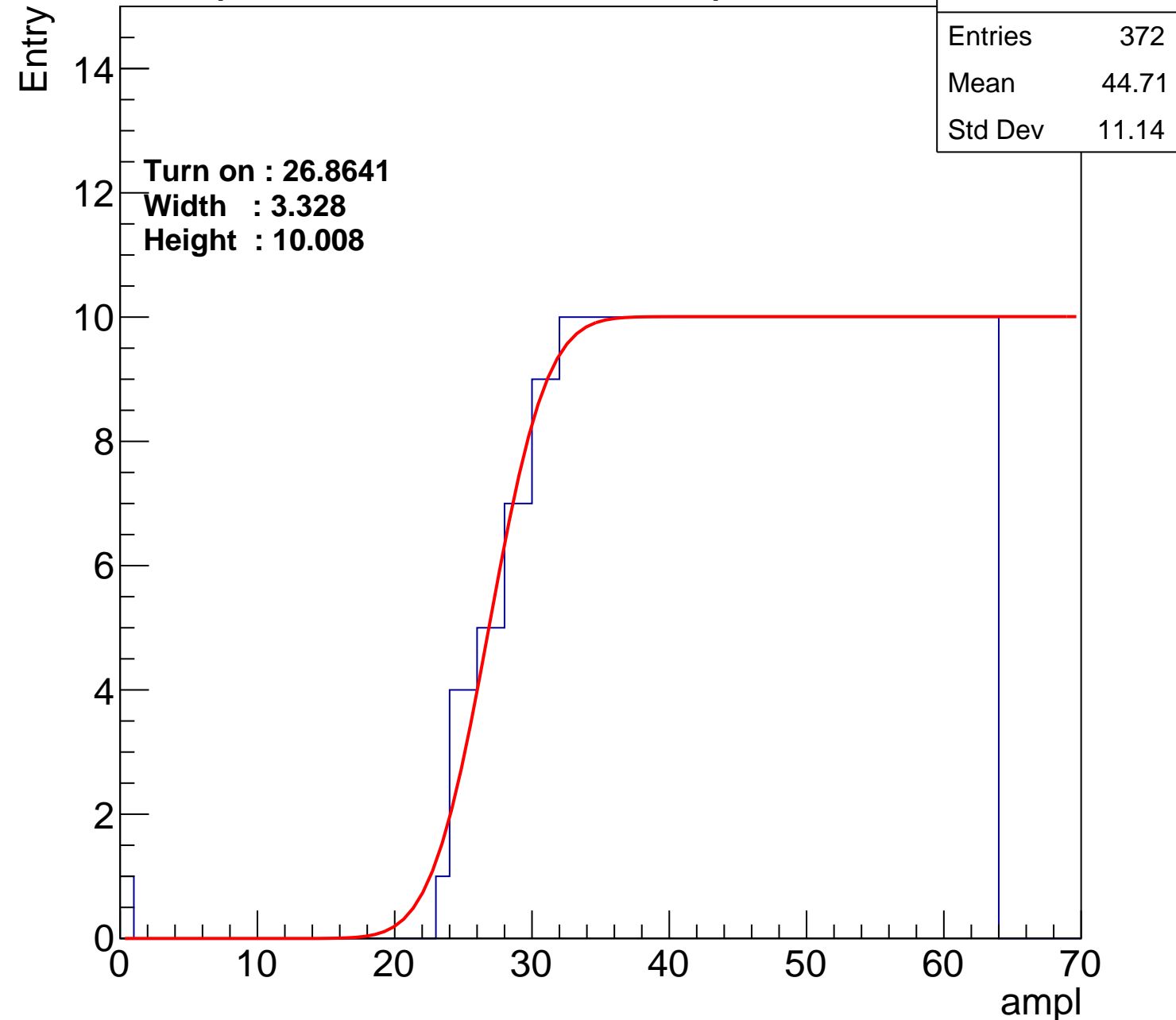
Width : 3.328

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch48

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.7
Std Dev	11.16

Turn on : 27.1784

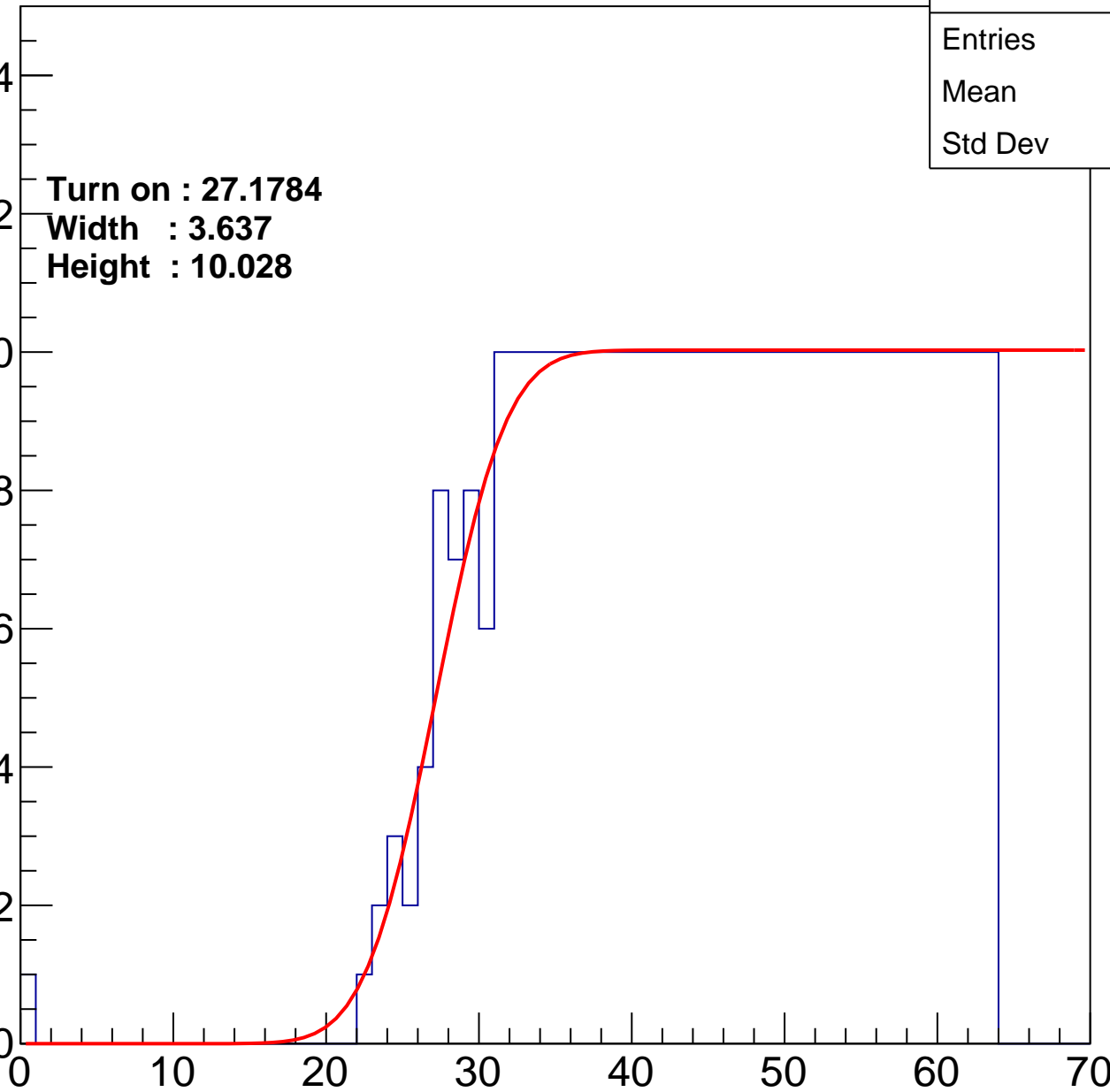
Width : 3.637

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch49

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 25.2076

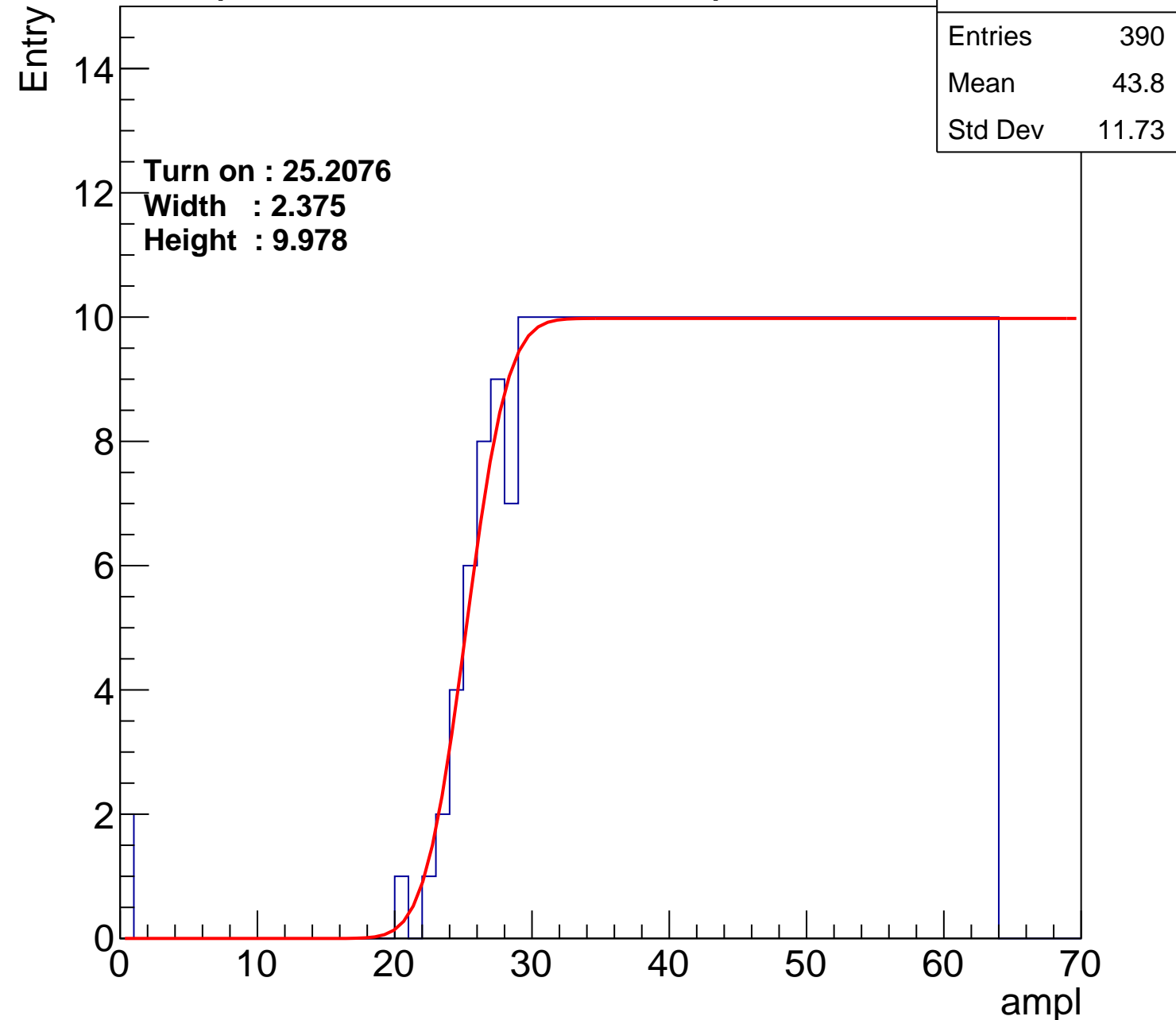
Width : 2.375

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch50

calib_packv5_042523_0143.root, FC#11, port A2

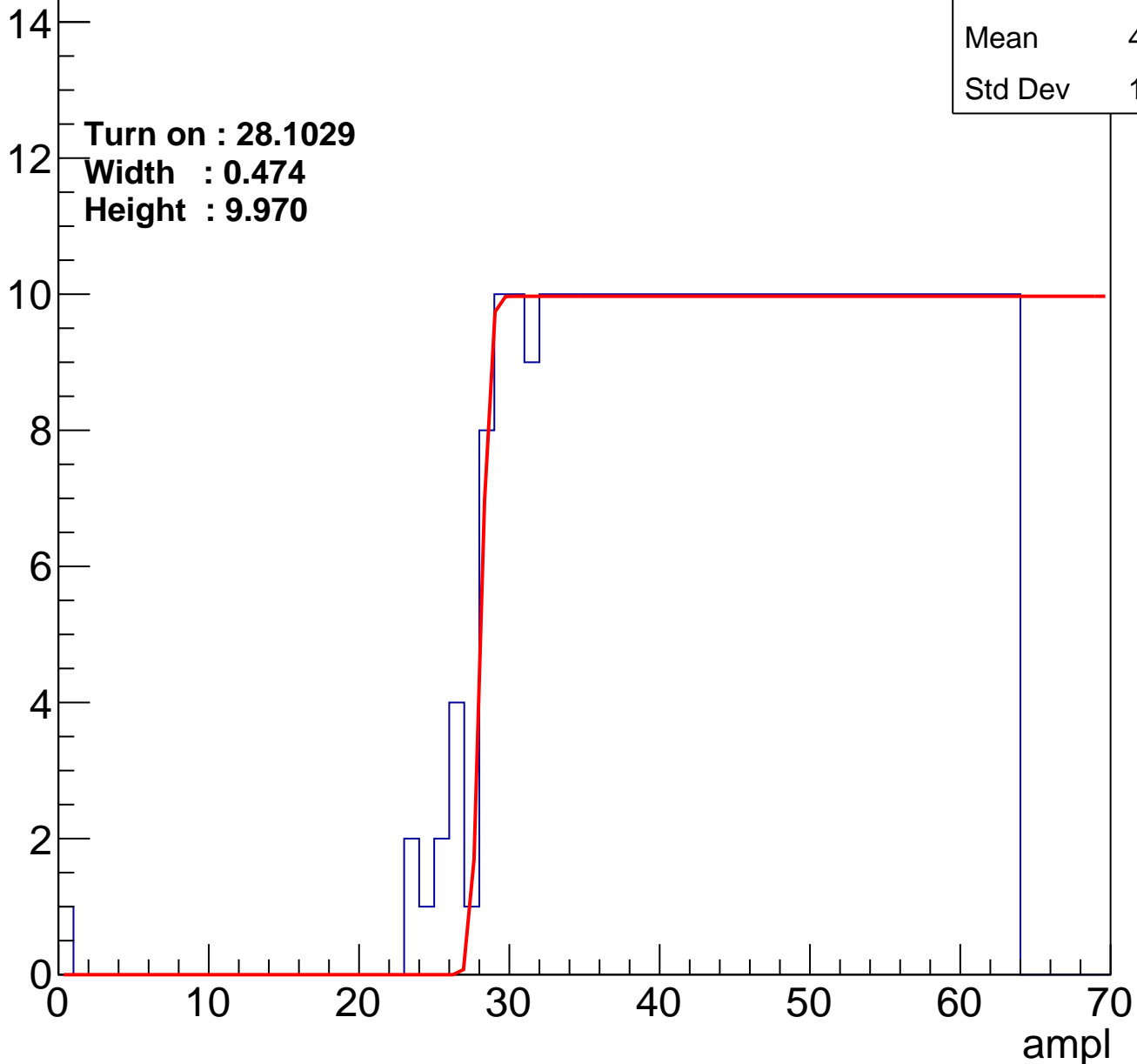
Entry

Entries	368
Mean	44.96
Std Dev	10.96

Turn on : 28.1029

Width : 0.474

Height : 9.970



B1L102S, U26-ch51

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.49
Std Dev	11.69

Turn on : 27.3491

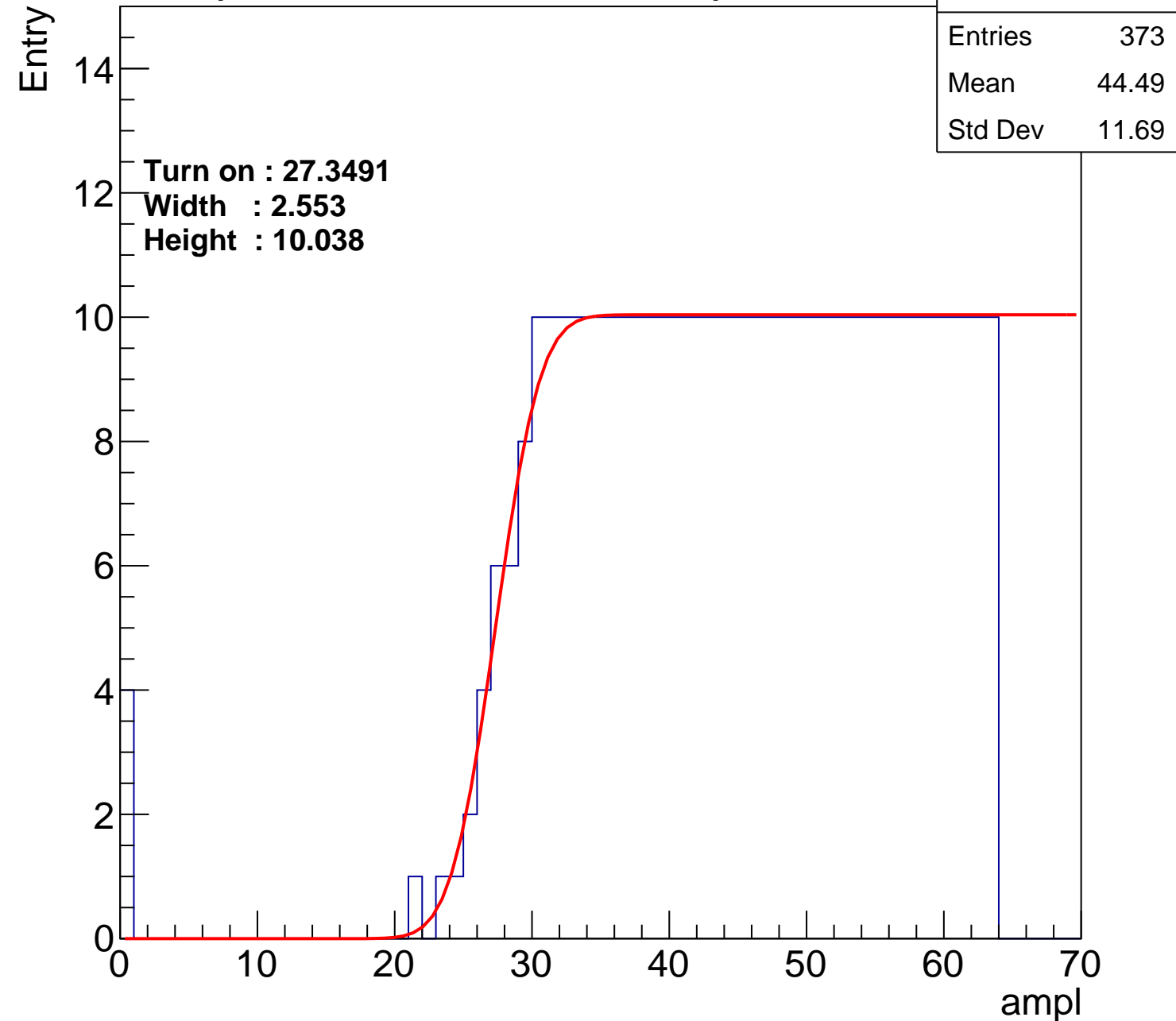
Width : 2.553

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch52

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.22
Std Dev	11.66

Turn on : 26.8710

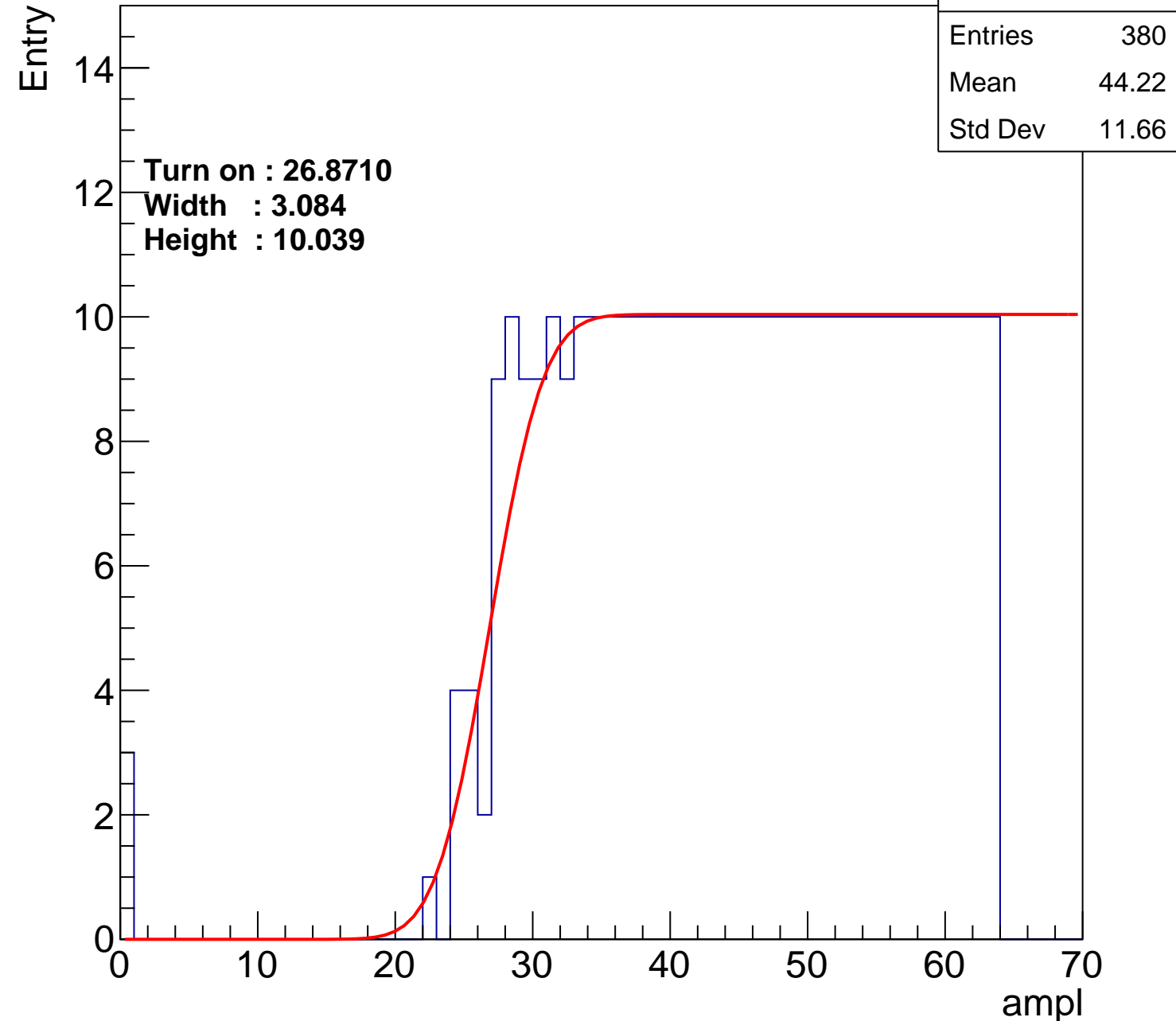
Width : 3.084

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch53

calib_packv5_042523_0143.root, FC#11, port A2

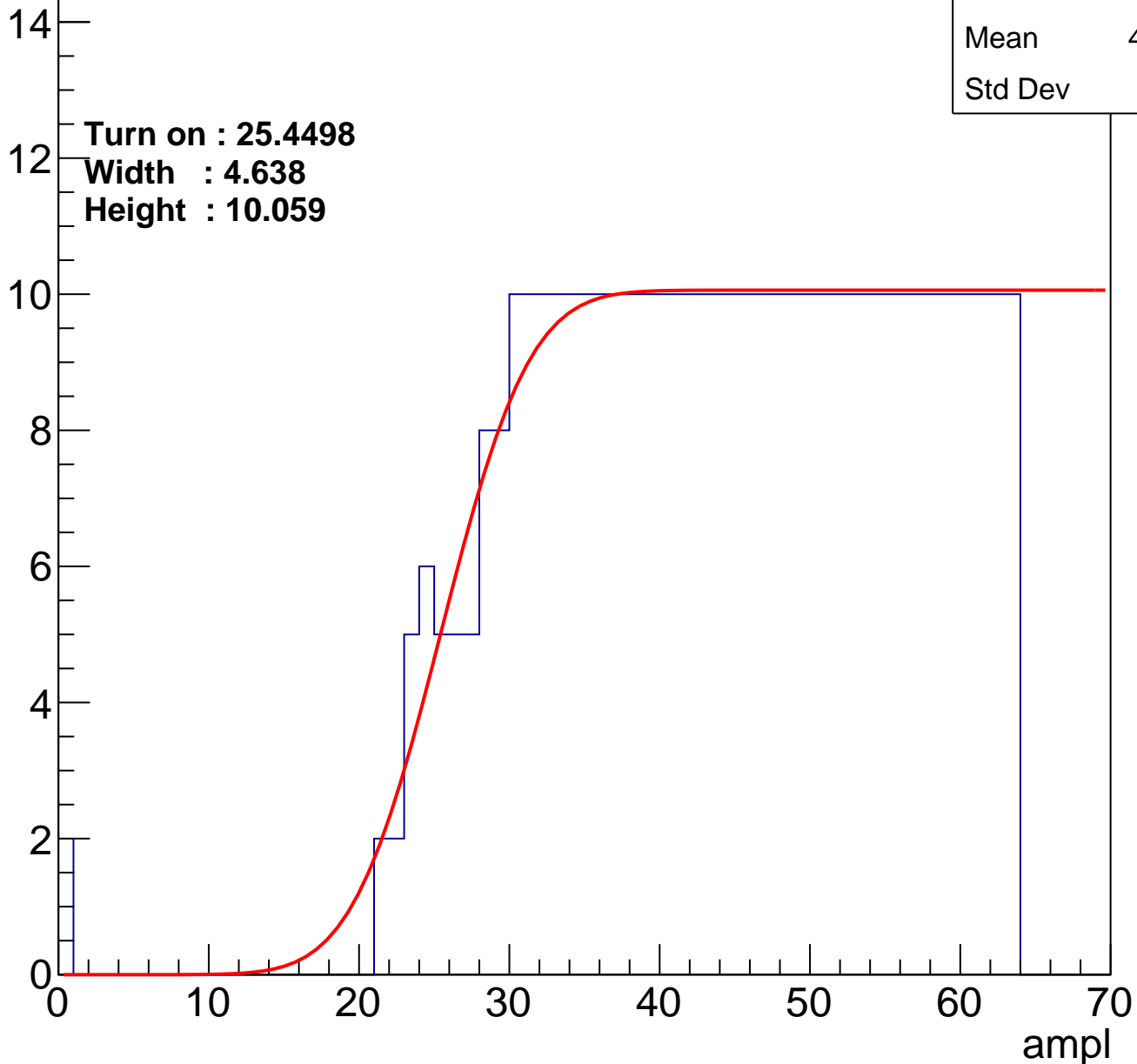
Entries	388
Mean	43.82
Std Dev	11.8

Turn on : 25.4498

Width : 4.638

Height : 10.059

Entry



B1L102S, U26-ch54

calib_packv5_042523_0143.root, FC#11, port A2

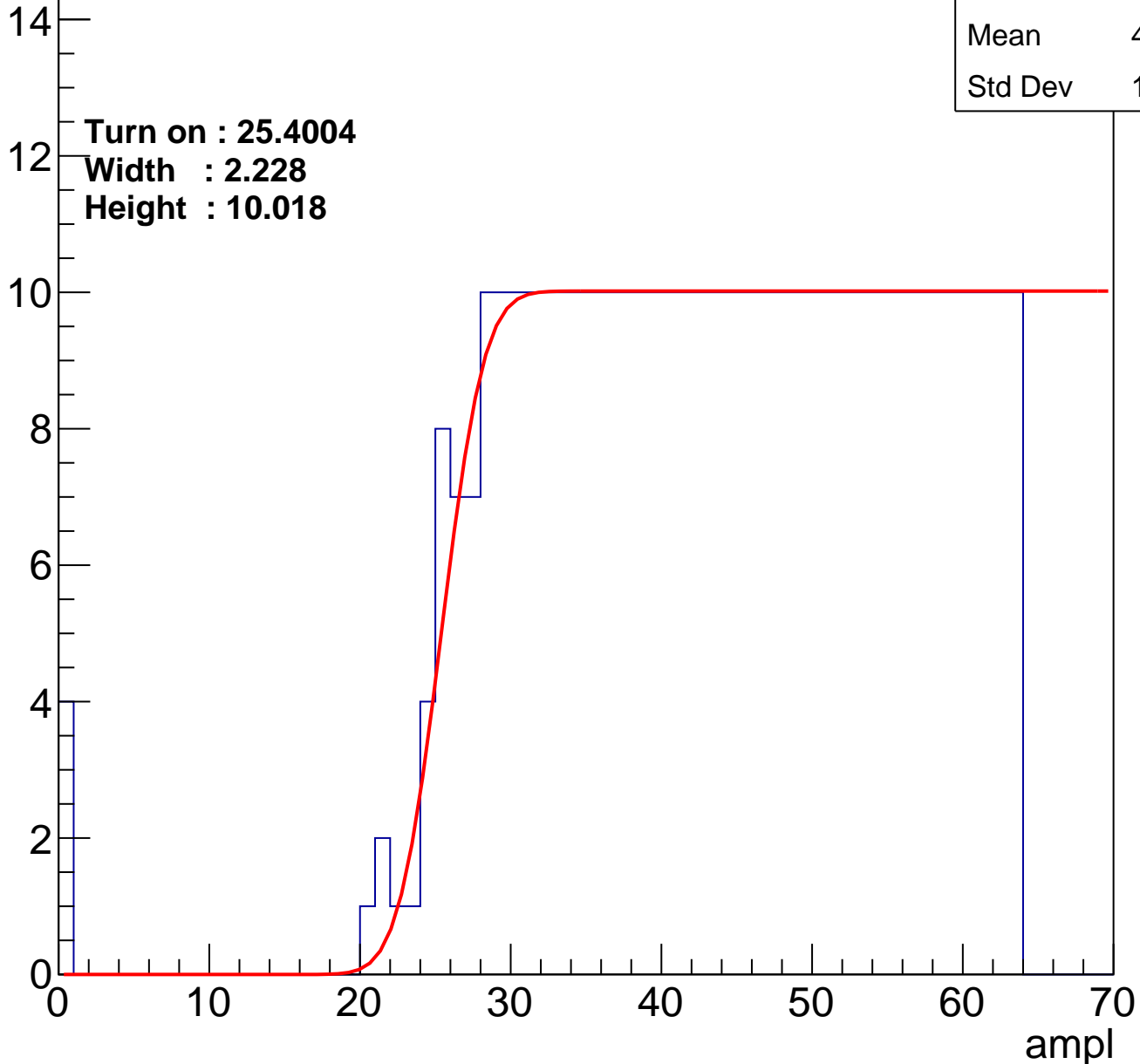
Entries	395
Mean	43.43
Std Dev	12.18

Turn on : 25.4004

Width : 2.228

Height : 10.018

Entry



B1L102S, U26-ch55

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.77
Std Dev	11.77

Turn on : 25.5696

Width : 3.508

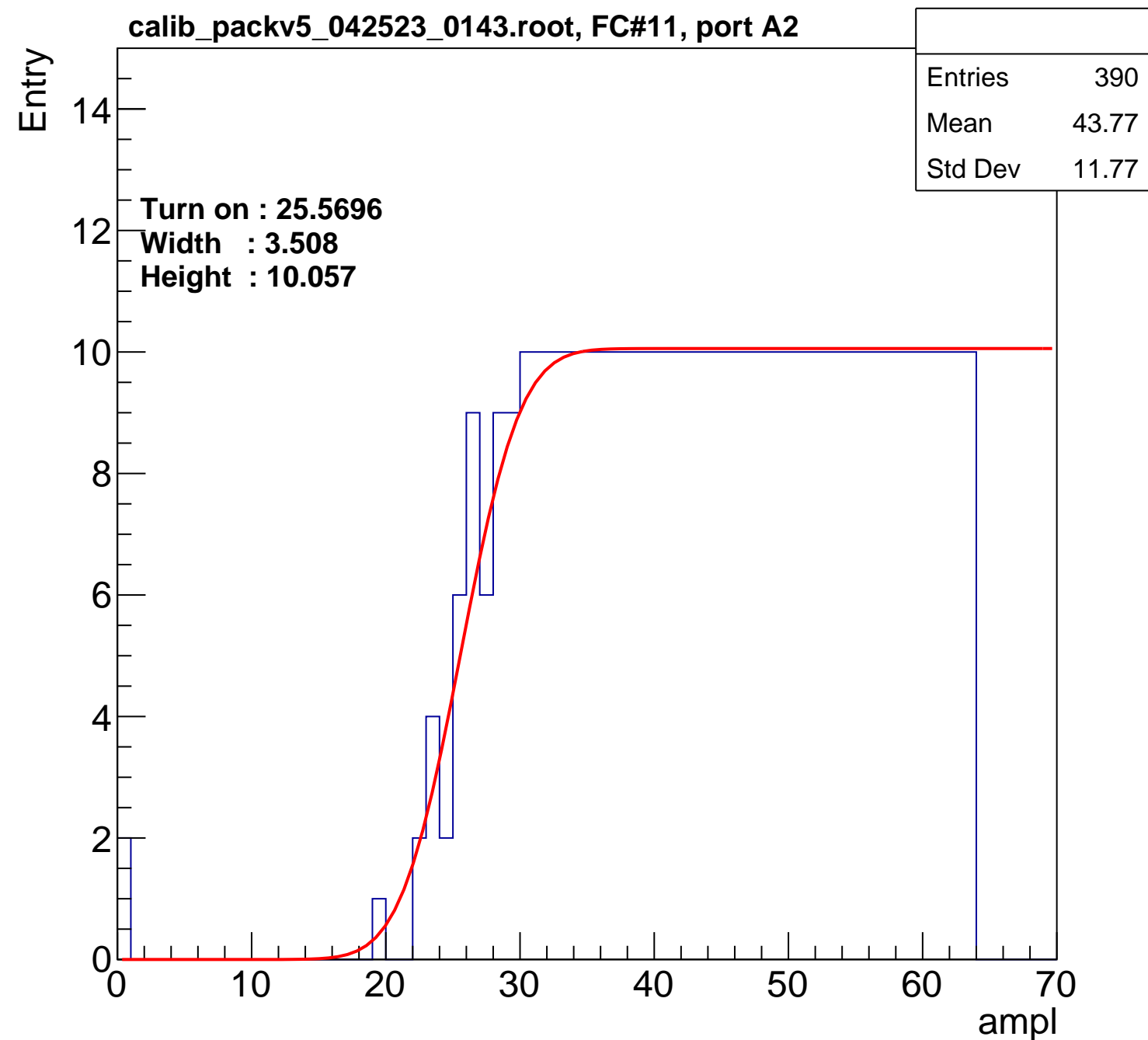
Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U26-ch56

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.84
Std Dev	11.71

Turn on : 24.9944

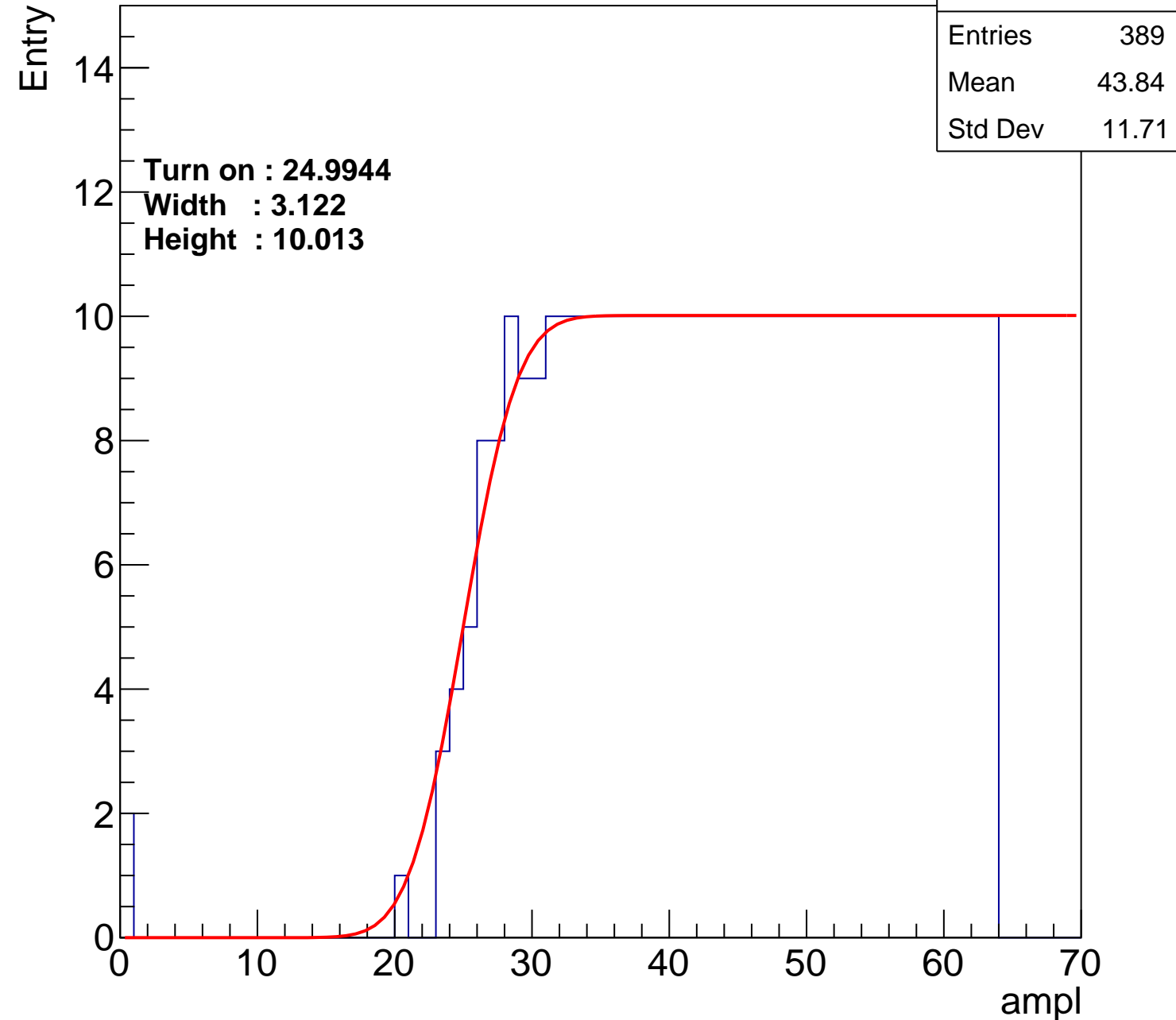
Width : 3.122

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch57

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.45
Std Dev	11.44

Turn on : 26.9613

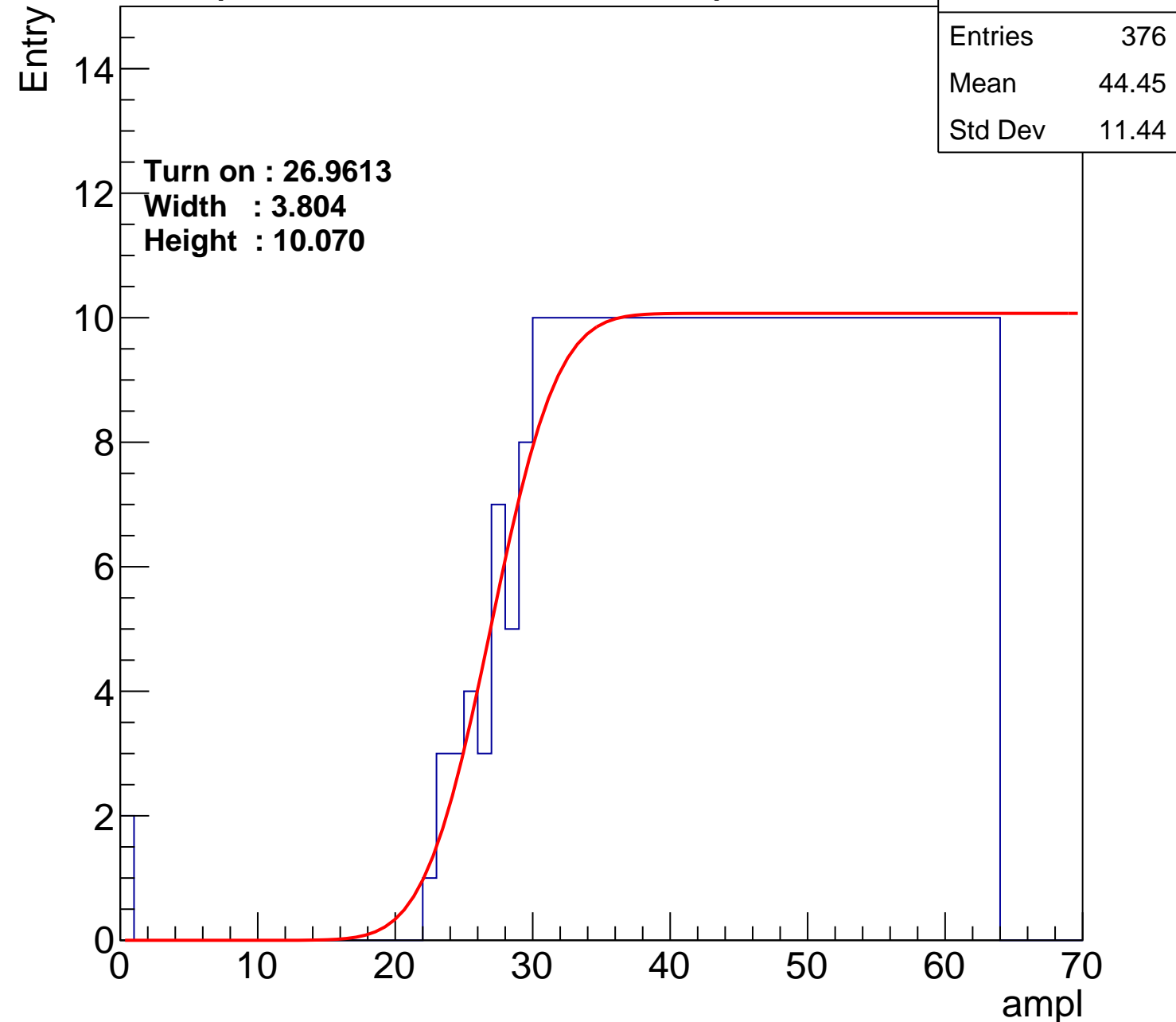
Width : 3.804

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch58

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.81
Std Dev	11.71

Turn on : 25.6152

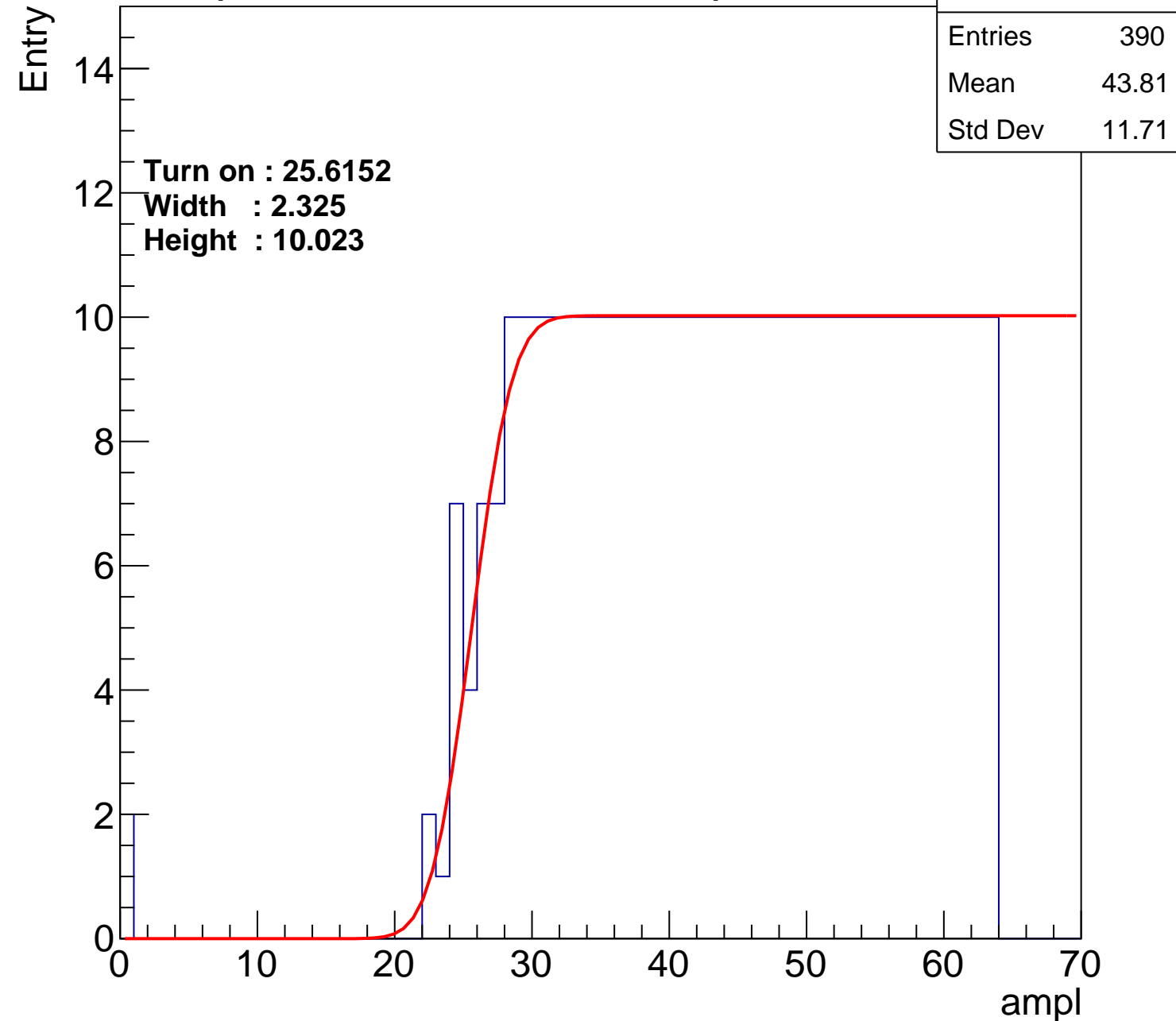
Width : 2.325

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch59

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.29
Std Dev	11.46

Turn on : 26.2957

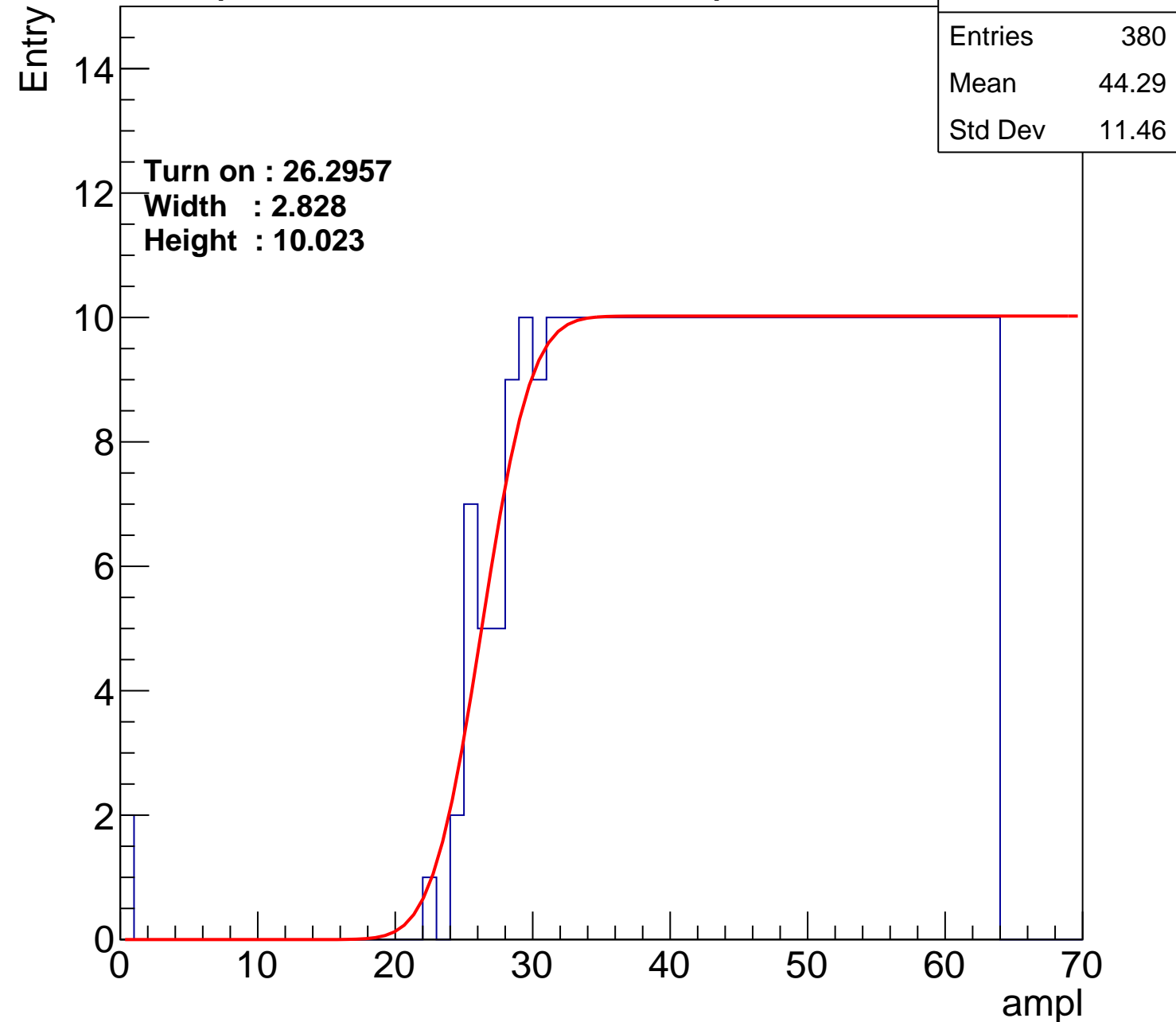
Width : 2.828

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch60

calib_packv5_042523_0143.root, FC#11, port A2

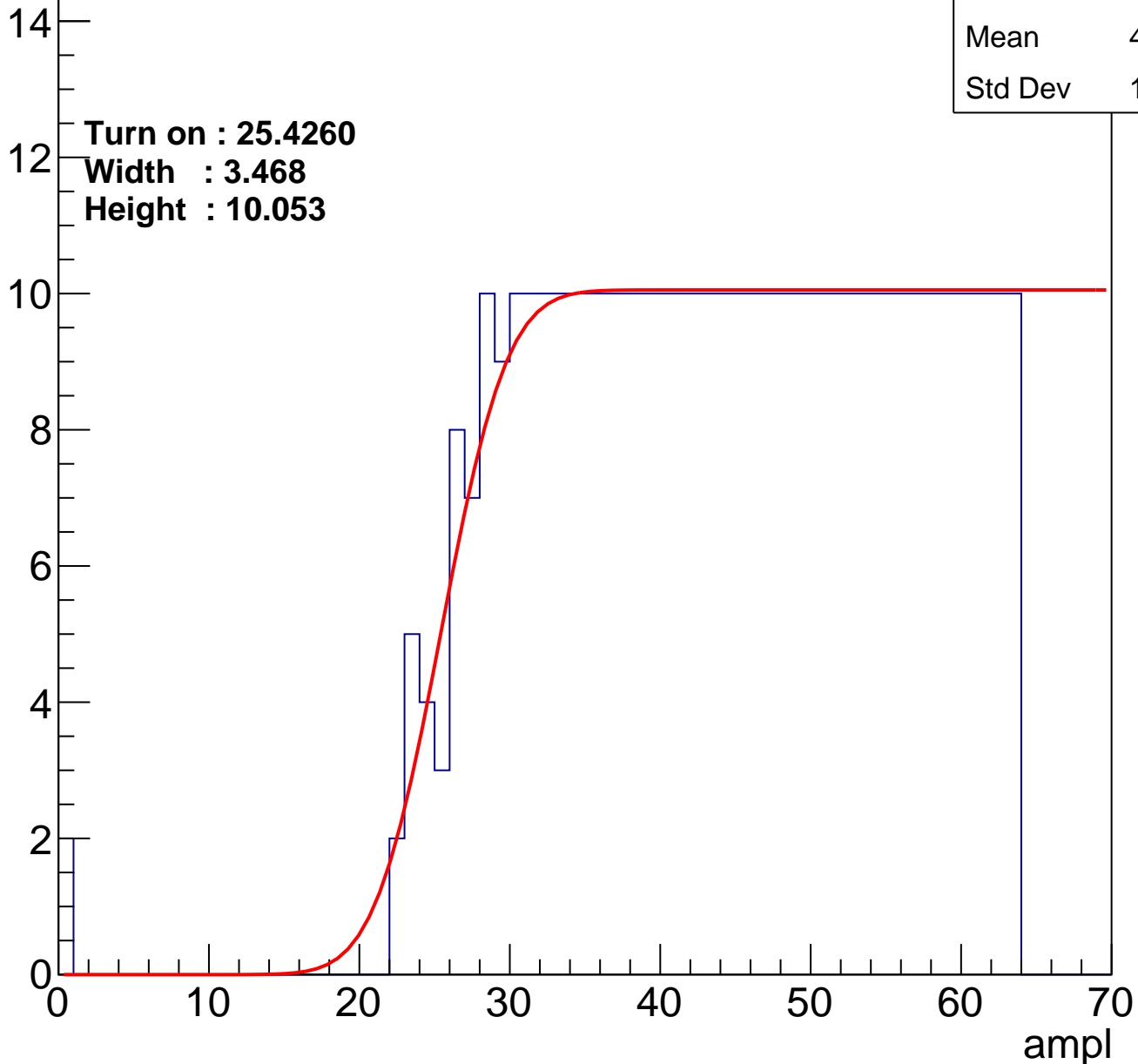
Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.4260

Width : 3.468

Height : 10.053

Entry



B1L102S, U26-ch61

calib_packv5_042523_0143.root, FC#11, port A2

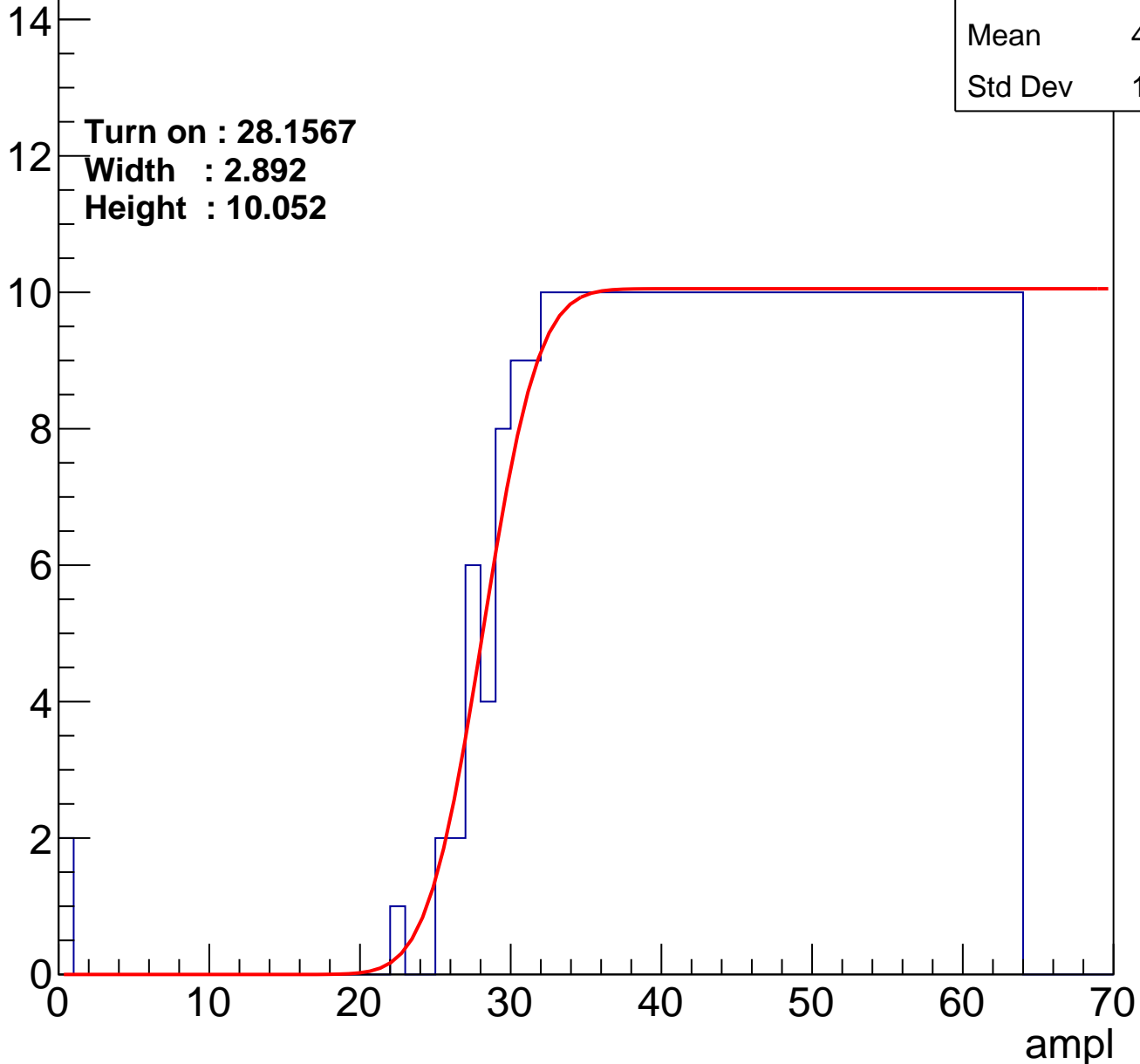
Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 28.1567

Width : 2.892

Height : 10.052

Entry



B1L102S, U26-ch62

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	42.81
Std Dev	12.67

Turn on : 24.7314

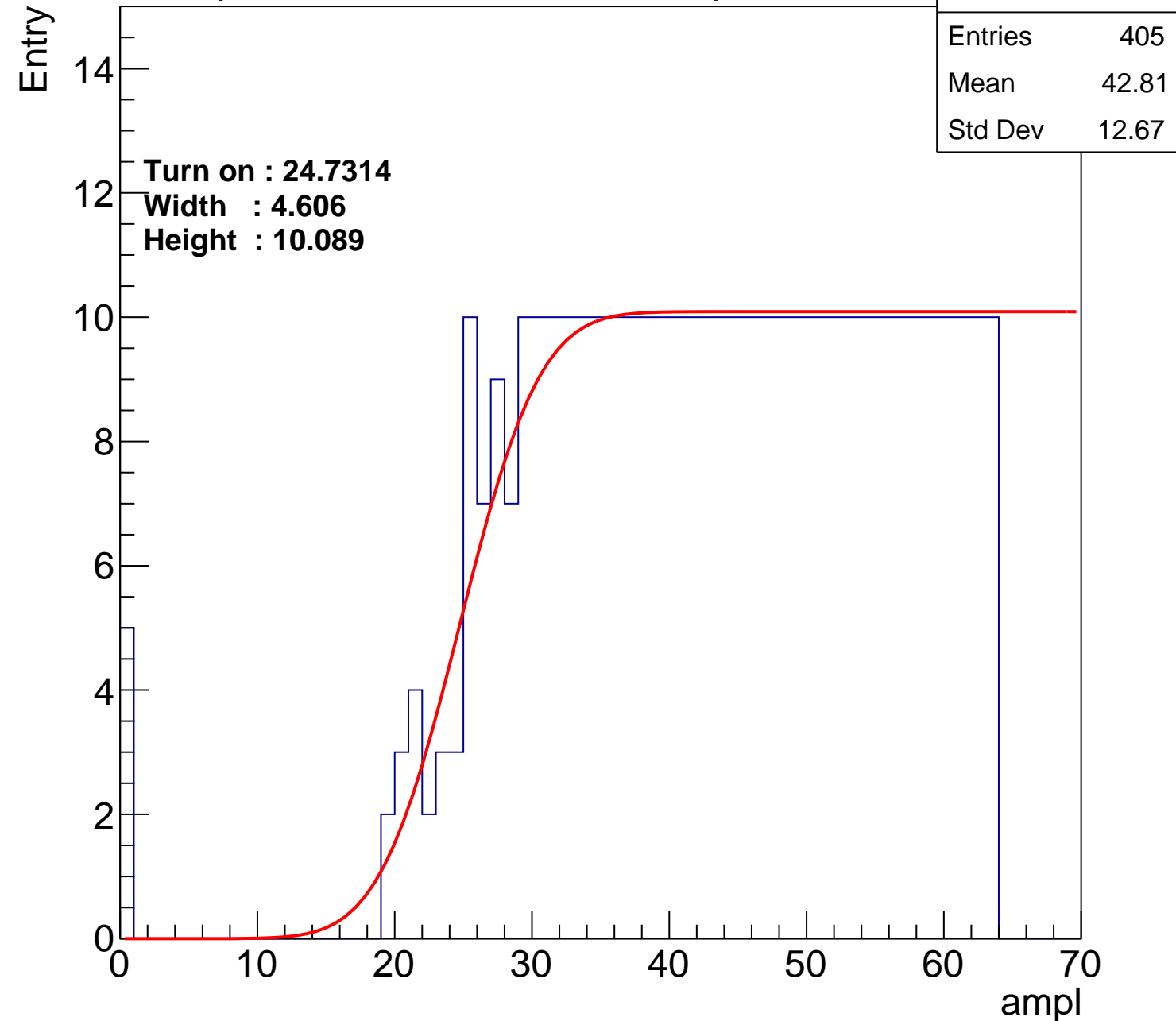
Width : 4.606

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch63

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.9
Std Dev	11.33

Turn on : 27.4339

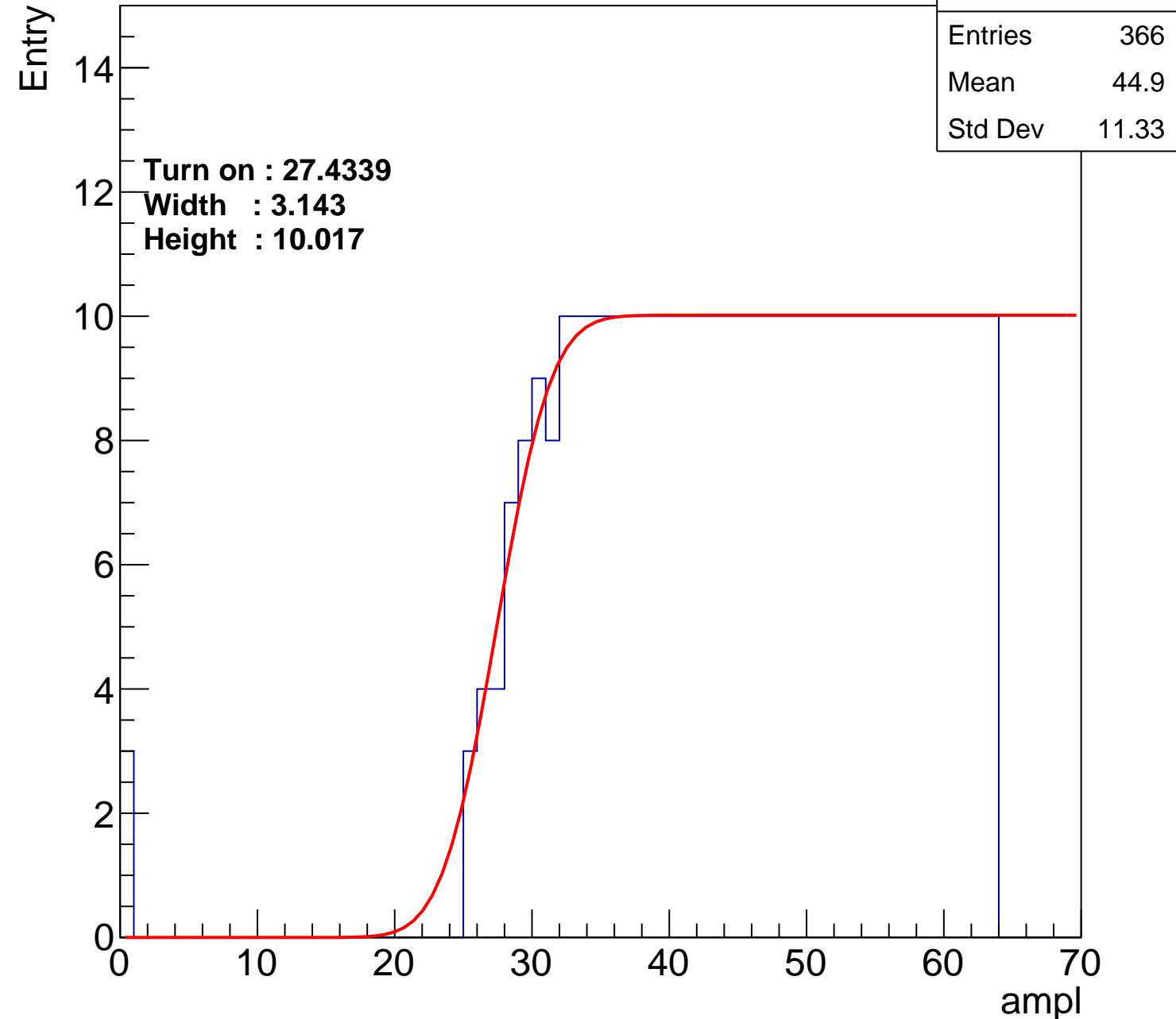
Width : 3.143

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch64

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.9
Std Dev	11.74

Turn on : 26.2531

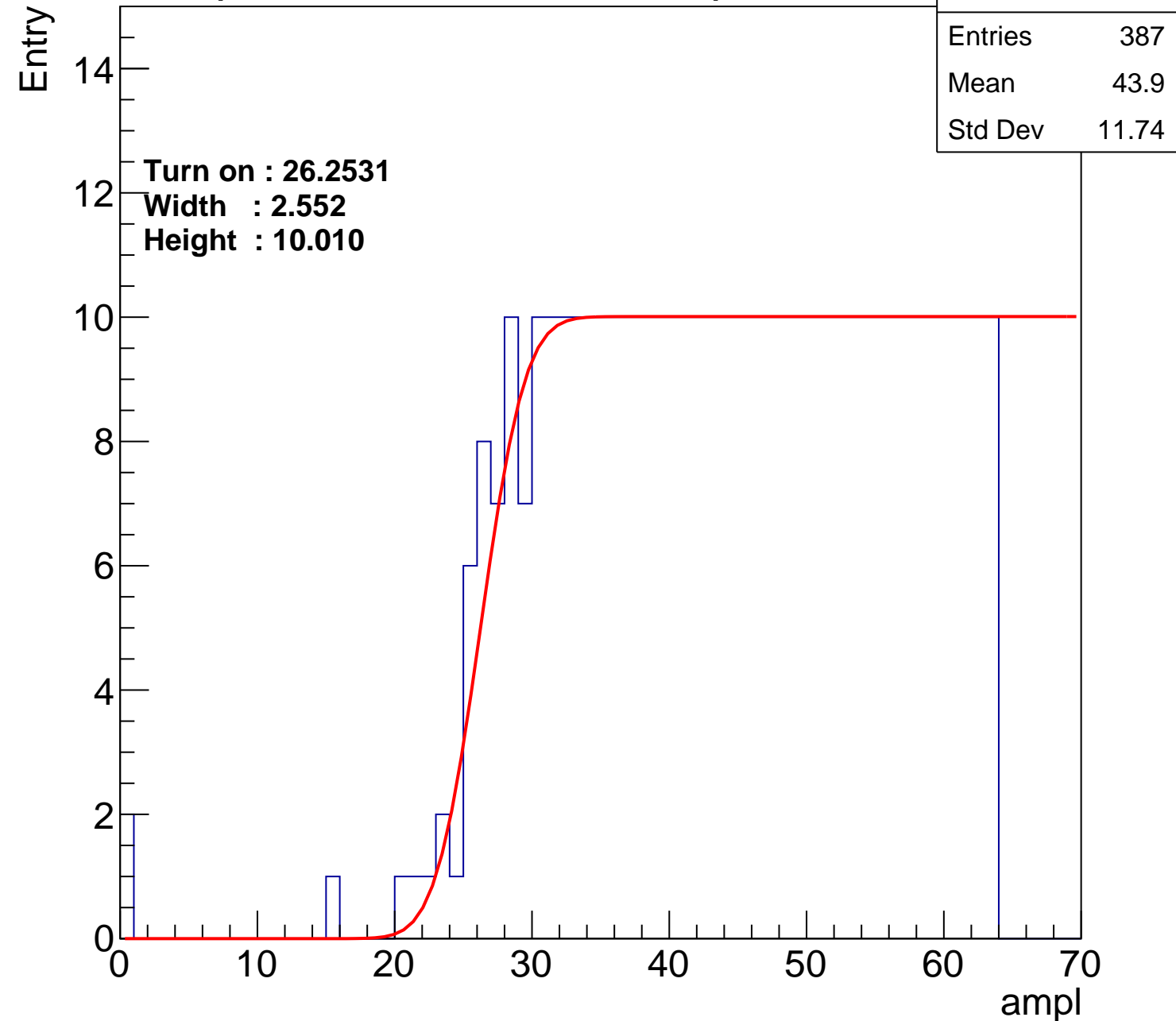
Width : 2.552

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch65

calib_packv5_042523_0143.root, FC#11, port A2

Entries	399
Mean	43.36
Std Dev	11.95

Turn on : 23.8951

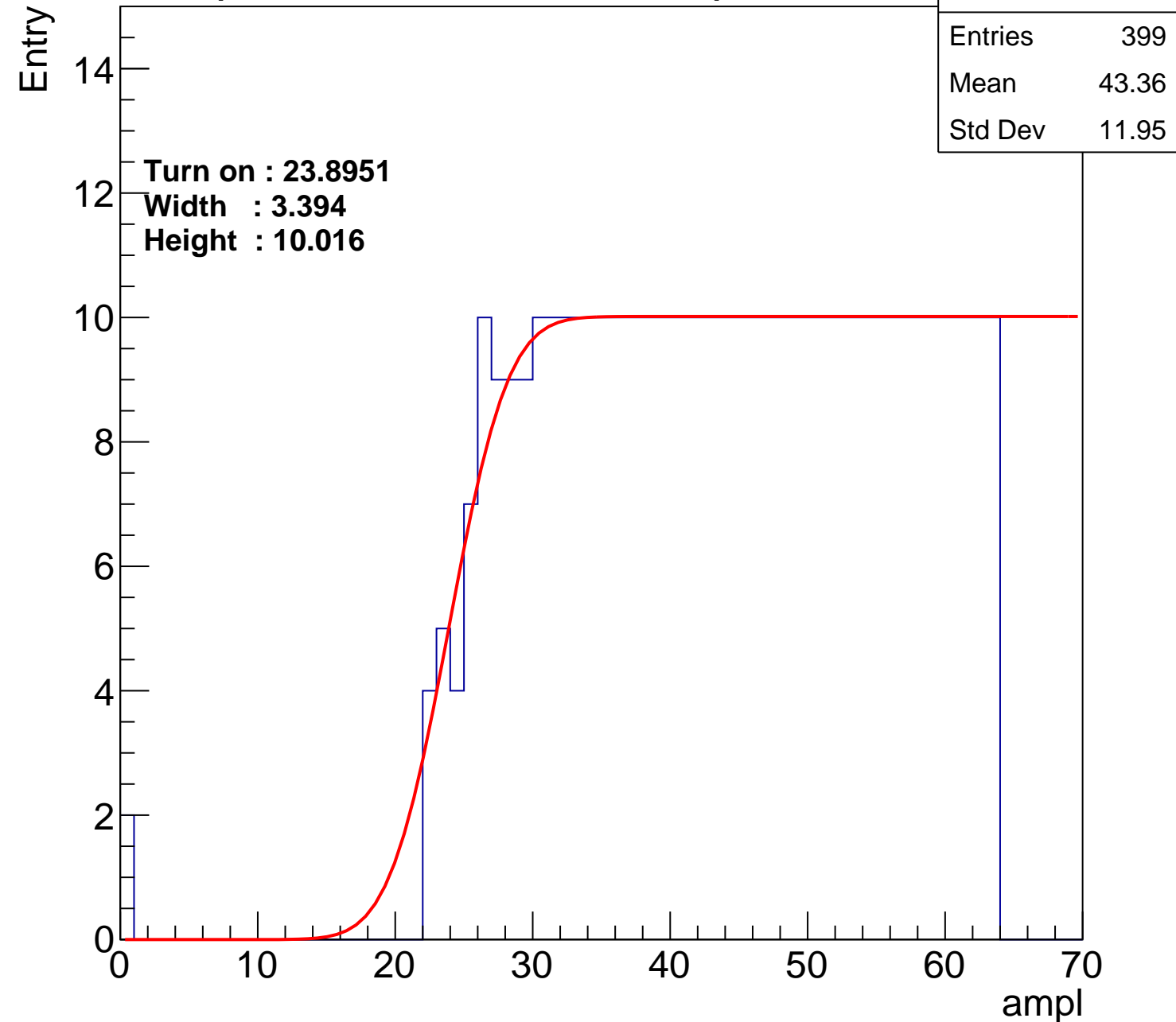
Width : 3.394

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch66

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.88
Std Dev	11.08

Turn on : 27.6381

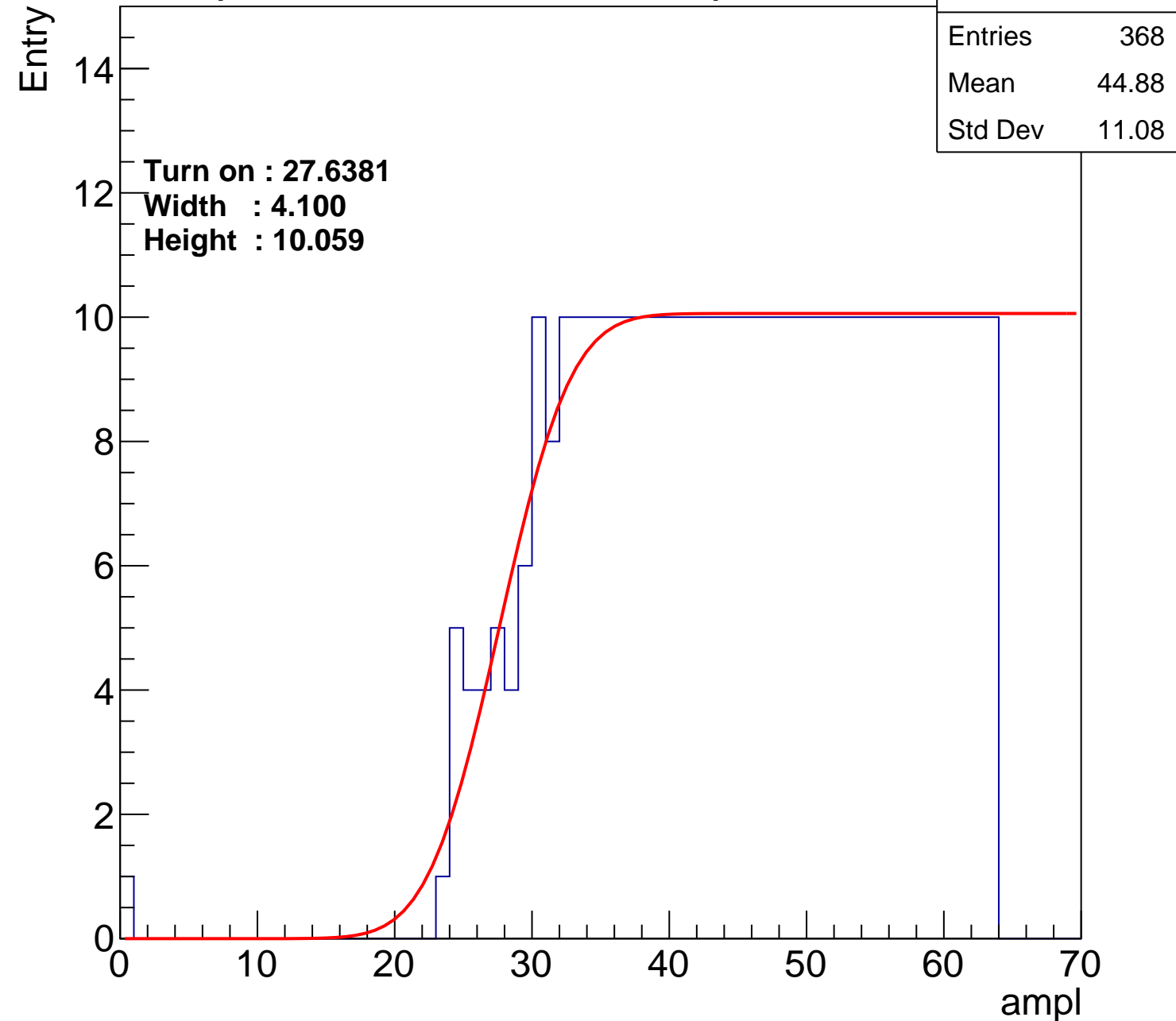
Width : 4.100

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch67

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.71
Std Dev	11.81

Turn on : 25.2175

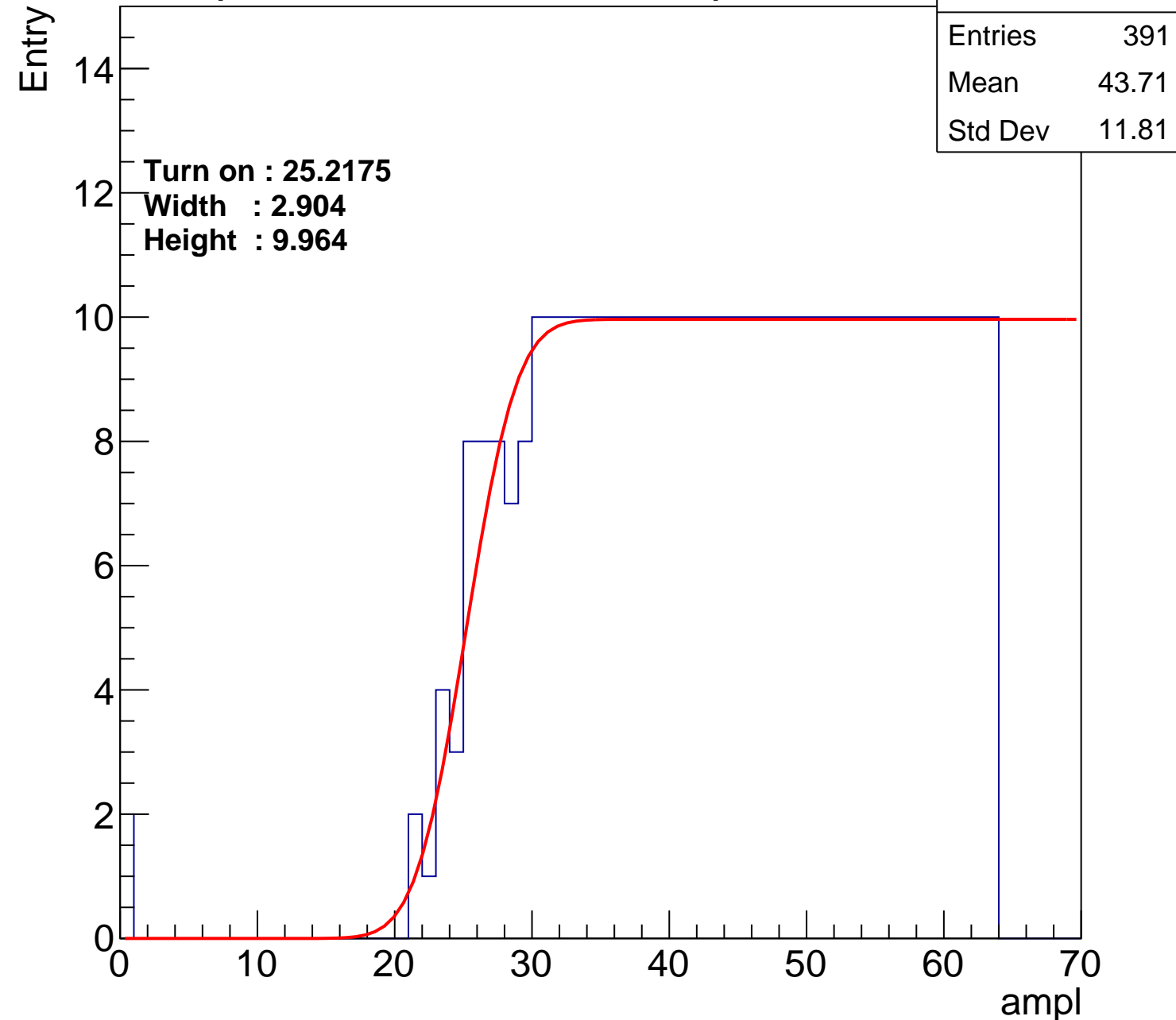
Width : 2.904

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch68

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.13
Std Dev	12.05

Turn on : 26.6371

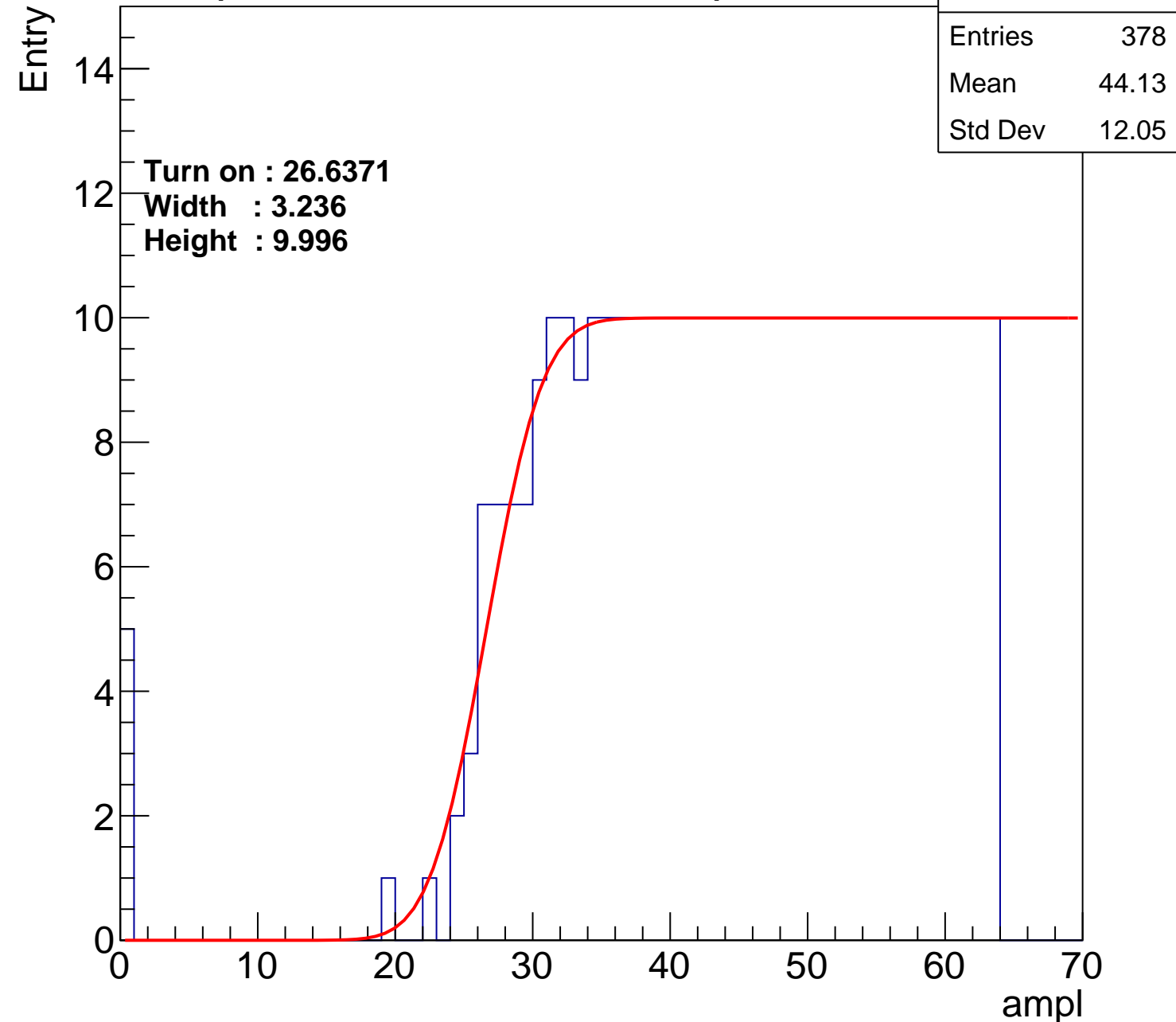
Width : 3.236

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch69

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.37
Std Dev	11.59

Turn on : 26.8625

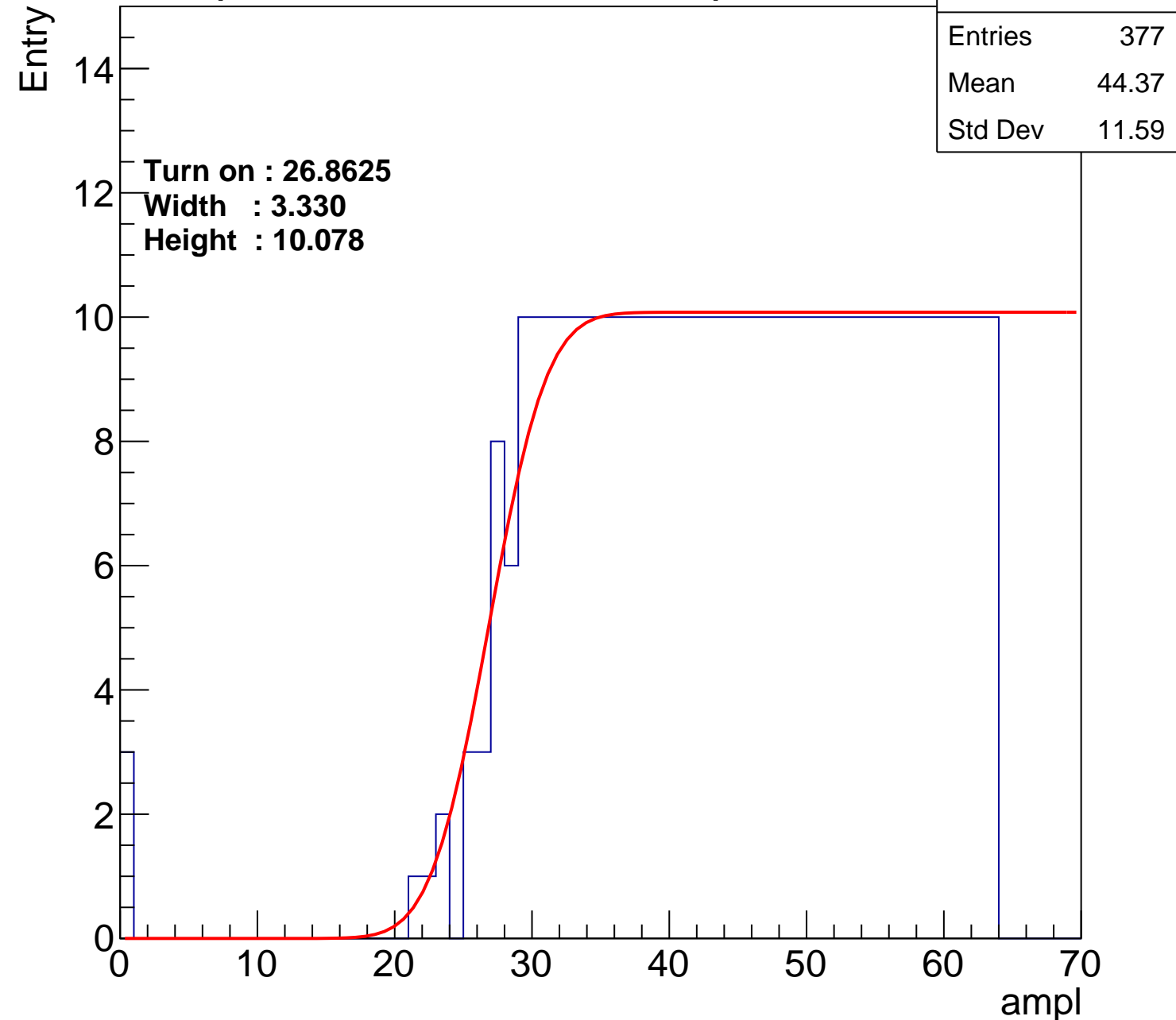
Width : 3.330

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch70

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.77
Std Dev	11.77

Turn on : 25.3240

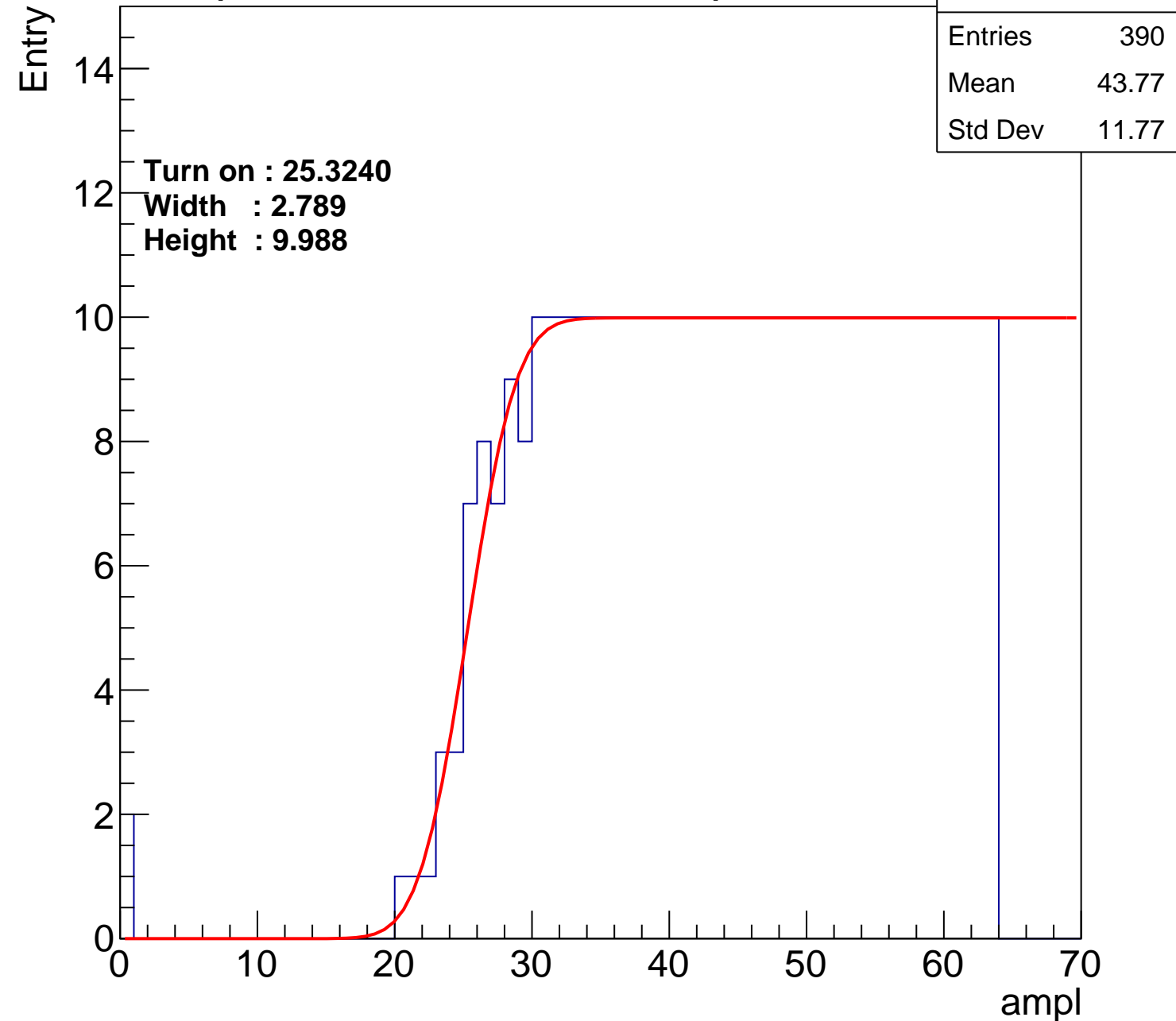
Width : 2.789

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch71

calib_packv5_042523_0143.root, FC#11, port A2

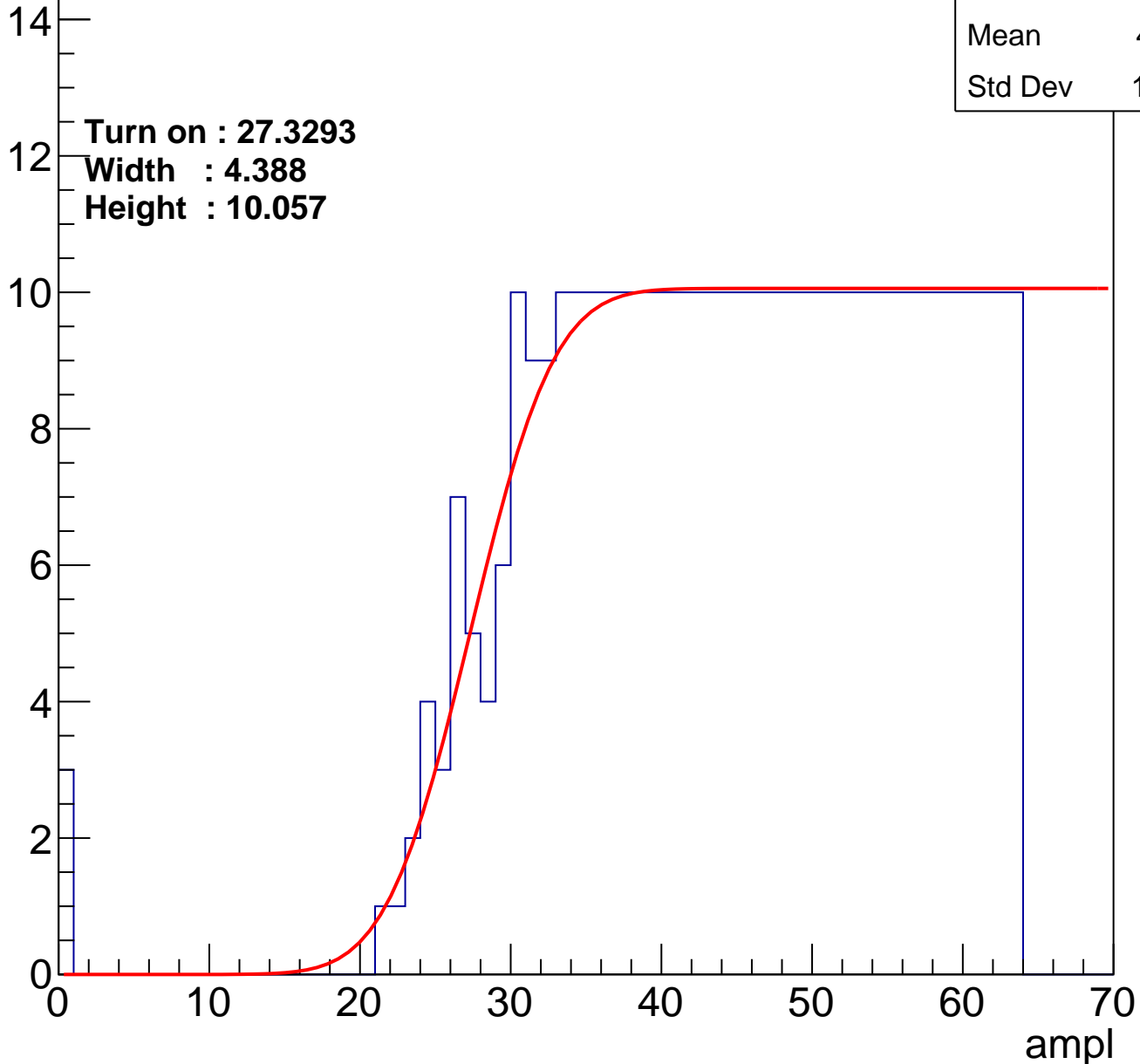
Entries	374
Mean	44.41
Std Dev	11.67

Turn on : 27.3293

Width : 4.388

Height : 10.057

Entry



B1L102S, U26-ch72

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.3
Std Dev	12.28

Turn on : 25.1503

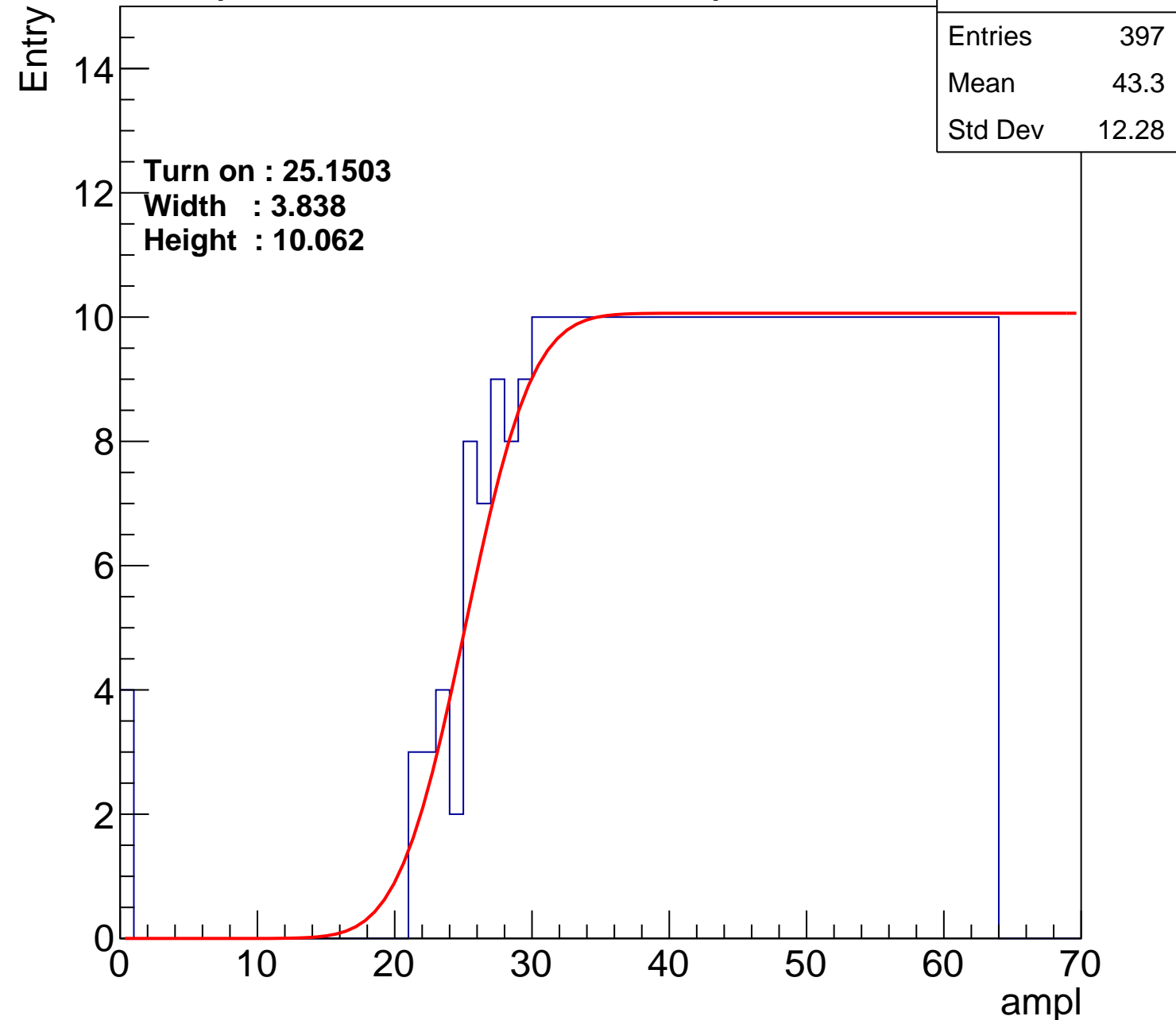
Width : 3.838

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch73

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.02
Std Dev	11.89

Turn on : 26.1369

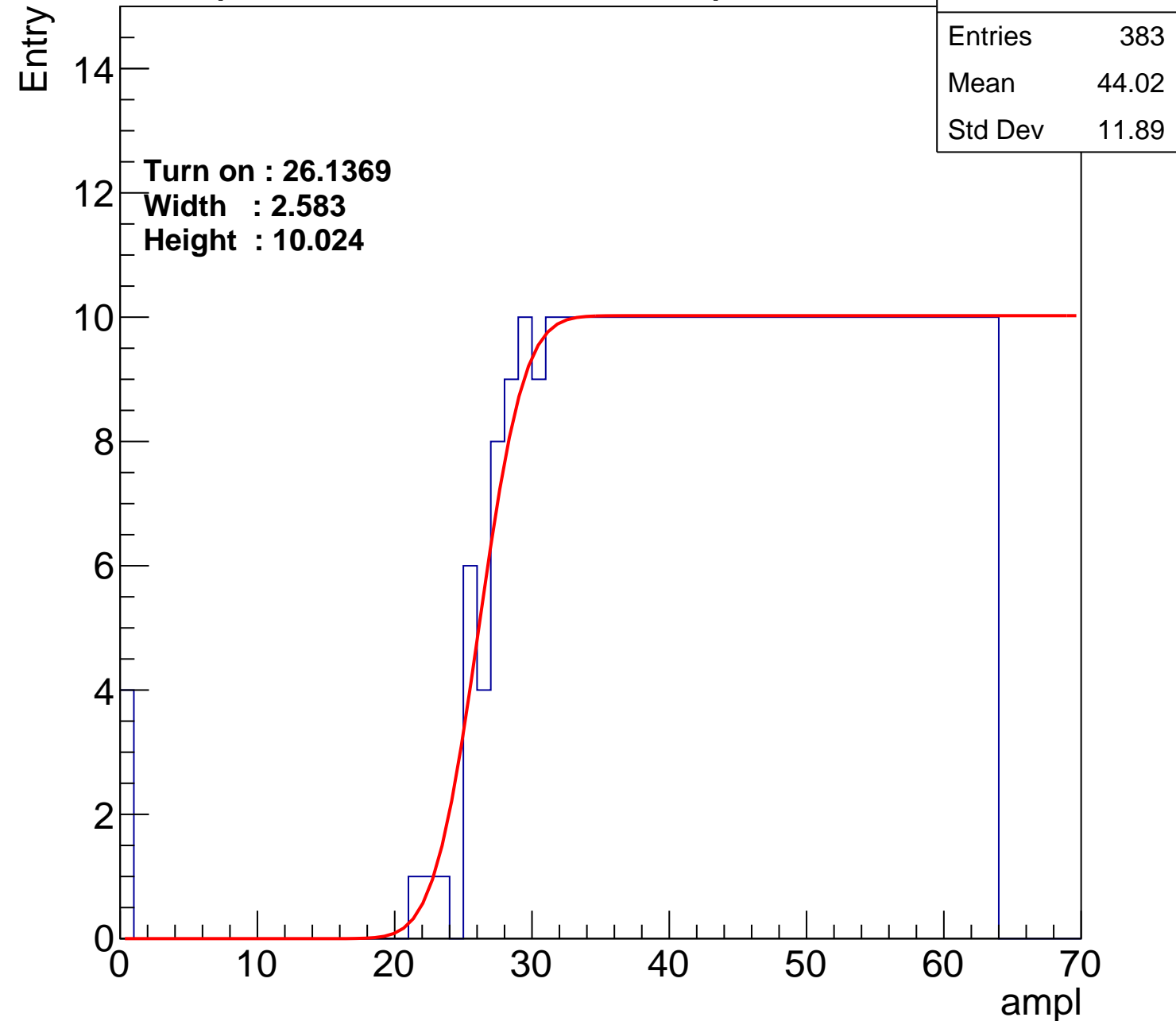
Width : 2.583

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch74

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.28
Std Dev	11.67

Turn on : 26.7723

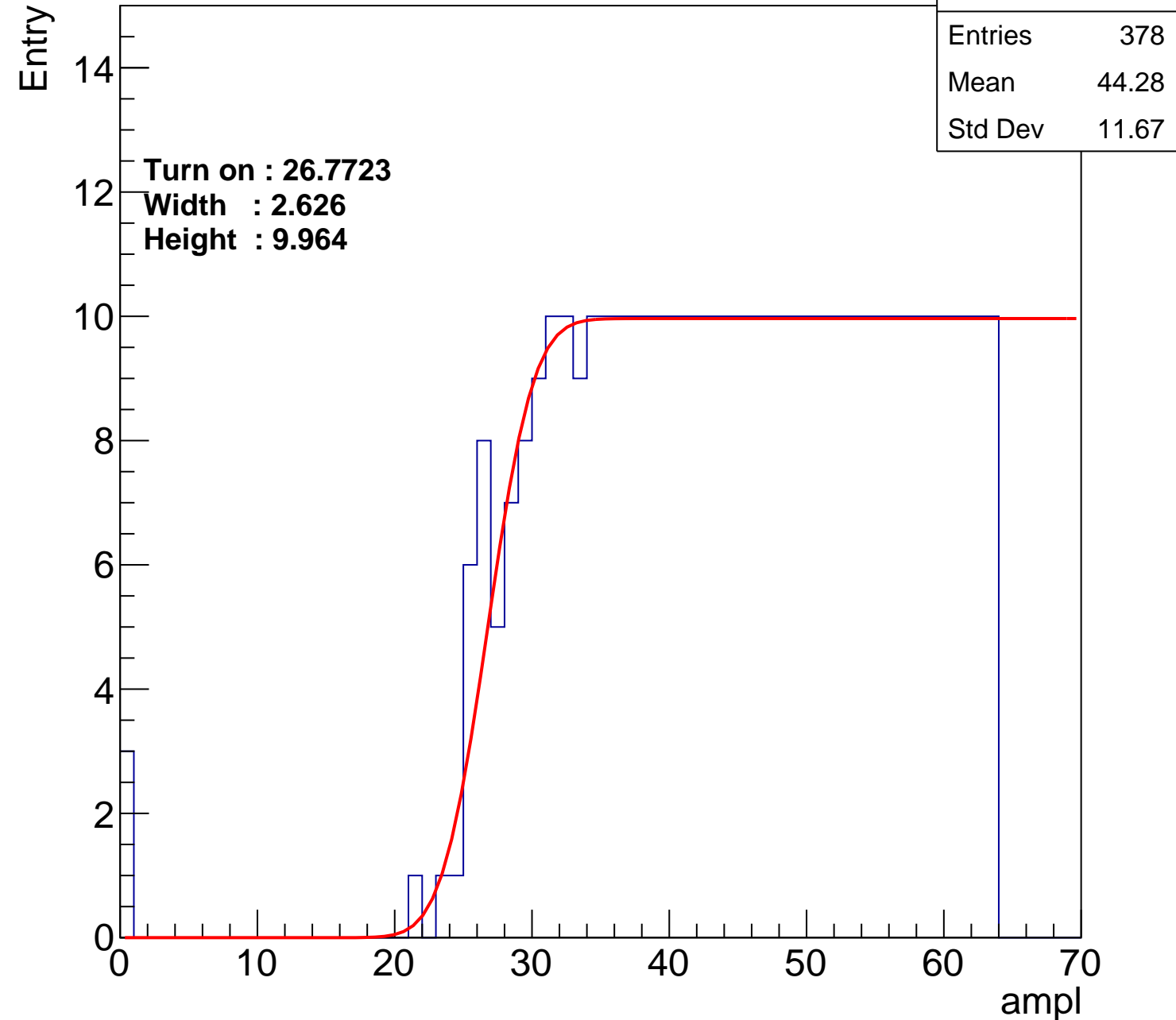
Width : 2.626

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch75

calib_packv5_042523_0143.root, FC#11, port A2

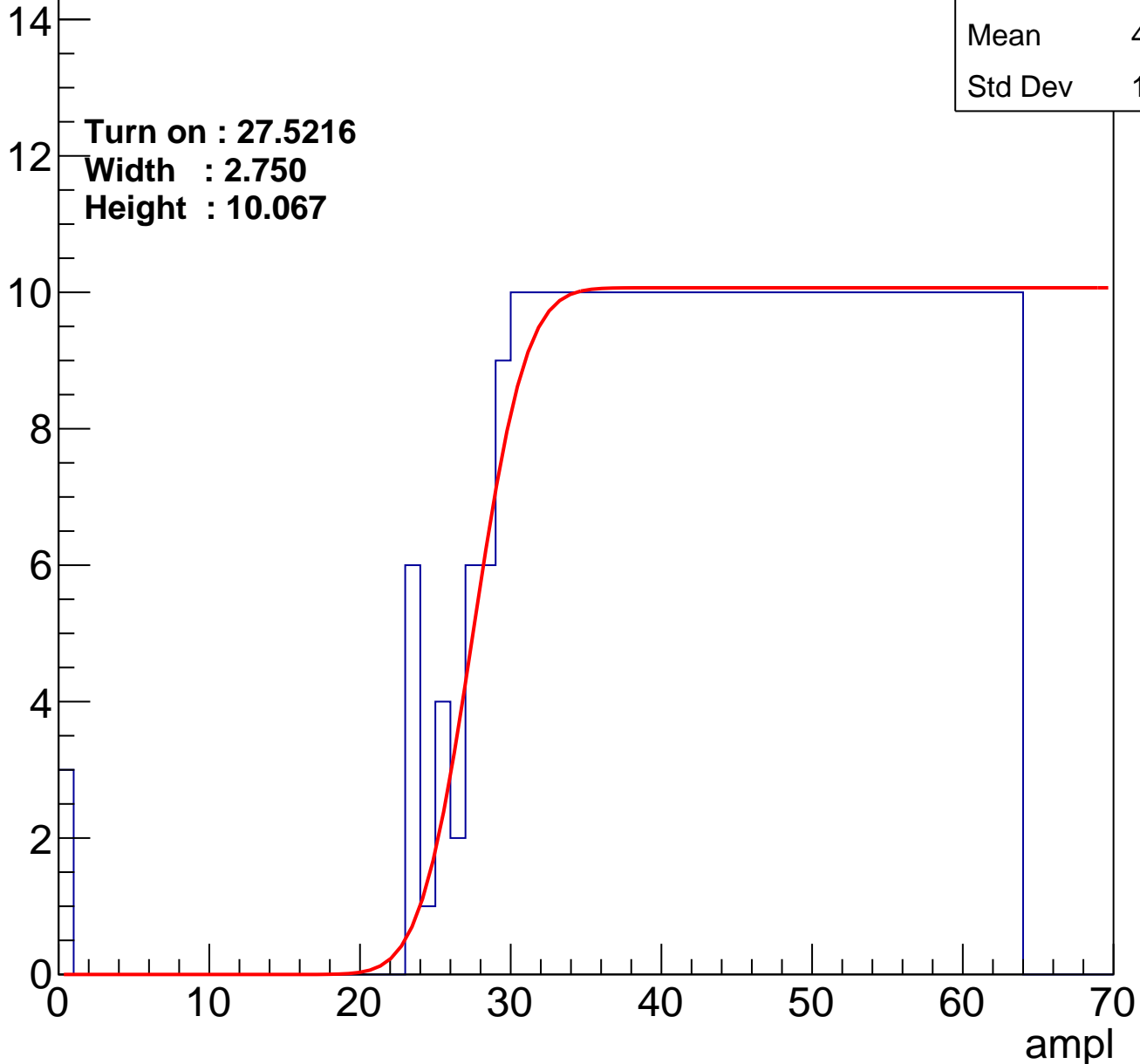
Entries	377
Mean	44.34
Std Dev	11.64

Turn on : 27.5216

Width : 2.750

Height : 10.067

Entry



B1L102S, U26-ch76

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.63
Std Dev	12.19

Turn on : 25.1134

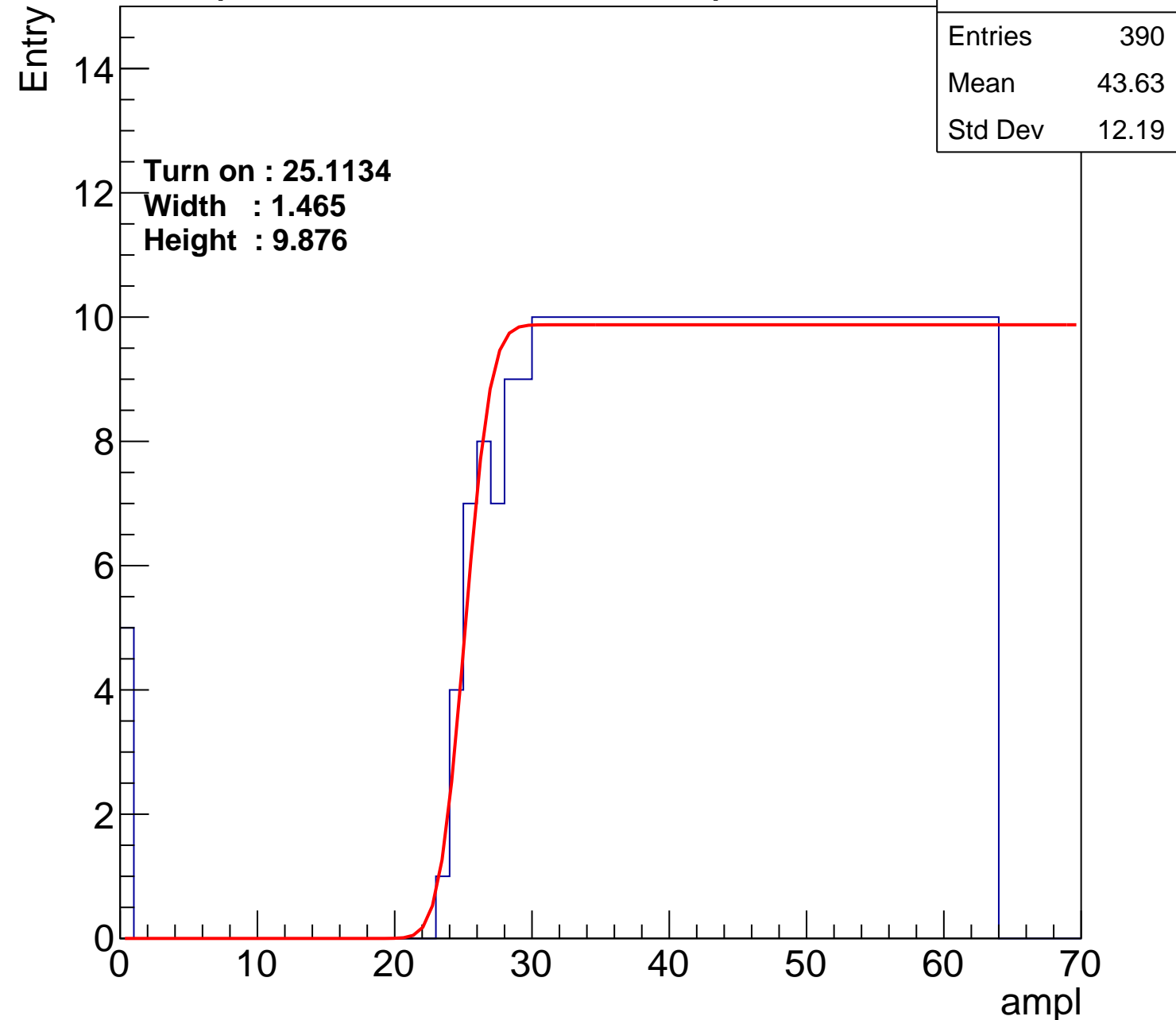
Width : 1.465

Height : 9.876

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch77

calib_packv5_042523_0143.root, FC#11, port A2

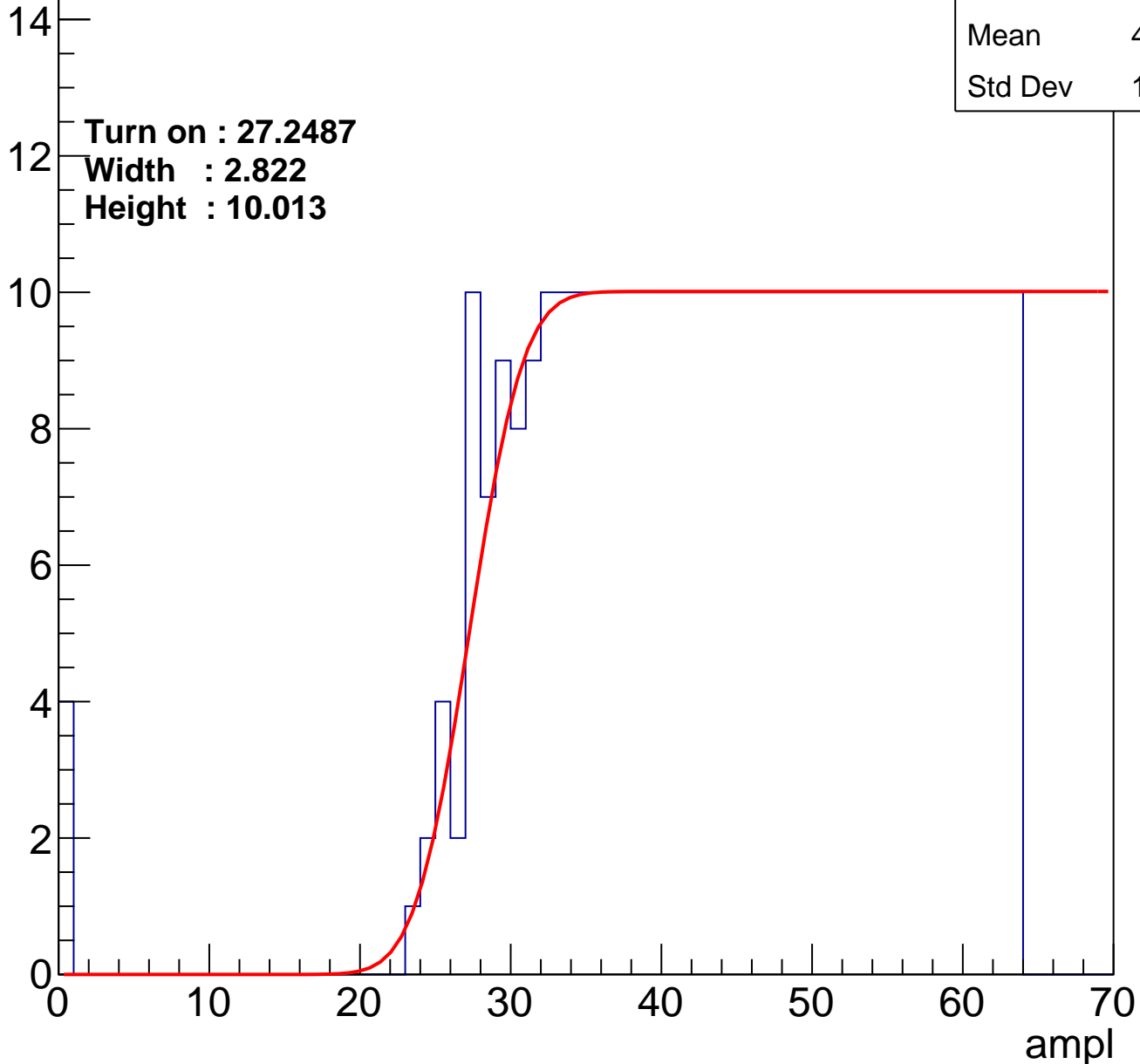
Entries	376
Mean	44.33
Std Dev	11.76

Turn on : 27.2487

Width : 2.822

Height : 10.013

Entry



B1L102S, U26-ch78

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.45
Std Dev	11.96

Turn on : 24.6570

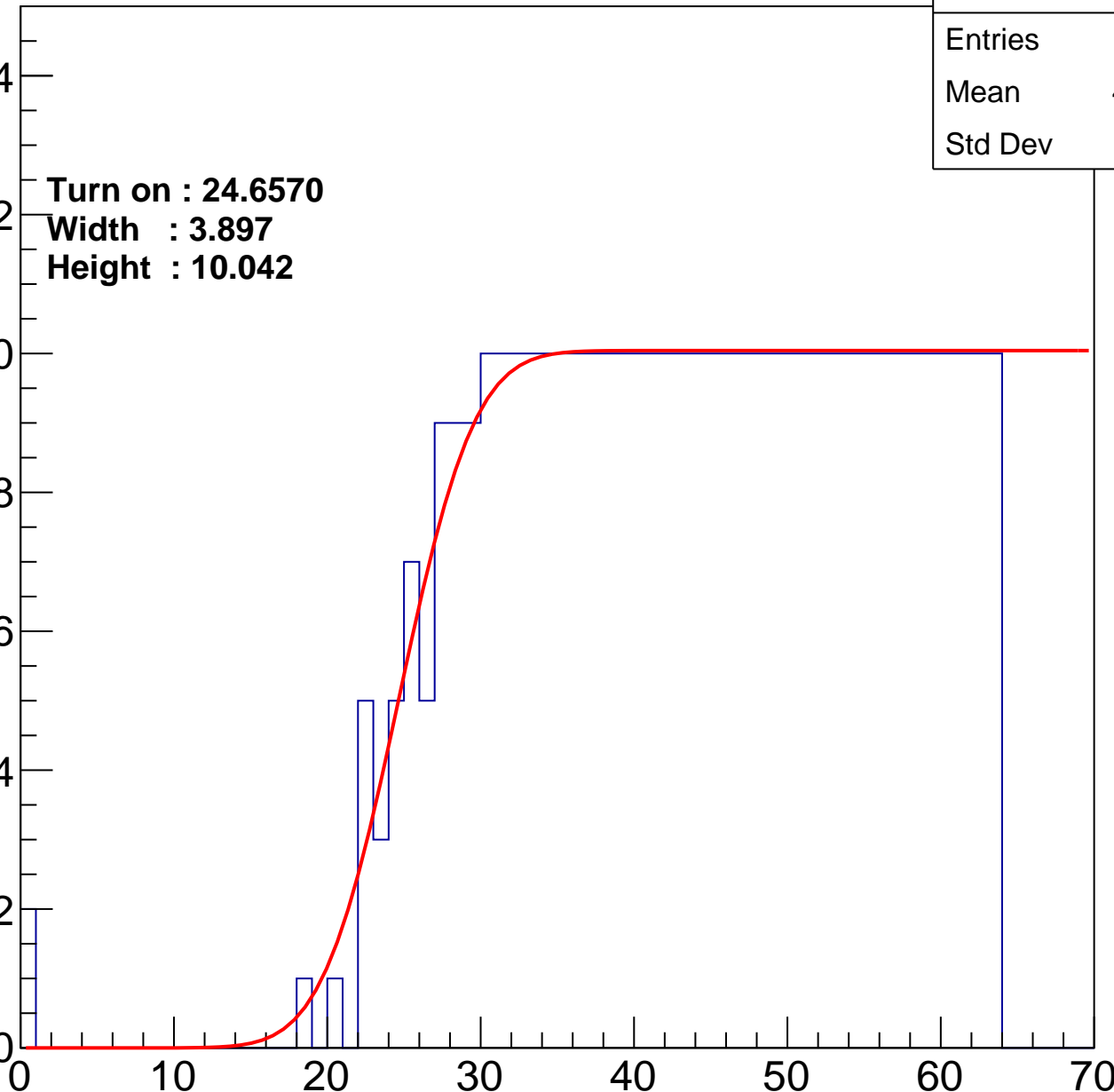
Width : 3.897

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch79

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.21
Std Dev	11.84

Turn on : 26.7750

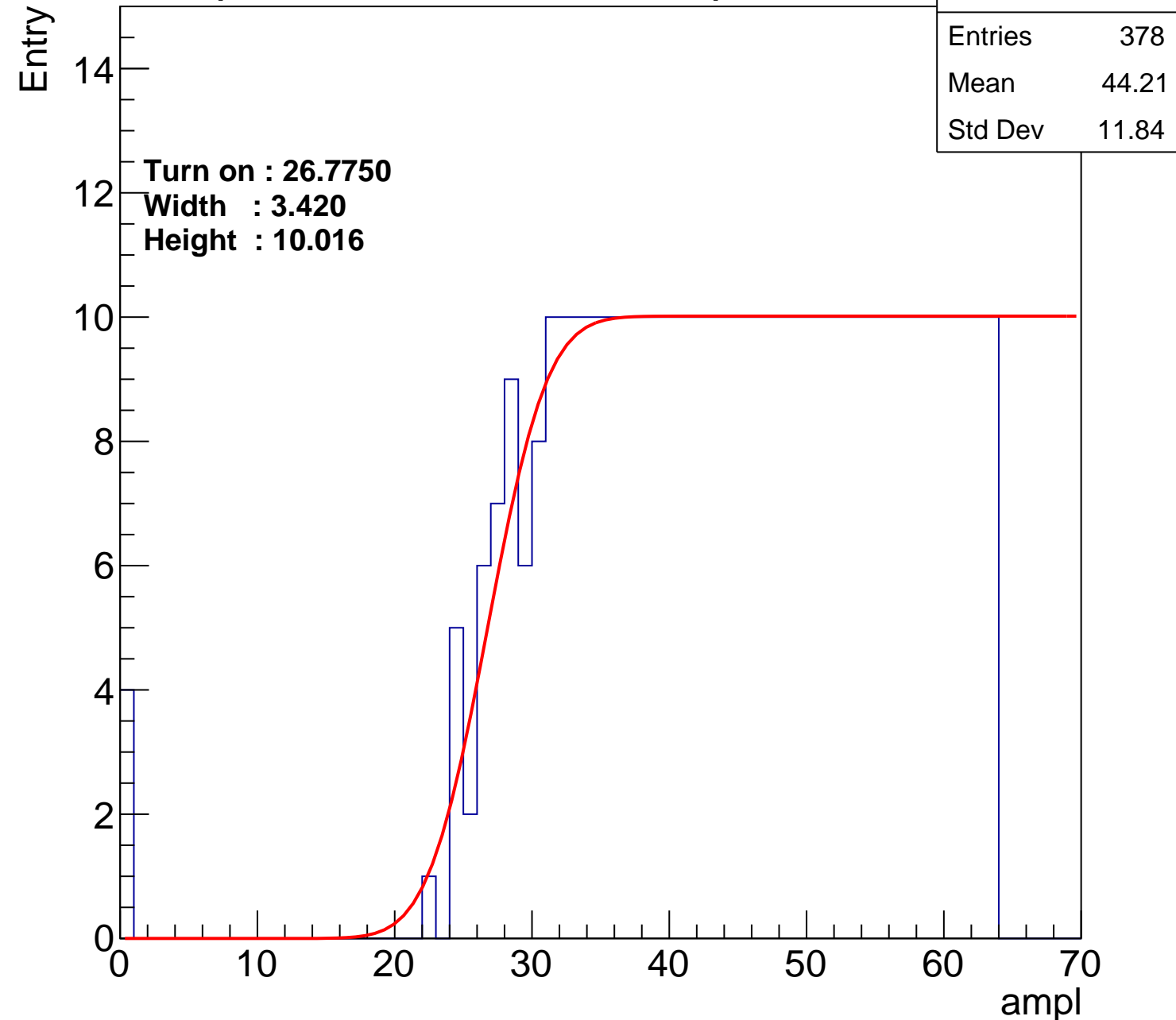
Width : 3.420

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch80

calib_packv5_042523_0143.root, FC#11, port A2

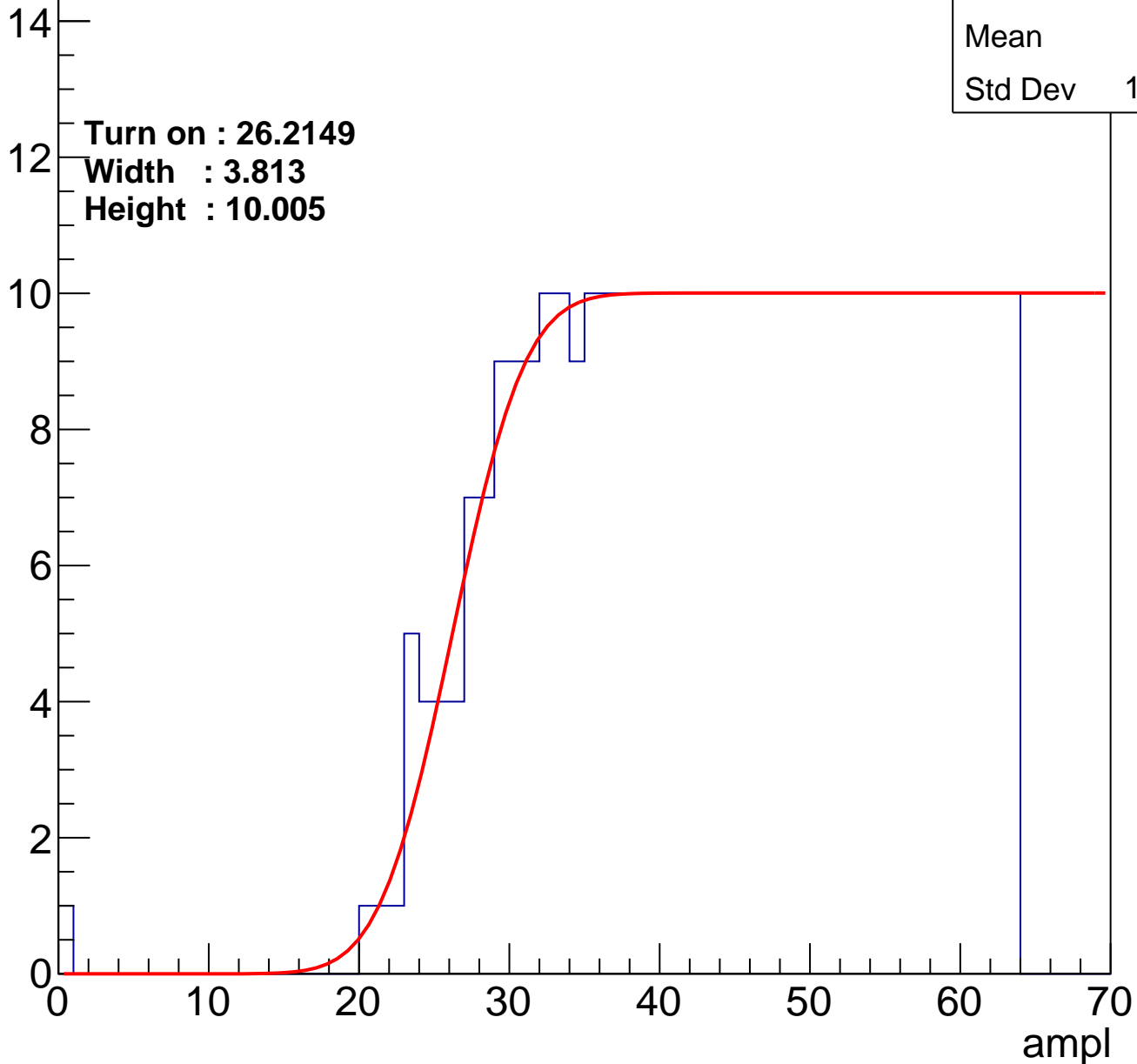
Entries	381
Mean	44.2
Std Dev	11.49

Turn on : 26.2149

Width : 3.813

Height : 10.005

Entry



B1L102S, U26-ch81

calib_packv5_042523_0143.root, FC#11, port A2

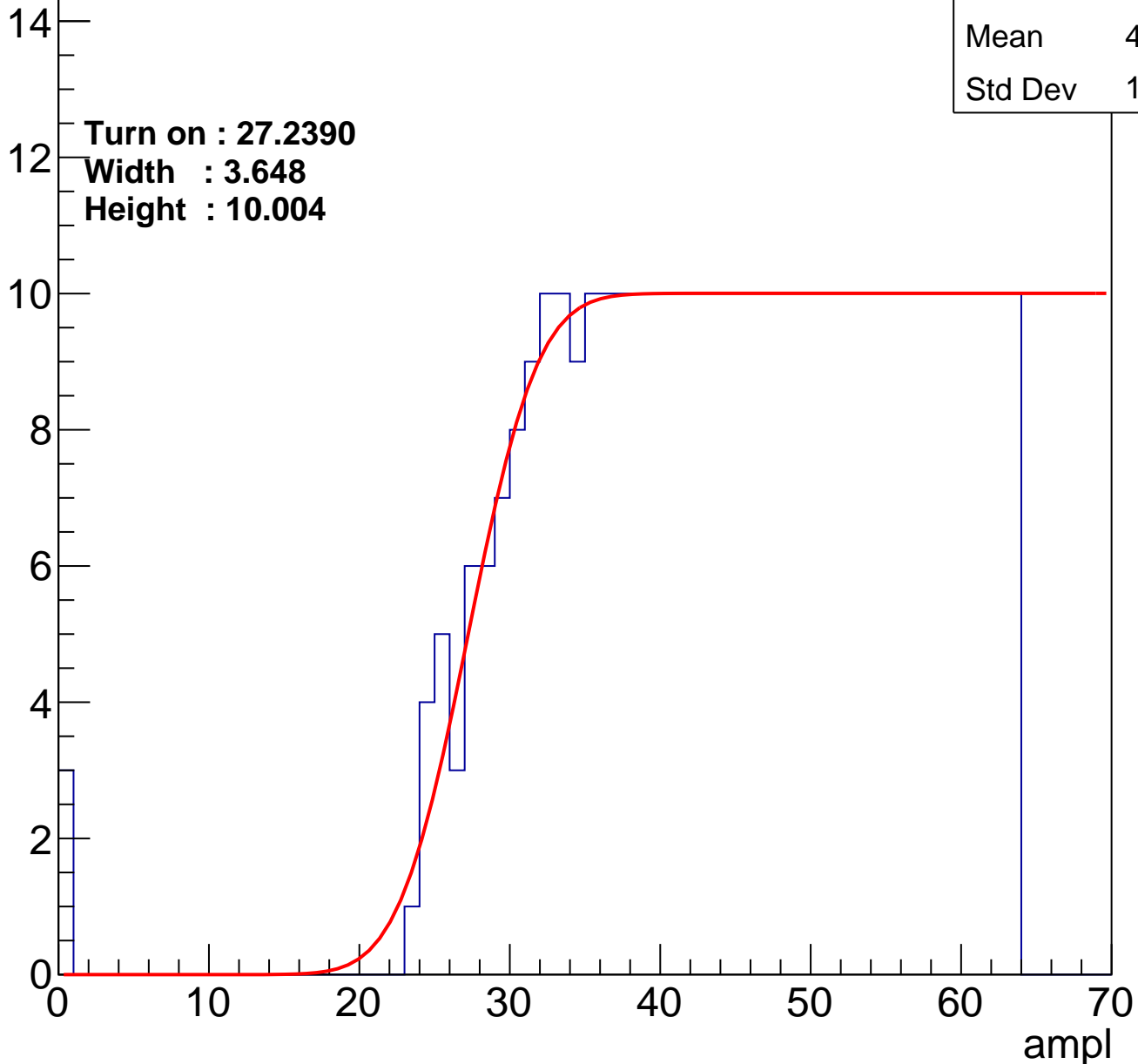
Entries	371
Mean	44.58
Std Dev	11.55

Turn on : 27.2390

Width : 3.648

Height : 10.004

Entry



B1L102S, U26-ch82

calib_packv5_042523_0143.root, FC#11, port A2

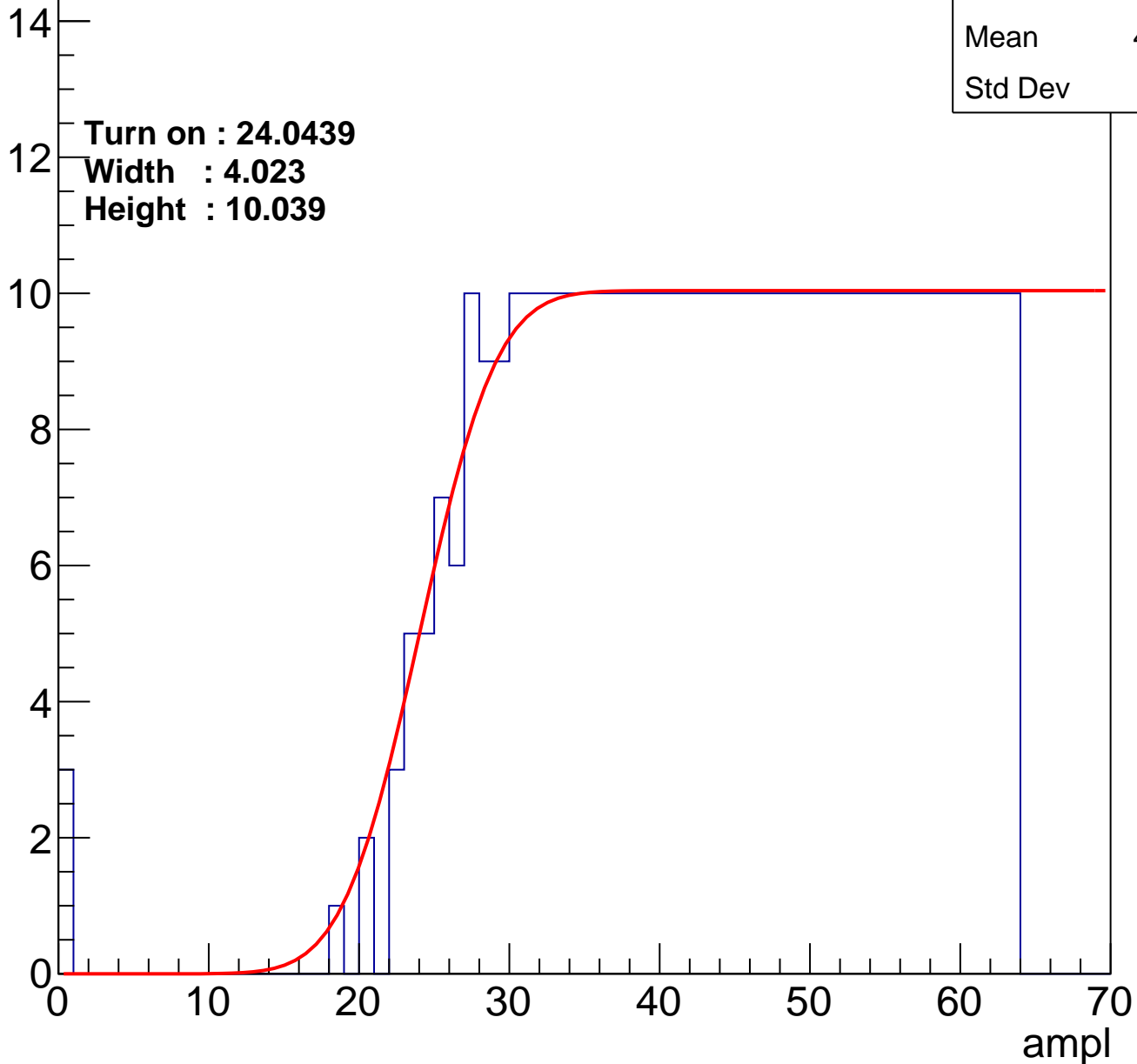
Entries	400
Mean	43.21
Std Dev	12.2

Turn on : 24.0439

Width : 4.023

Height : 10.039

Entry



B1L102S, U26-ch83

calib_packv5_042523_0143.root, FC#11, port A2

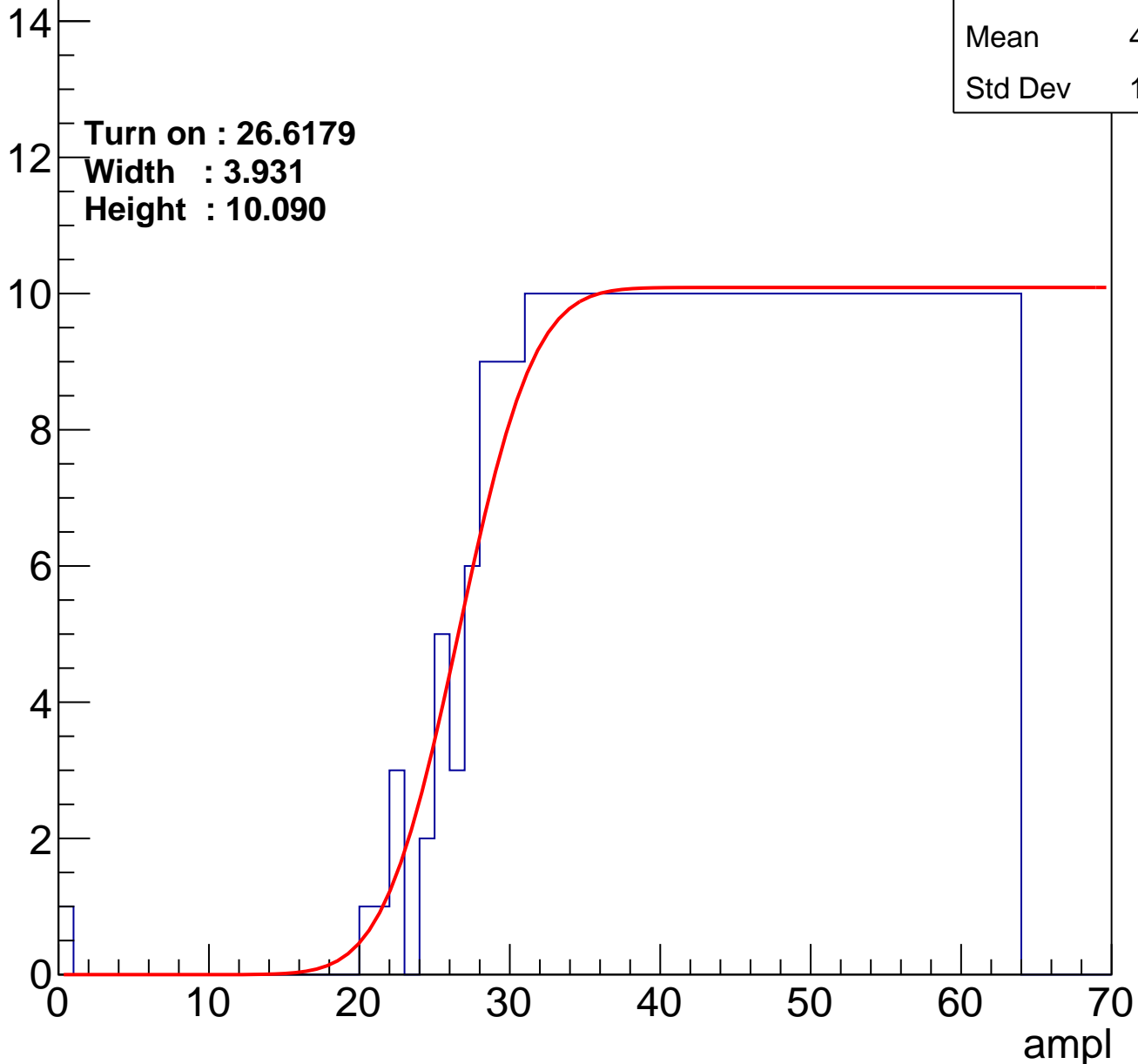
Entries	379
Mean	44.36
Std Dev	11.34

Turn on : 26.6179

Width : 3.931

Height : 10.090

Entry



B1L102S, U26-ch84

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.49
Std Dev	12.07

Turn on : 24.9465

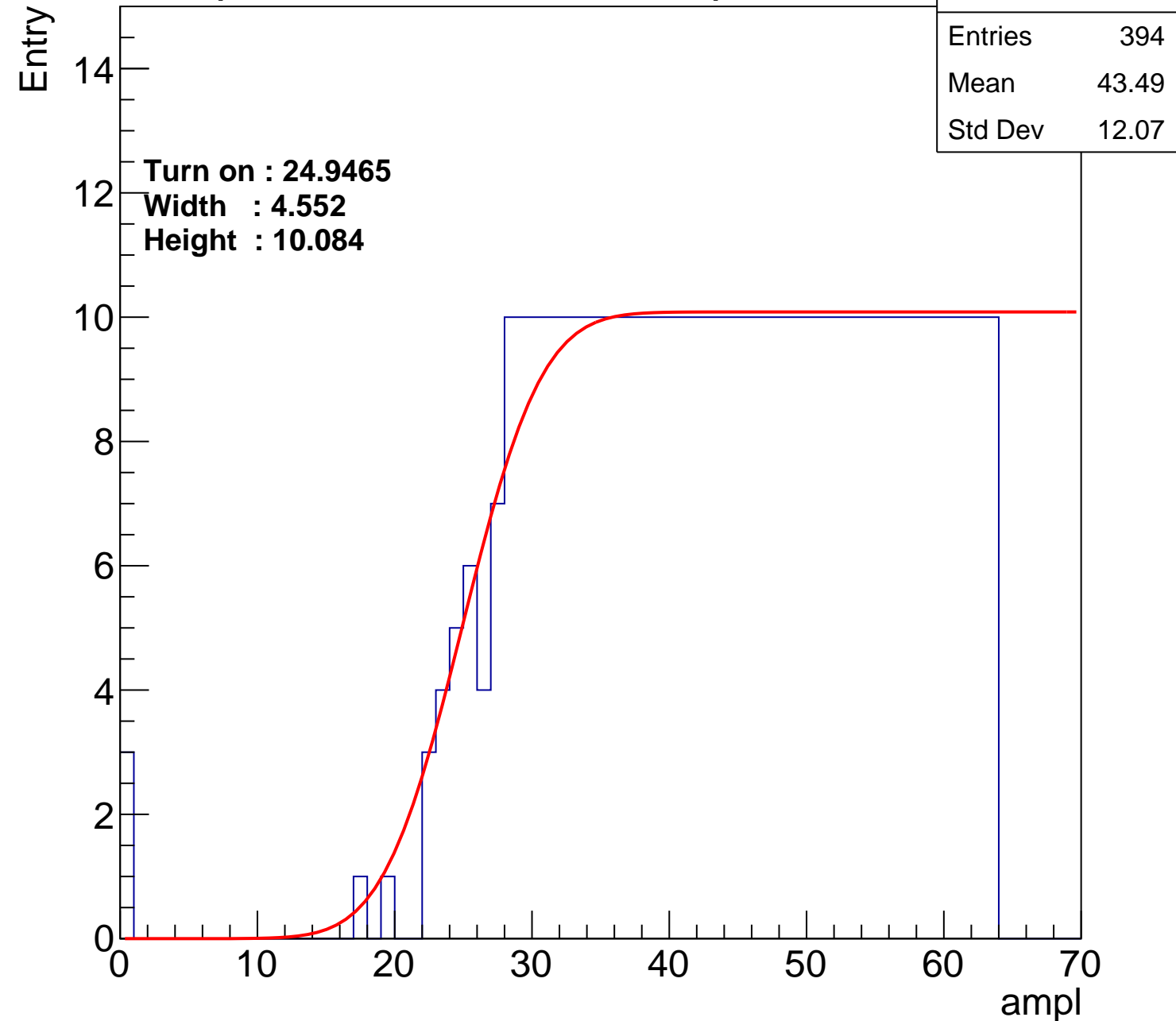
Width : 4.552

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch85

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	45.08
Std Dev	11.17

Turn on : 28.7179

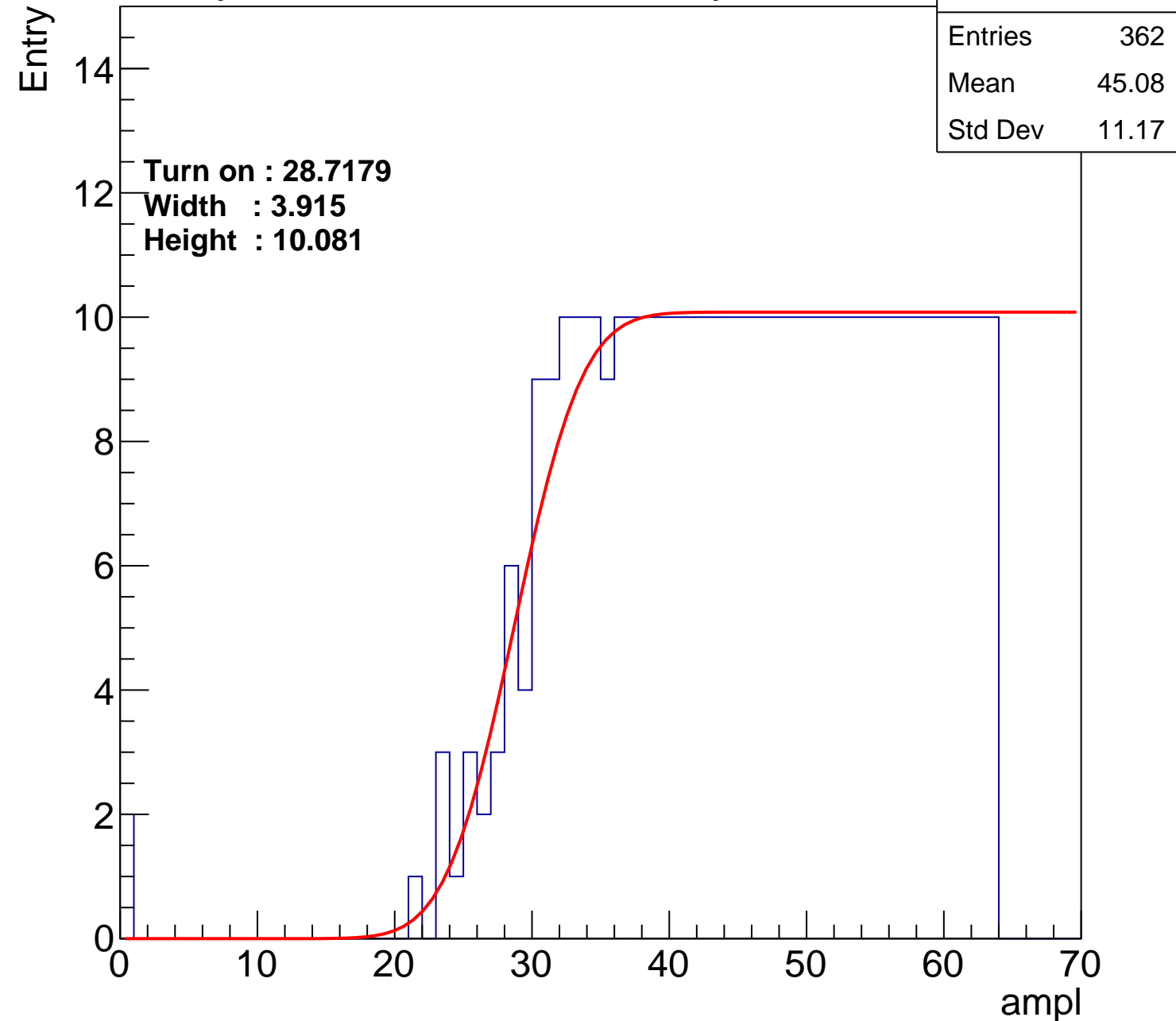
Width : 3.915

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch86

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	44.08
Std Dev	11.41

Turn on : 25.2478

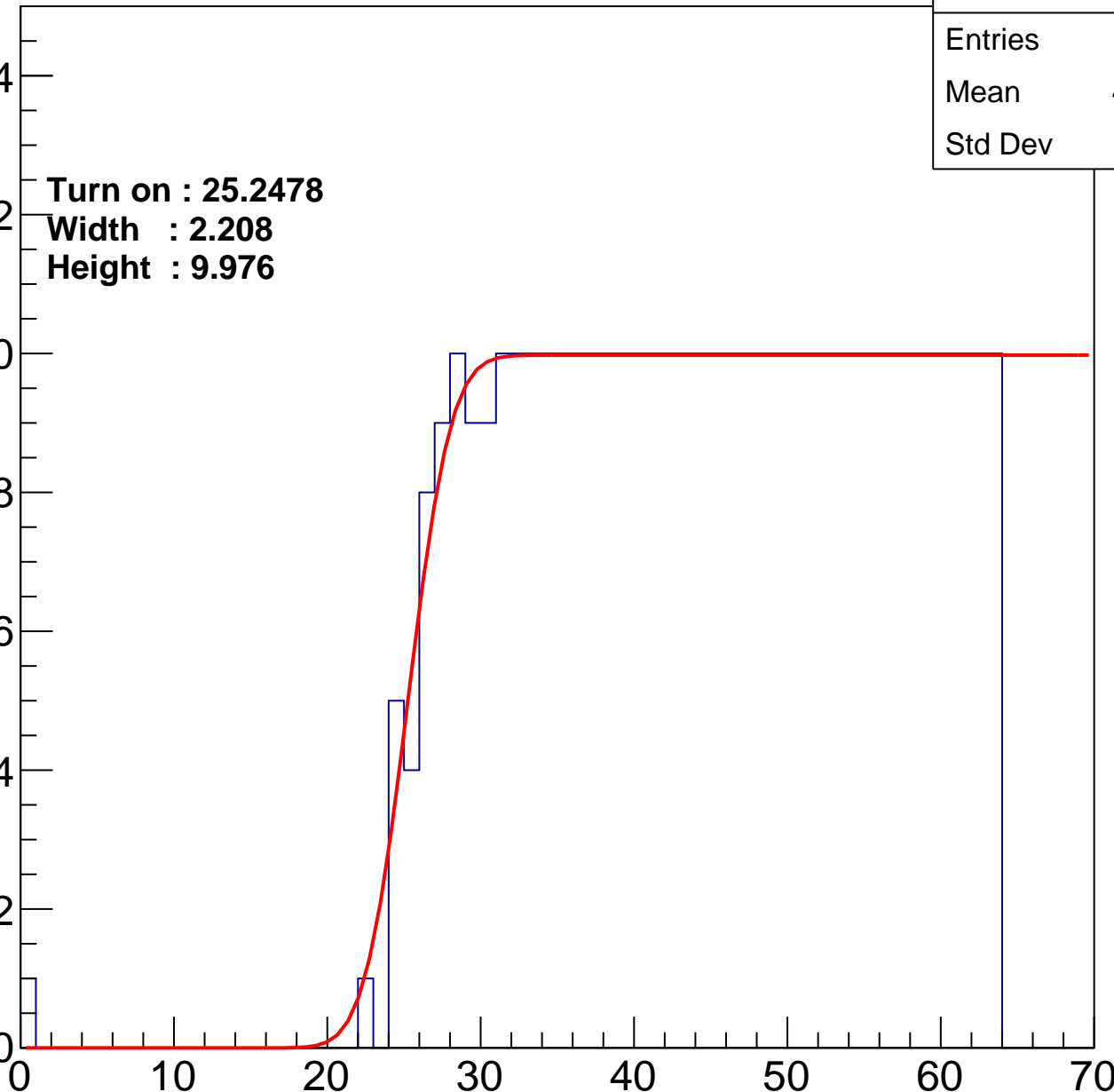
Width : 2.208

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch87

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.6
Std Dev	12.13

Turn on : 25.1614

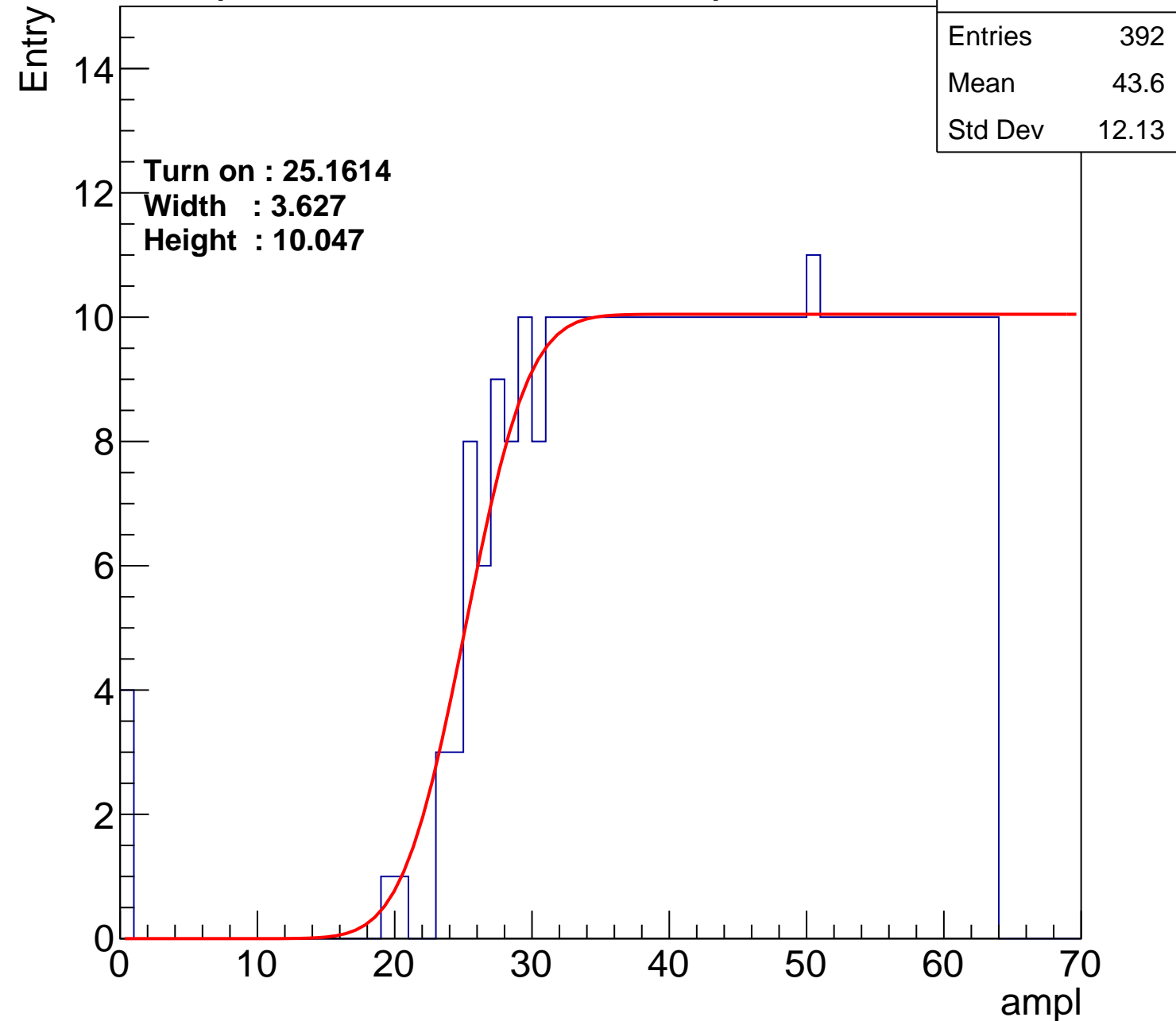
Width : 3.627

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch88

calib_packv5_042523_0143.root, FC#11, port A2

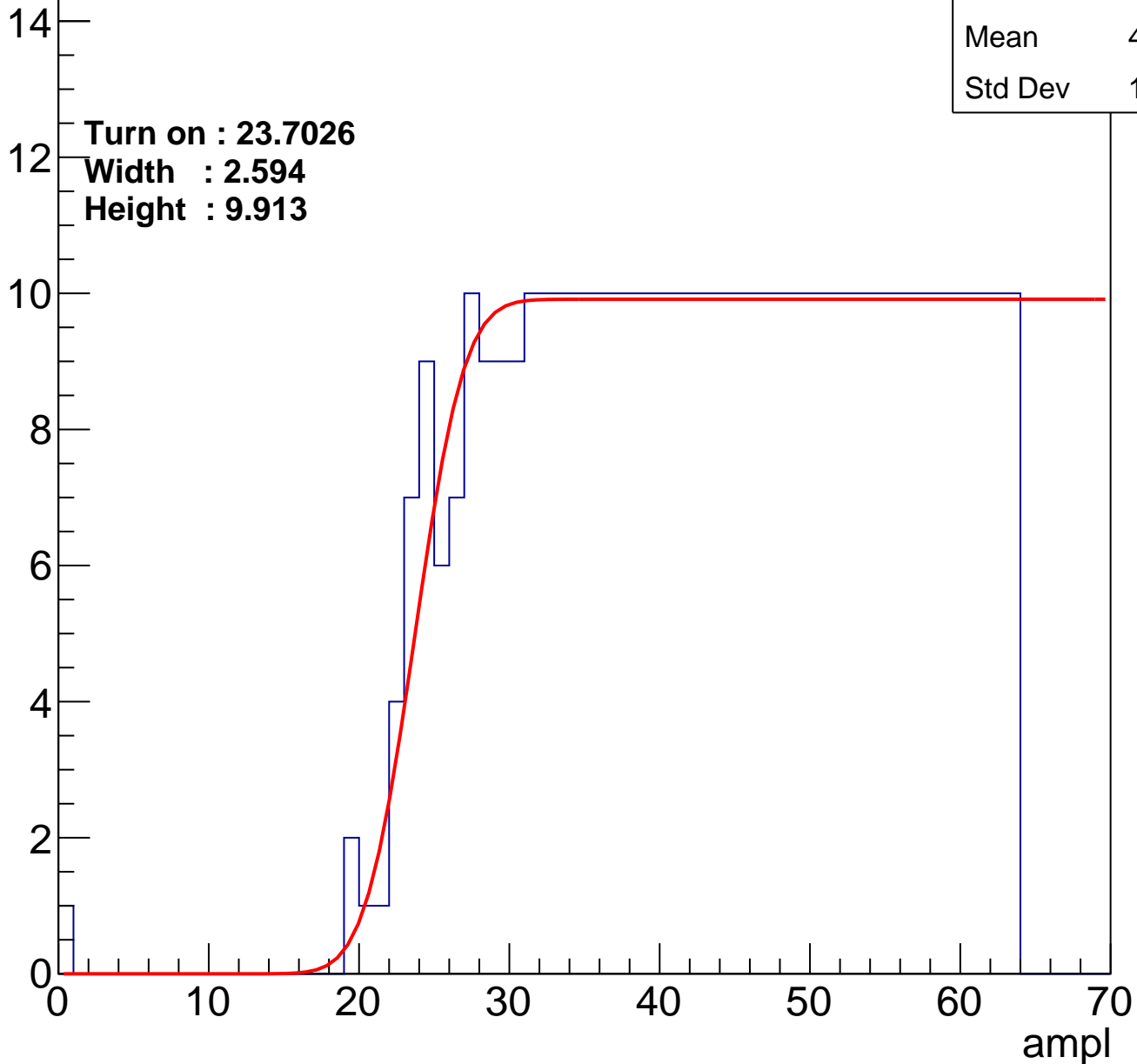
Entries	405
Mean	43.06
Std Dev	12.06

Turn on : 23.7026

Width : 2.594

Height : 9.913

Entry



B1L102S, U26-ch89

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.4
Std Dev	11.59

Turn on : 26.8618

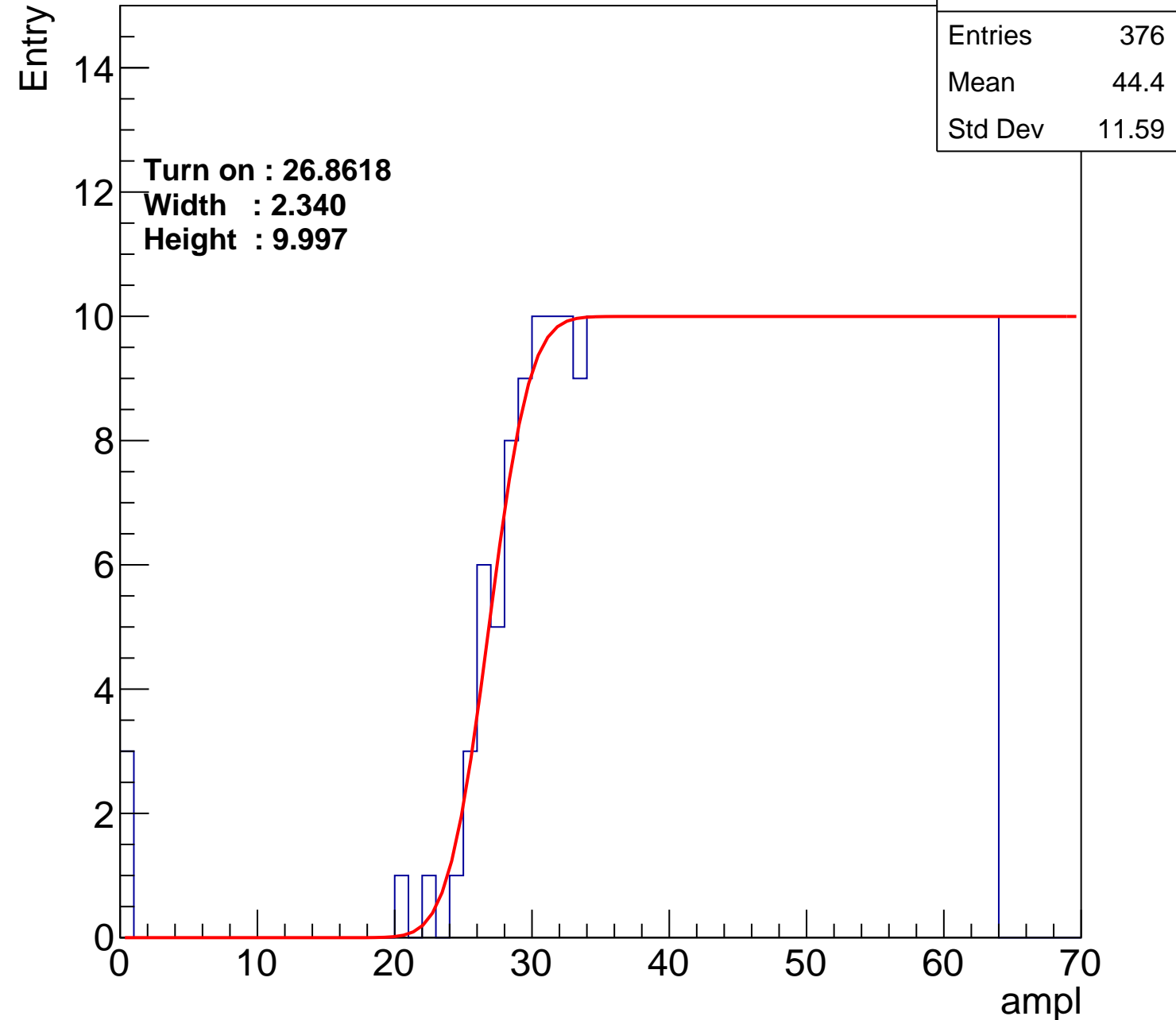
Width : 2.340

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch90

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.05
Std Dev	11.65

Turn on : 26.7344

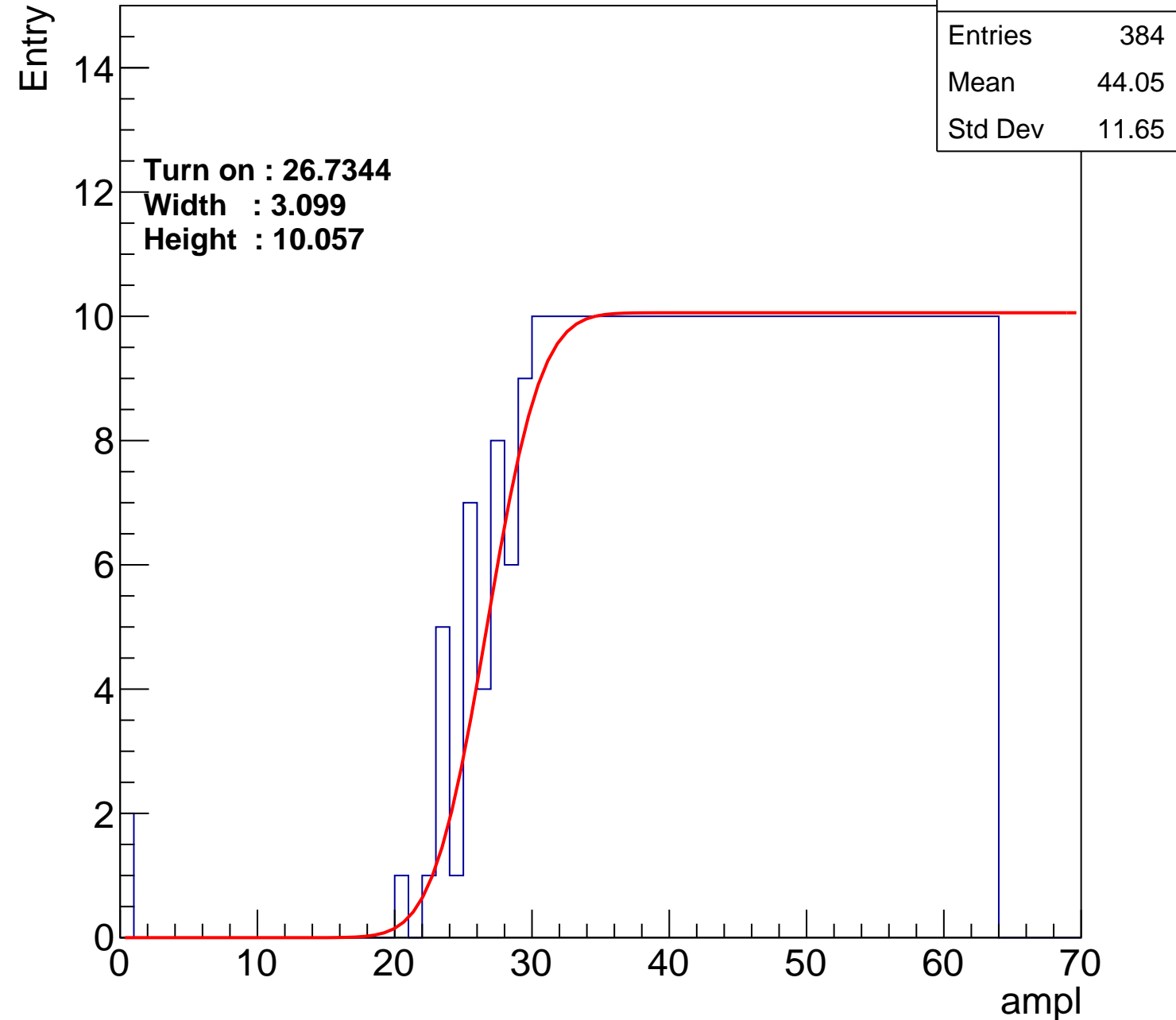
Width : 3.099

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch91

calib_packv5_042523_0143.root, FC#11, port A2

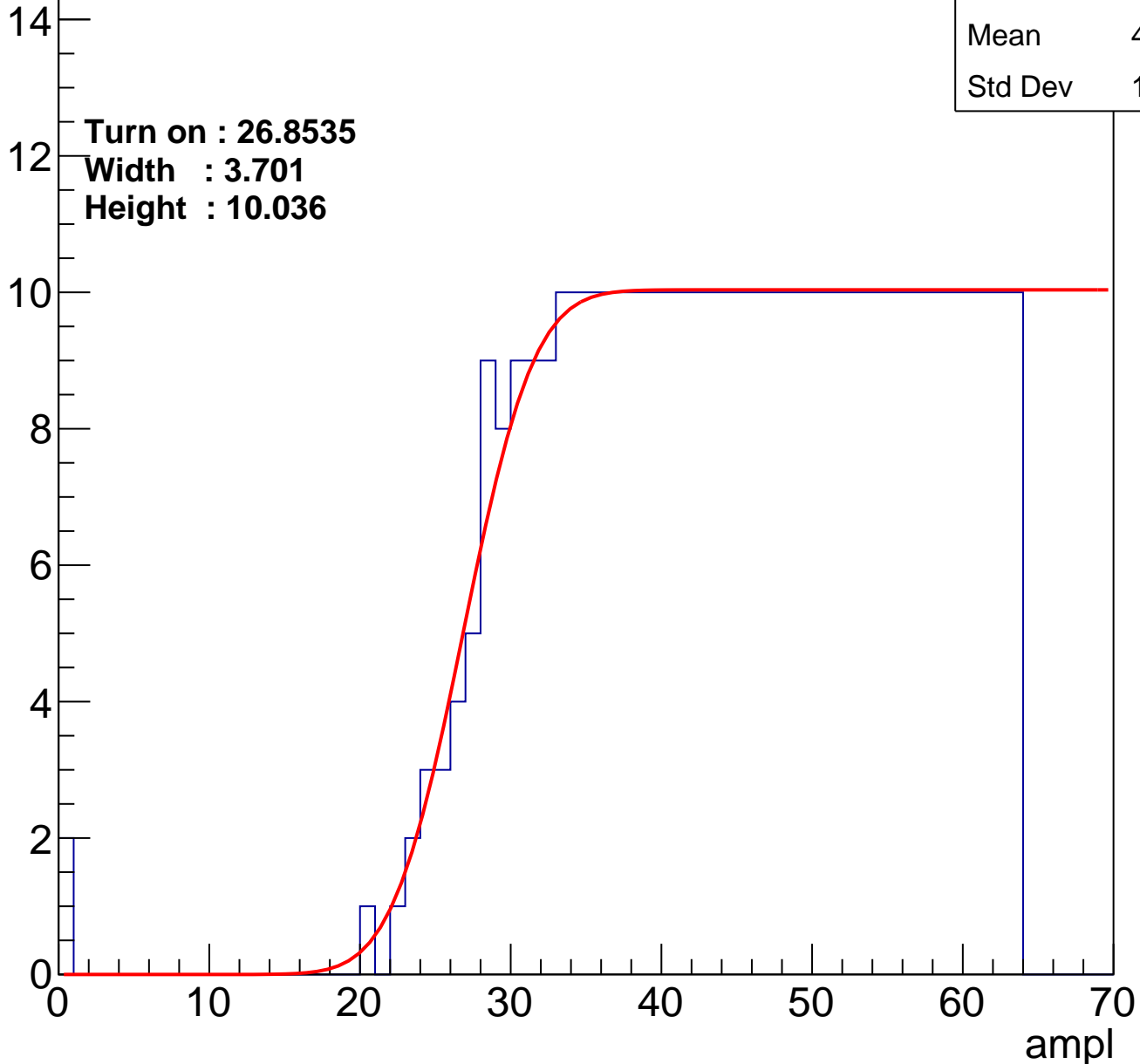
Entries	375
Mean	44.47
Std Dev	11.45

Turn on : 26.8535

Width : 3.701

Height : 10.036

Entry



B1L102S, U26-ch92

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 26.8035

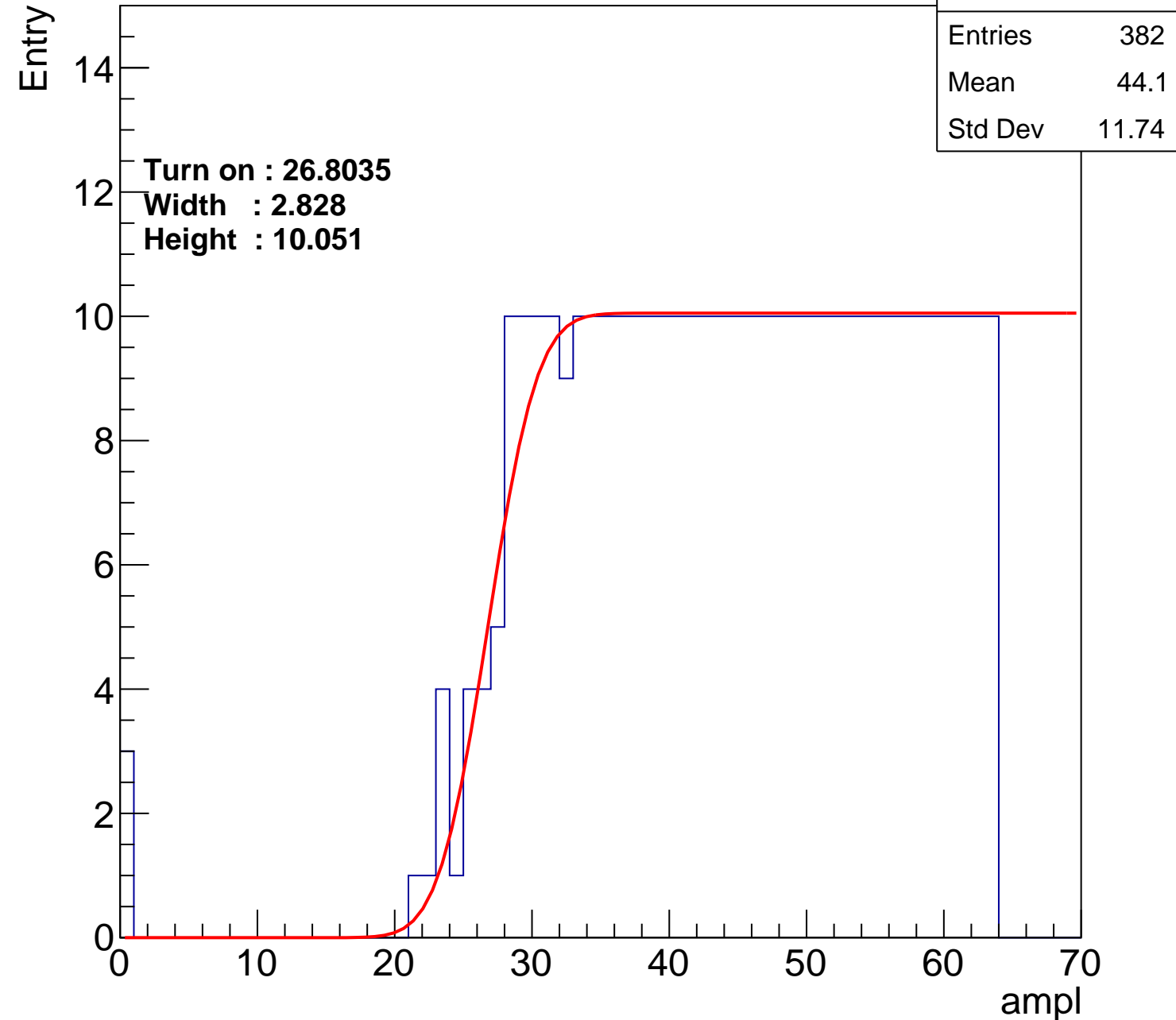
Width : 2.828

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch93

calib_packv5_042523_0143.root, FC#11, port A2

Entries	347
Mean	45.89
Std Dev	10.68

Turn on : 29.7180

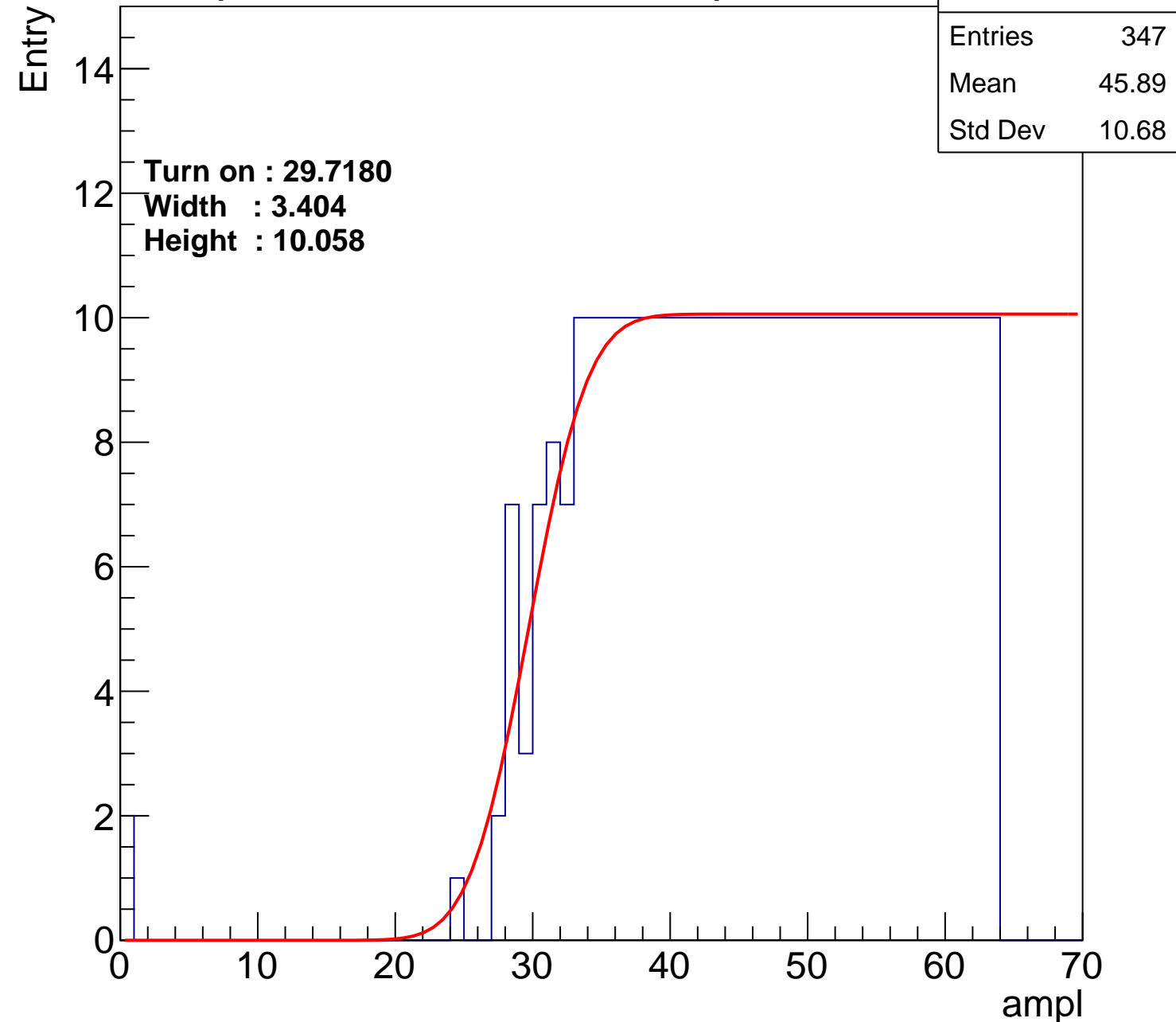
Width : 3.404

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch94

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.48
Std Dev	11.31

Turn on : 27.4075

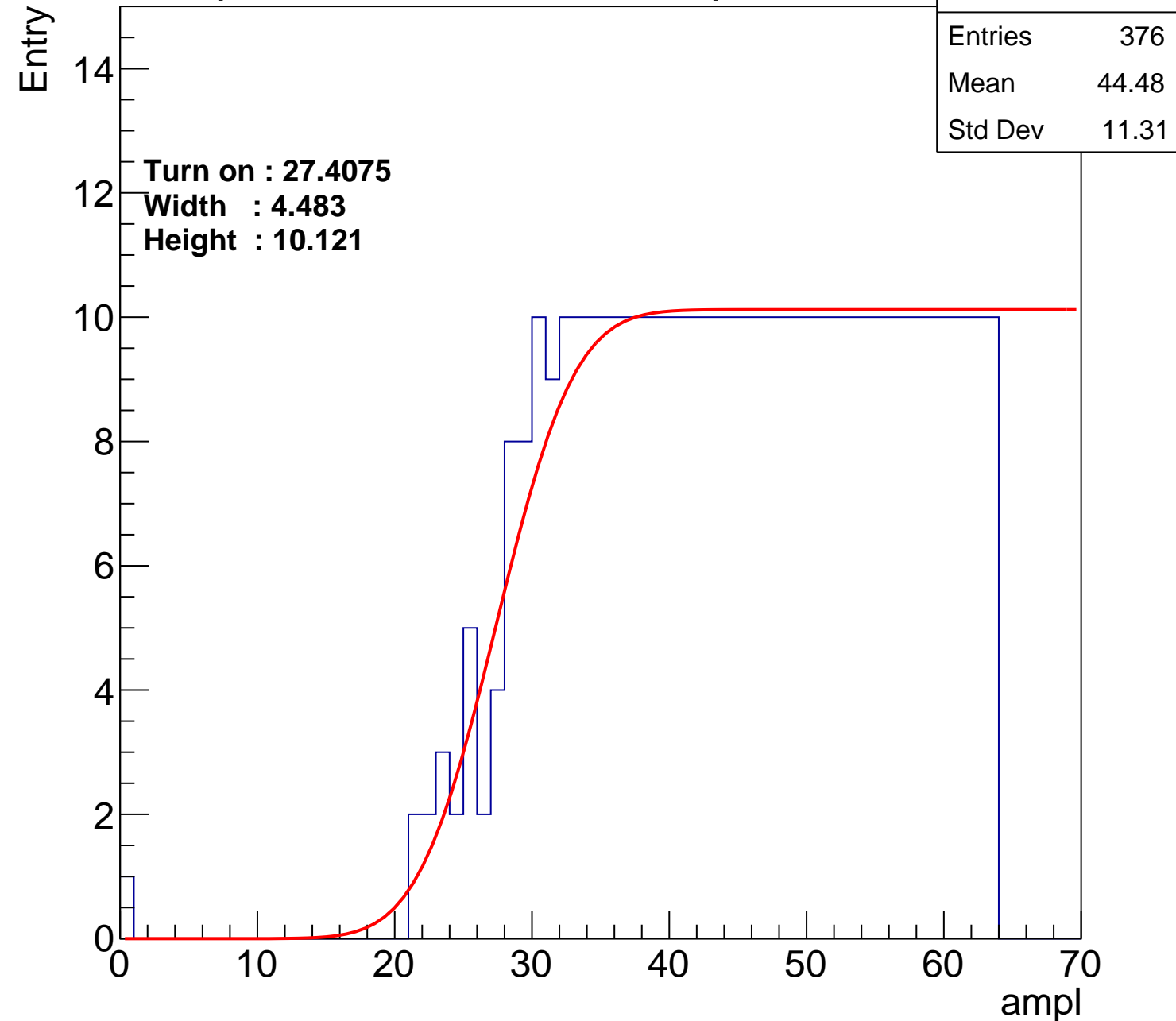
Width : 4.483

Height : 10.121

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch95

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.79
Std Dev	11.85

Turn on : 25.7036

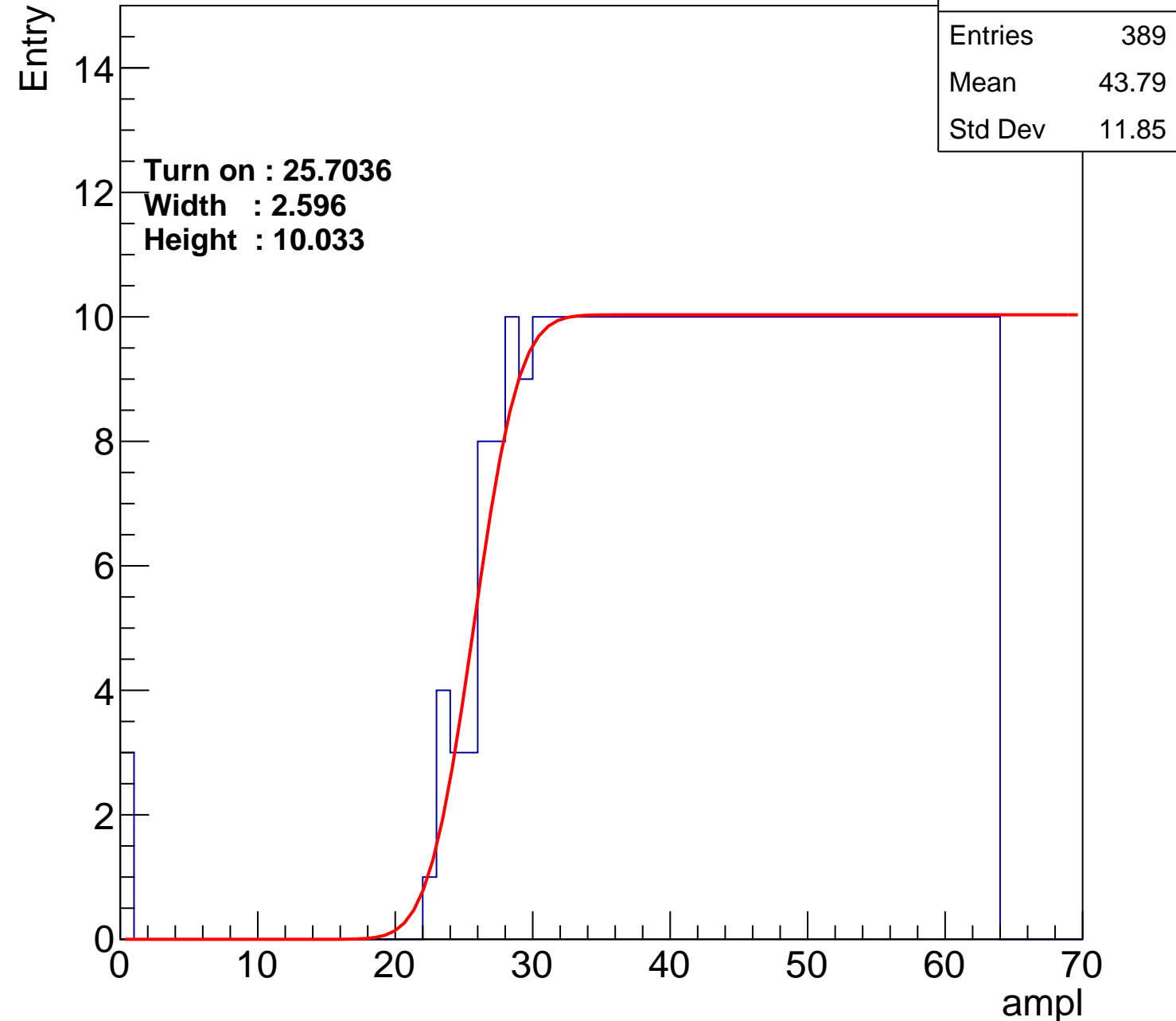
Width : 2.596

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch96

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.48
Std Dev	11.57

Turn on : 27.3920

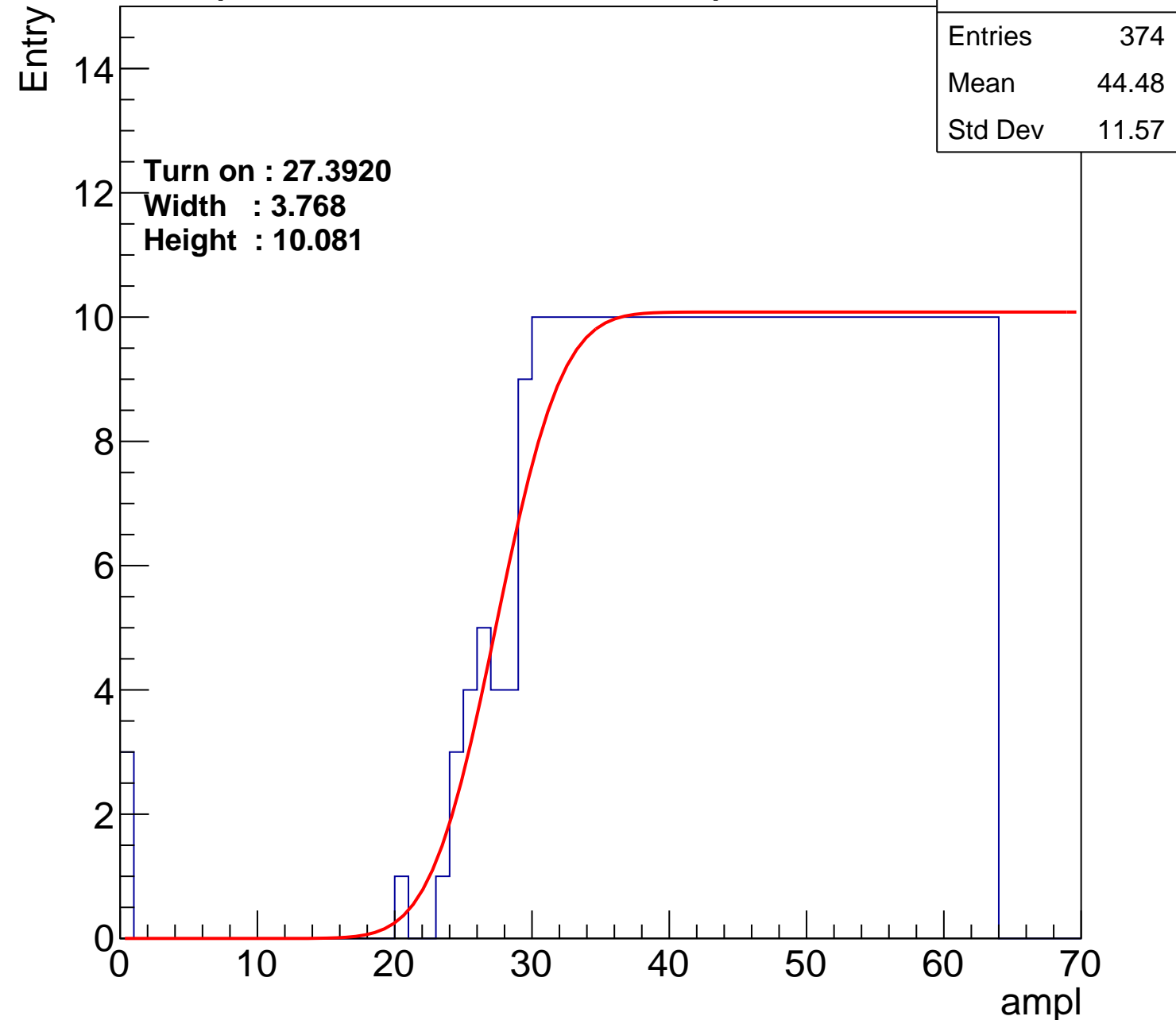
Width : 3.768

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch97

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.54
Std Dev	11.44

Turn on : 27.0166

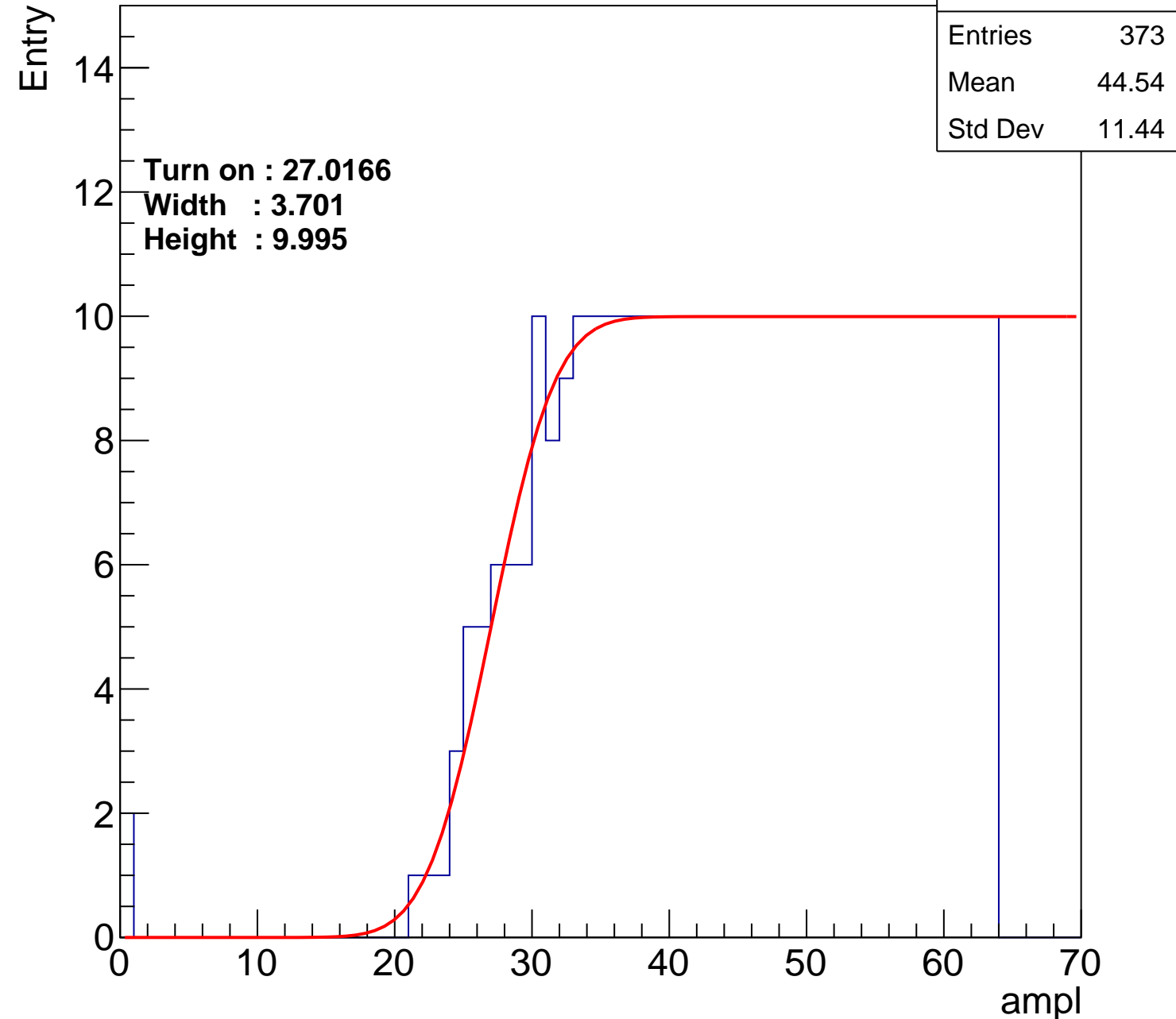
Width : 3.701

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch98

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 24.5802

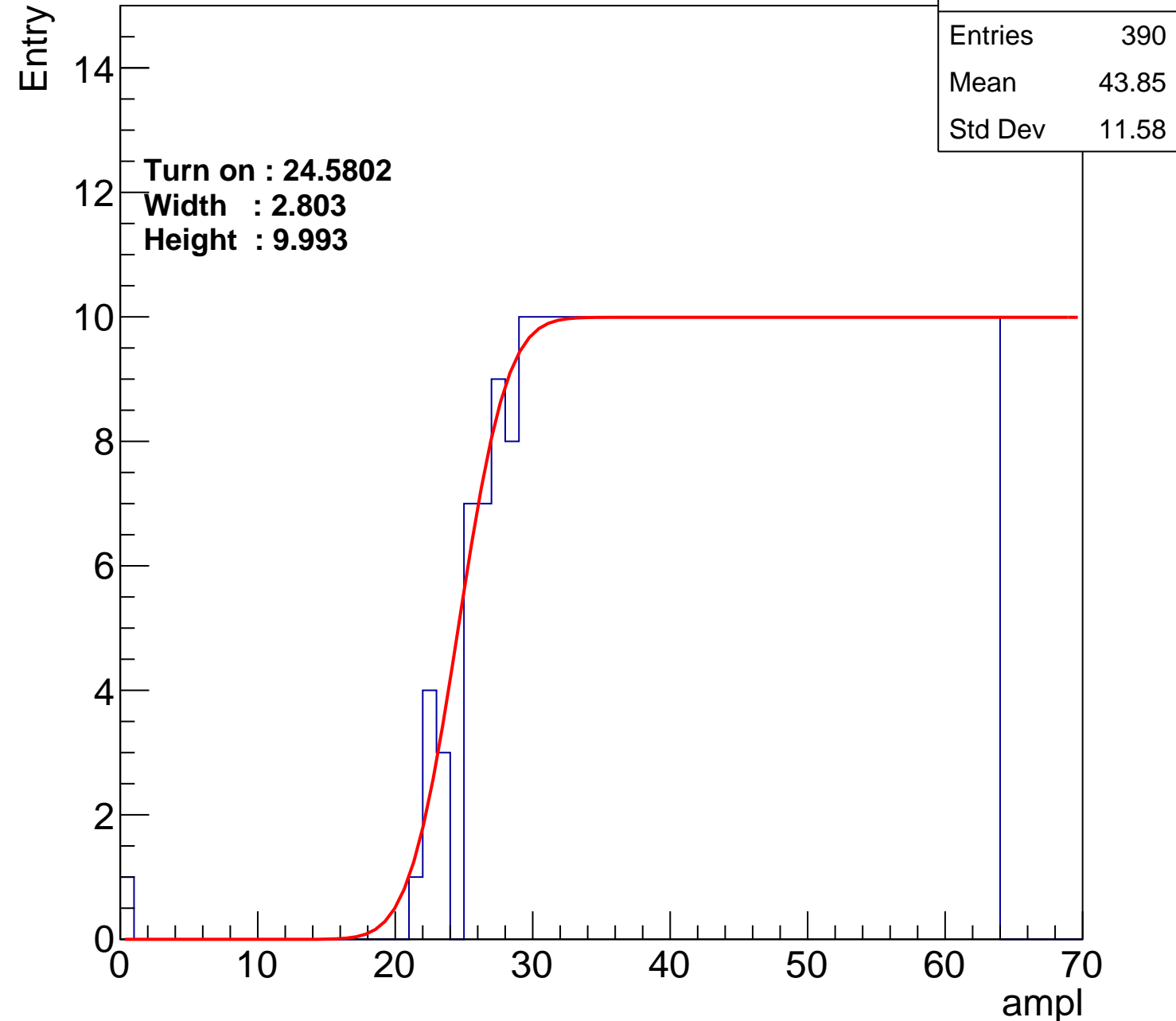
Width : 2.803

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch99

calib_packv5_042523_0143.root, FC#11, port A2

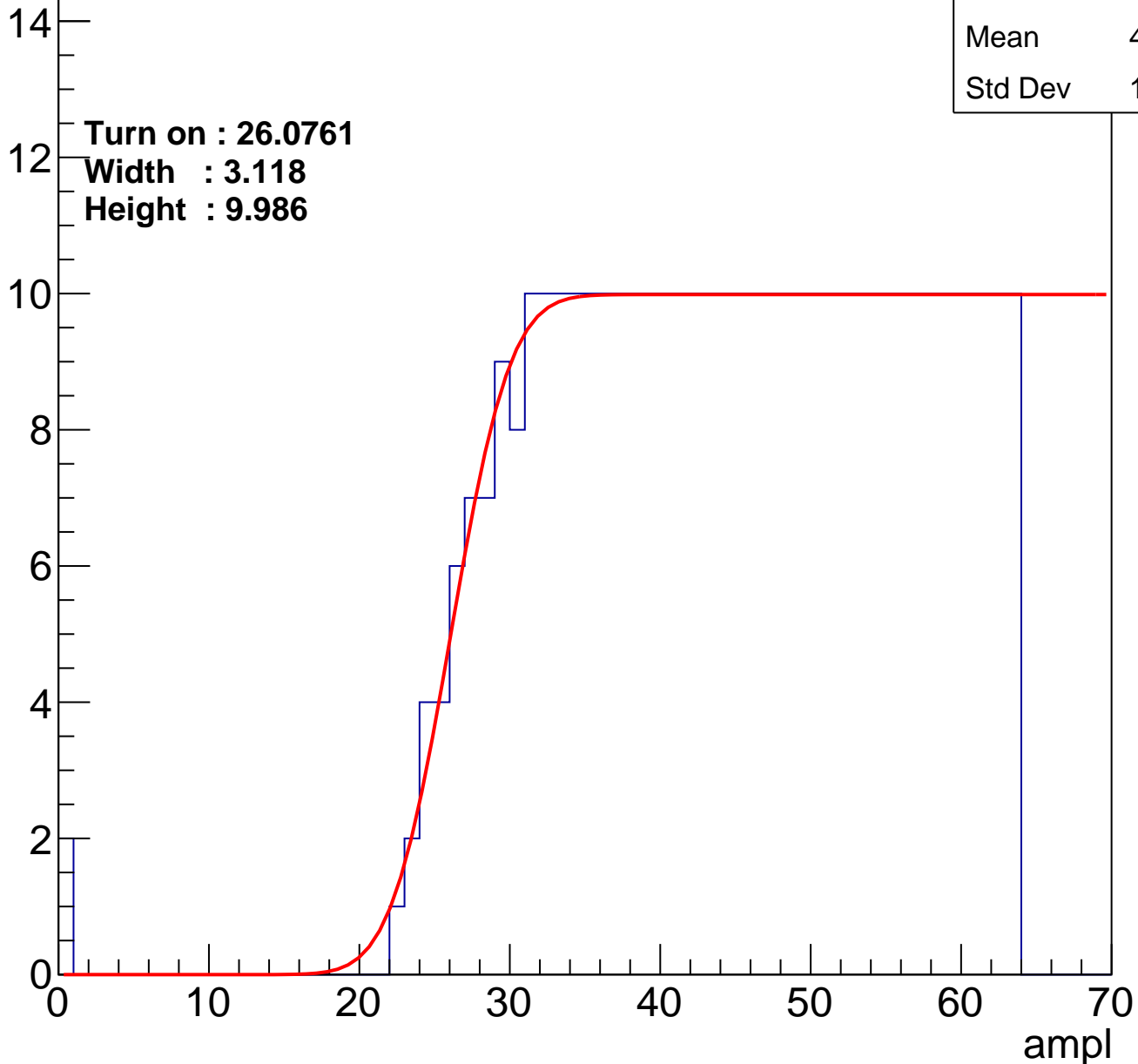
Entries	380
Mean	44.25
Std Dev	11.53

Turn on : 26.0761

Width : 3.118

Height : 9.986

Entry



B1L102S, U26-ch100

calib_packv5_042523_0143.root, FC#11, port A2

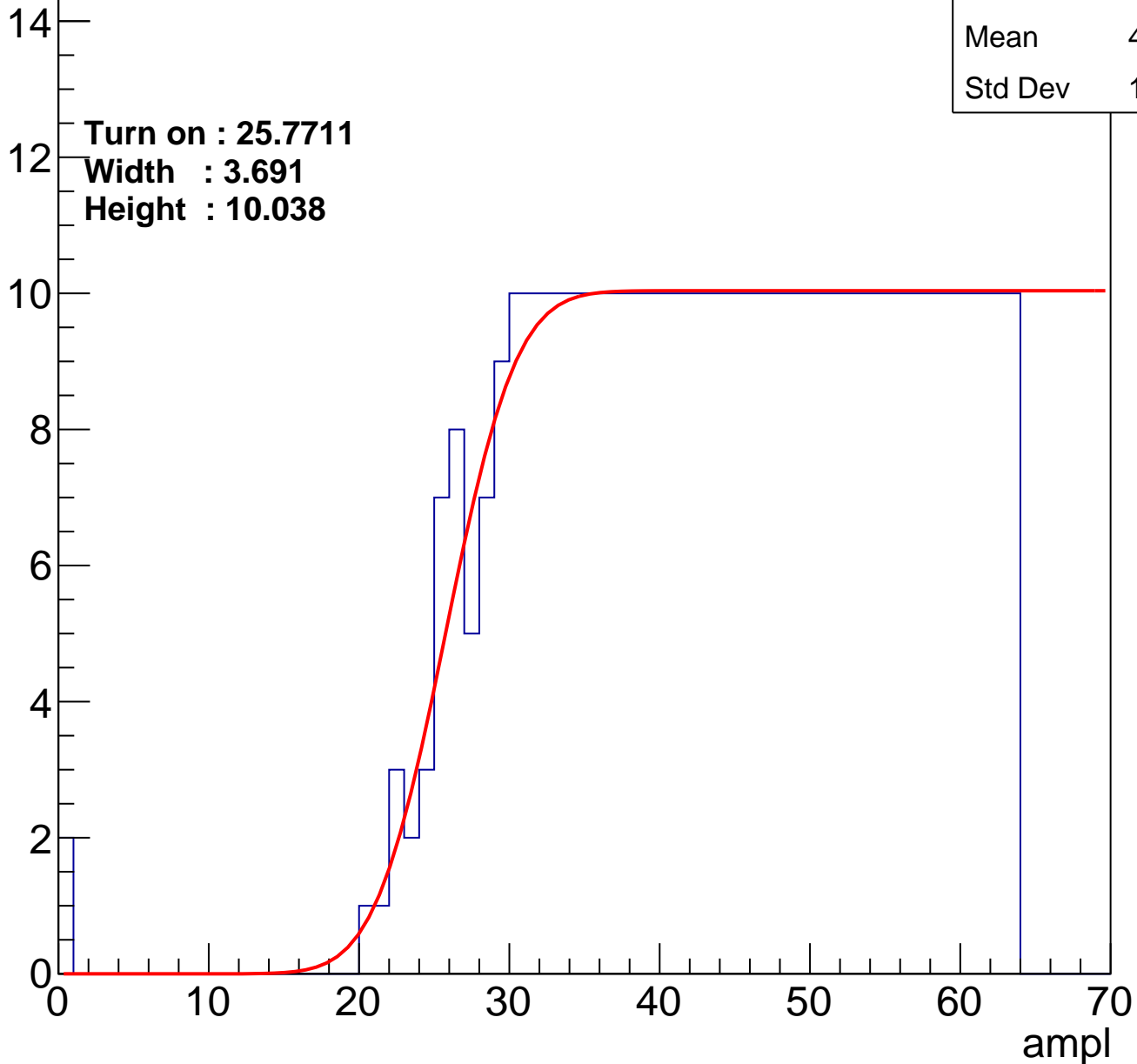
Entries	388
Mean	43.84
Std Dev	11.77

Turn on : 25.7711

Width : 3.691

Height : 10.038

Entry



B1L102S, U26-ch101

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.2
Std Dev	11.56

Turn on : 26.2560

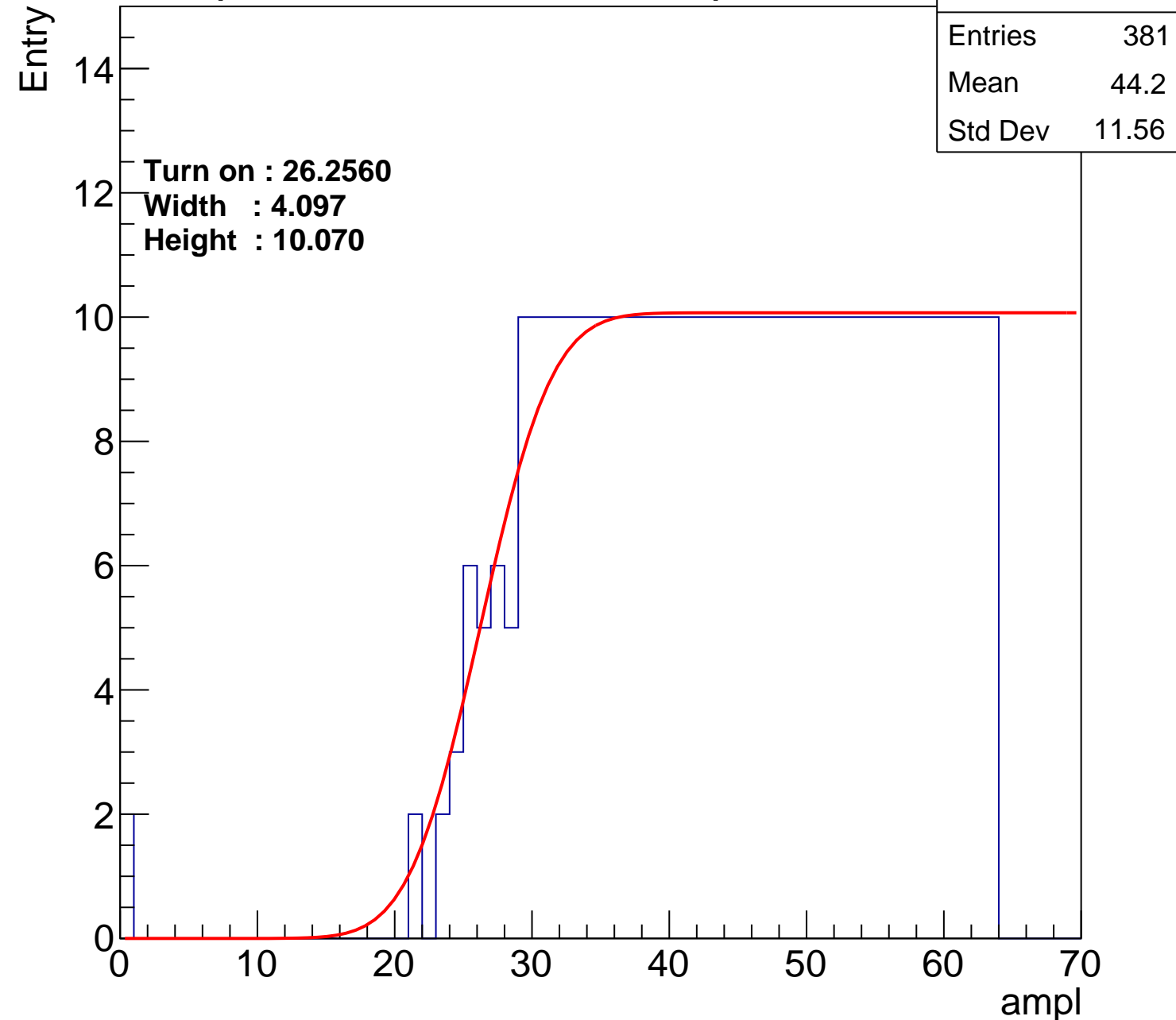
Width : 4.097

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch102

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.64
Std Dev	11.94

Turn on : 25.3978

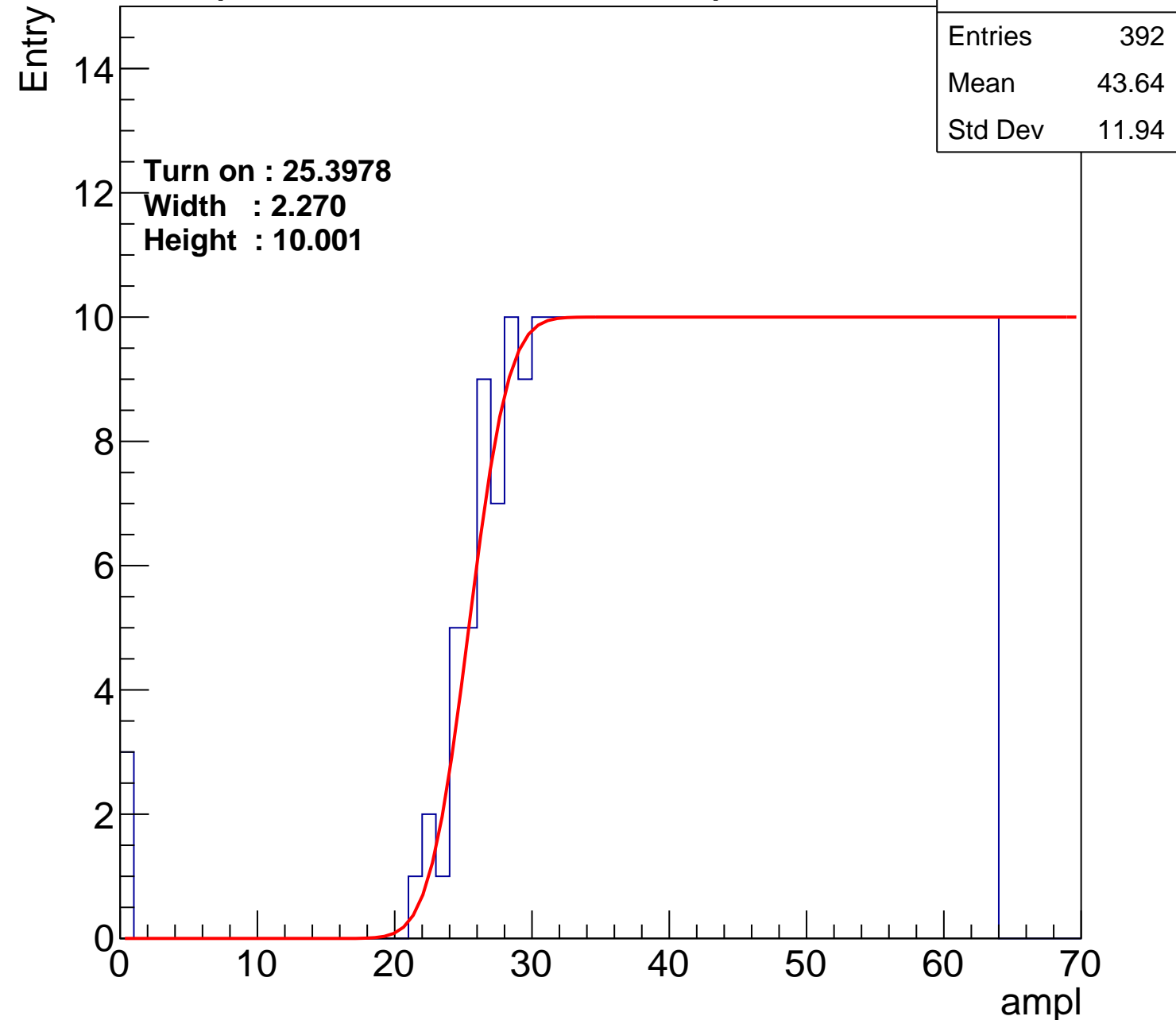
Width : 2.270

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch103

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.79
Std Dev	11.8

Turn on : 25.2443

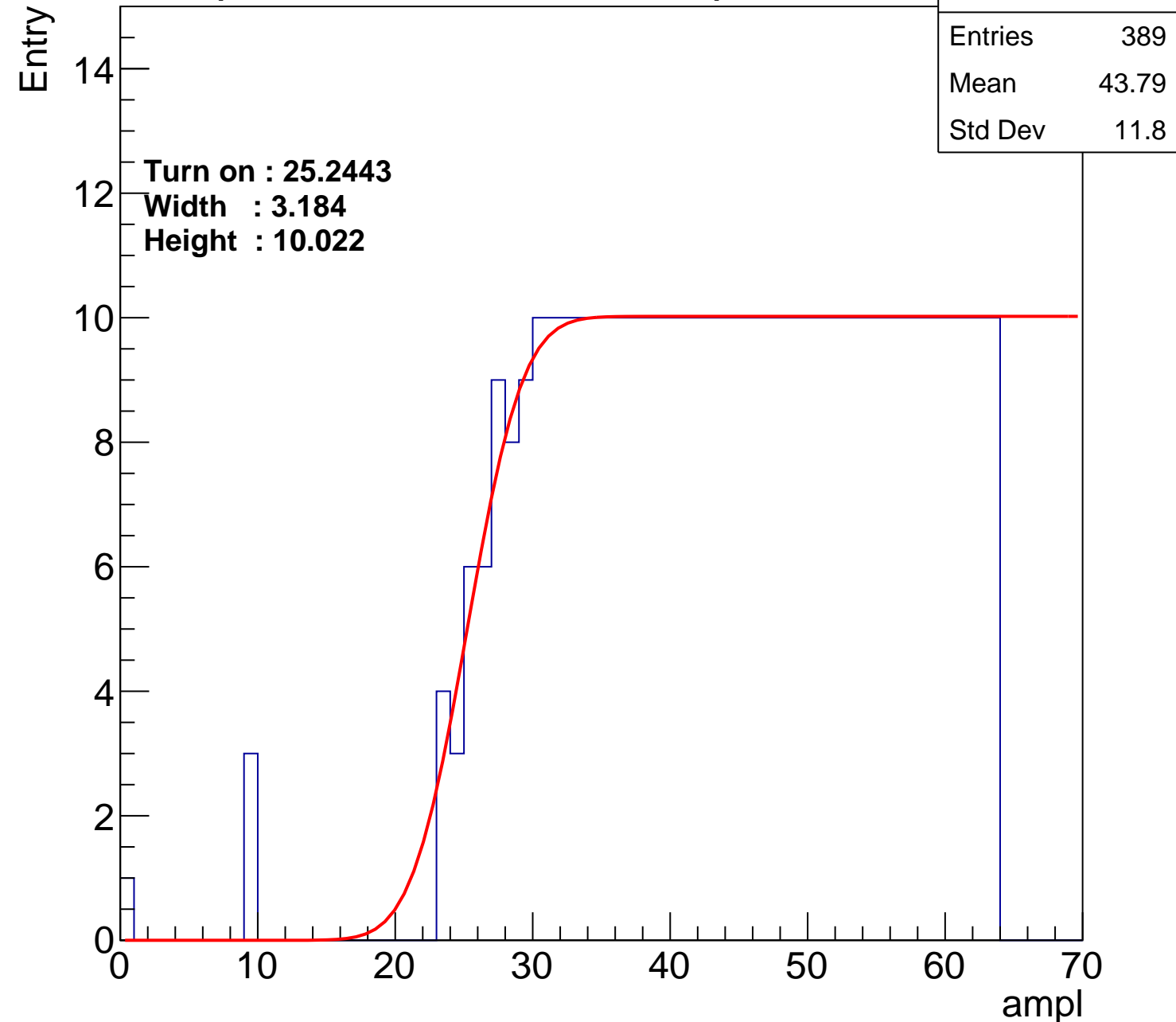
Width : 3.184

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch104

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.55
Std Dev	12.03

Turn on : 25.8723

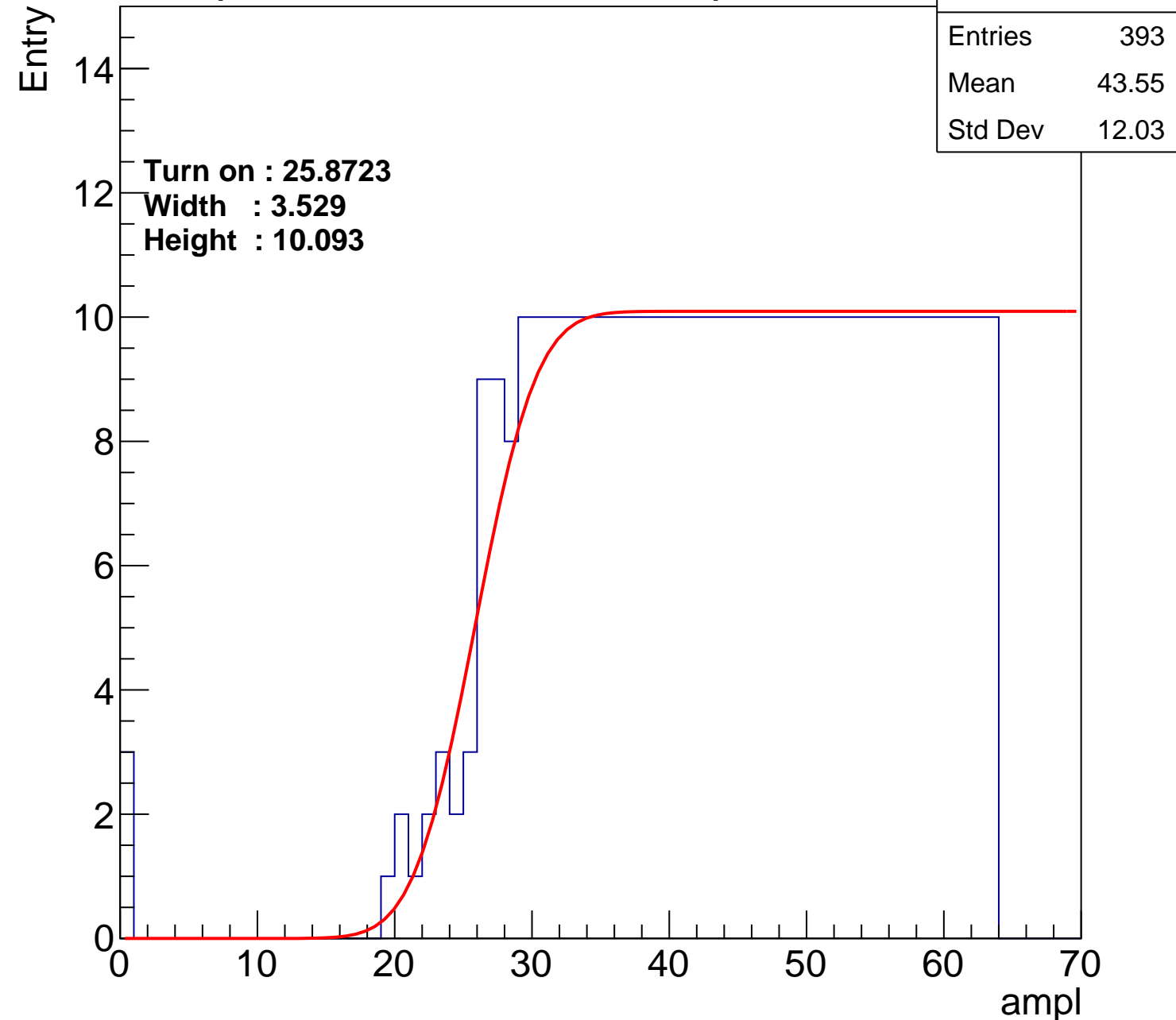
Width : 3.529

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch105

calib_packv5_042523_0143.root, FC#11, port A2

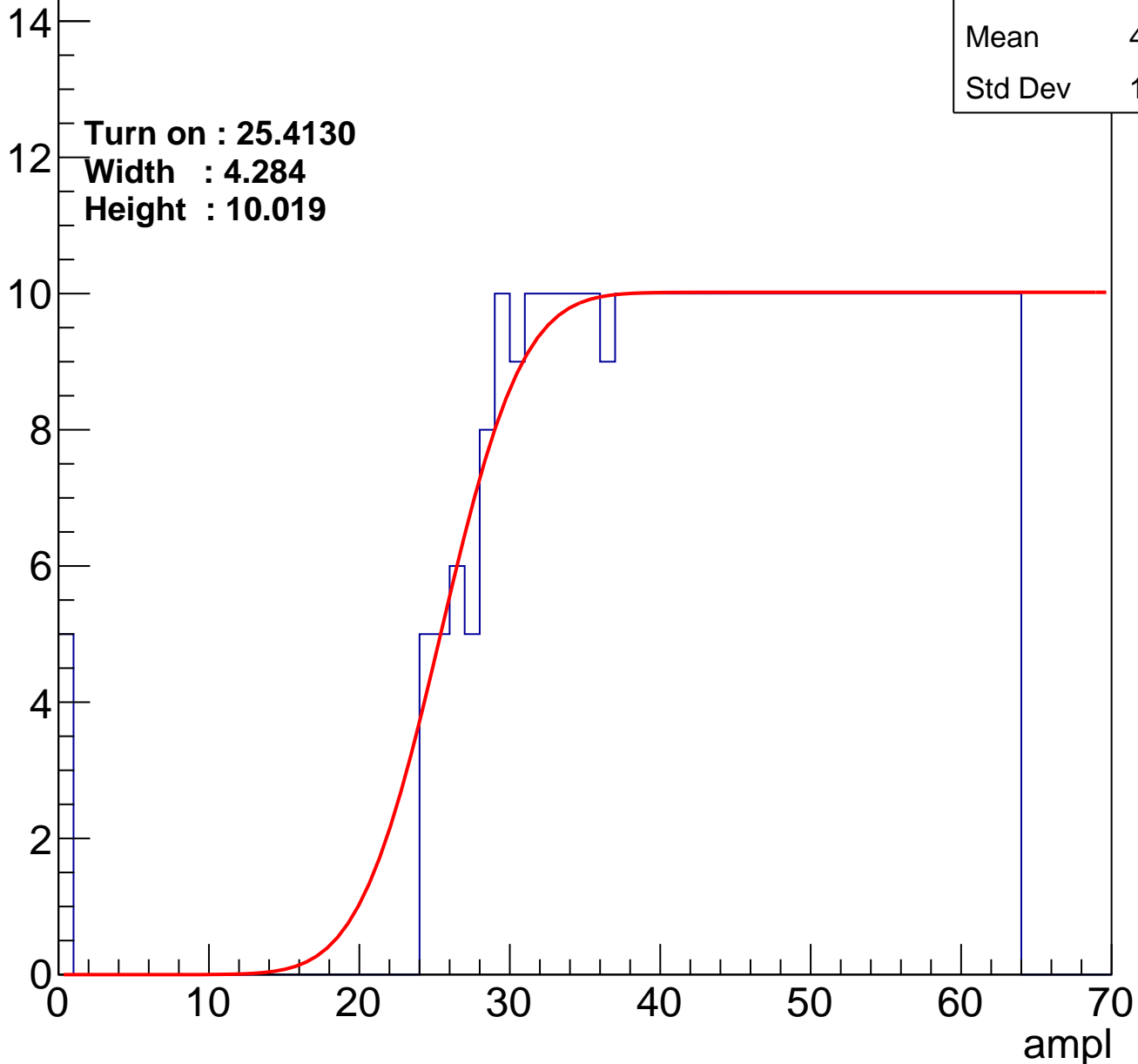
Entries	382
Mean	43.96
Std Dev	12.08

Turn on : 25.4130

Width : 4.284

Height : 10.019

Entry



B1L102S, U26-ch106

calib_packv5_042523_0143.root, FC#11, port A2

Entries	400
Mean	43.13
Std Dev	12.38

Turn on : 24.6484

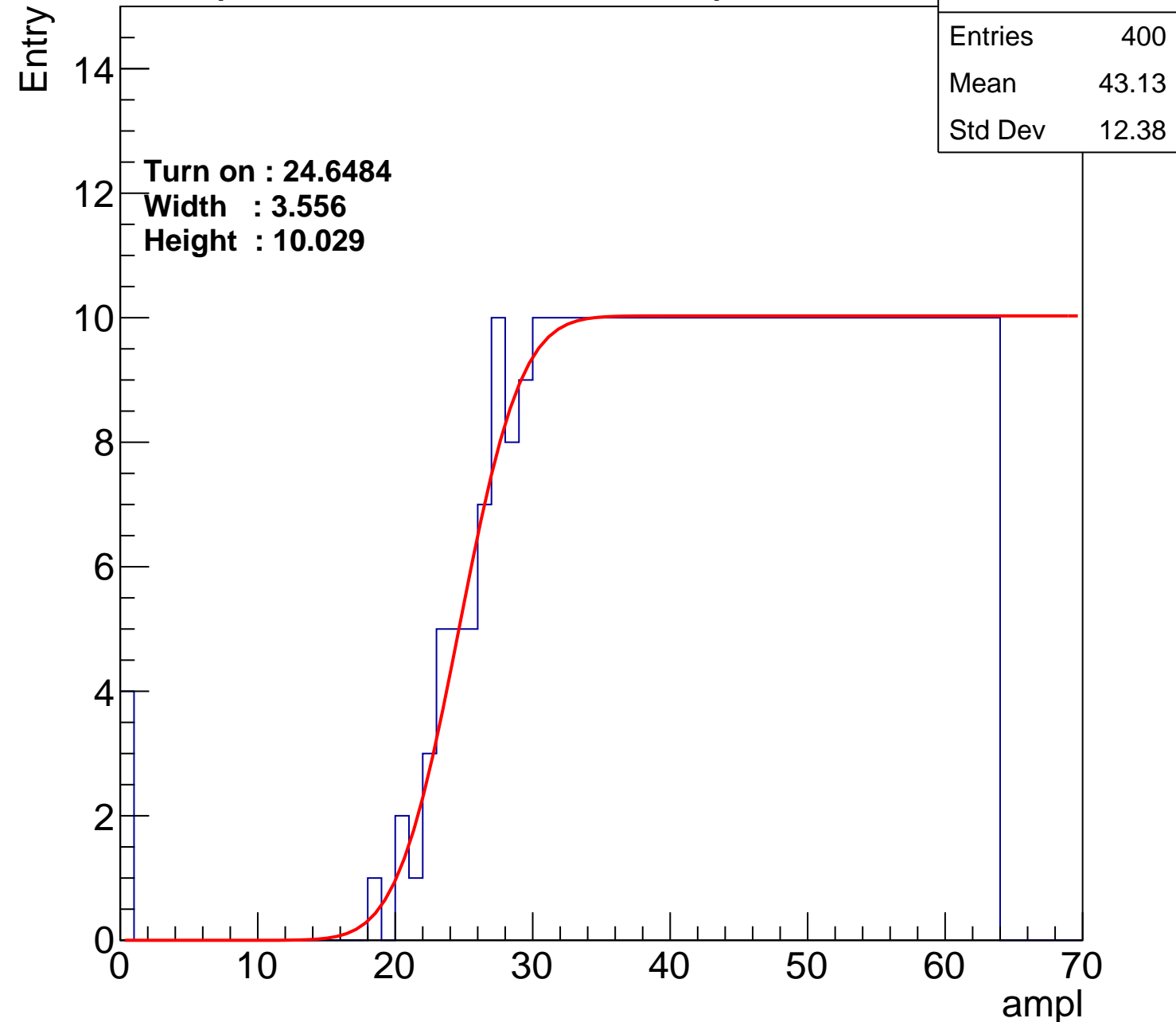
Width : 3.556

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch107

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.4
Std Dev	11.5

Turn on : 26.9731

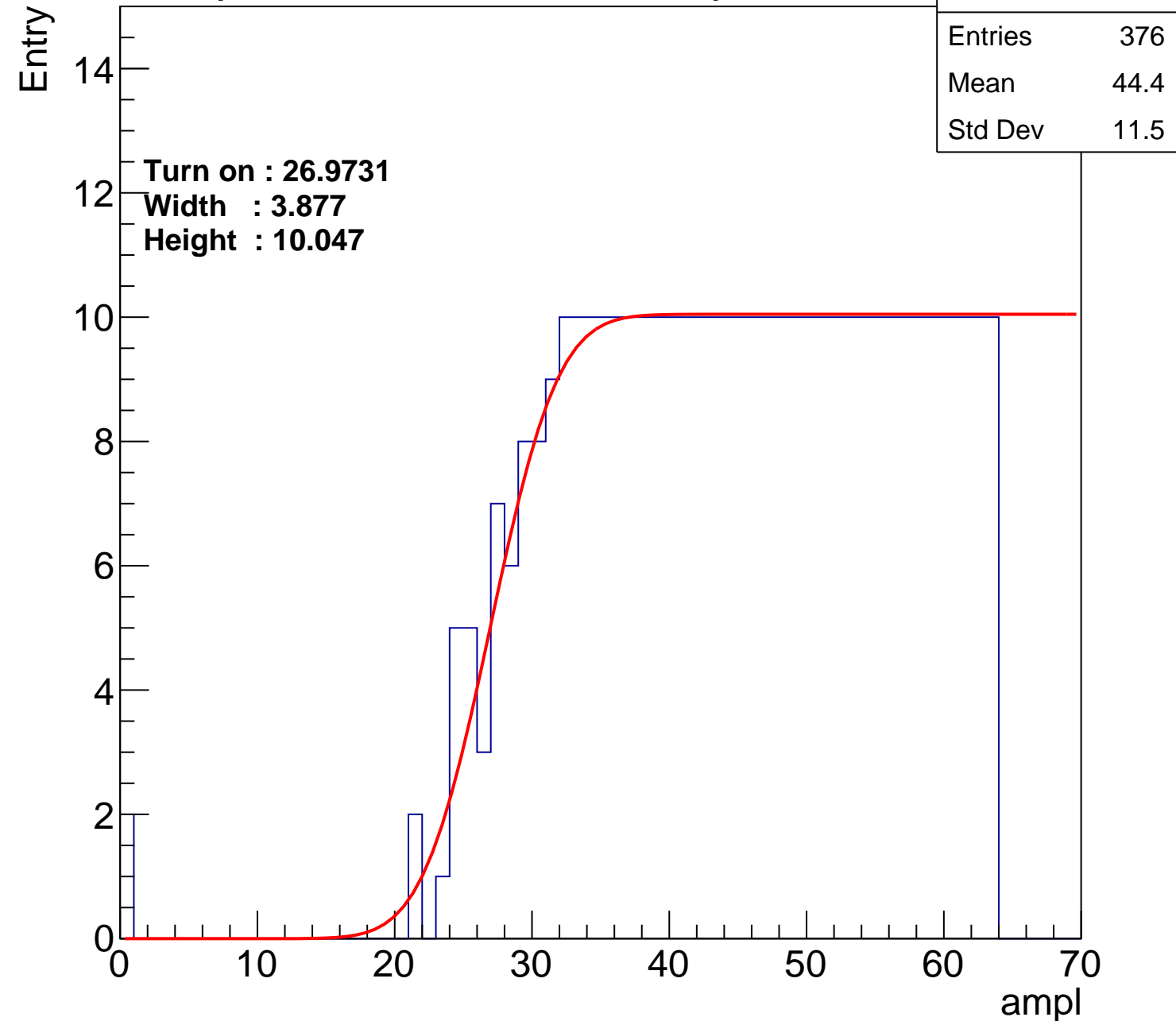
Width : 3.877

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch108

calib_packv5_042523_0143.root, FC#11, port A2

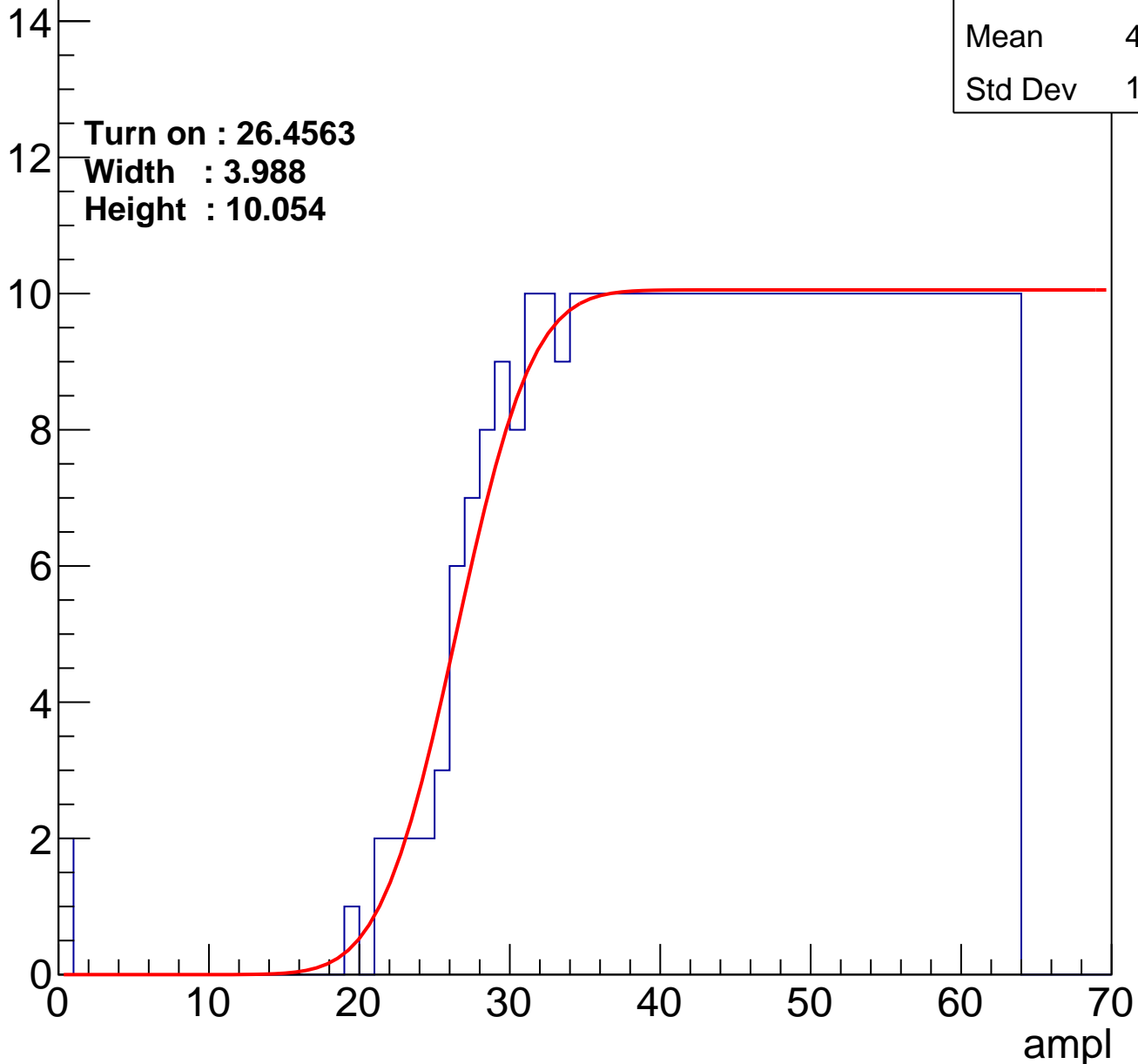
Entries	381
Mean	44.15
Std Dev	11.64

Turn on : 26.4563

Width : 3.988

Height : 10.054

Entry



B1L102S, U26-ch109

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.74
Std Dev	11.15

Turn on : 26.9175

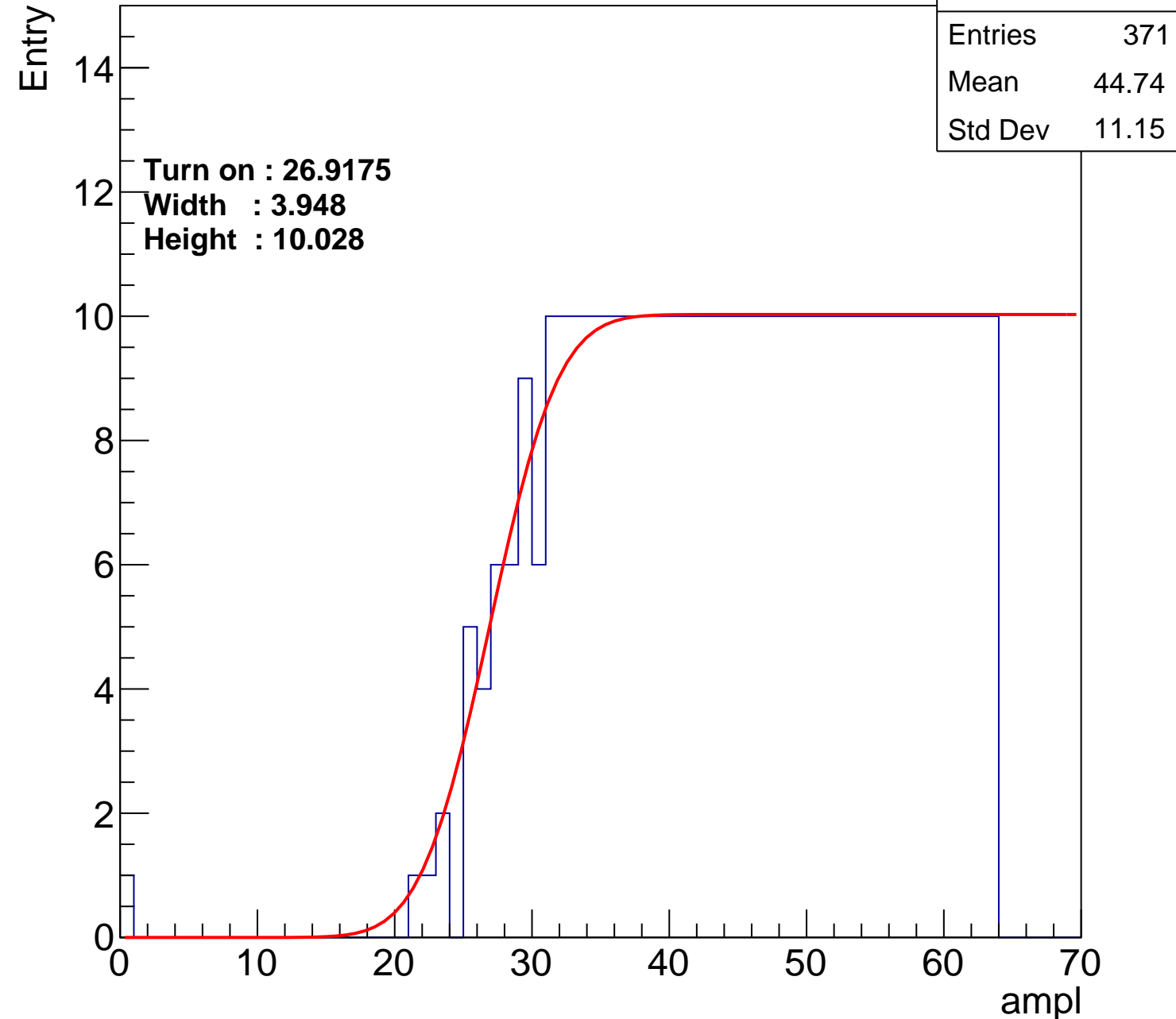
Width : 3.948

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch110

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.84
Std Dev	12.01

Turn on : 25.8151

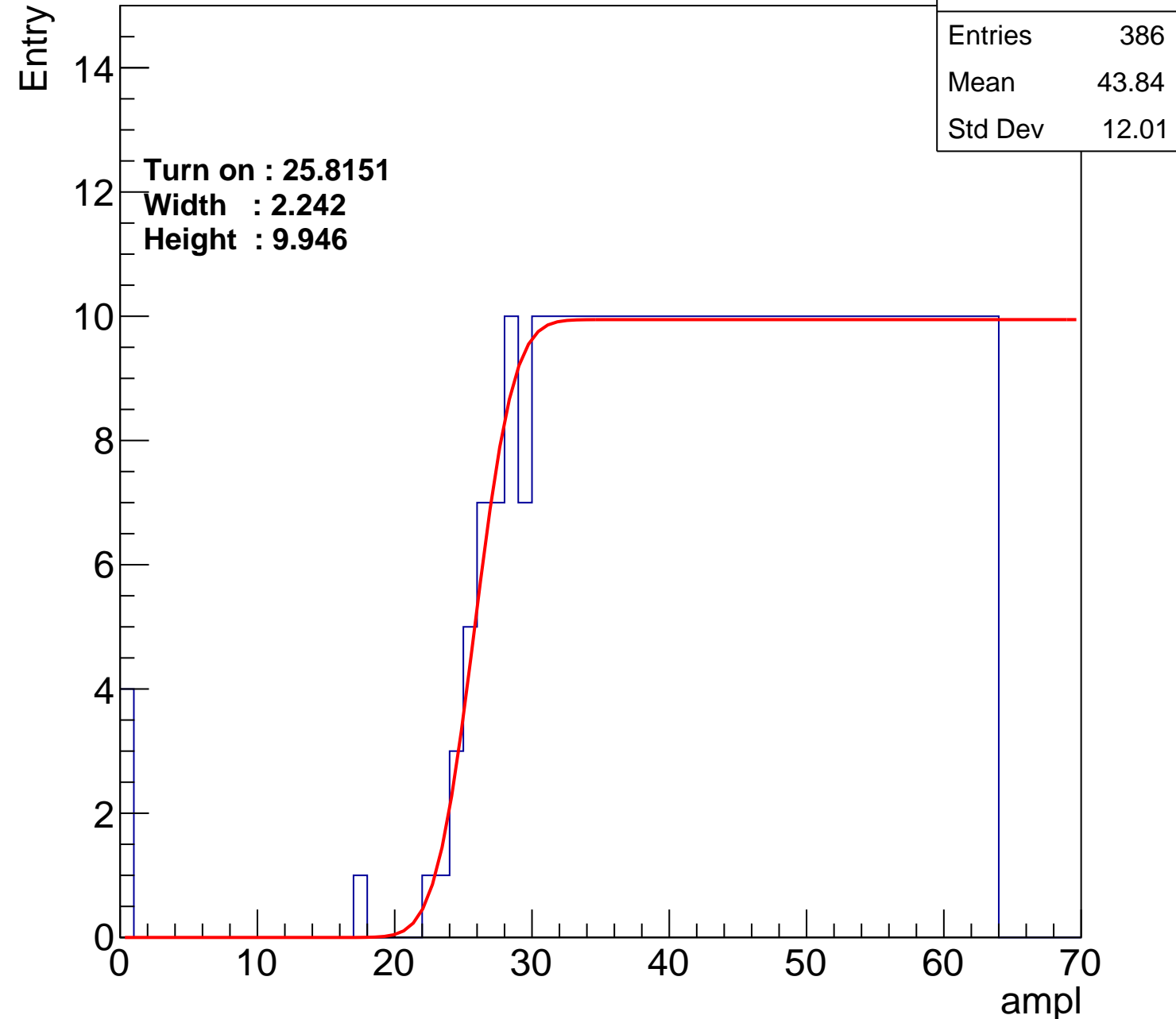
Width : 2.242

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch111

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.16
Std Dev	12.11

Turn on : 27.7670

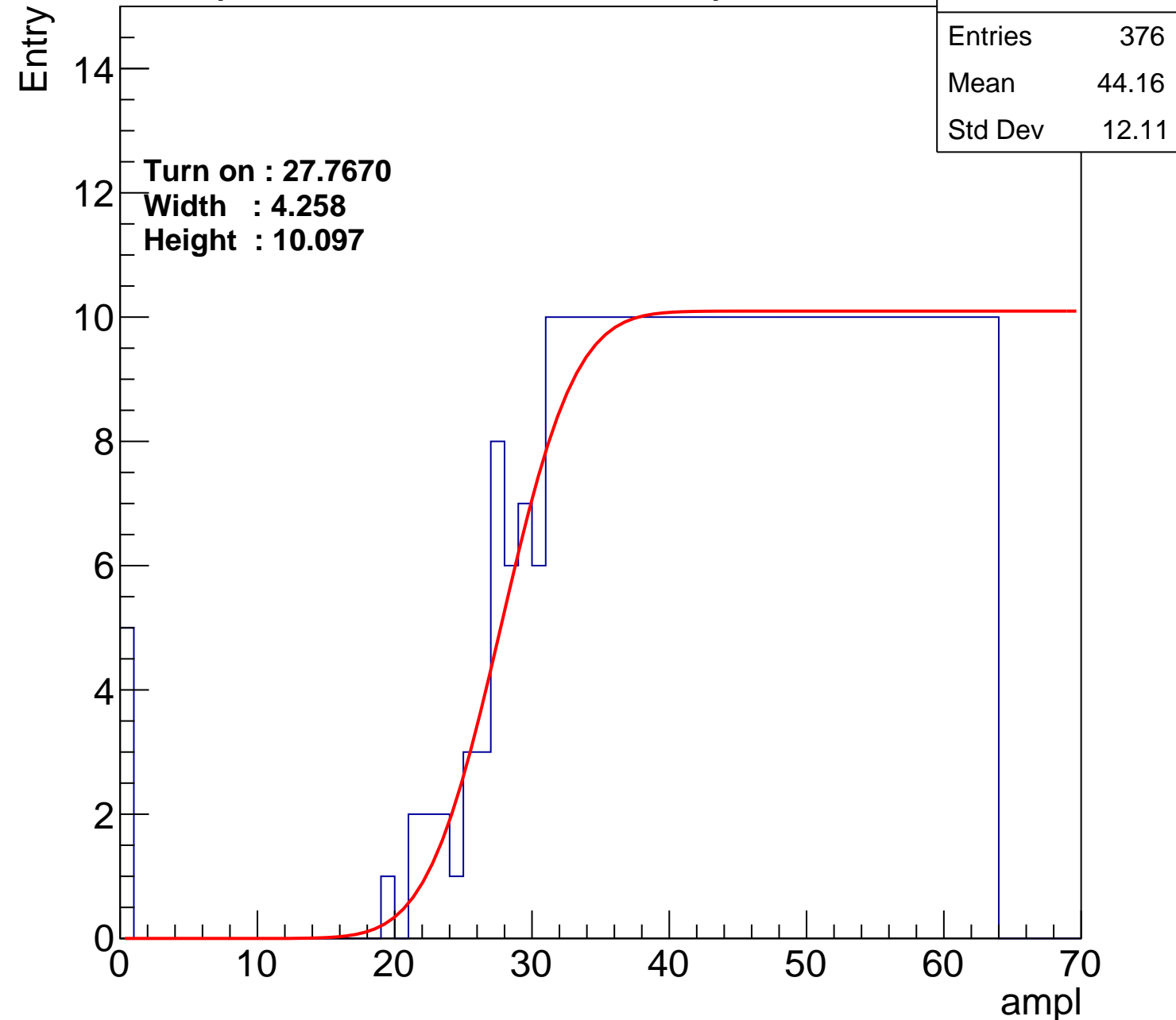
Width : 4.258

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch112

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.44
Std Dev	12.11

Turn on : 24.8648

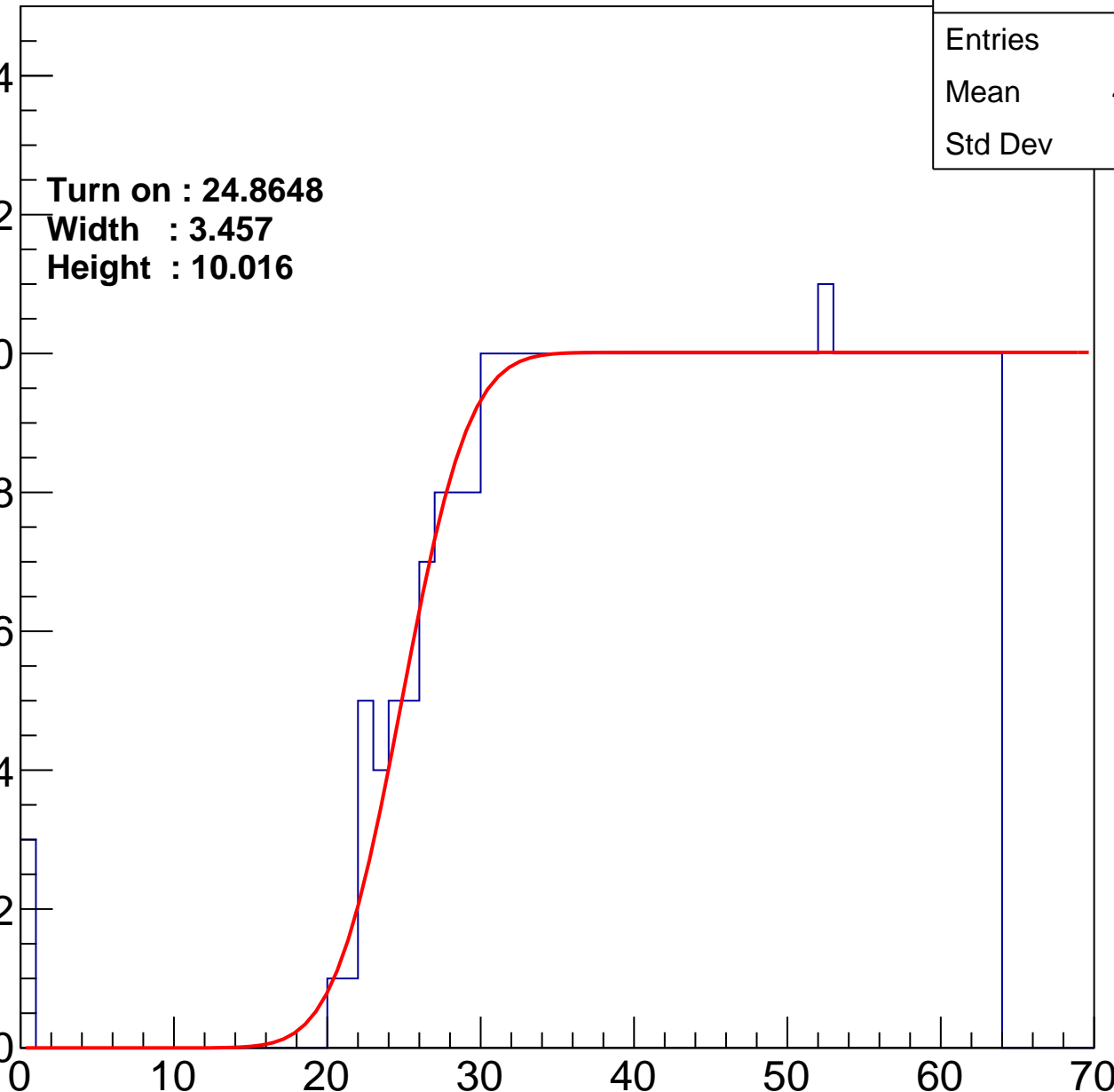
Width : 3.457

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch113

calib_packv5_042523_0143.root, FC#11, port A2

Entries	349
Mean	45.53
Std Dev	11.44

Turn on : 29.5791

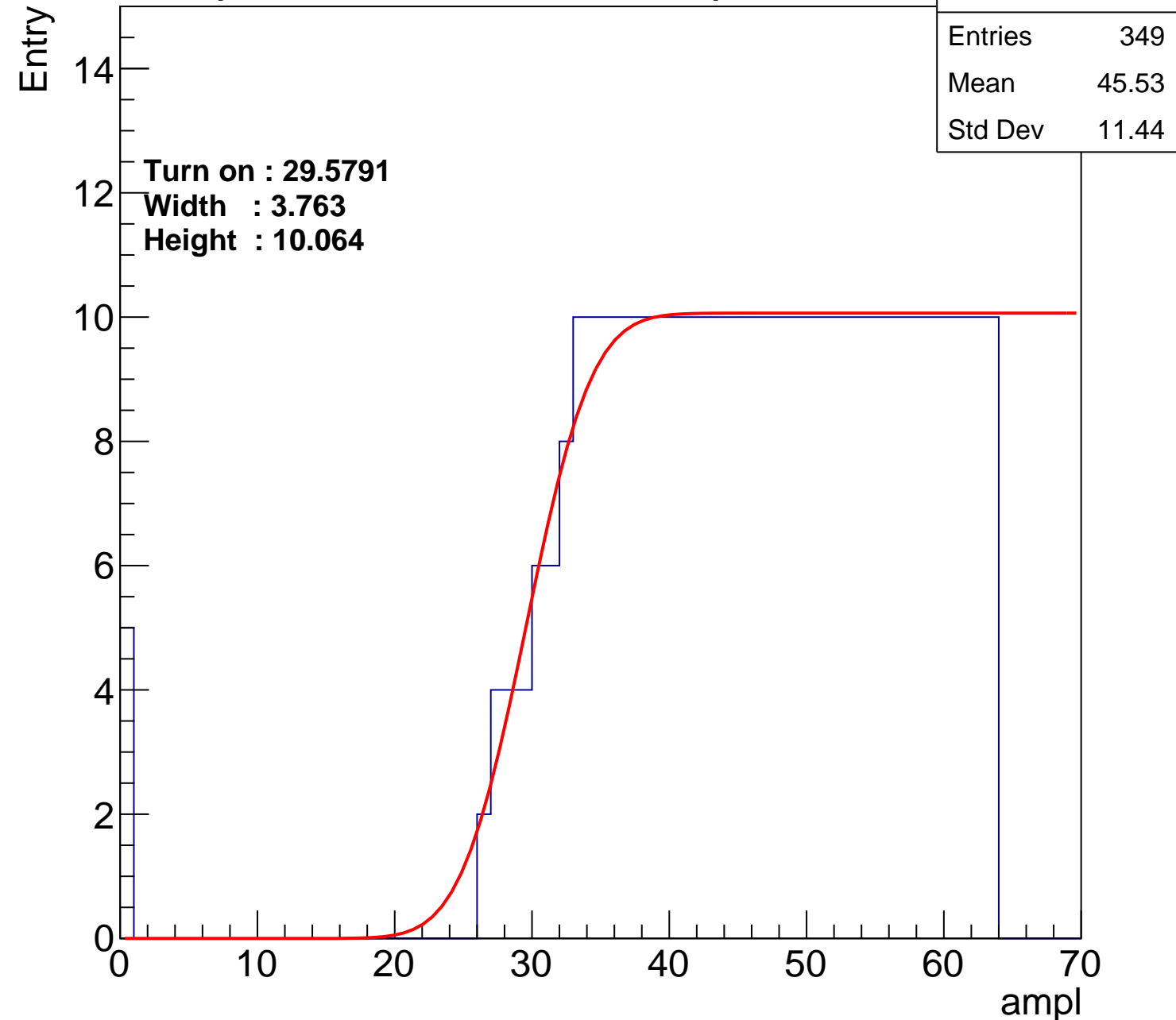
Width : 3.763

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch114

calib_packv5_042523_0143.root, FC#11, port A2

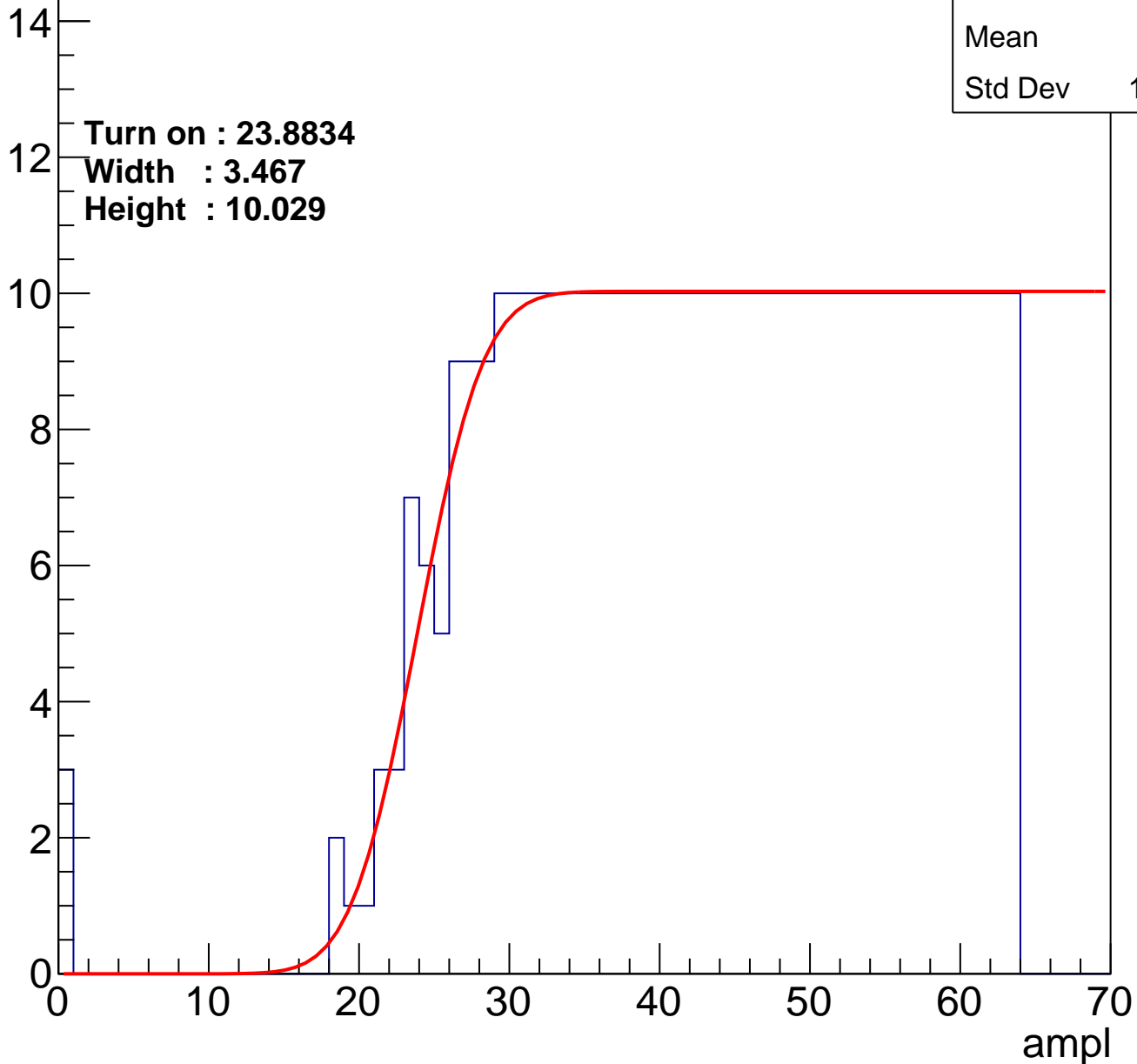
Entries	408
Mean	42.8
Std Dev	12.43

Turn on : 23.8834

Width : 3.467

Height : 10.029

Entry



B1L102S, U26-ch115

calib_packv5_042523_0143.root, FC#11, port A2

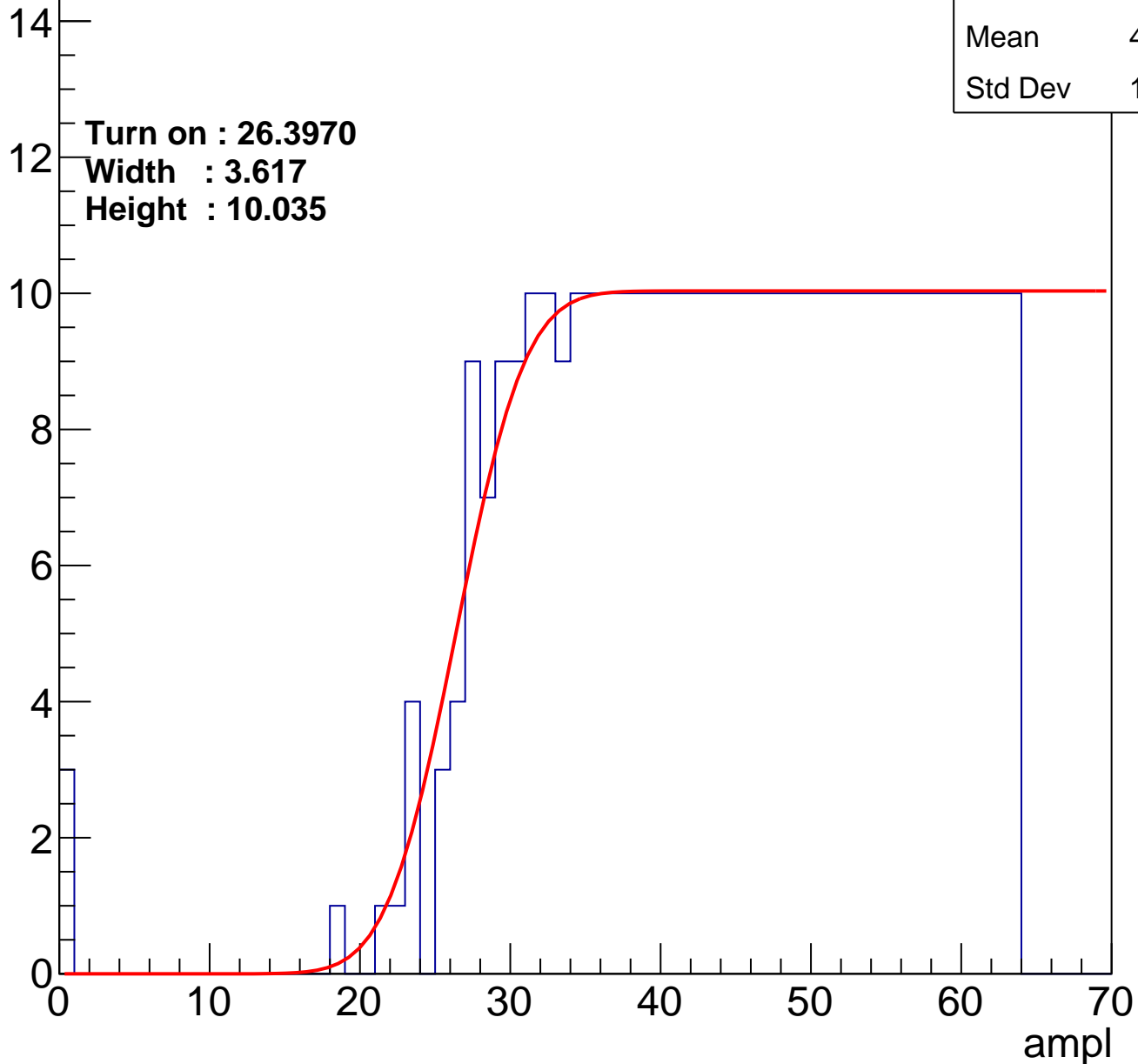
Entries	380
Mean	44.16
Std Dev	11.76

Turn on : 26.3970

Width : 3.617

Height : 10.035

Entry



B1L102S, U26-ch116

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.6
Std Dev	11.88

Turn on : 25.2841

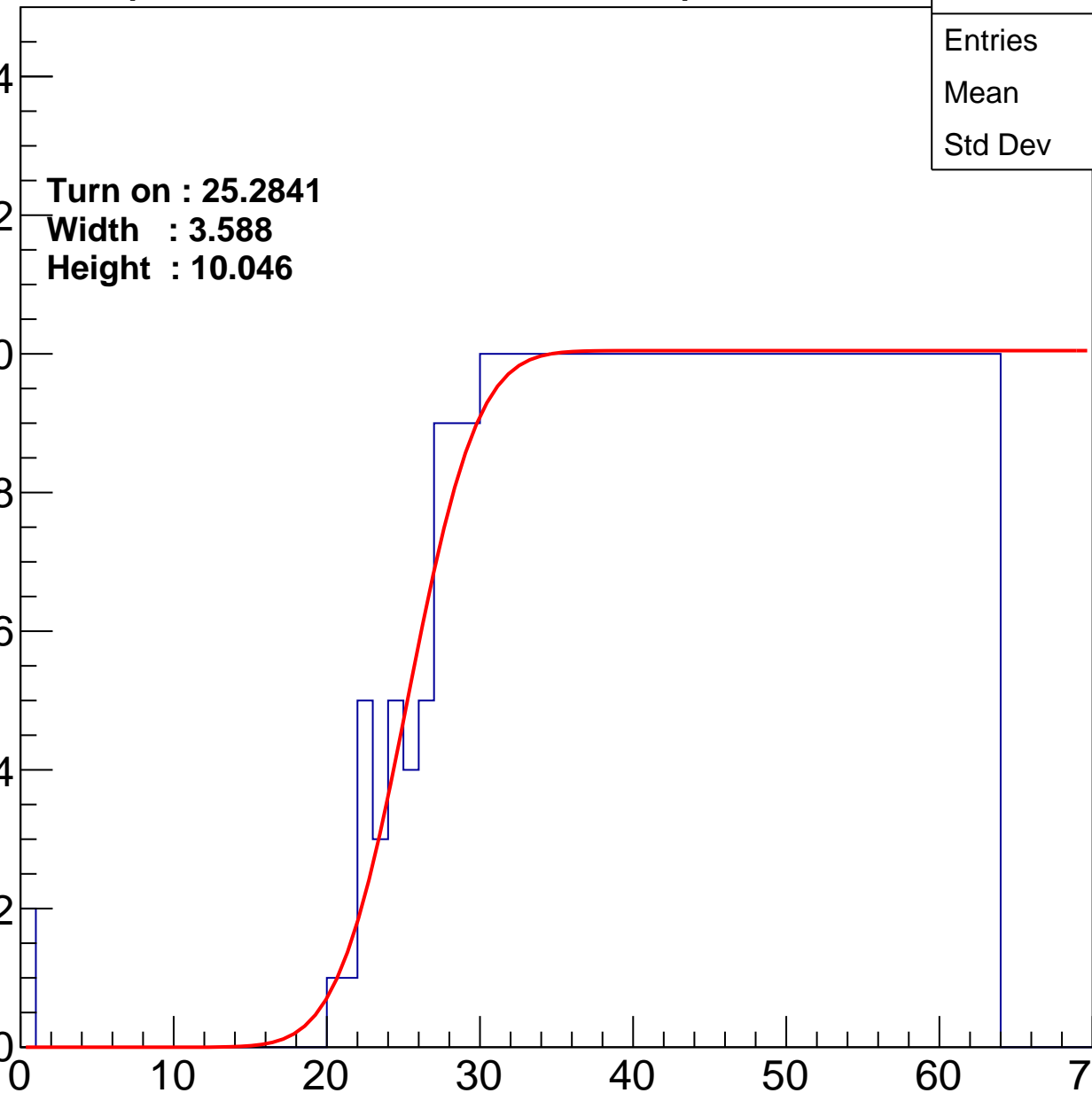
Width : 3.588

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch117

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.97
Std Dev	11.19

Turn on : 27.4762

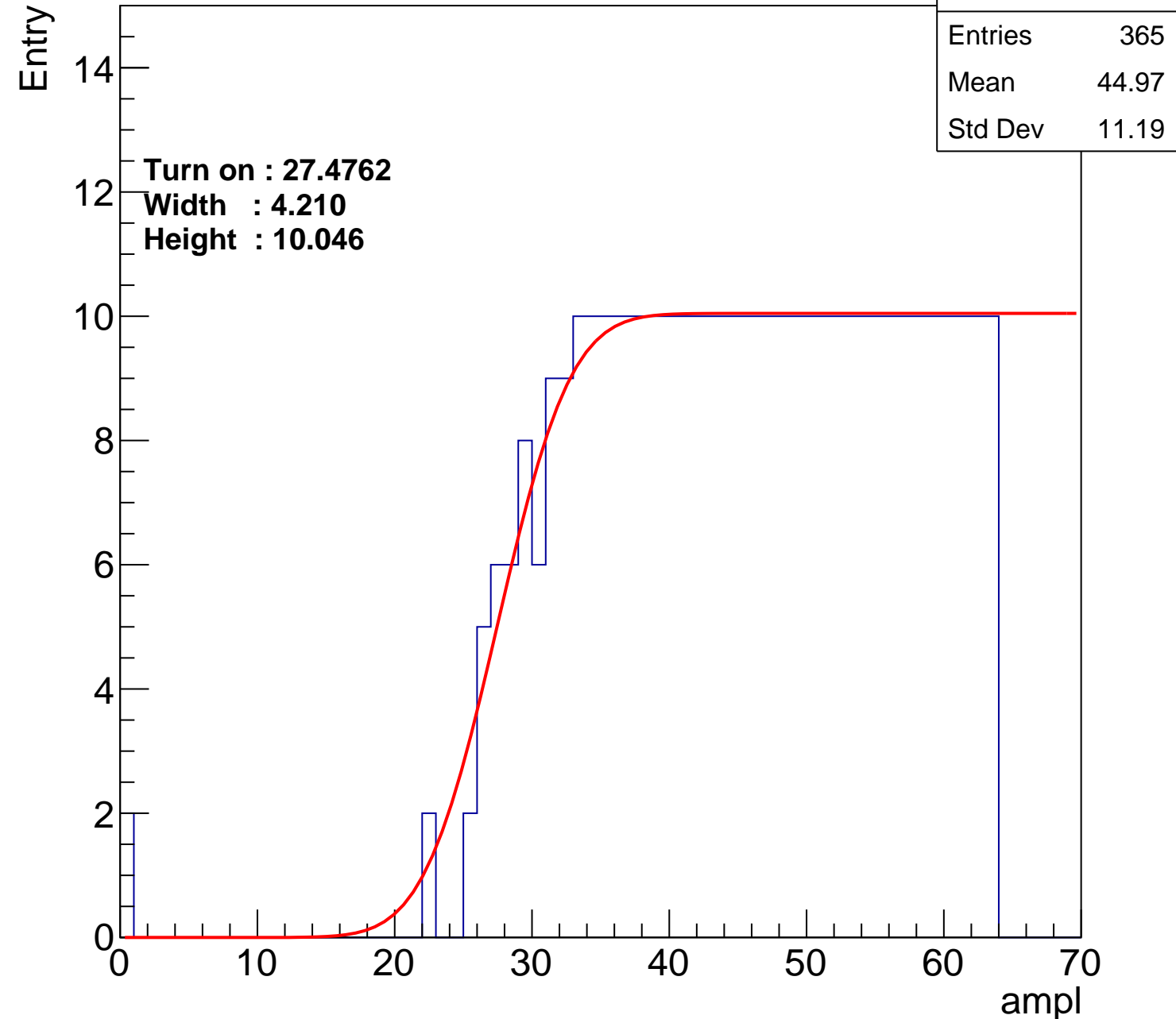
Width : 4.210

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch118

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.51
Std Dev	11.44

Turn on : 27.4763

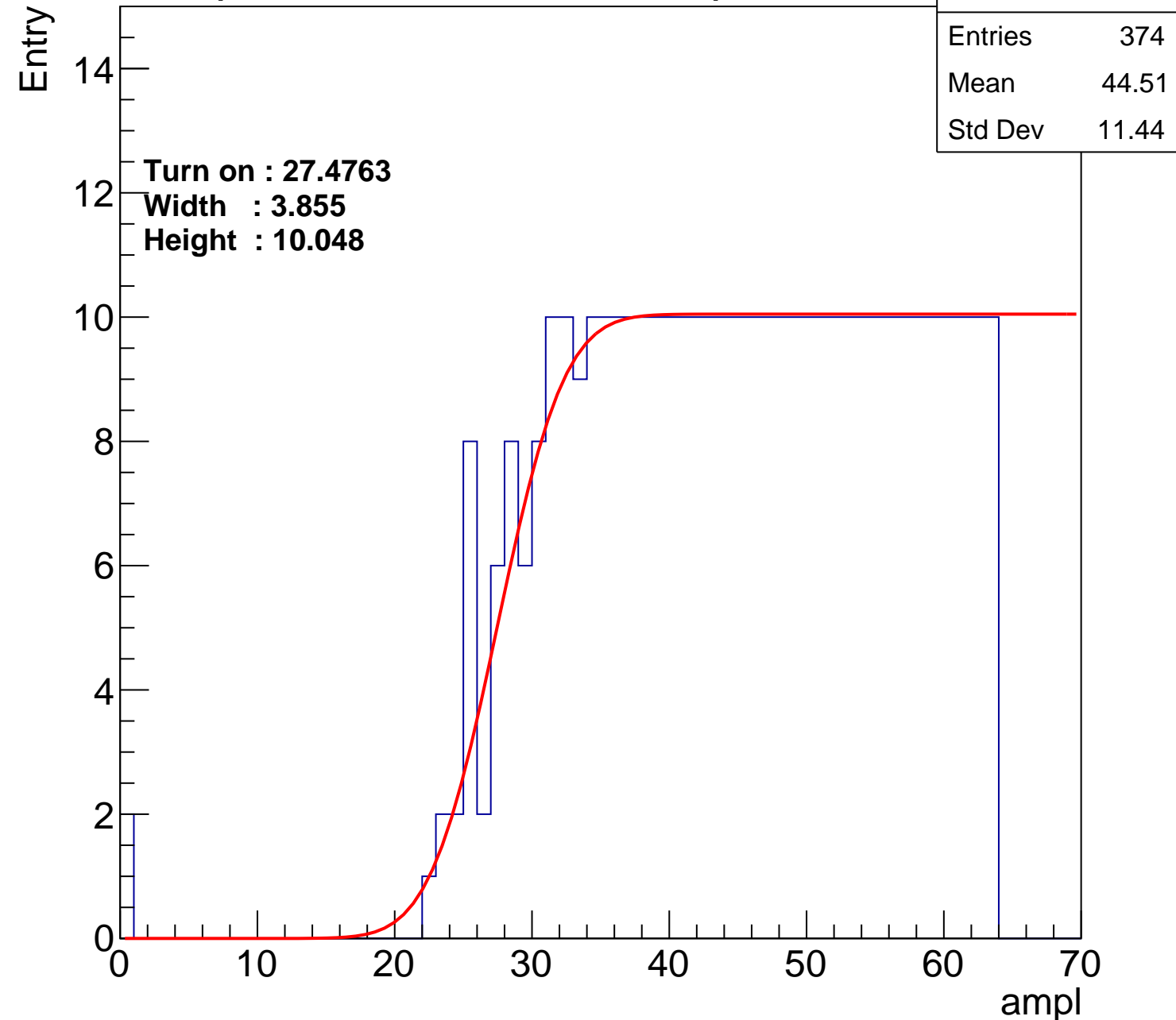
Width : 3.855

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch119

calib_packv5_042523_0143.root, FC#11, port A2

Entries	363
Mean	45.09
Std Dev	11.11

Turn on : 28.2467

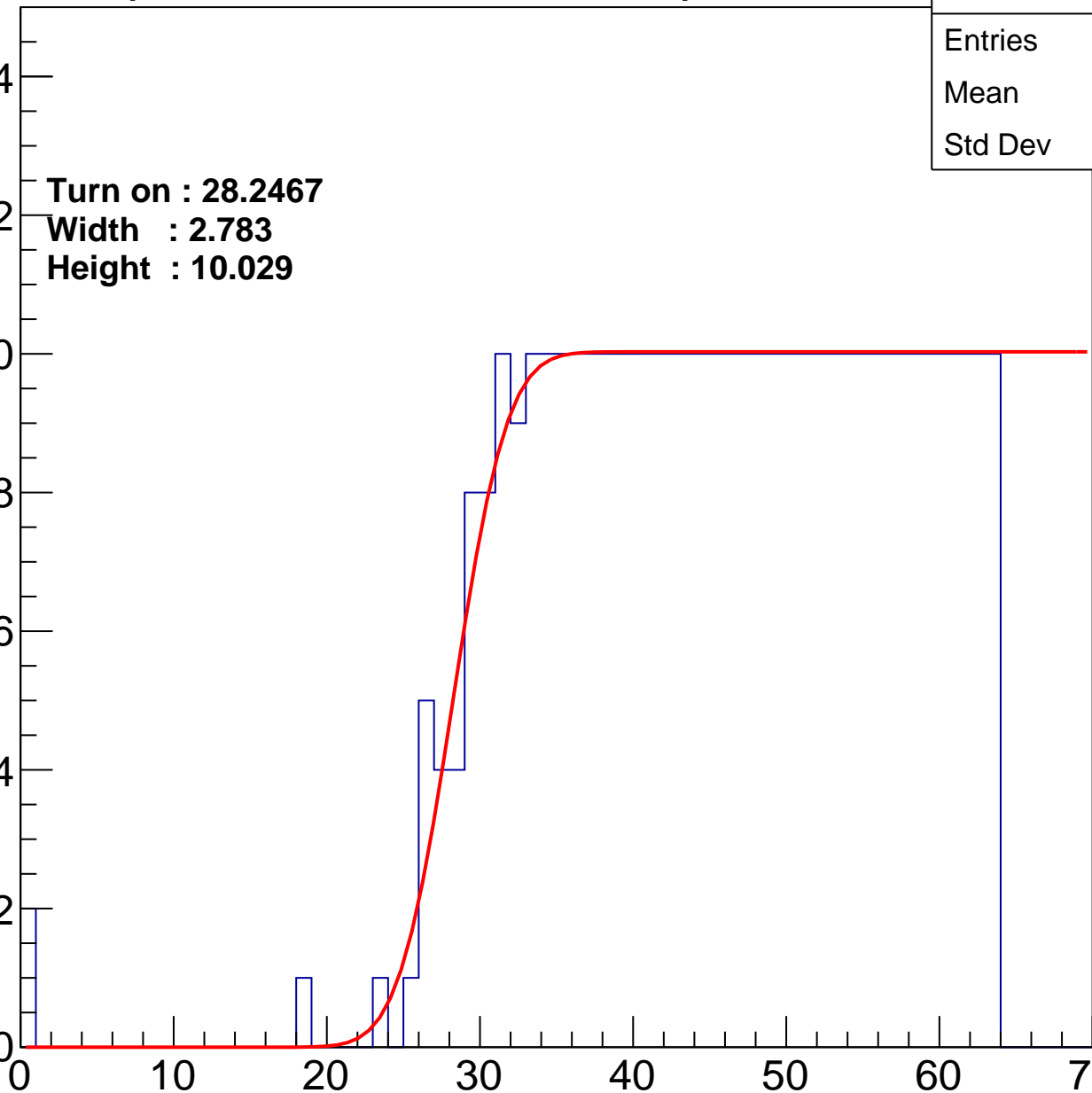
Width : 2.783

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch120

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.58
Std Dev	12.2

Turn on : 25.7041

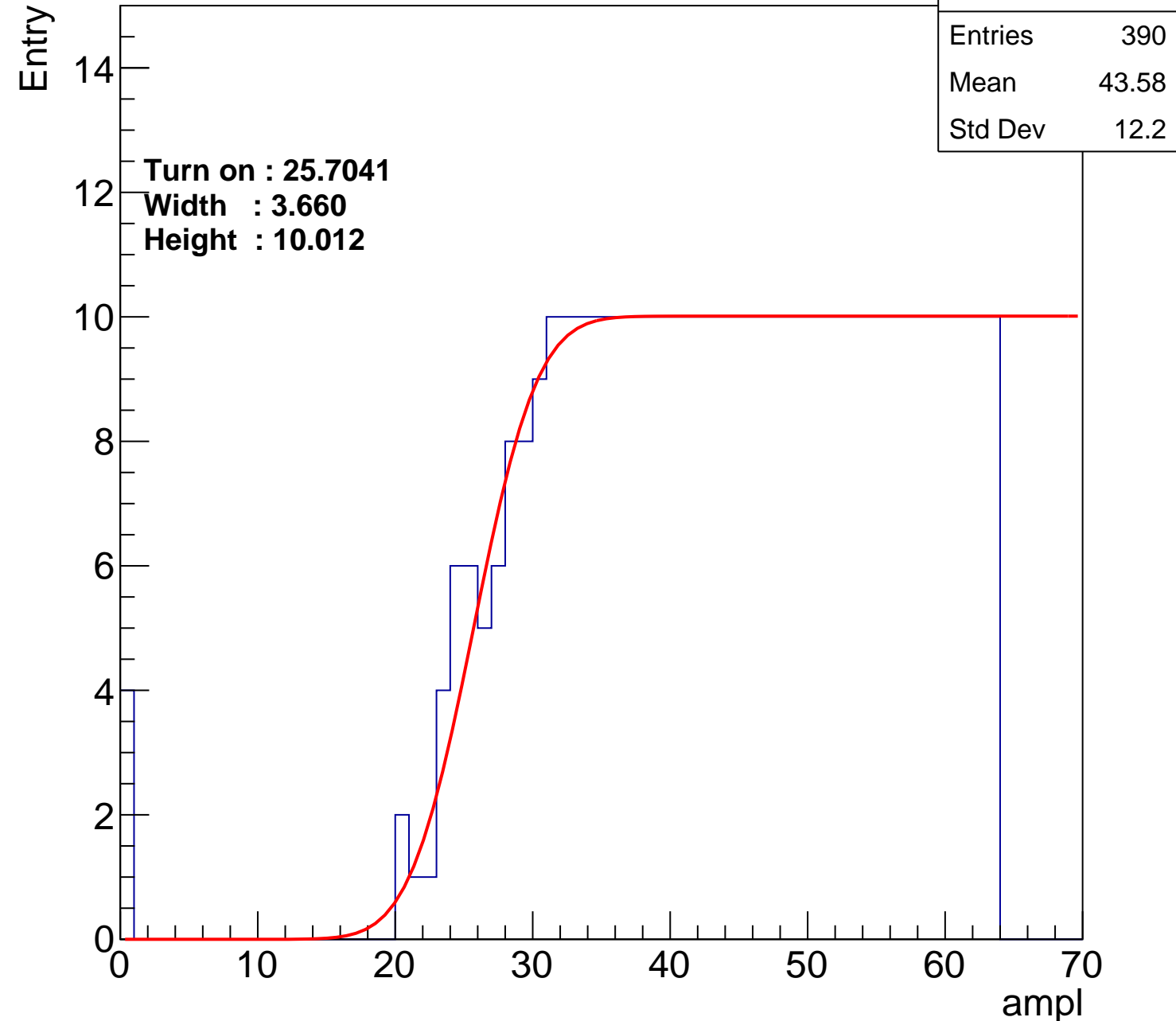
Width : 3.660

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch121

calib_packv5_042523_0143.root, FC#11, port A2

Entries	402
Mean	43.09
Std Dev	12.27

Turn on : 24.1591

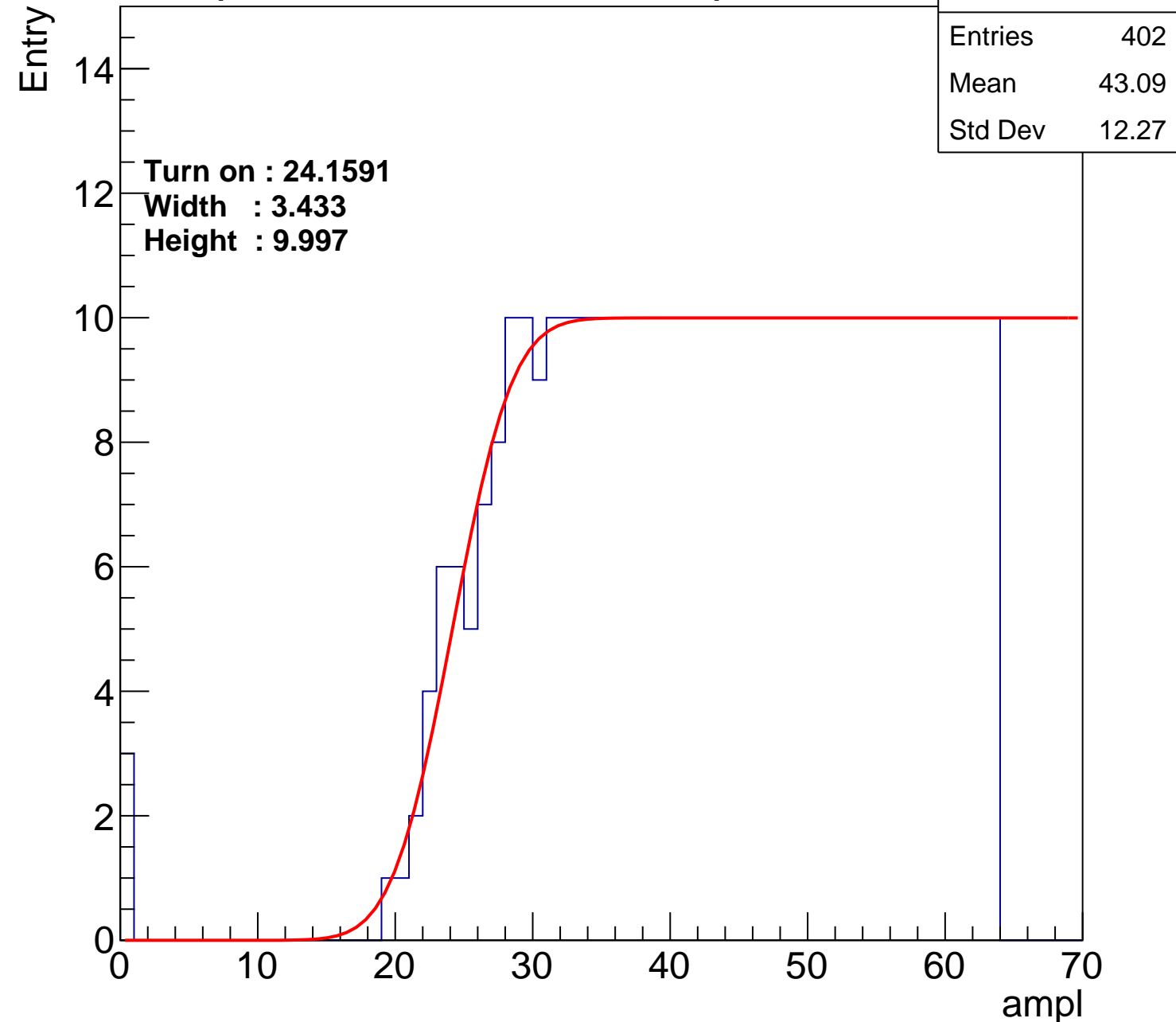
Width : 3.433

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch122

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	45.05
Std Dev	10.96

Turn on : 27.8128

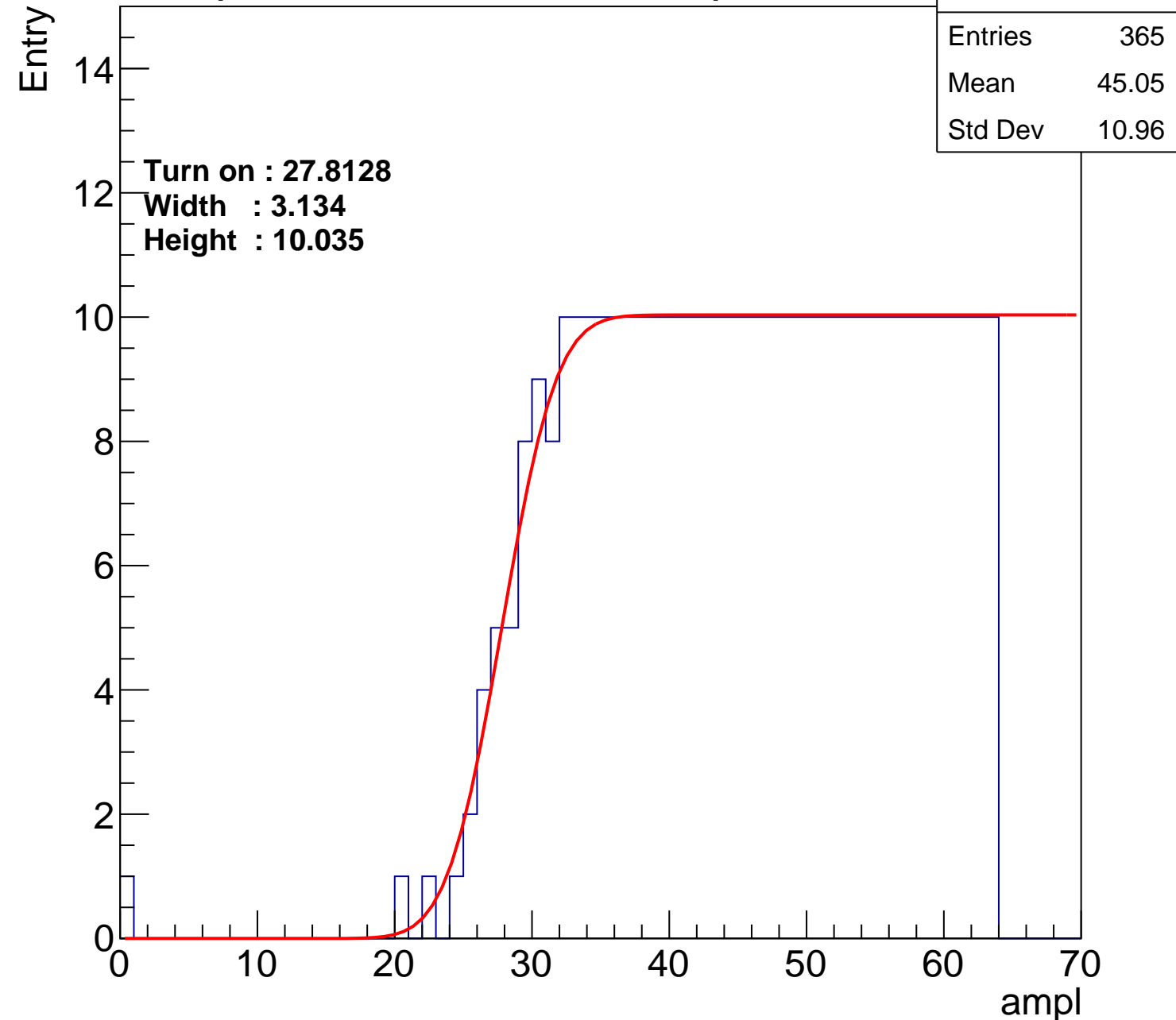
Width : 3.134

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch123

calib_packv5_042523_0143.root, FC#11, port A2

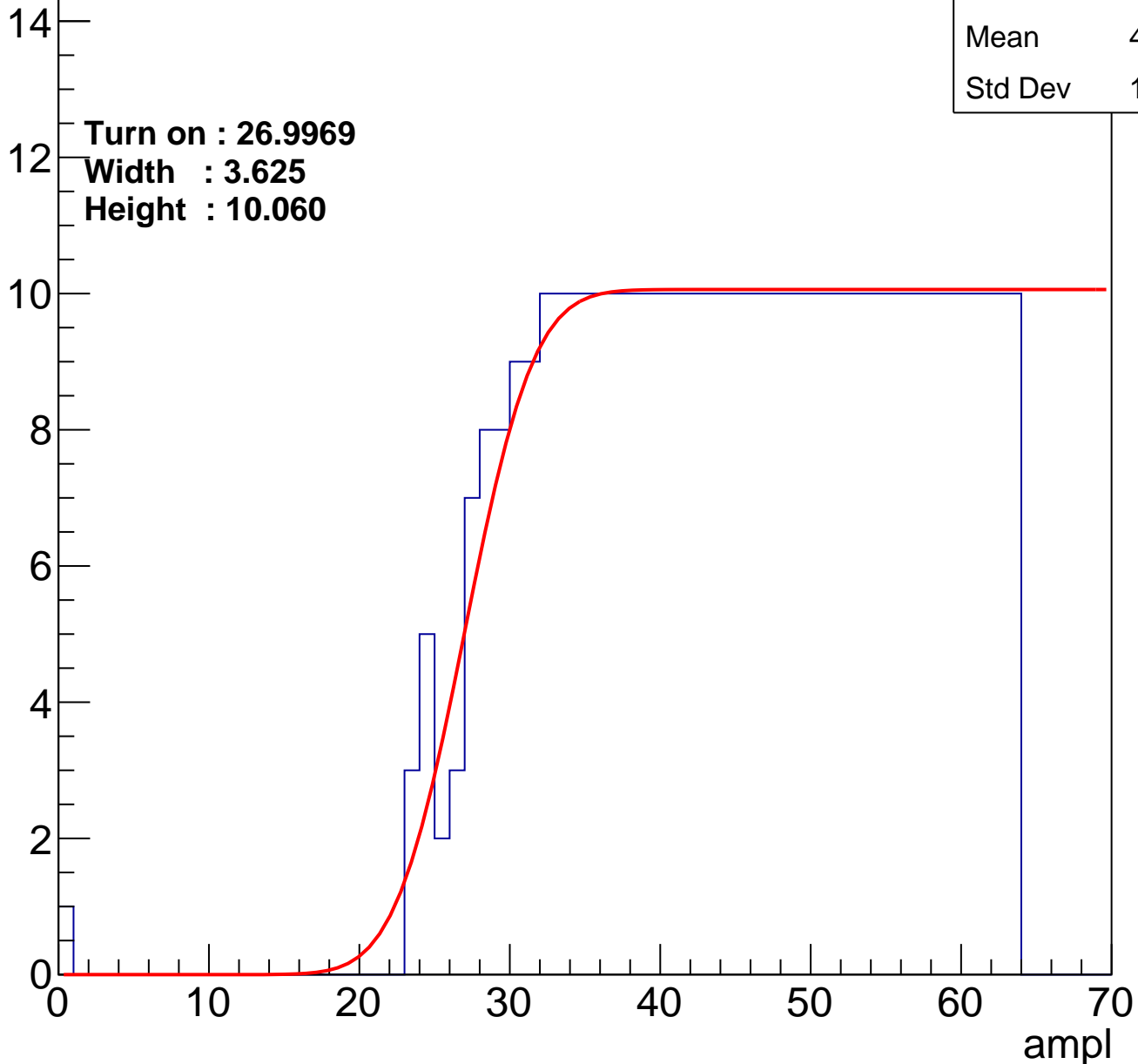
Entries	375
Mean	44.56
Std Dev	11.22

Turn on : 26.9969

Width : 3.625

Height : 10.060

Entry



B1L102S, U26-ch124

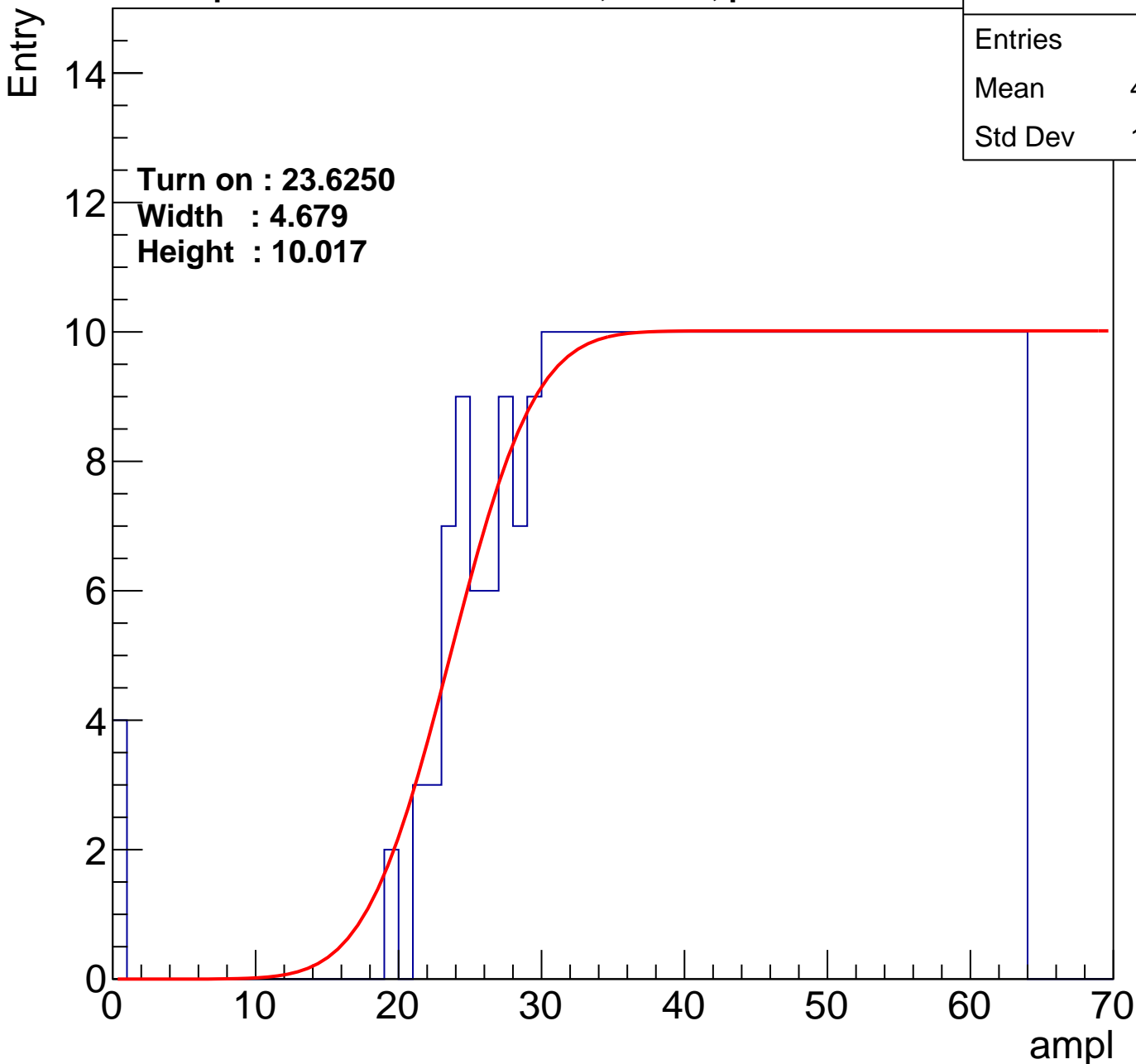
calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	42.86
Std Dev	12.53

Turn on : 23.6250

Width : 4.679

Height : 10.017



B1L102S, U26-ch125

calib_packv5_042523_0143.root, FC#11, port A2

Entries	392
Mean	43.64
Std Dev	11.88

Turn on : 25.2607

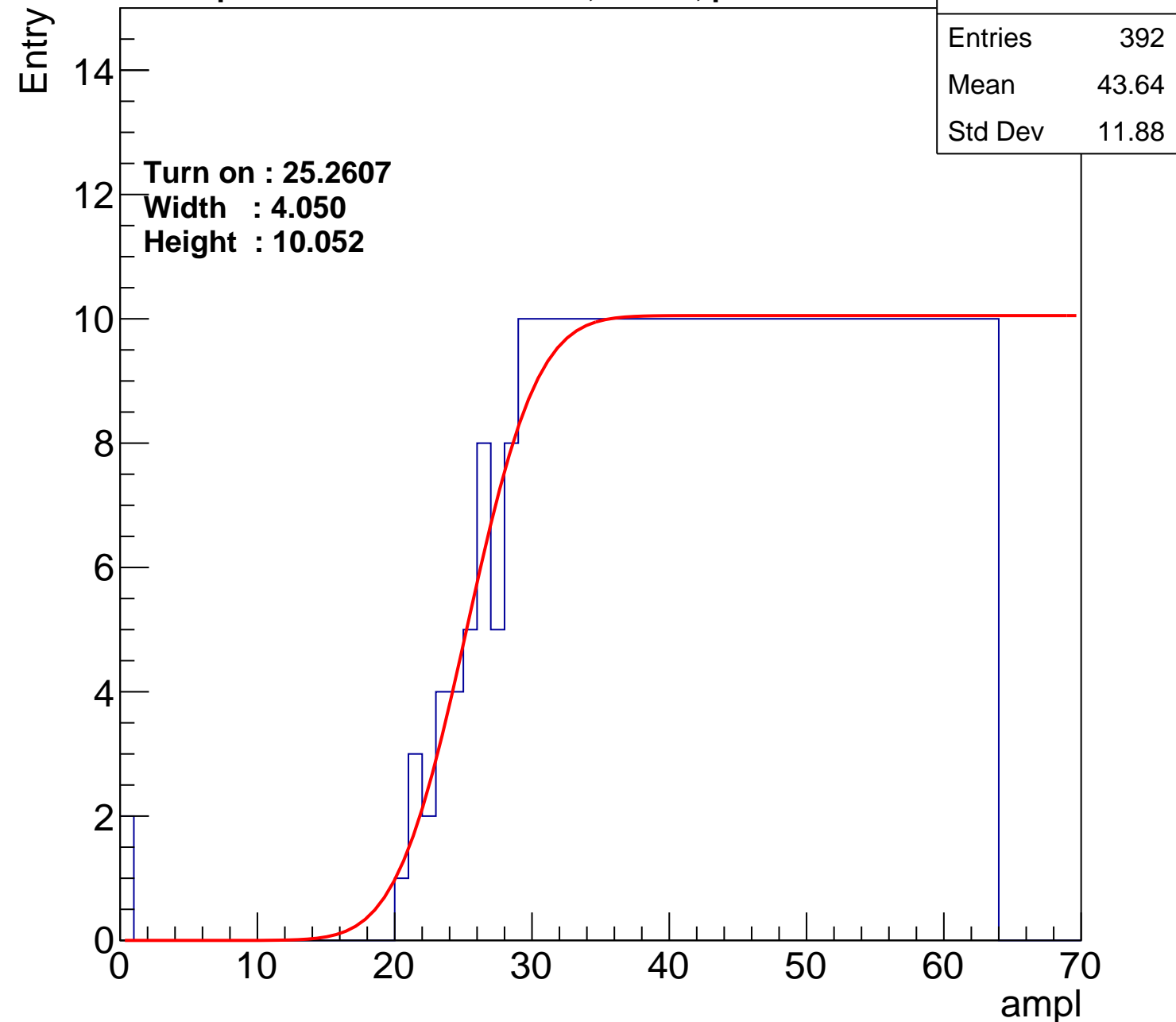
Width : 4.050

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch126

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.11
Std Dev	11.85

Turn on : 27.0298

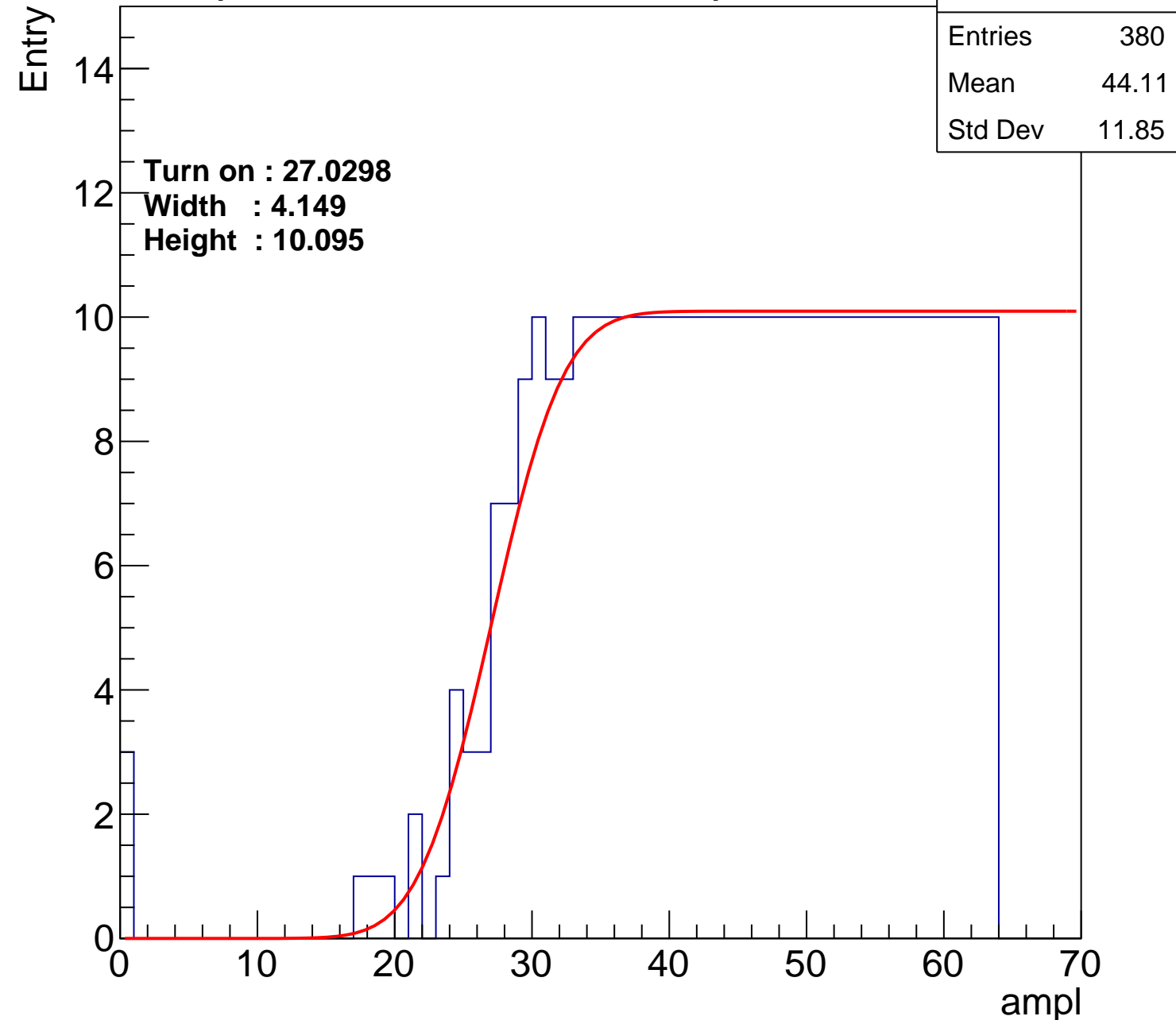
Width : 4.149

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U26-ch127

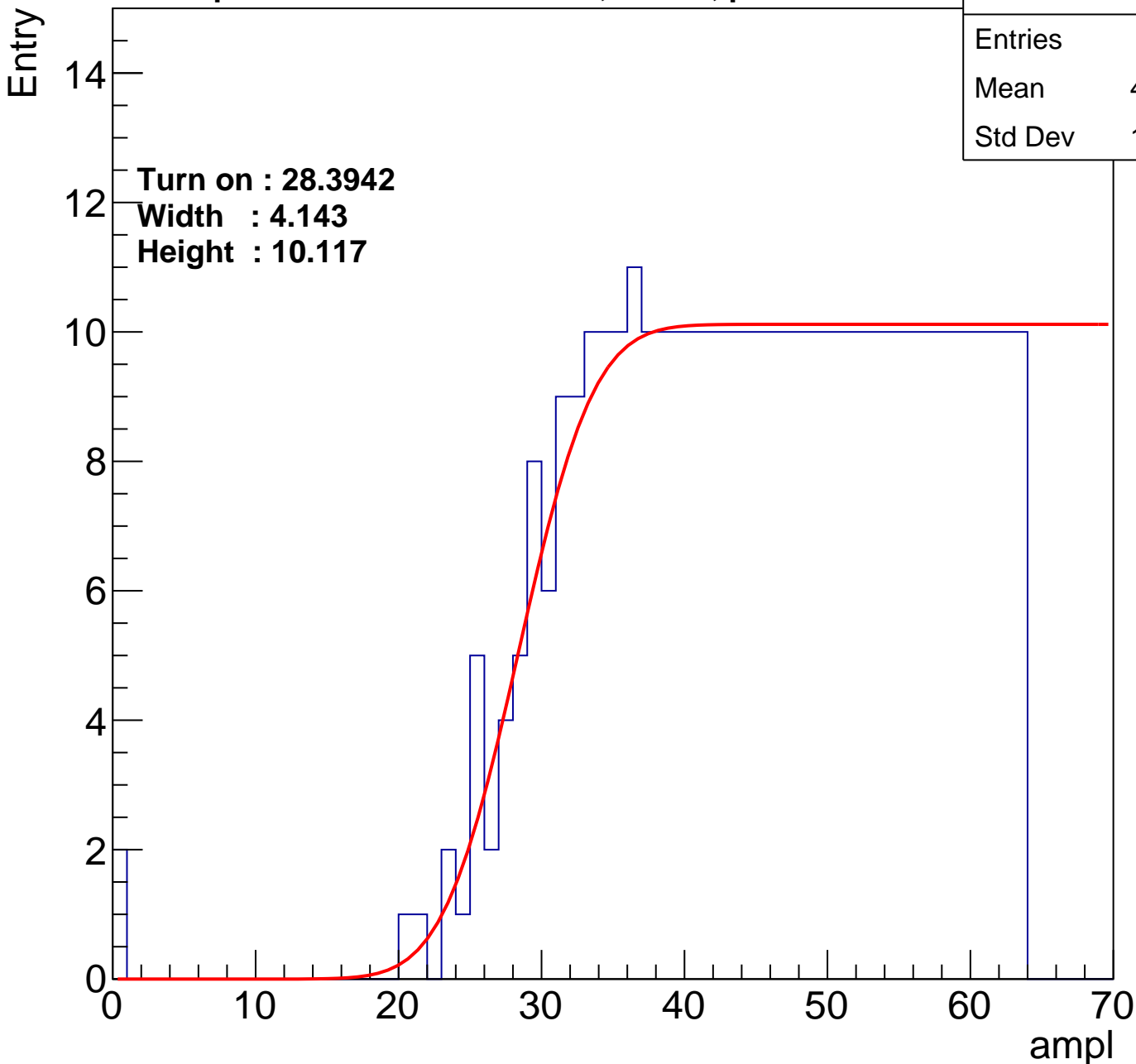
calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.89
Std Dev	11.27

Turn on : 28.3942

Width : 4.143

Height : 10.117



B1L102S, U26-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	366
Mean	44.89
Std Dev	11.27

Turn on : 28.3942

Width : 4.143

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl

