

B1L003S, U8-ch0

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.68 |
| Std Dev | 11.65 |

Turn on : 27.5579

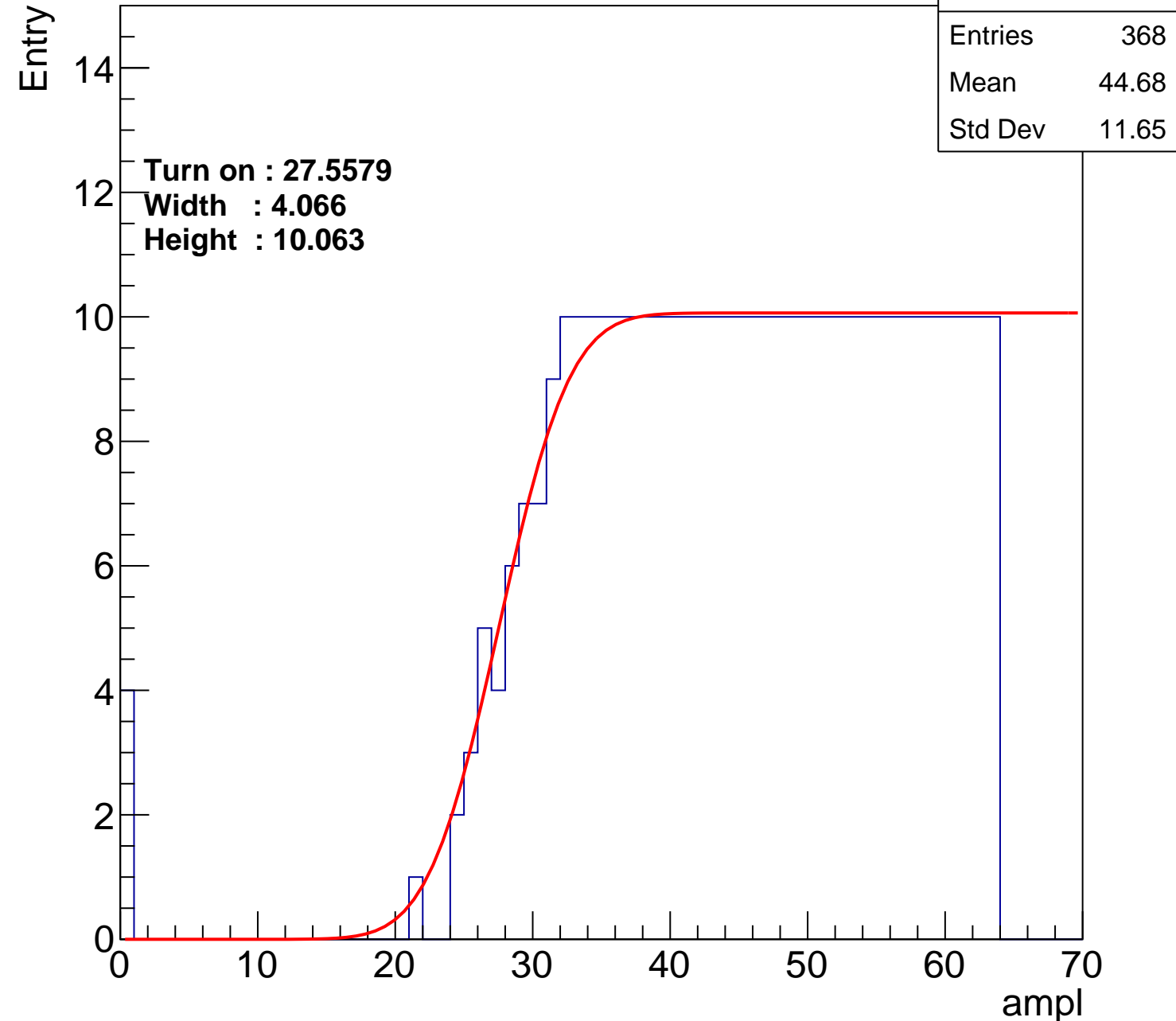
Width : 4.066

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch1

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.37 |
| Std Dev | 11.46 |

Turn on : 26.7806

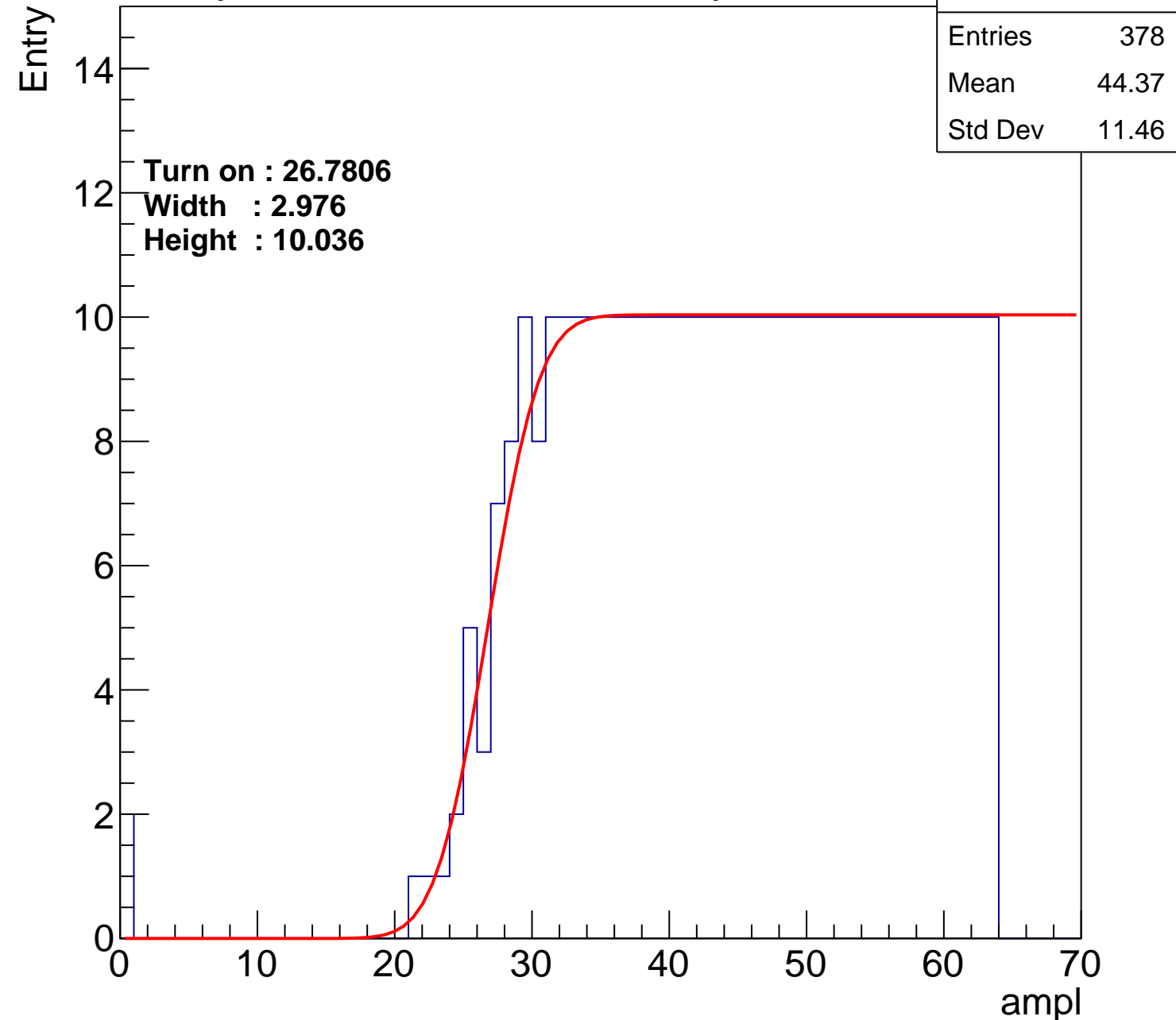
Width : 2.976

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch2

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.21 |
| Std Dev | 11 |

Turn on : 27.6237

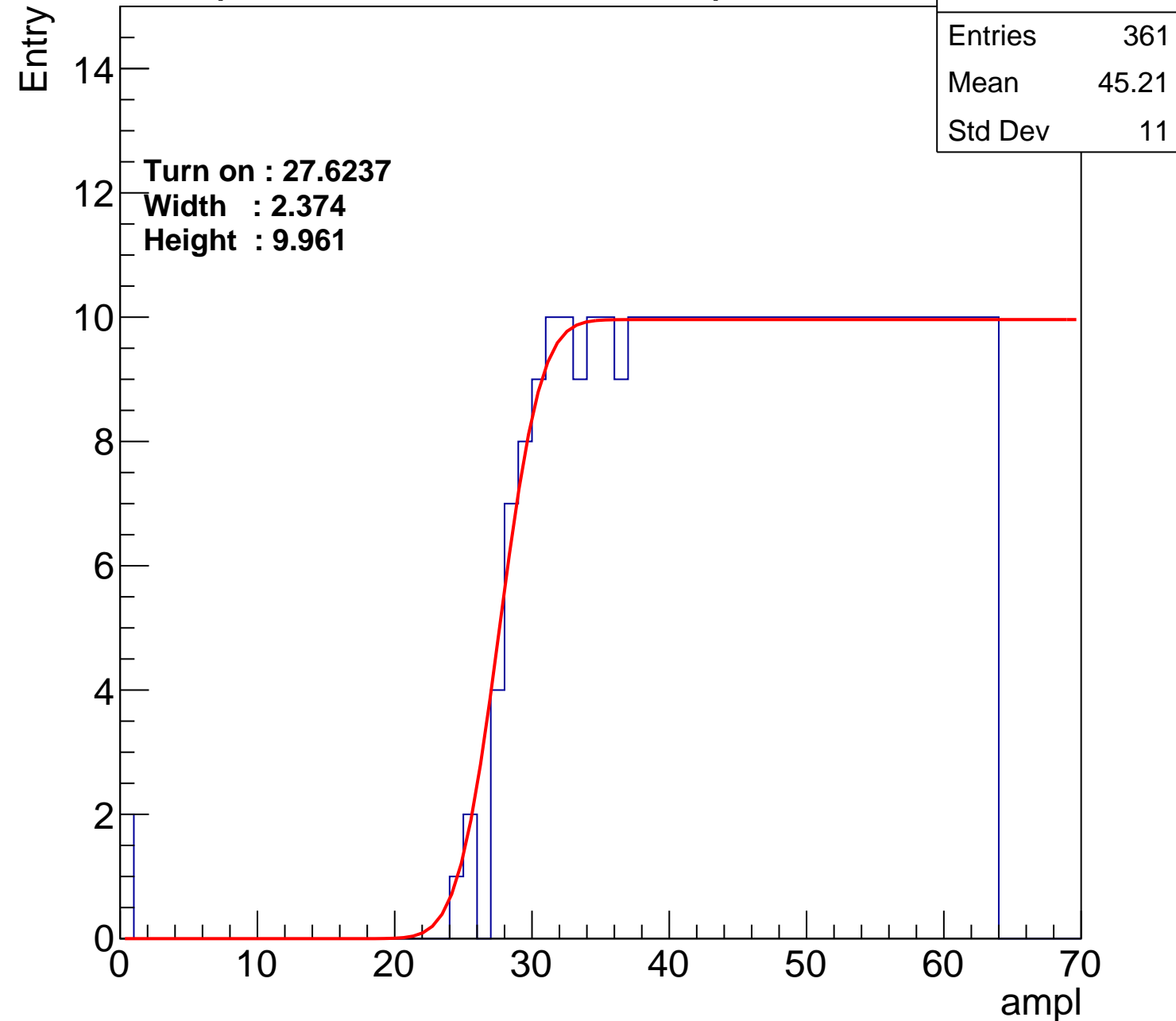
Width : 2.374

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch3

calib_packv5_042523_0143.root, FC#13, port D2

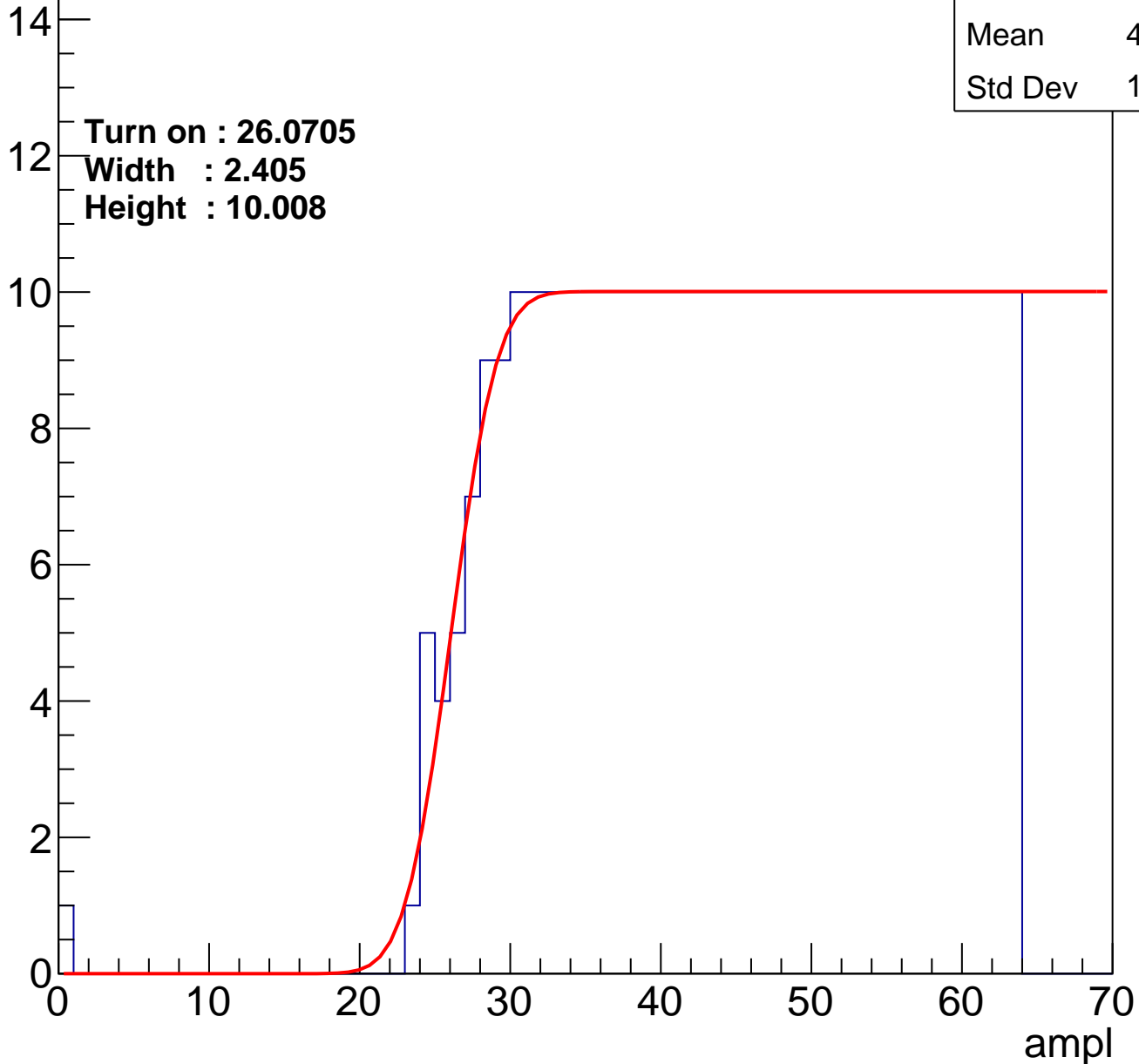
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.32 |
| Std Dev | 11.29 |

Turn on : 26.0705

Width : 2.405

Height : 10.008

Entry



B1L003S, U8-ch4

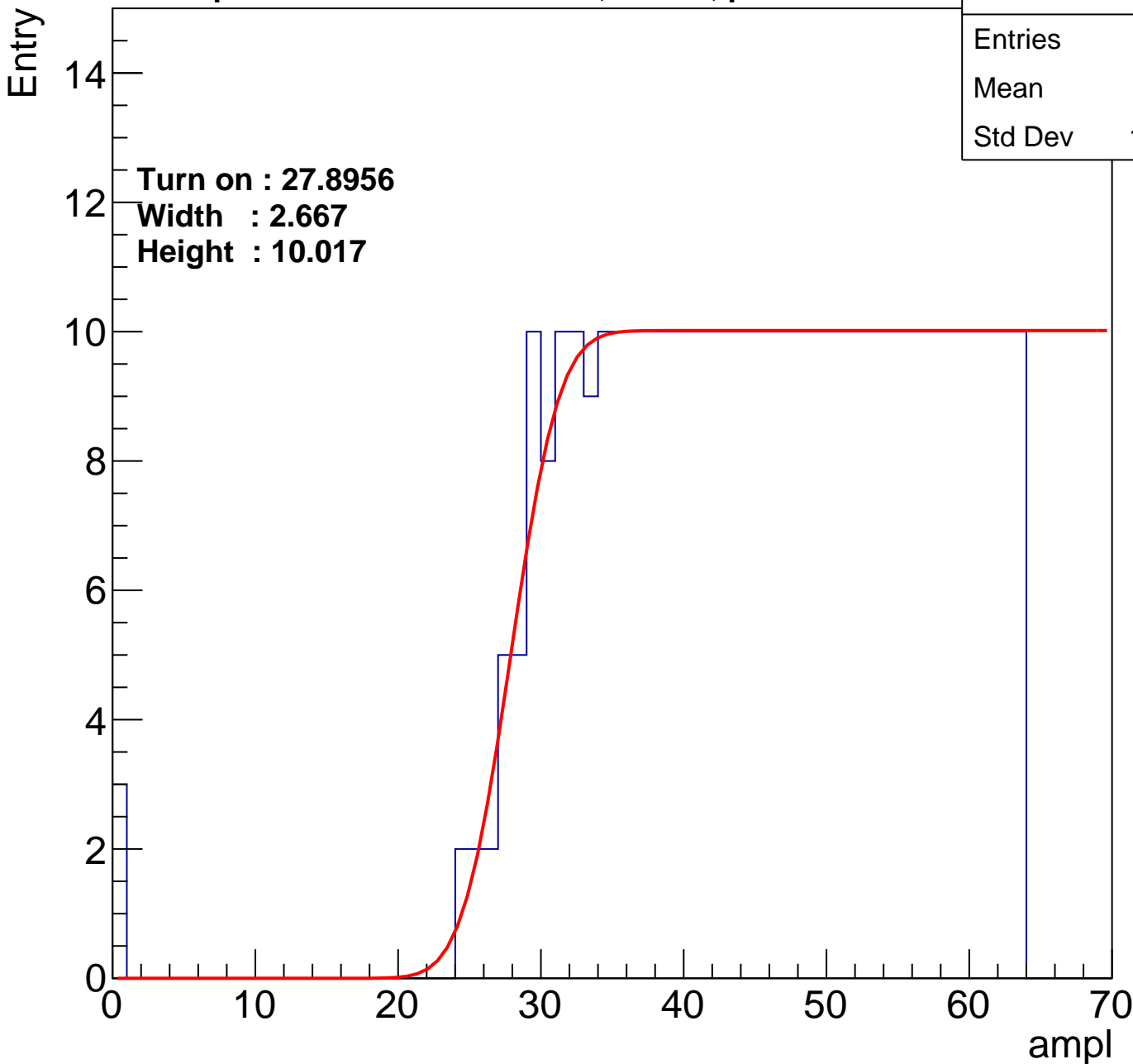
calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.9 |
| Std Dev | 11.33 |

Turn on : 27.8956

Width : 2.667

Height : 10.017



B1L003S, U8-ch5

calib_packv5_042523_0143.root, FC#13, port D2

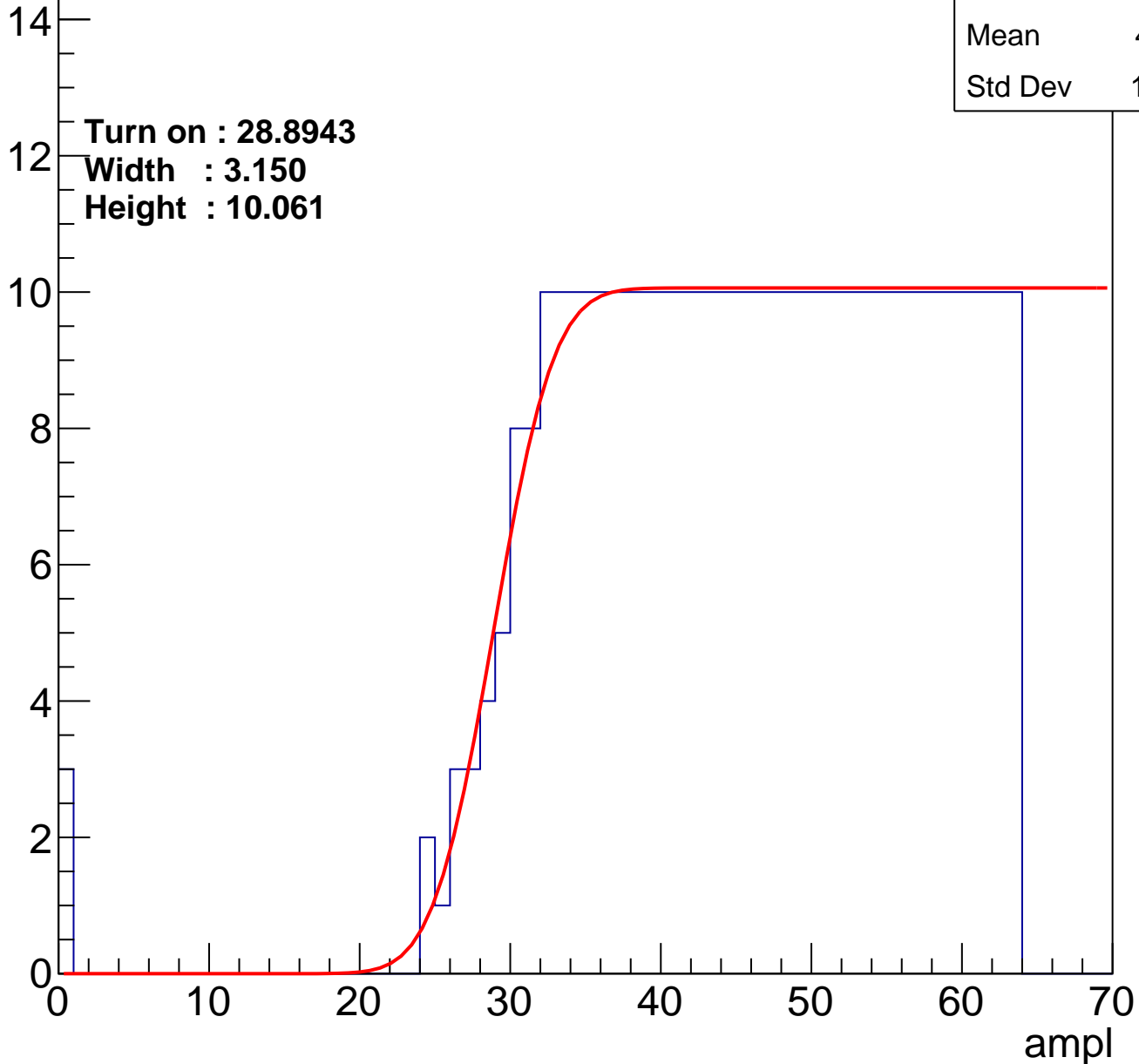
| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.31 |
| Std Dev | 11.16 |

Turn on : 28.8943

Width : 3.150

Height : 10.061

Entry



B1L003S, U8-ch6

calib_packv5_042523_0143.root, FC#13, port D2

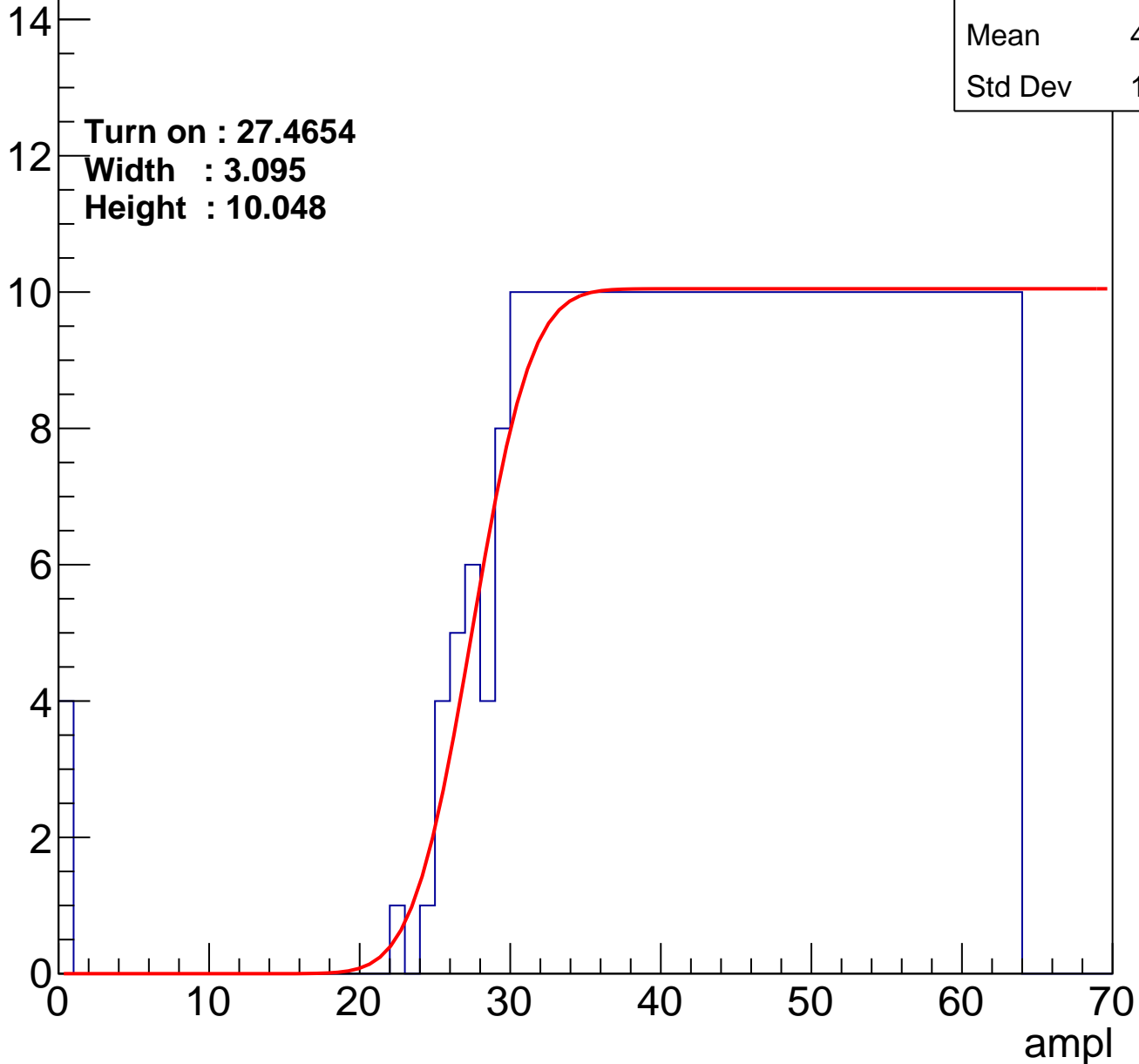
| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.48 |
| Std Dev | 11.69 |

Turn on : 27.4654

Width : 3.095

Height : 10.048

Entry



B1L003S, U8-ch7

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 44.9 |
| Std Dev | 11.51 |

Turn on : 27.6721

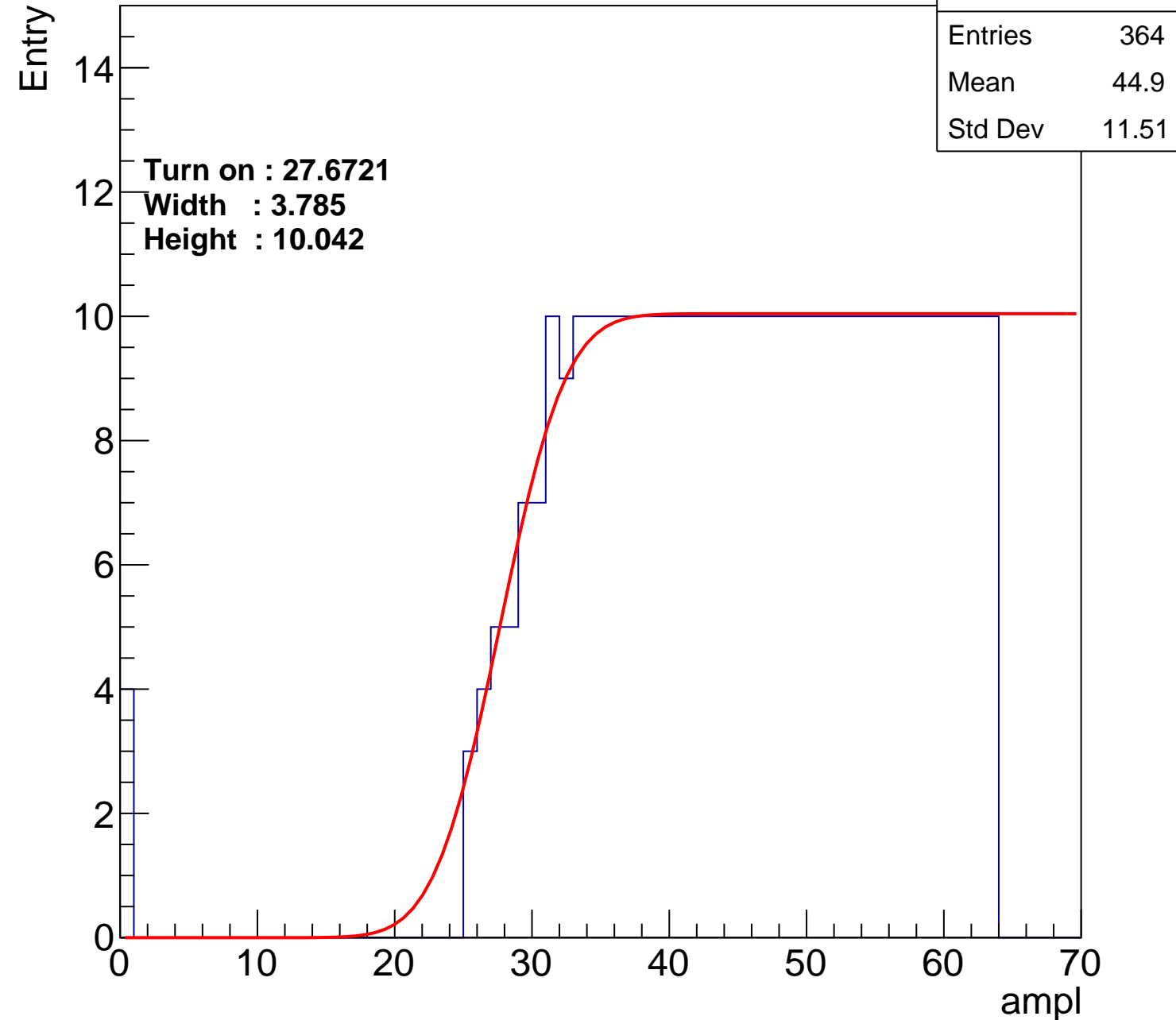
Width : 3.785

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch8

calib_packv5_042523_0143.root, FC#13, port D2

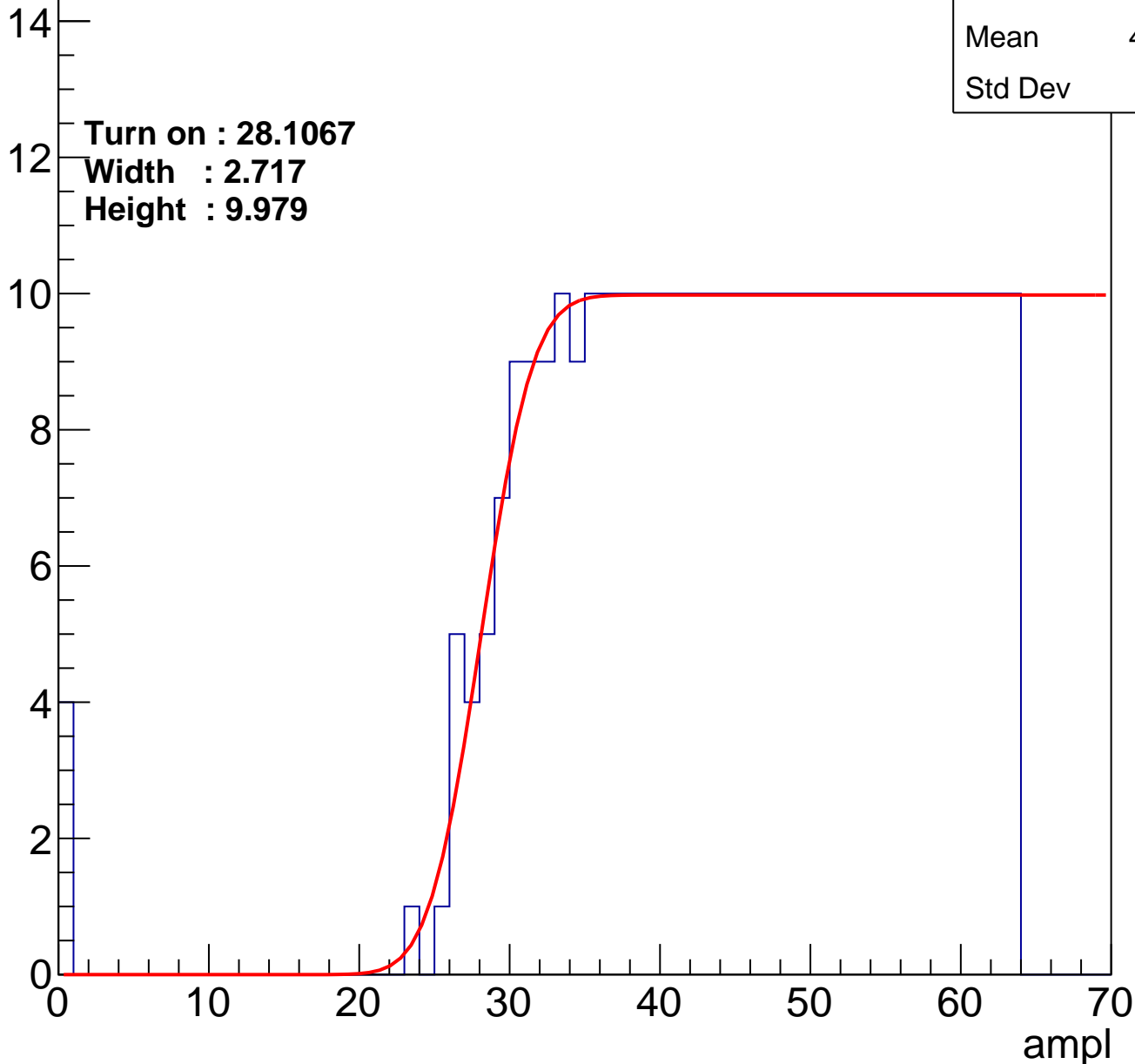
Entry

| | |
|---------|-------|
| Entries | 363 |
| Mean | 44.94 |
| Std Dev | 11.51 |

Turn on : 28.1067

Width : 2.717

Height : 9.979



B1L003S, U8-ch9

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.79 |
| Std Dev | 11.25 |

Turn on : 27.2412

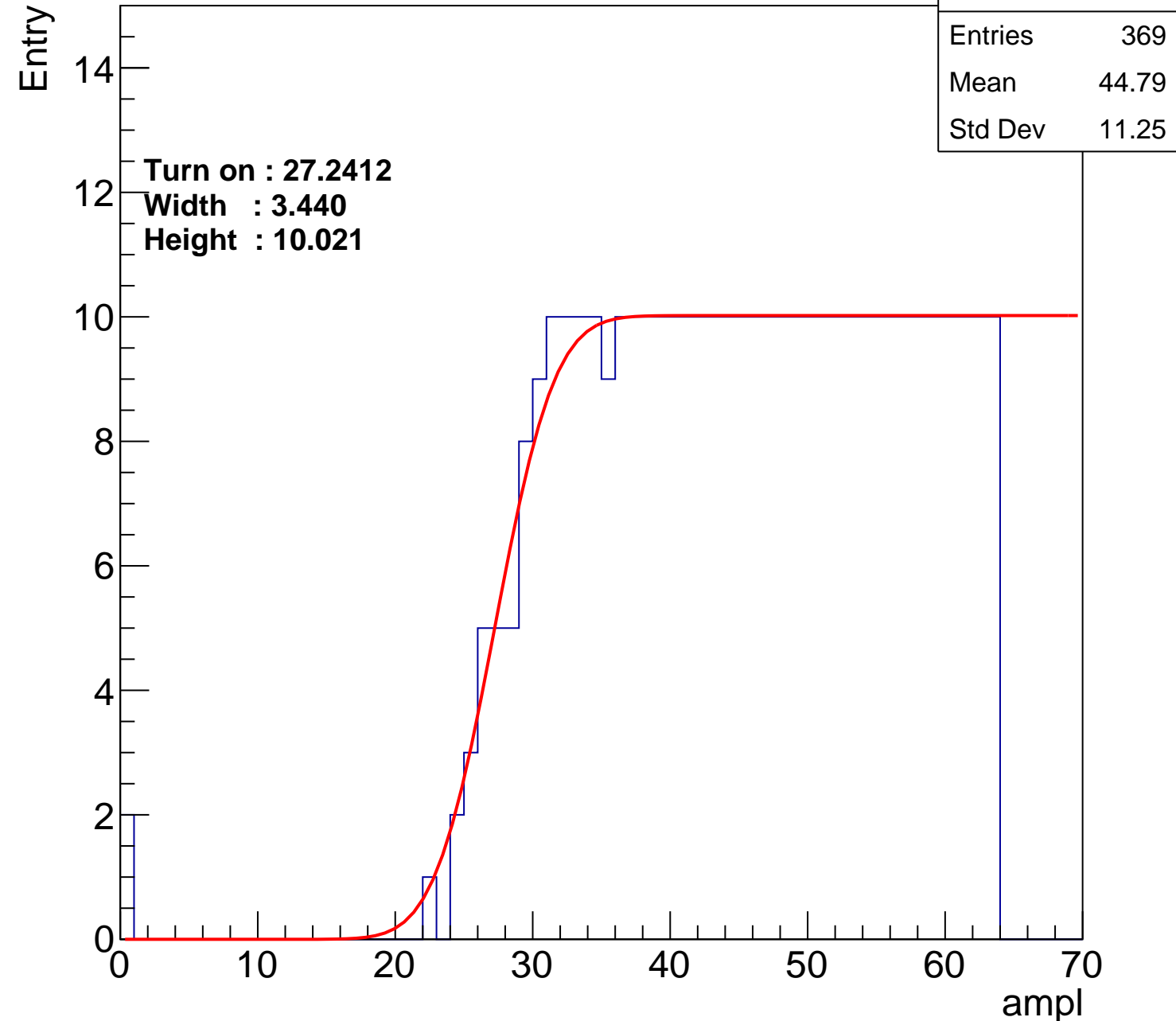
Width : 3.440

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch10

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 386 |
| Mean | 44.07 |
| Std Dev | 11.43 |

Turn on : 25.6755

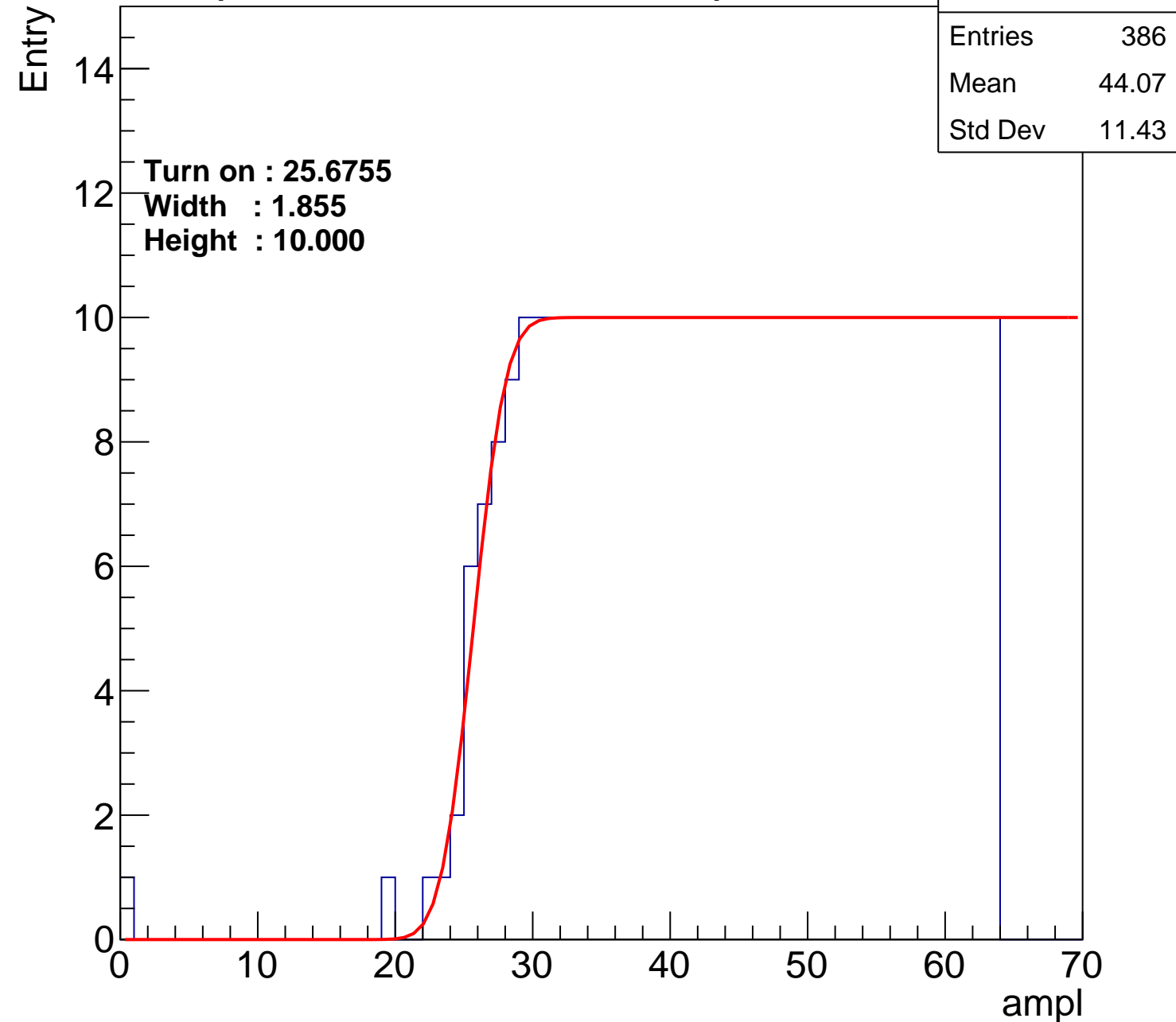
Width : 1.855

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch11

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 338 |
| Mean | 46.43 |
| Std Dev | 10.17 |

Turn on : 30.6689

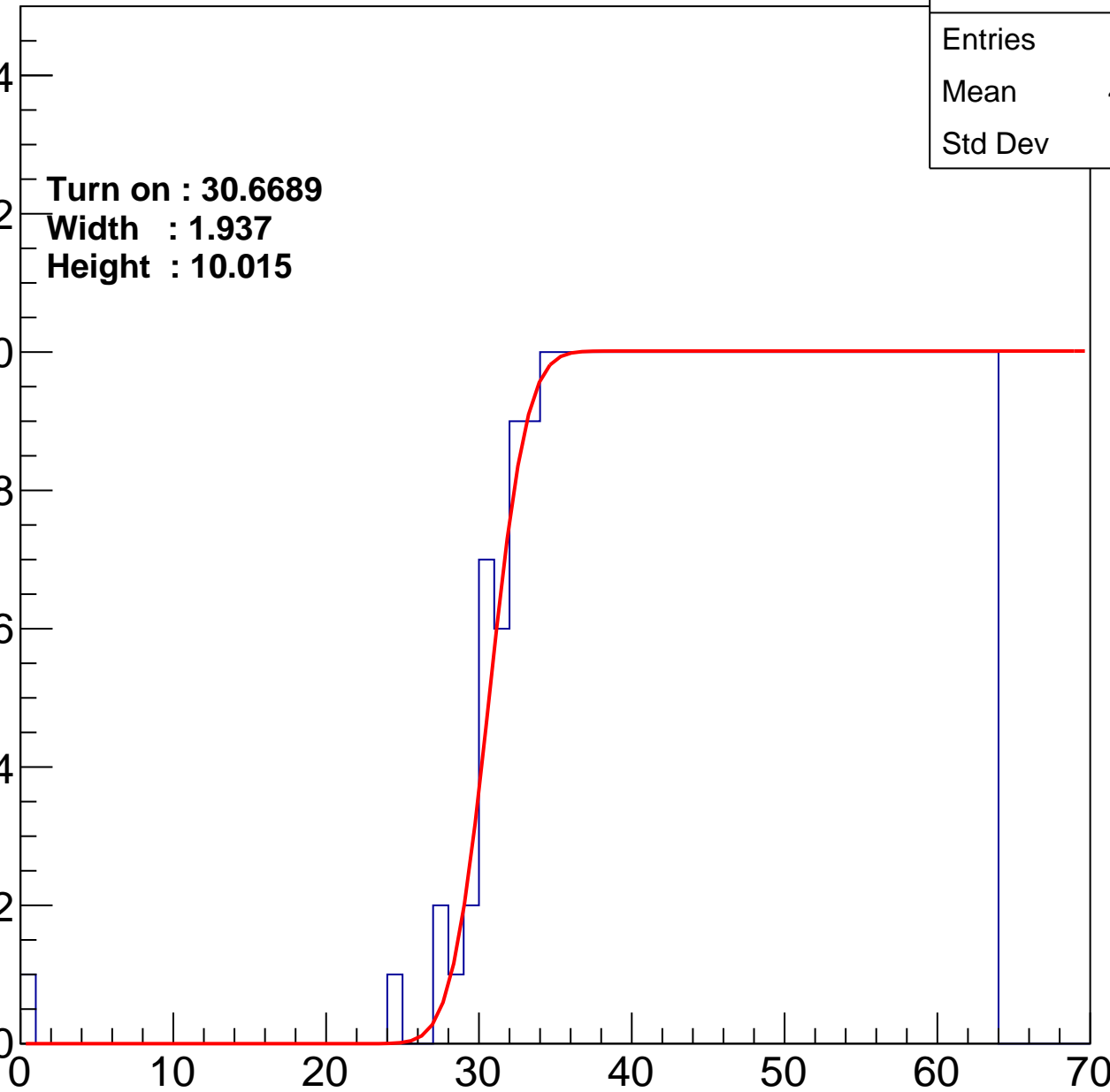
Width : 1.937

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch12

calib_packv5_042523_0143.root, FC#13, port D2

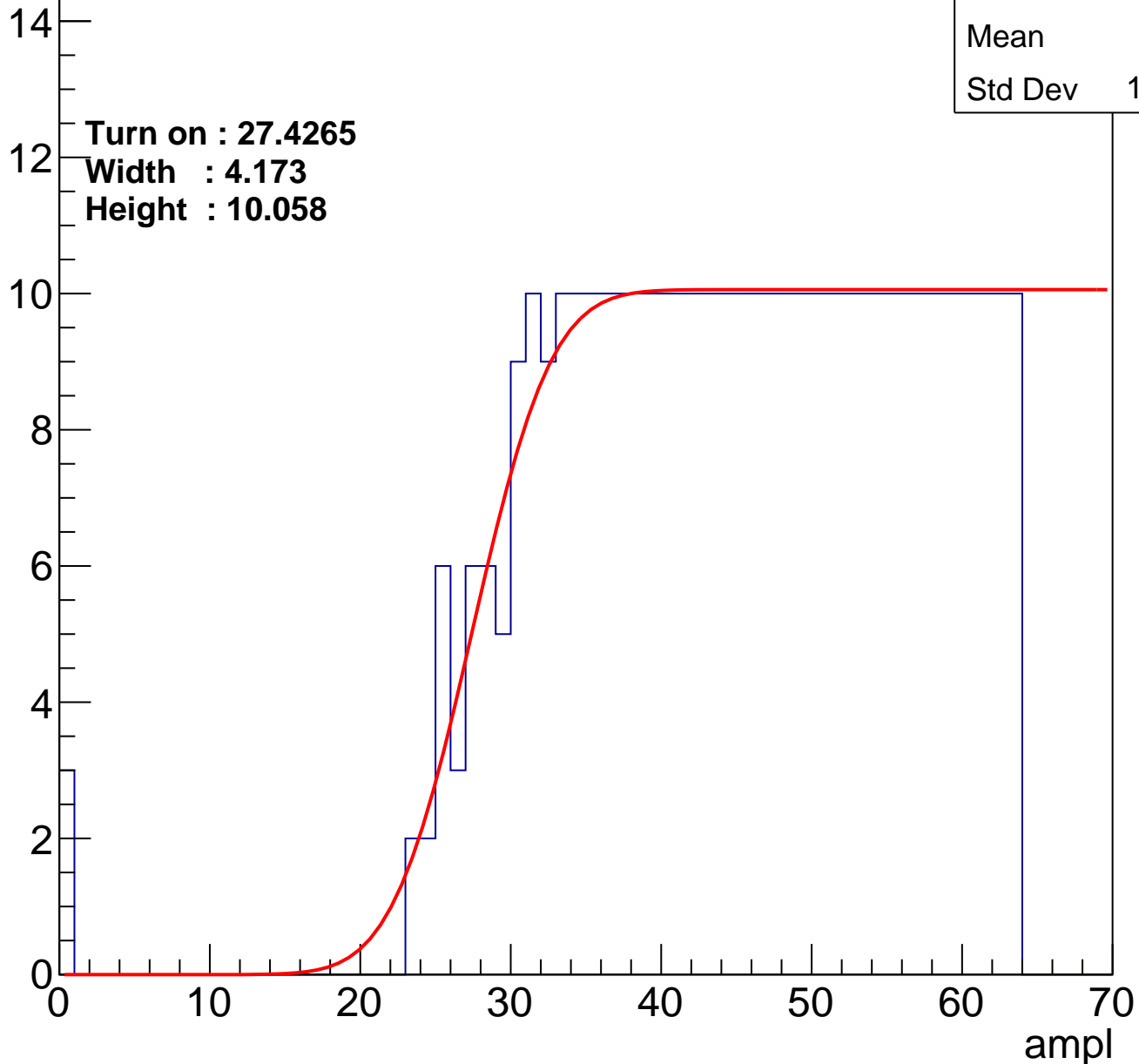
| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.6 |
| Std Dev | 11.54 |

Turn on : 27.4265

Width : 4.173

Height : 10.058

Entry



B1L003S, U8-ch13

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 366 |
| Mean | 45.02 |
| Std Dev | 10.95 |

Turn on : 28.0665

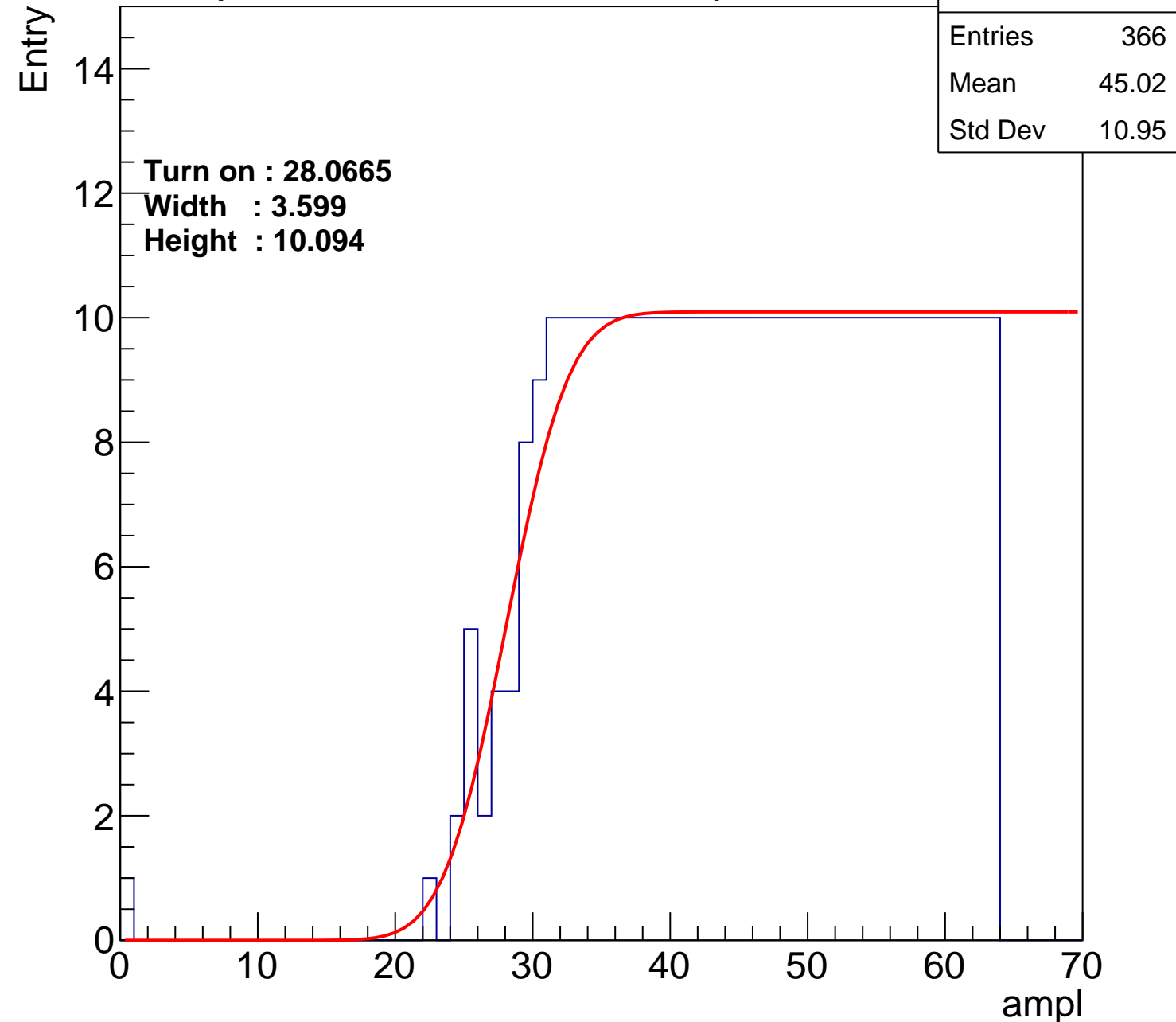
Width : 3.599

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch14

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.33 |
| Std Dev | 11.46 |

Turn on : 26.4564

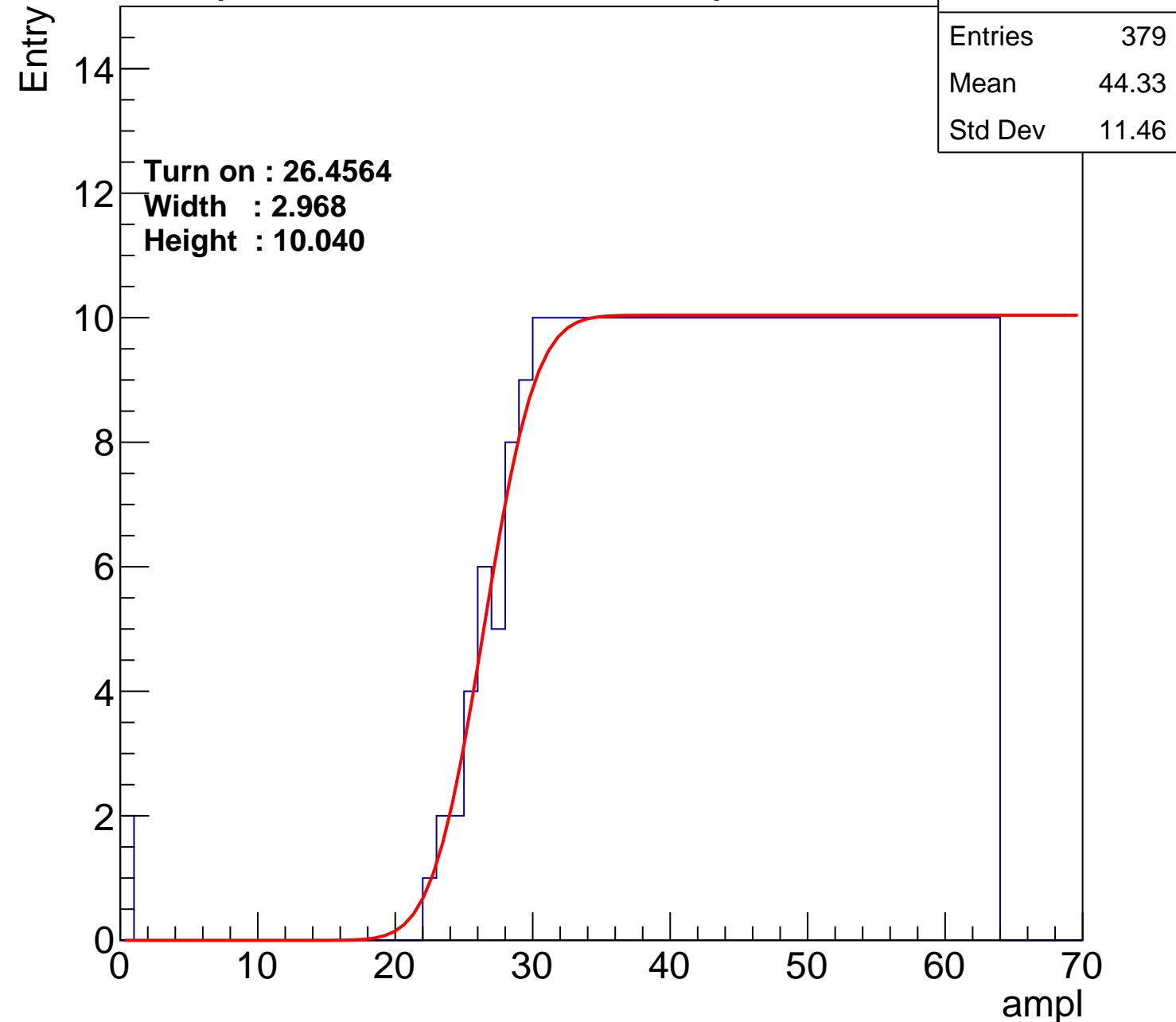
Width : 2.968

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch15

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.51 |
| Std Dev | 10.71 |

Turn on : 28.9430

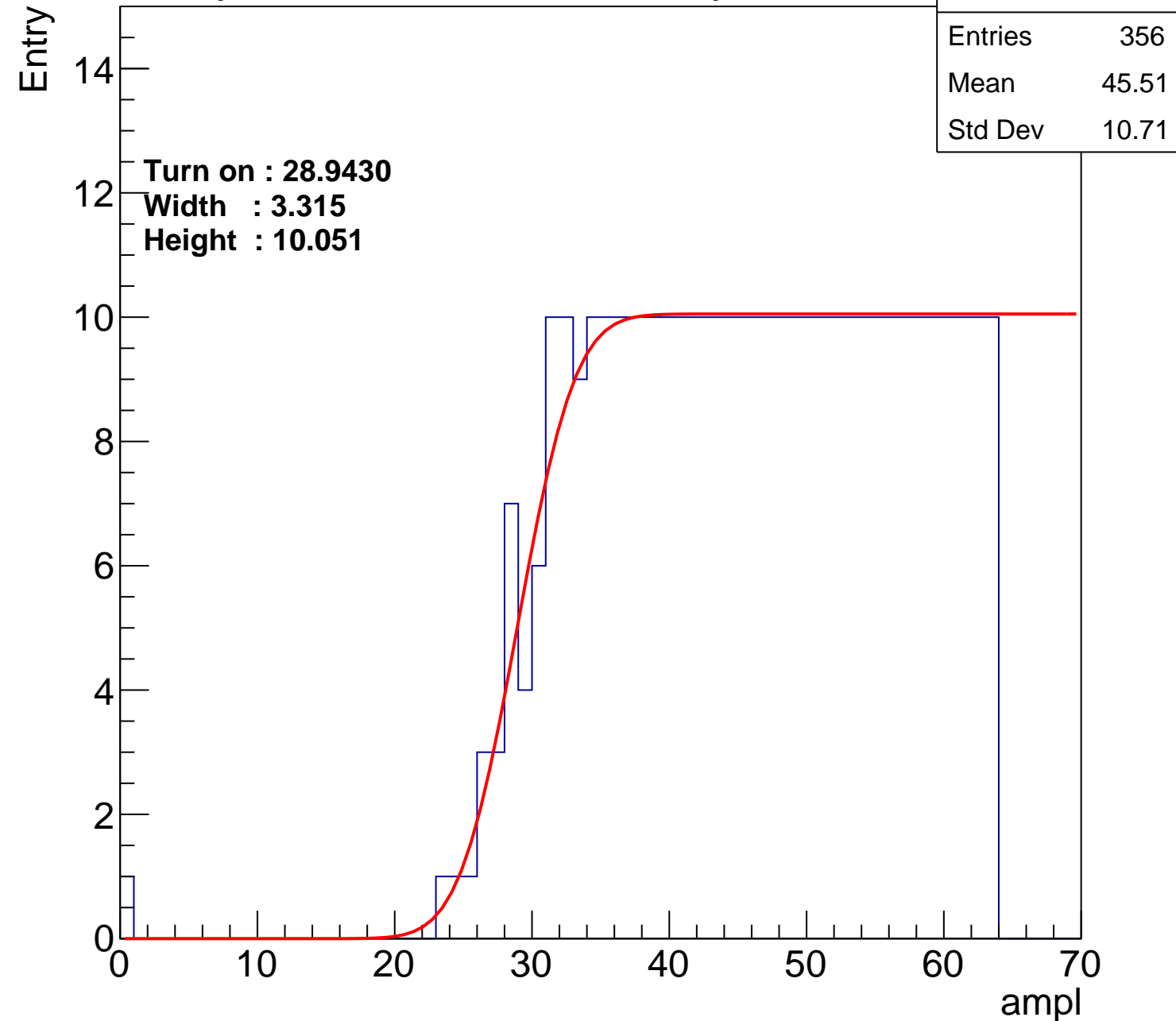
Width : 3.315

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch16

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.27 |
| Std Dev | 11.17 |

Turn on : 29.1422

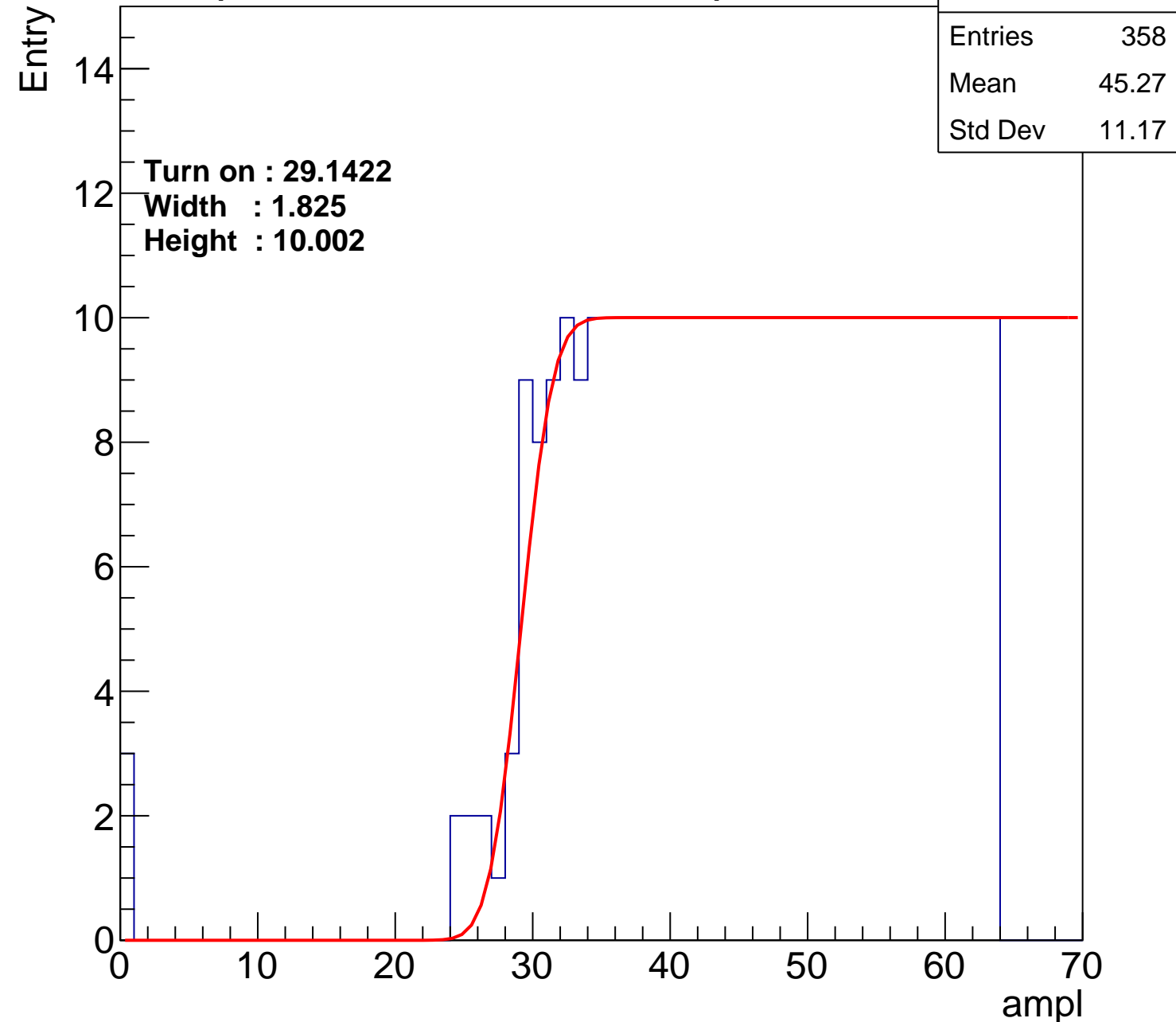
Width : 1.825

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch17

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.39 |
| Std Dev | 11.08 |

Turn on : 28.8143

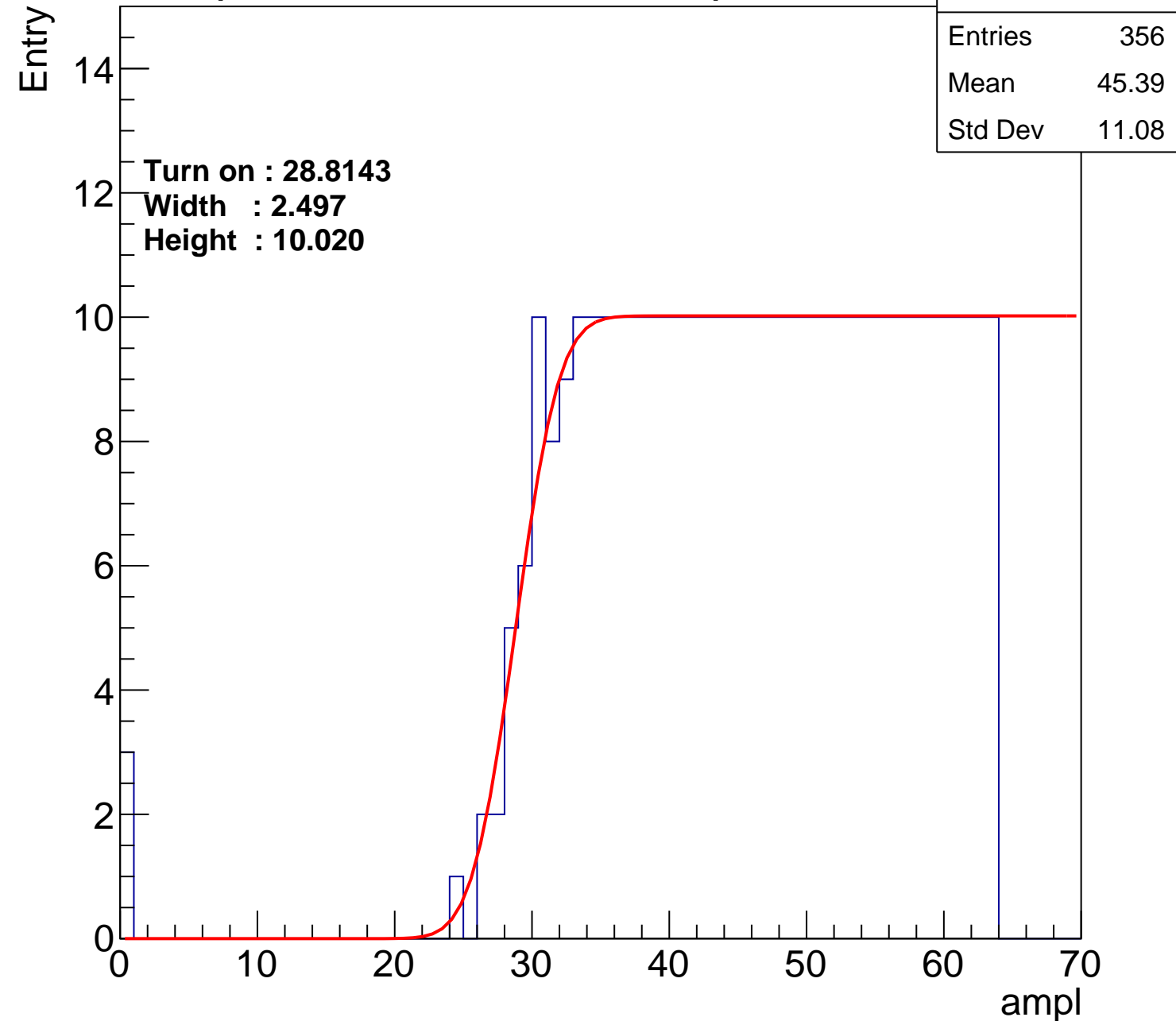
Width : 2.497

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch18

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.69 |
| Std Dev | 11.4 |

Turn on : 27.4094

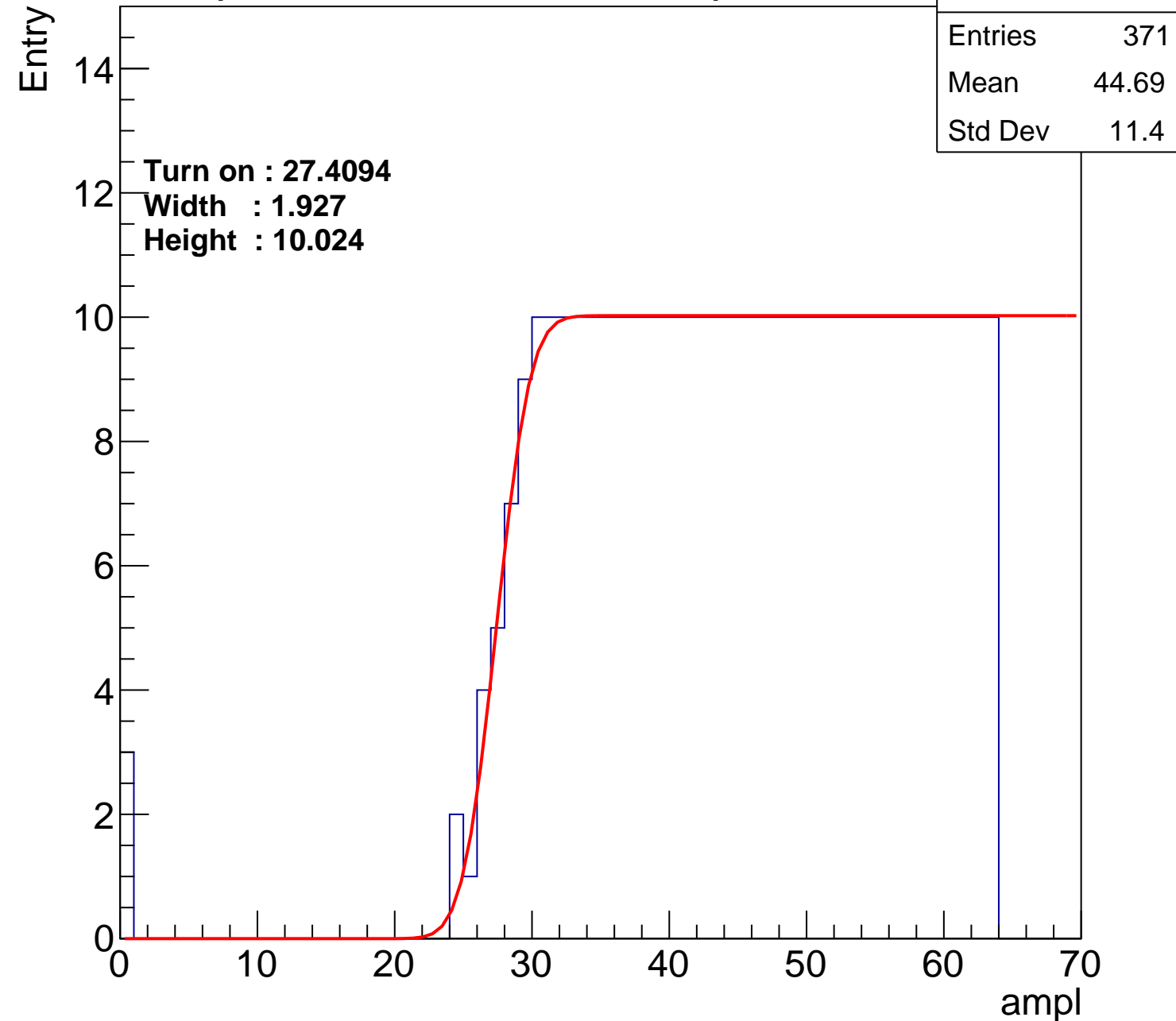
Width : 1.927

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch19

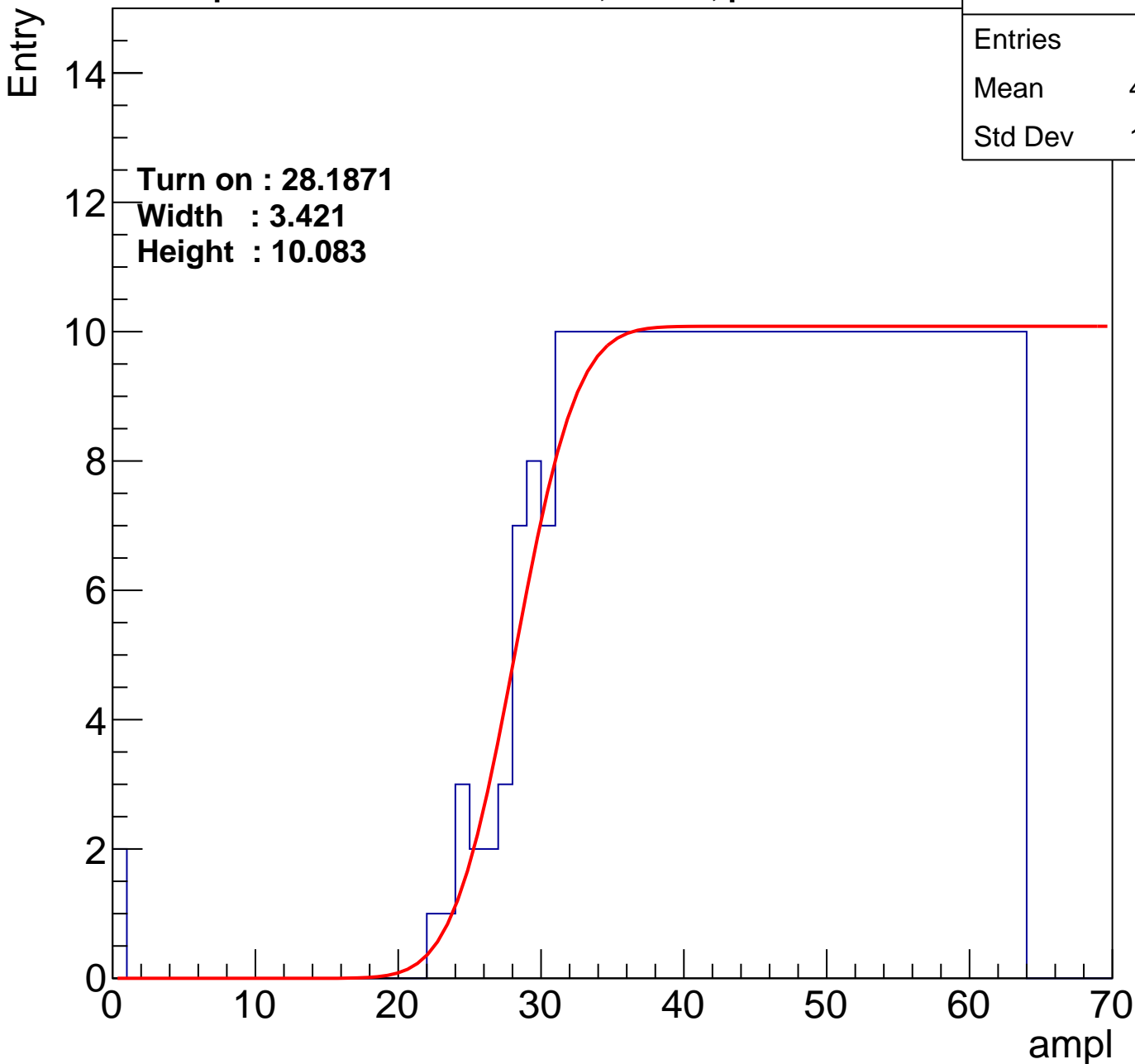
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 28.1871

Width : 3.421

Height : 10.083

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.94 |
| Std Dev | 11.18 |



B1L003S, U8-ch20

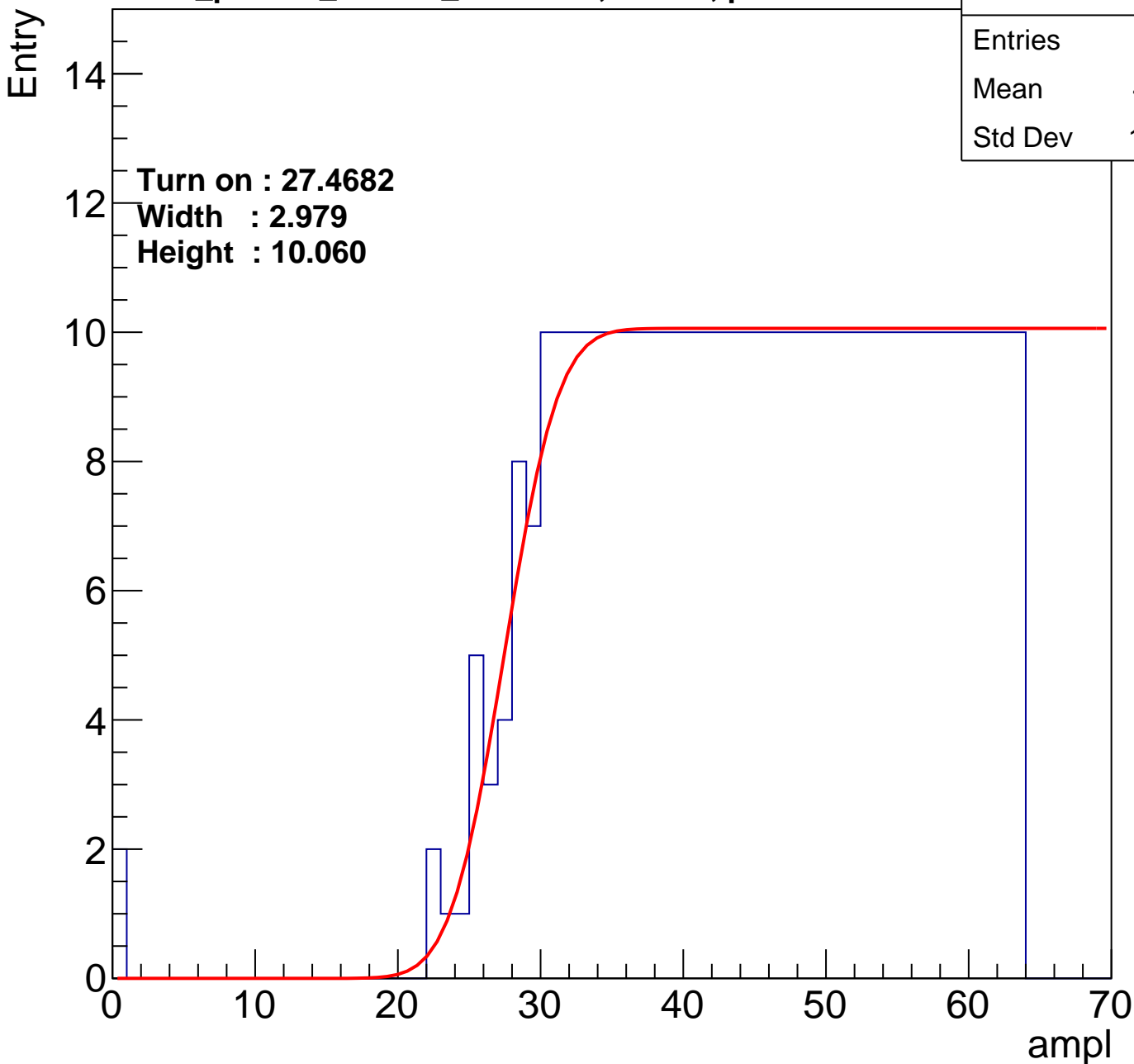
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 27.4682

Width : 2.979

Height : 10.060

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.61 |
| Std Dev | 11.34 |



B1L003S, U8-ch21

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.28 |
| Std Dev | 10.81 |

Turn on : 28.5932

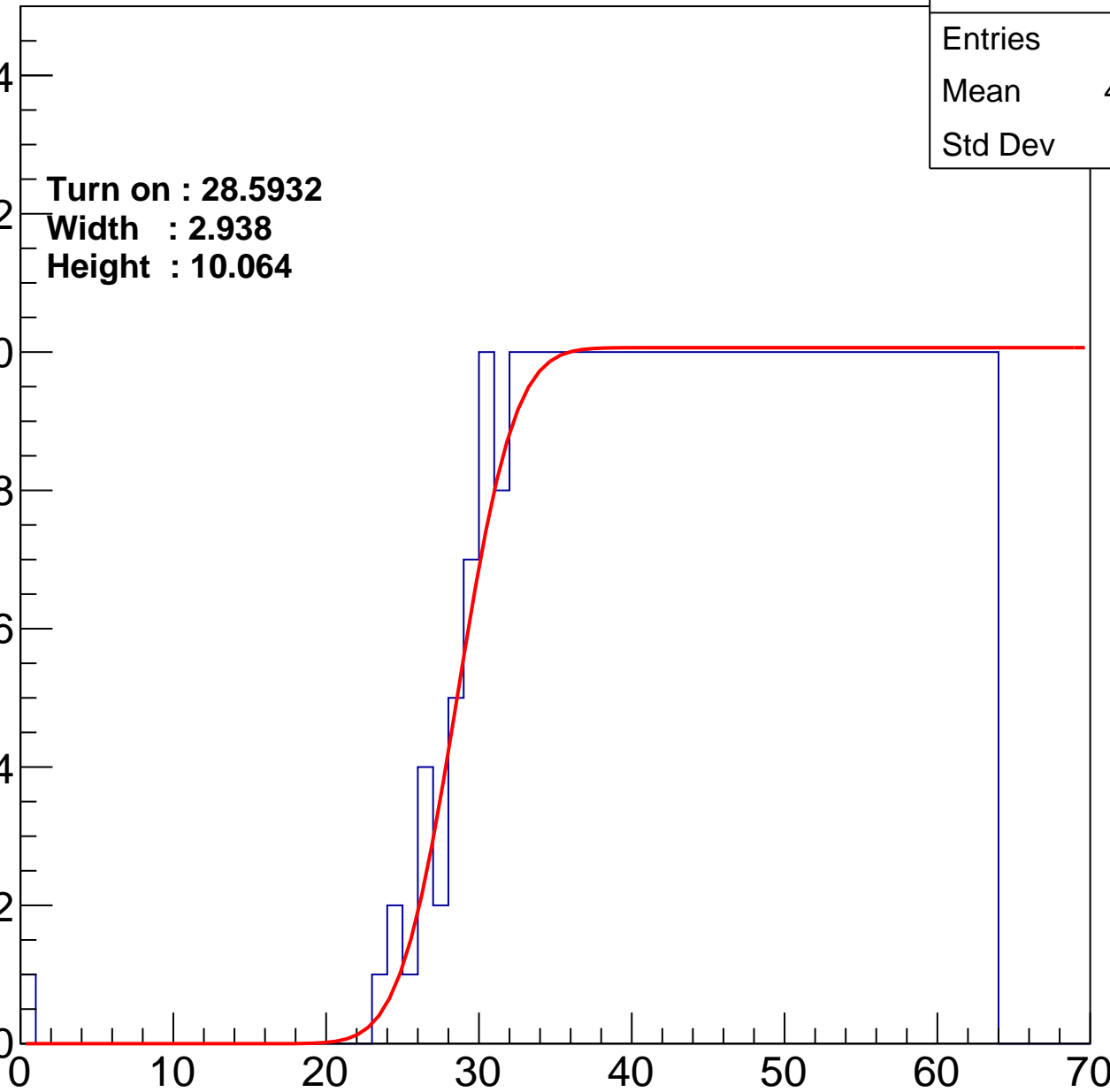
Width : 2.938

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch22

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.29 |
| Std Dev | 10.78 |

Turn on : 28.7338

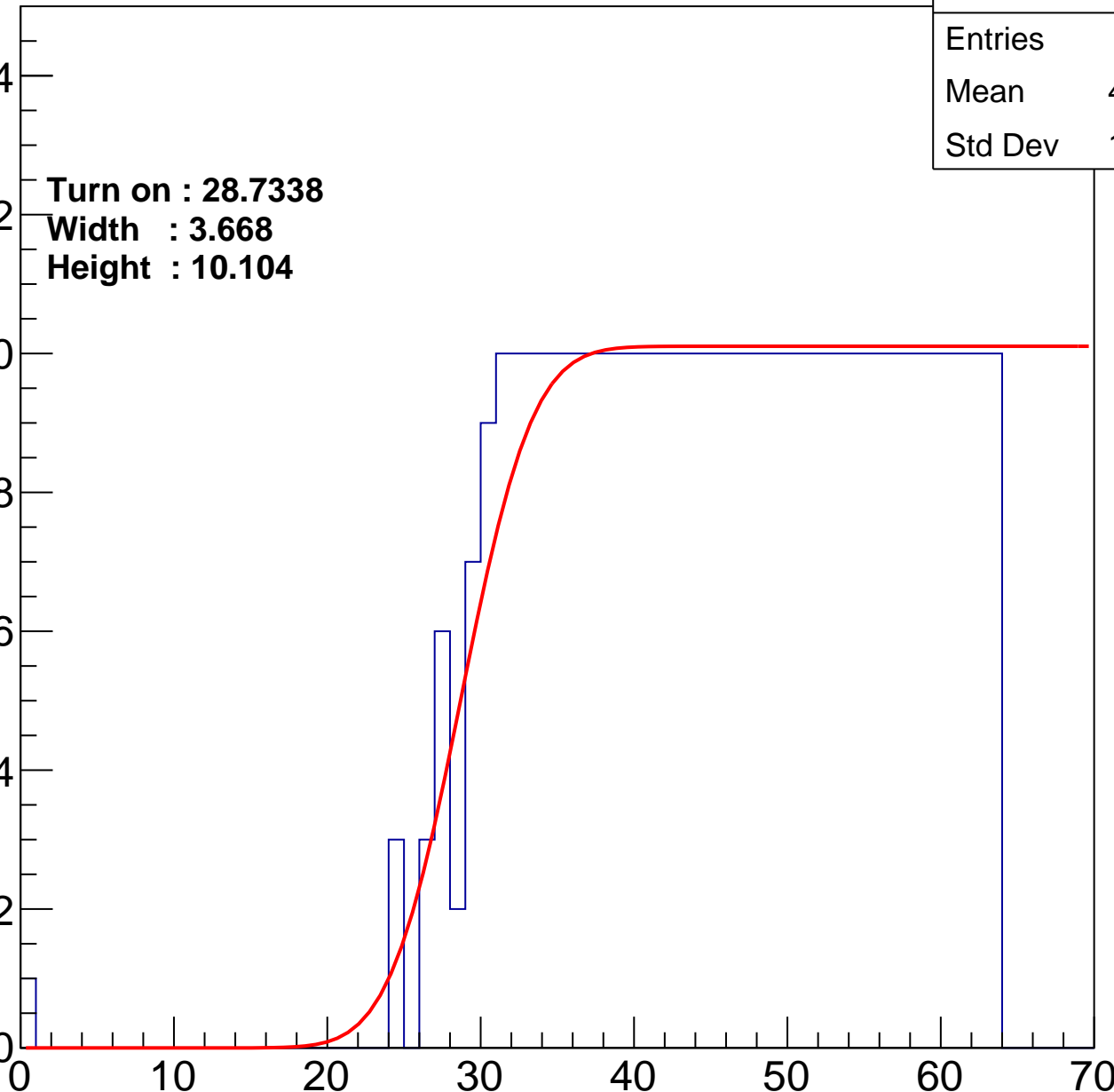
Width : 3.668

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch23

calib_packv5_042523_0143.root, FC#13, port D2

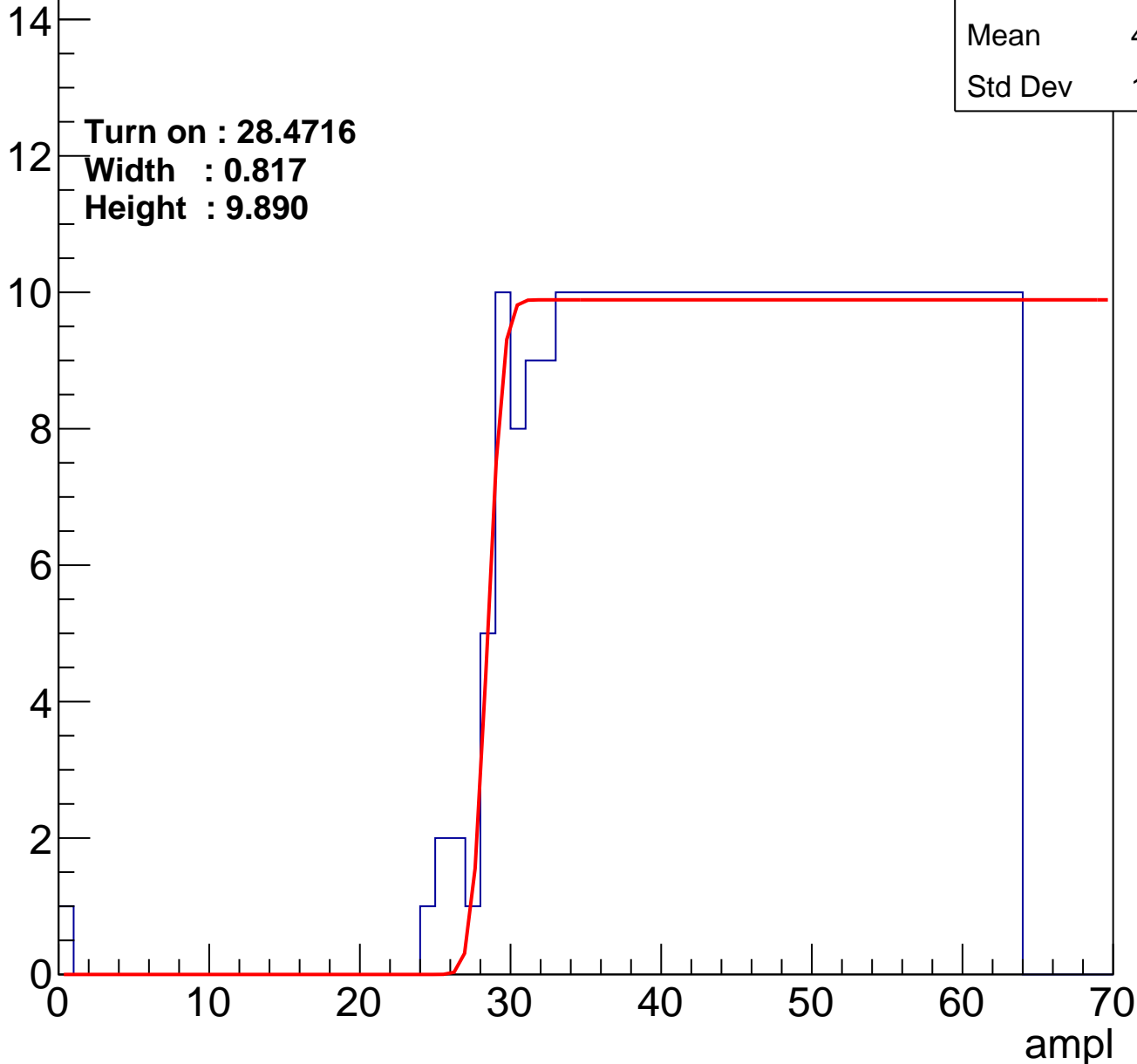
Entry

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.45 |
| Std Dev | 10.69 |

Turn on : 28.4716

Width : 0.817

Height : 9.890



B1L003S, U8-ch24

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.28 |
| Std Dev | 11.8 |

Turn on : 26.9492

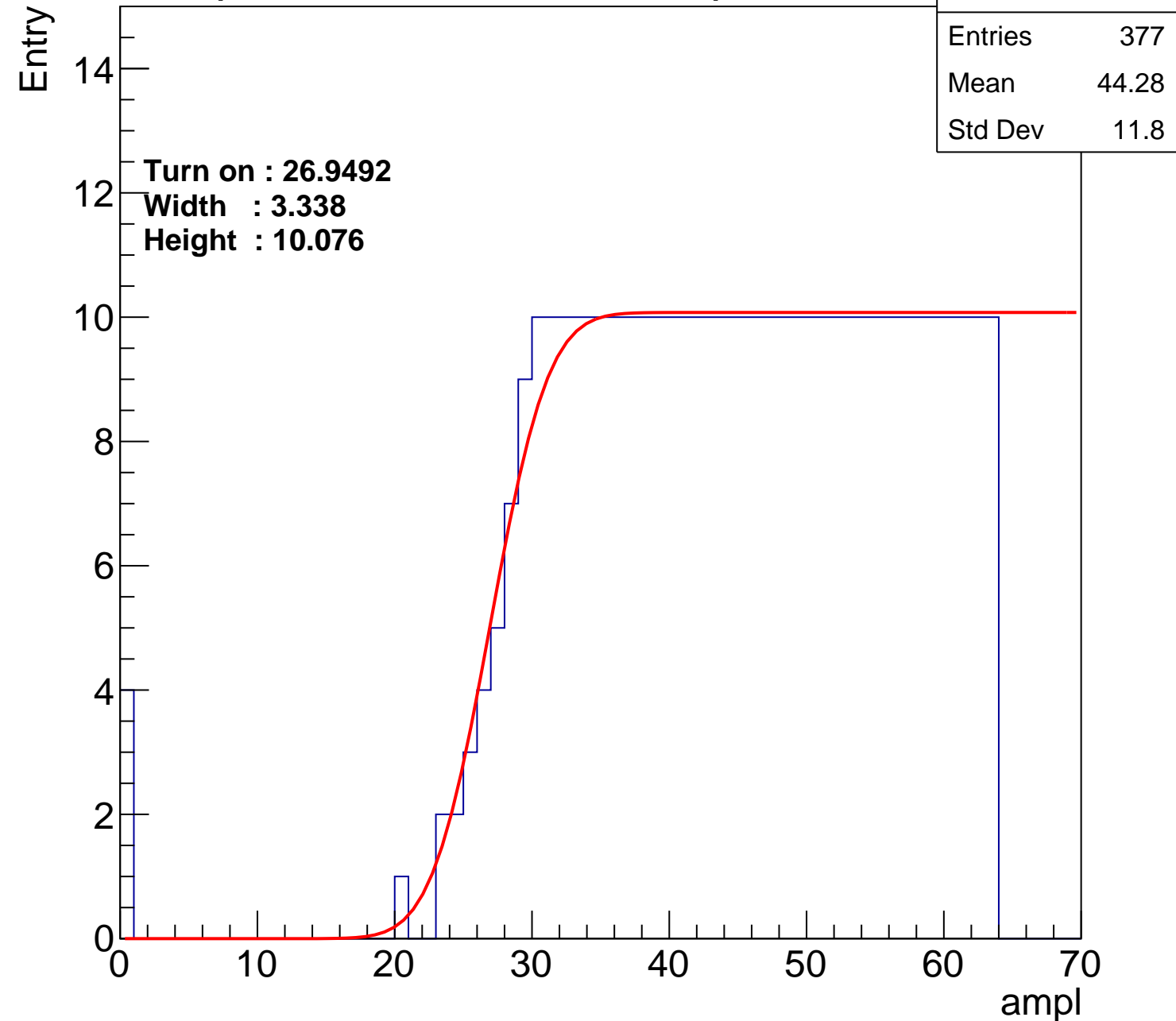
Width : 3.338

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch25

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 351 |
| Mean | 45.44 |
| Std Dev | 11.47 |

Turn on : 29.3852

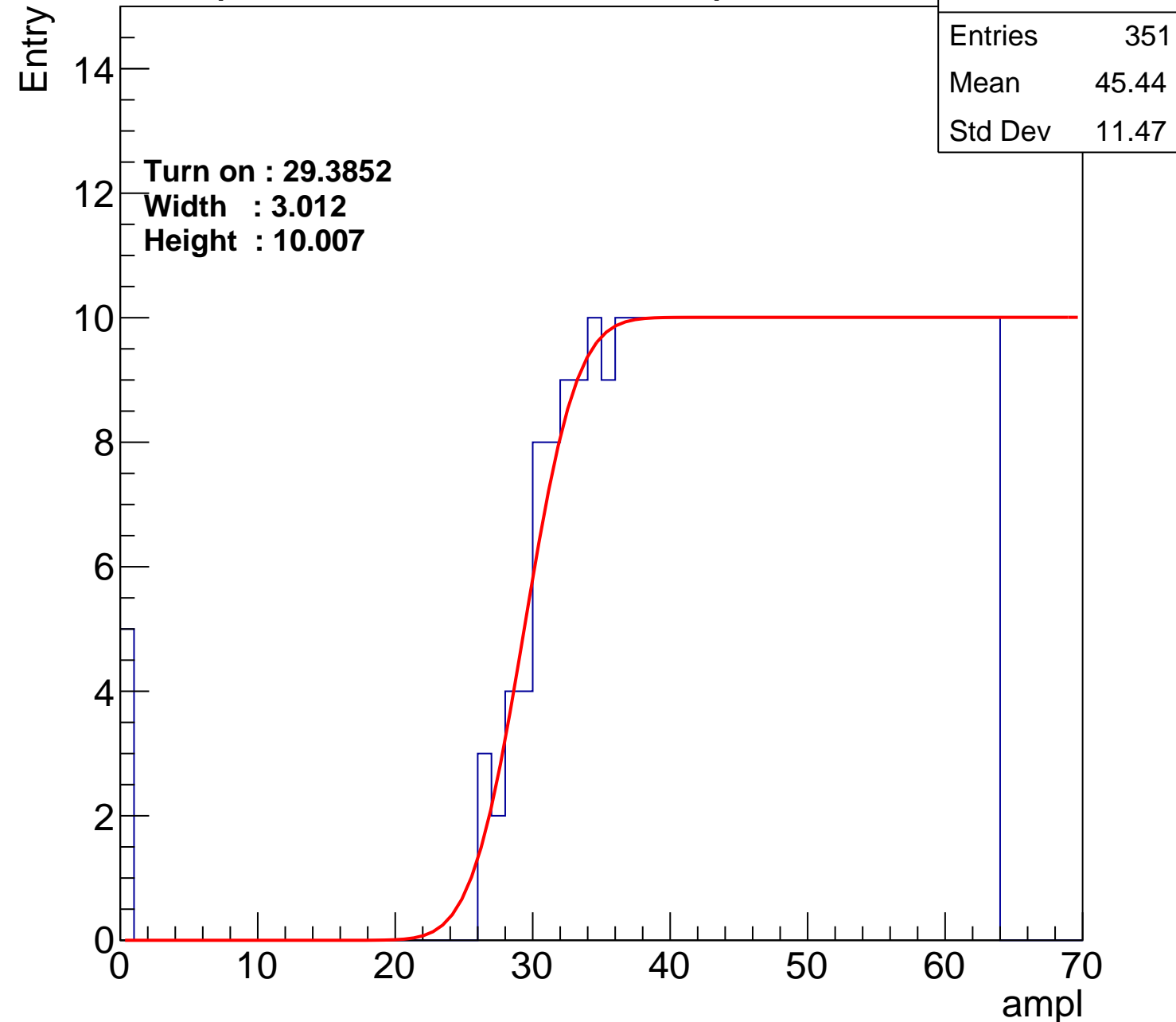
Width : 3.012

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch26

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.37 |
| Std Dev | 10.77 |

Turn on : 29.1503

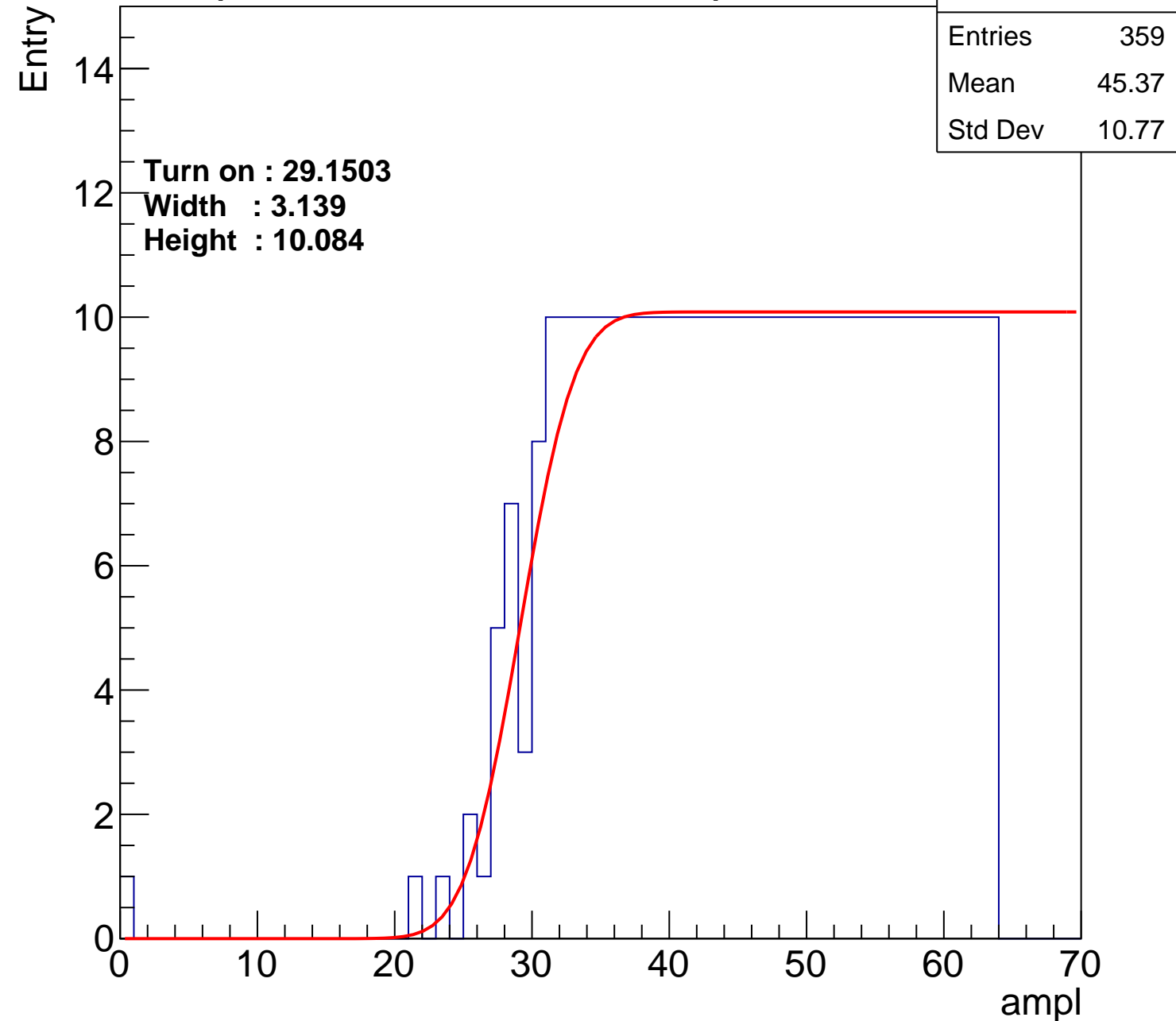
Width : 3.139

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch27

calib_packv5_042523_0143.root, FC#13, port D2

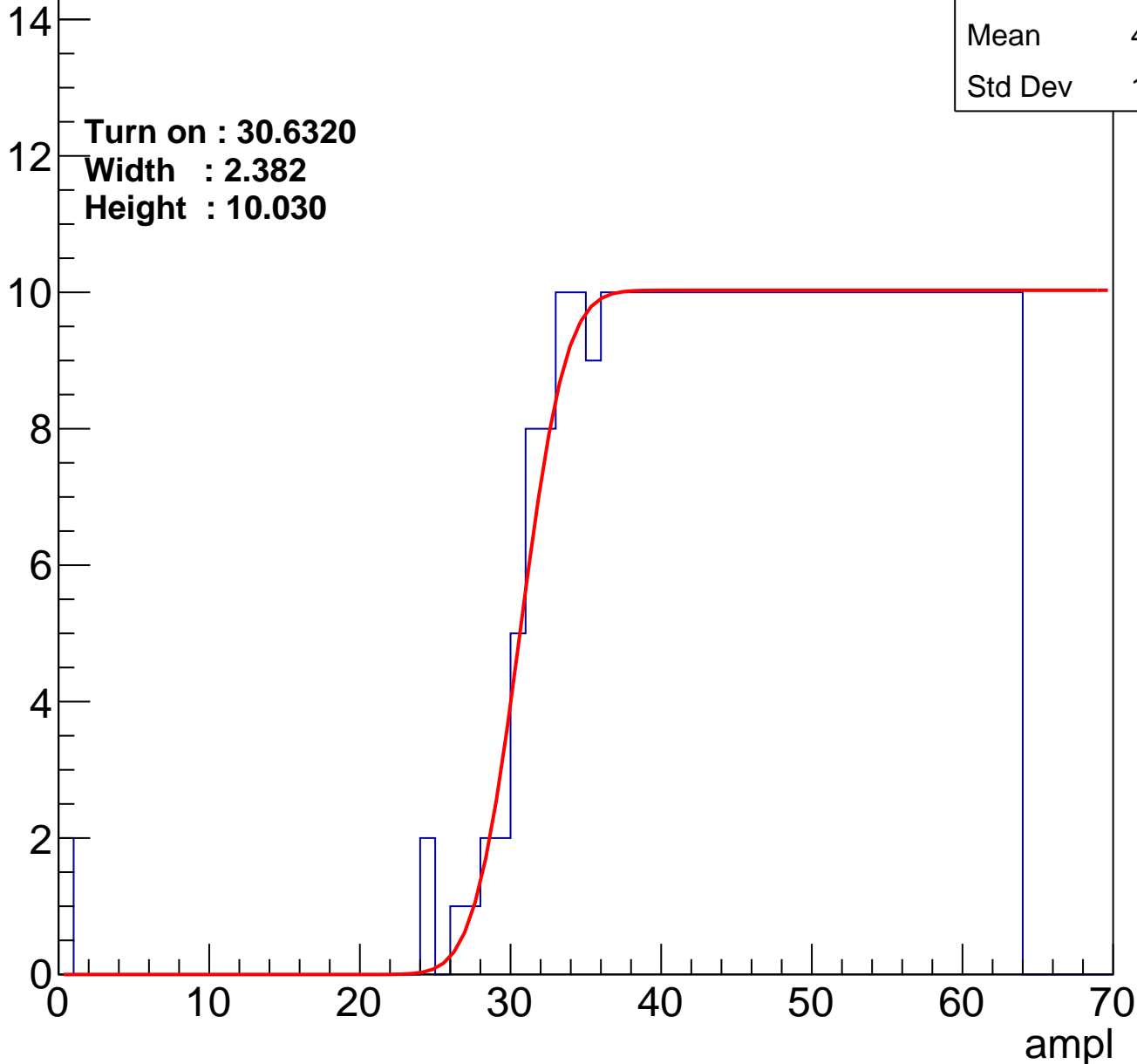
| | |
|---------|-------|
| Entries | 340 |
| Mean | 46.22 |
| Std Dev | 10.54 |

Turn on : 30.6320

Width : 2.382

Height : 10.030

Entry



B1L003S, U8-ch28

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 349 |
| Mean | 45.88 |
| Std Dev | 10.47 |

Turn on : 29.3201

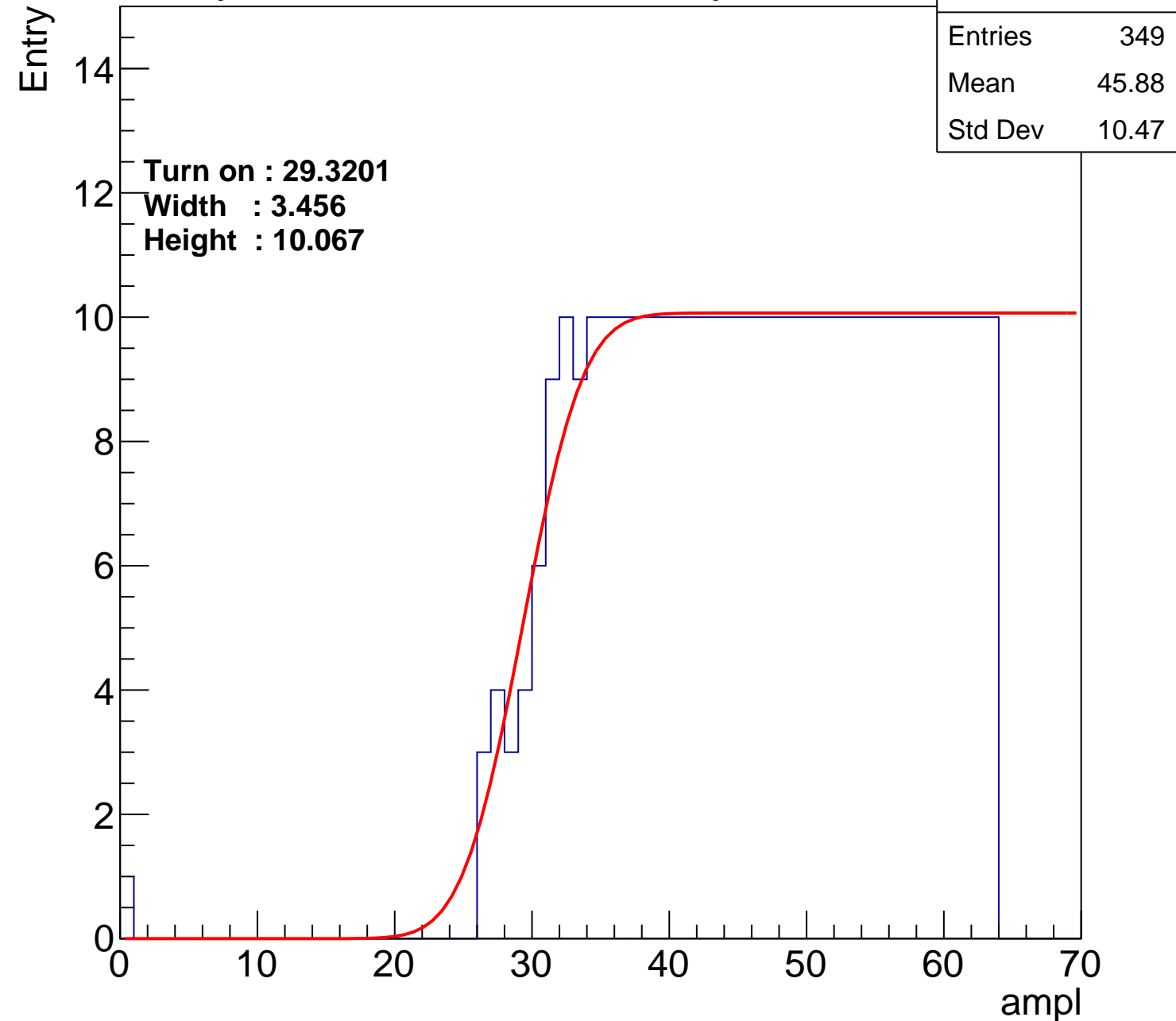
Width : 3.456

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch29

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.28 |
| Std Dev | 11.55 |

Turn on : 27.2020

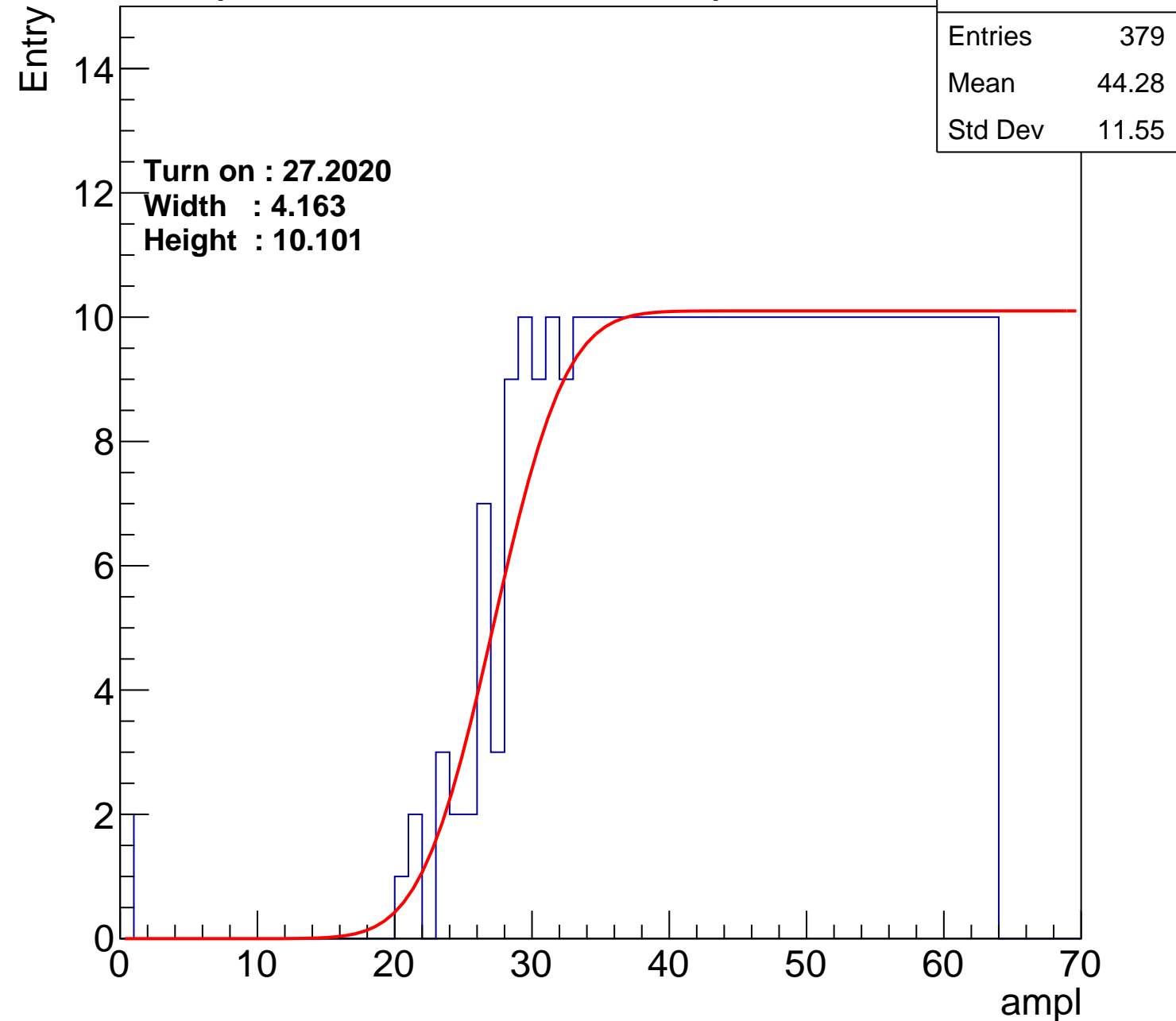
Width : 4.163

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch30

calib_packv5_042523_0143.root, FC#13, port D2

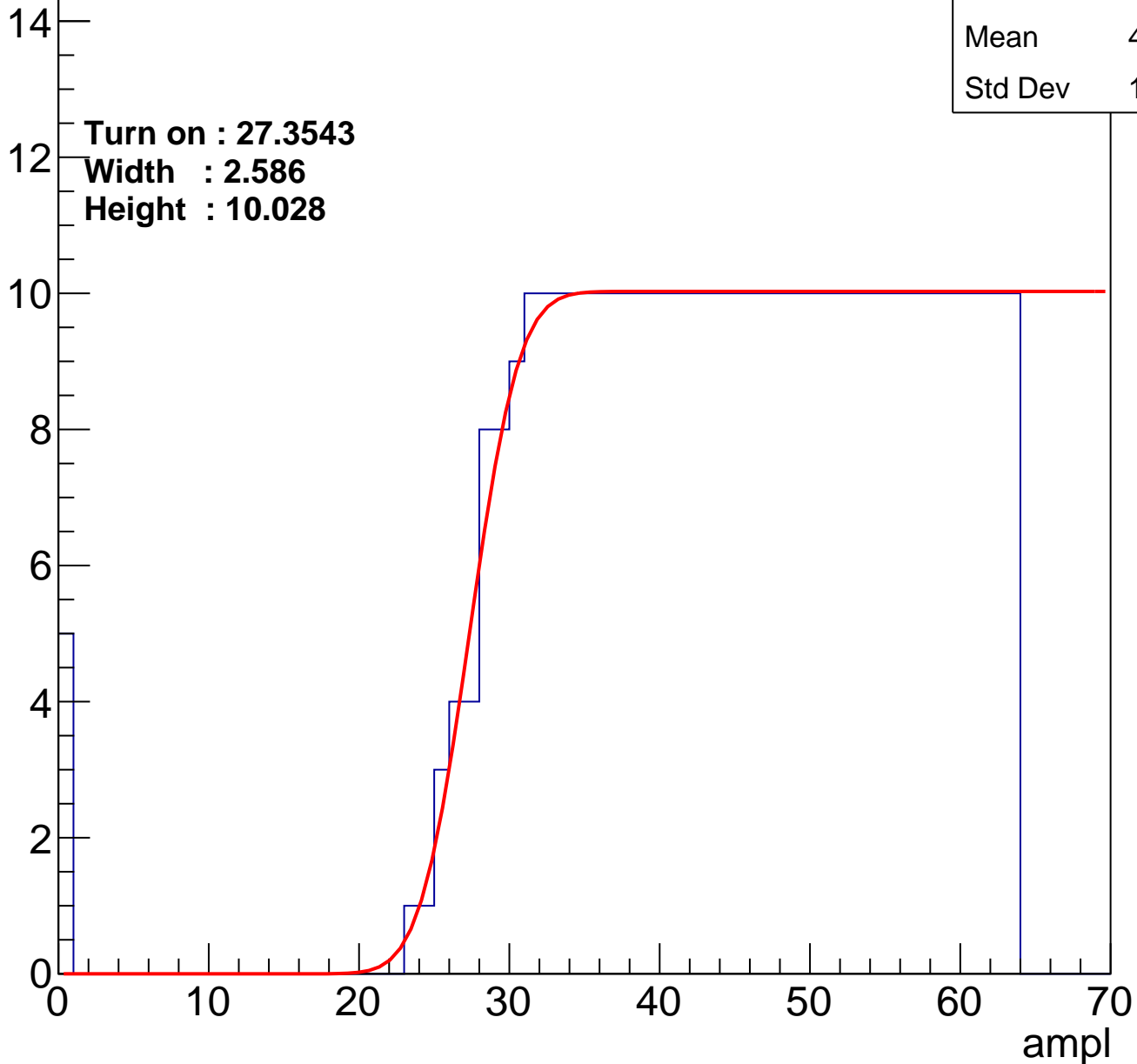
Entry

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.42 |
| Std Dev | 11.86 |

Turn on : 27.3543

Width : 2.586

Height : 10.028



B1L003S, U8-ch31

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.11 |
| Std Dev | 11.73 |

Turn on : 26.5828

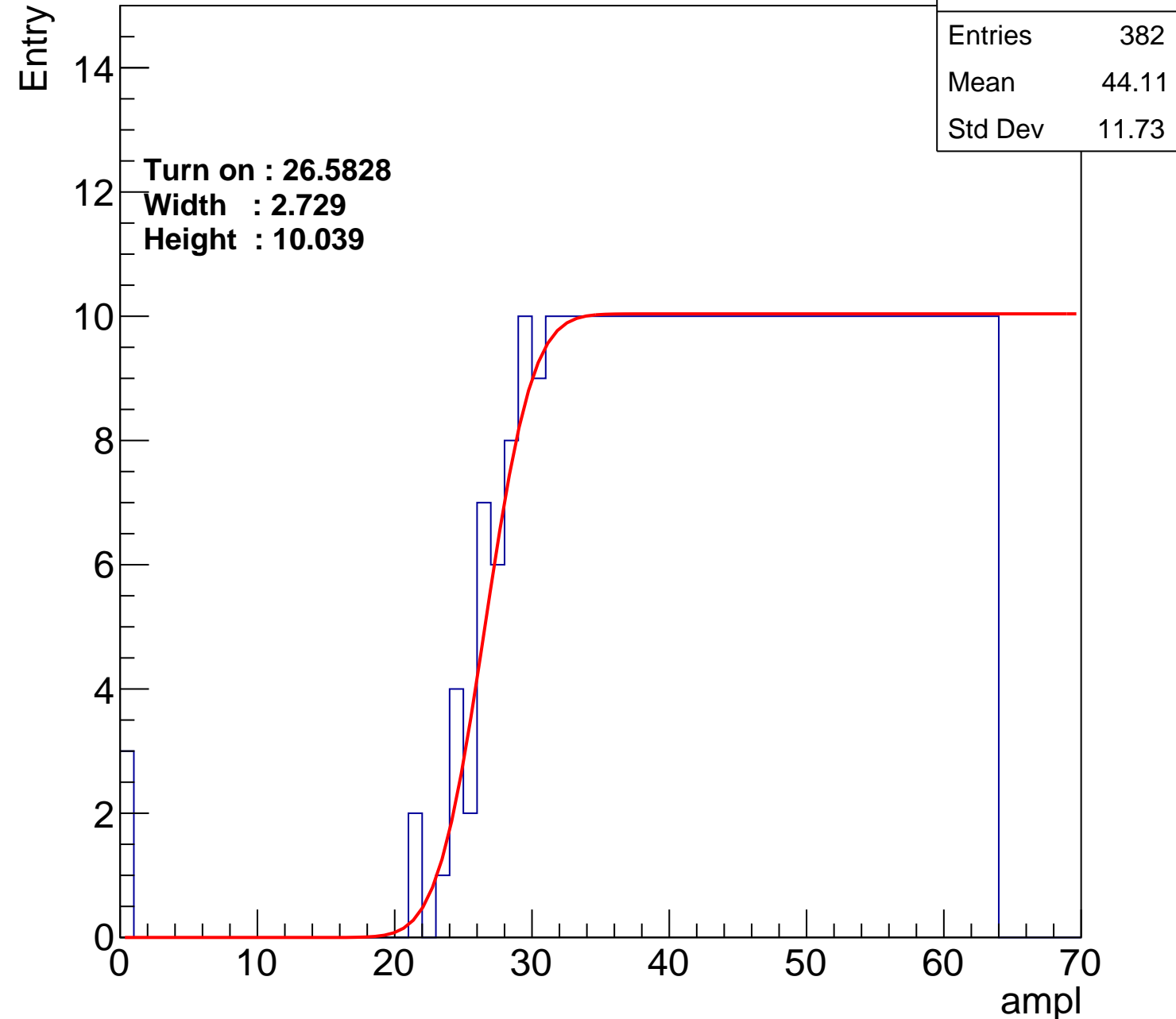
Width : 2.729

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch32

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.37 |
| Std Dev | 10.76 |

Turn on : 28.2749

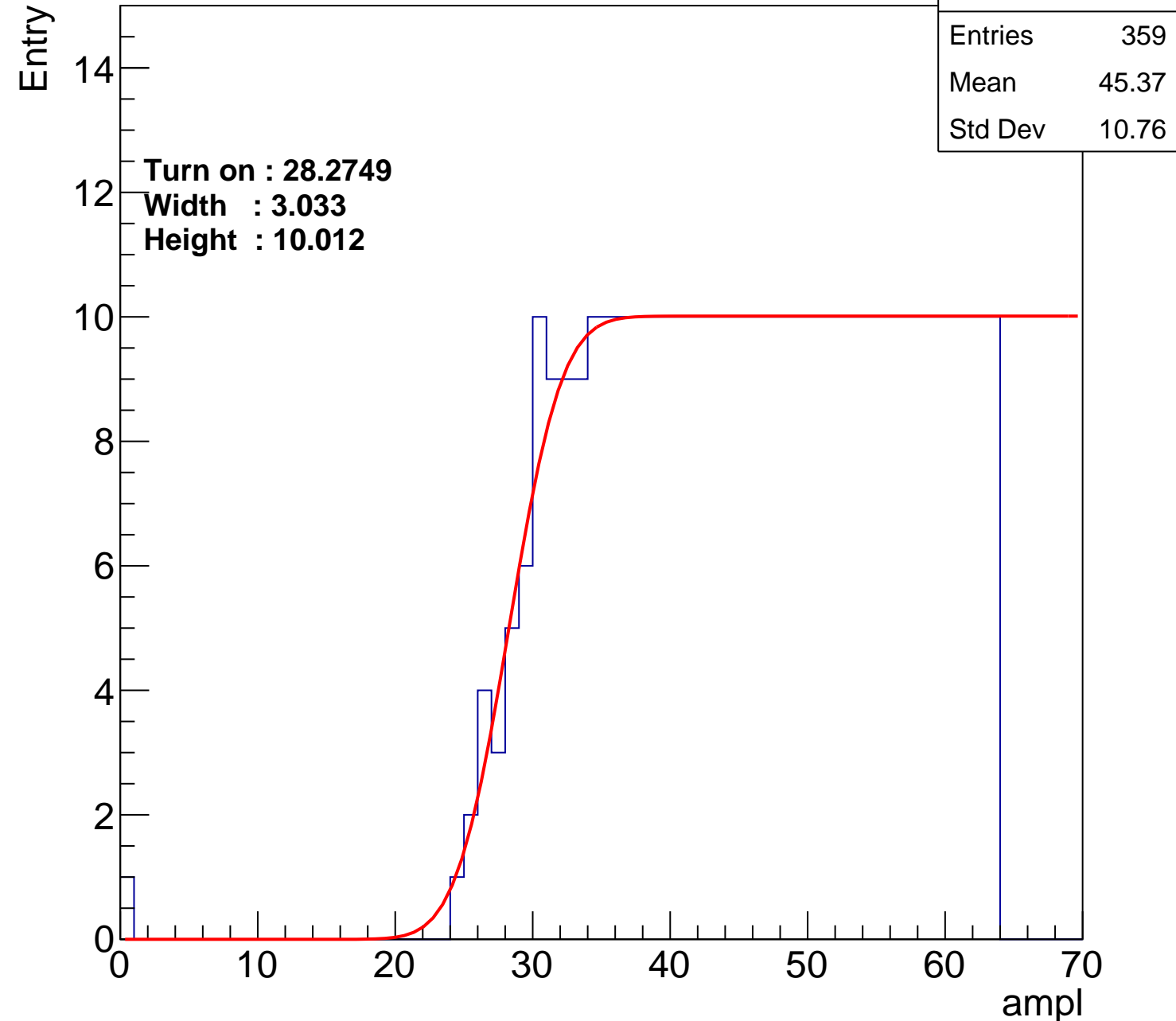
Width : 3.033

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch33

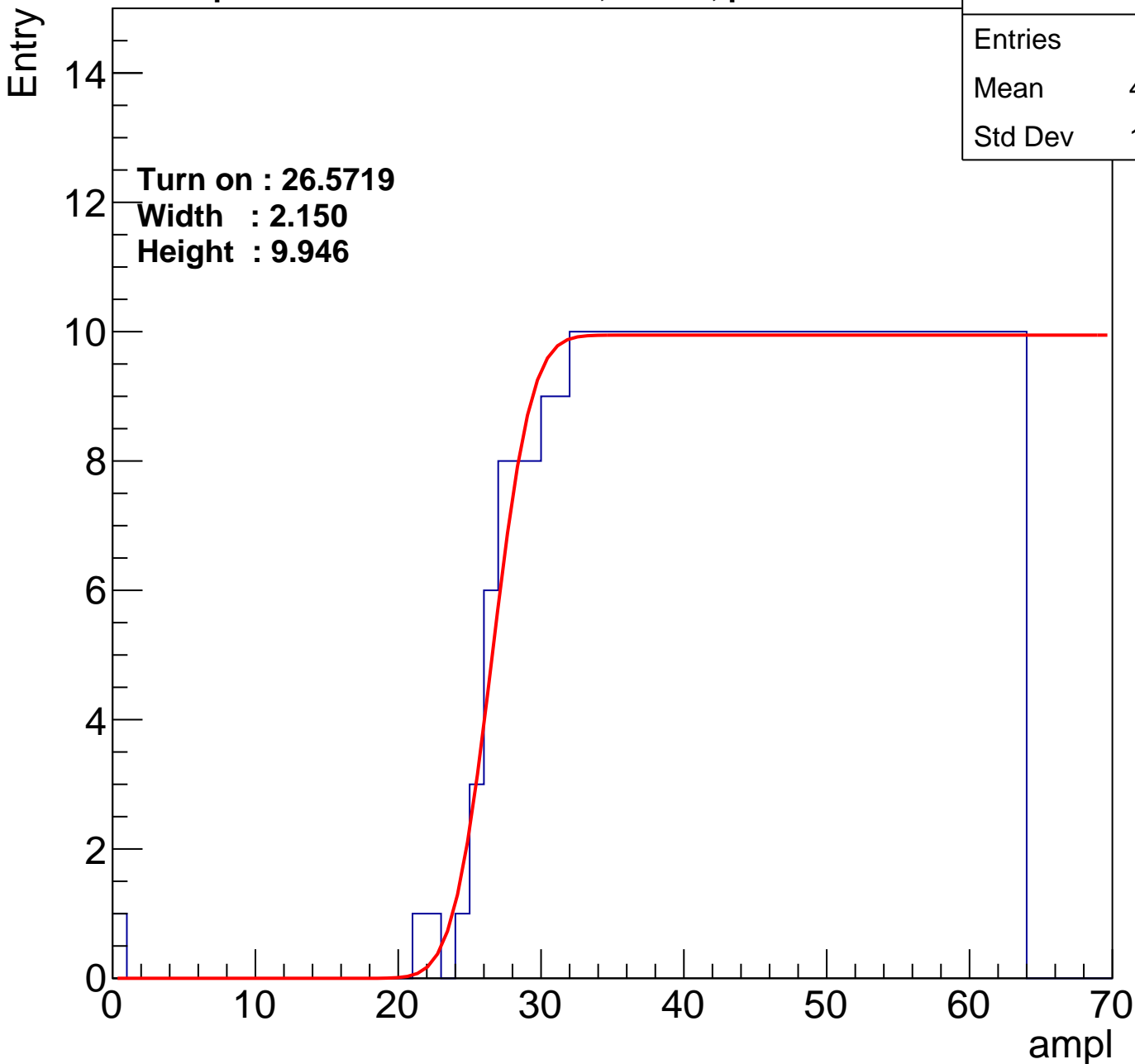
calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.58 |
| Std Dev | 11.18 |

Turn on : 26.5719

Width : 2.150

Height : 9.946



B1L003S, U8-ch34

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.1 |
| Std Dev | 11.08 |

Turn on : 29.1145

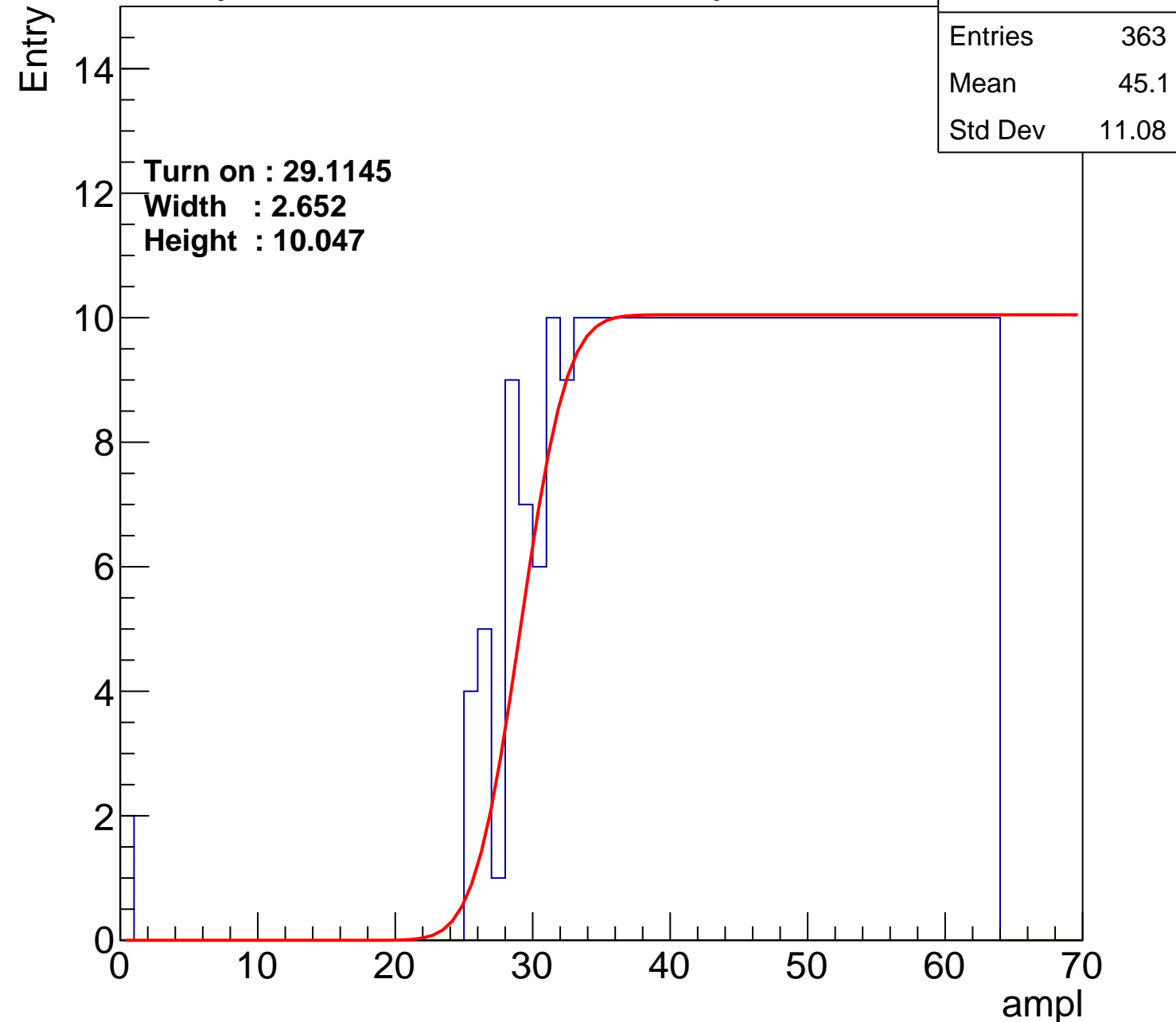
Width : 2.652

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch35

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.18 |
| Std Dev | 10.85 |

Turn on : 27.9534

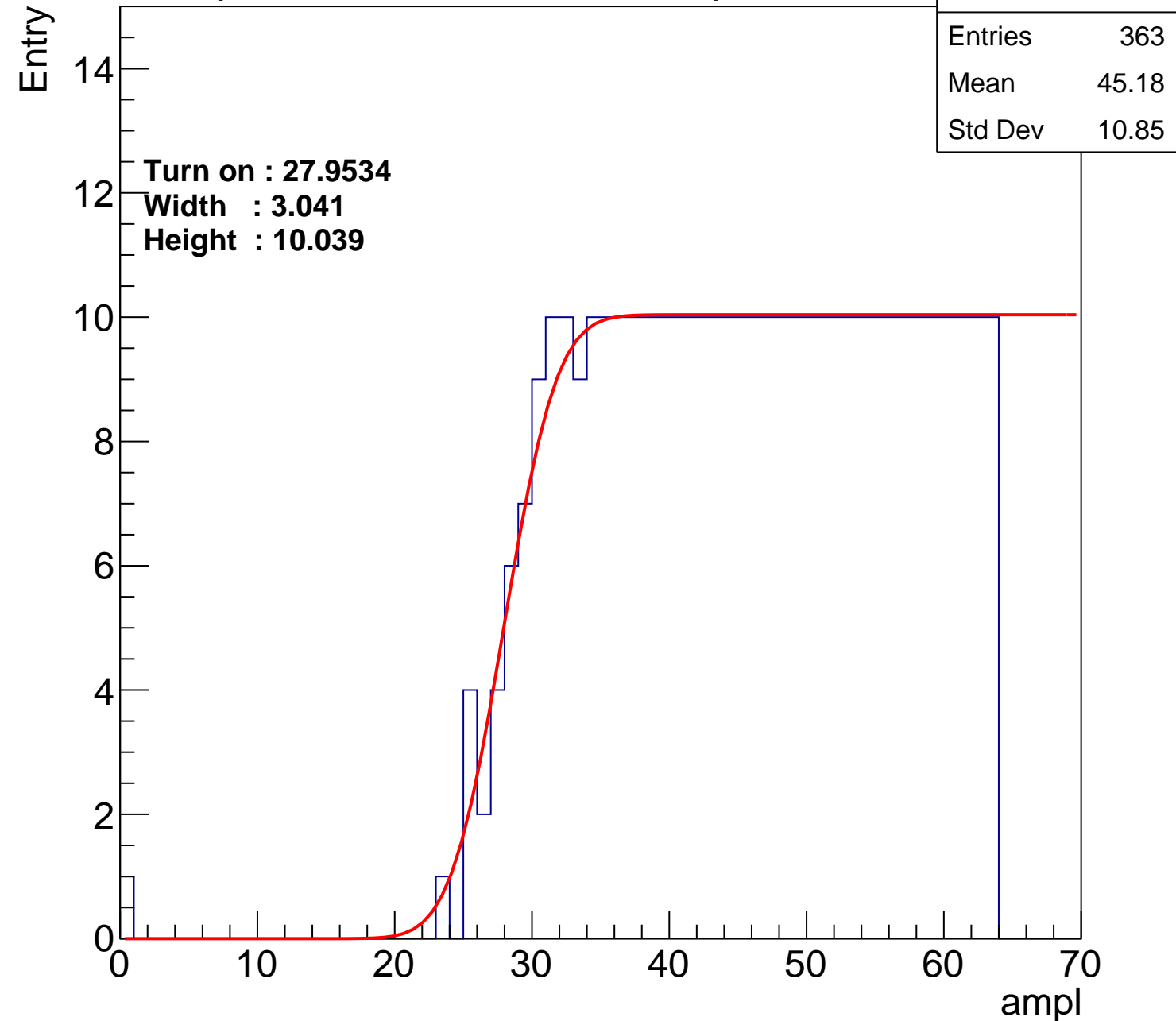
Width : 3.041

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch36

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.18 |
| Std Dev | 11.01 |

Turn on : 28.1586

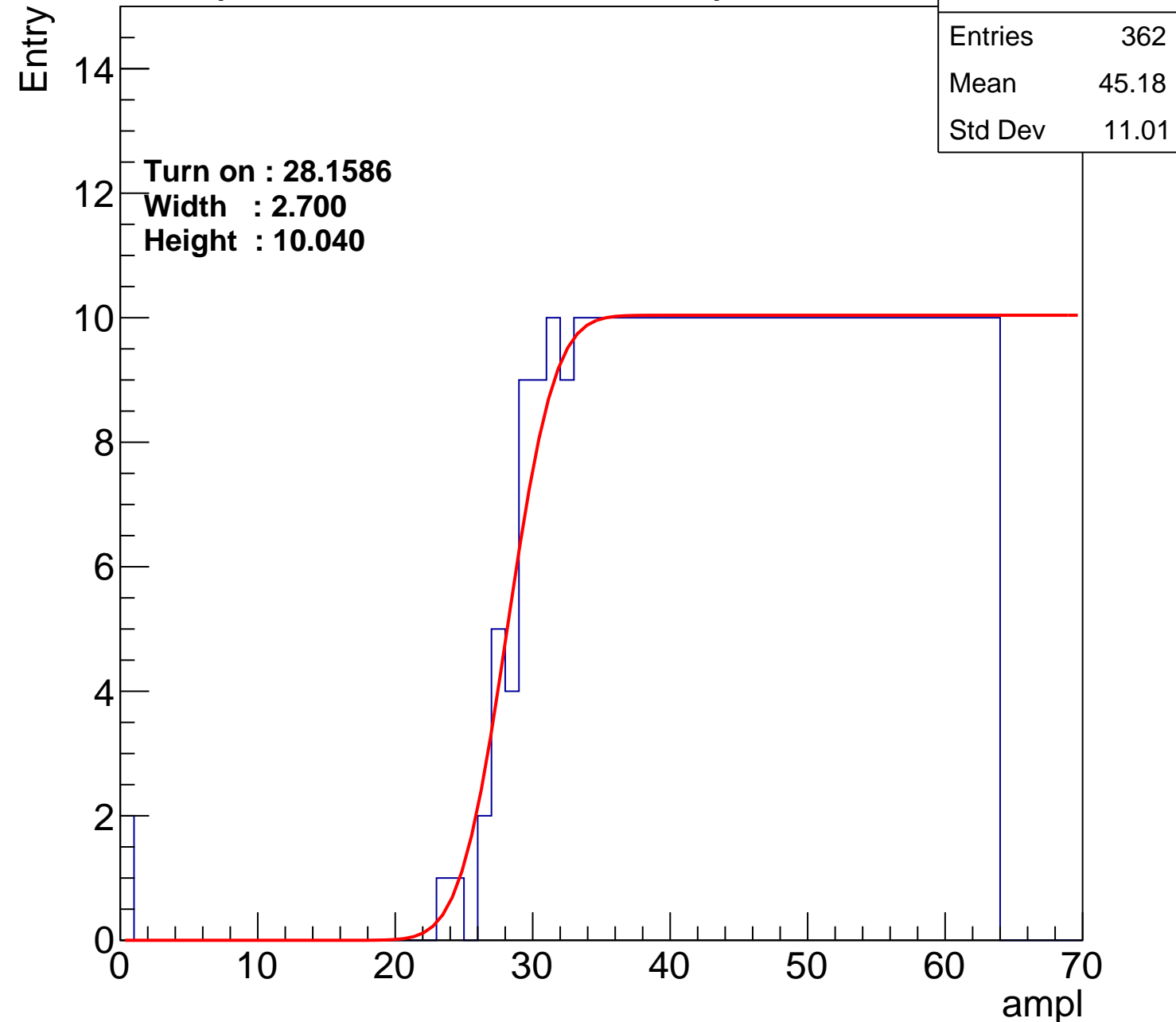
Width : 2.700

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch37

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.9 |
| Std Dev | 11.33 |

Turn on : 27.7580

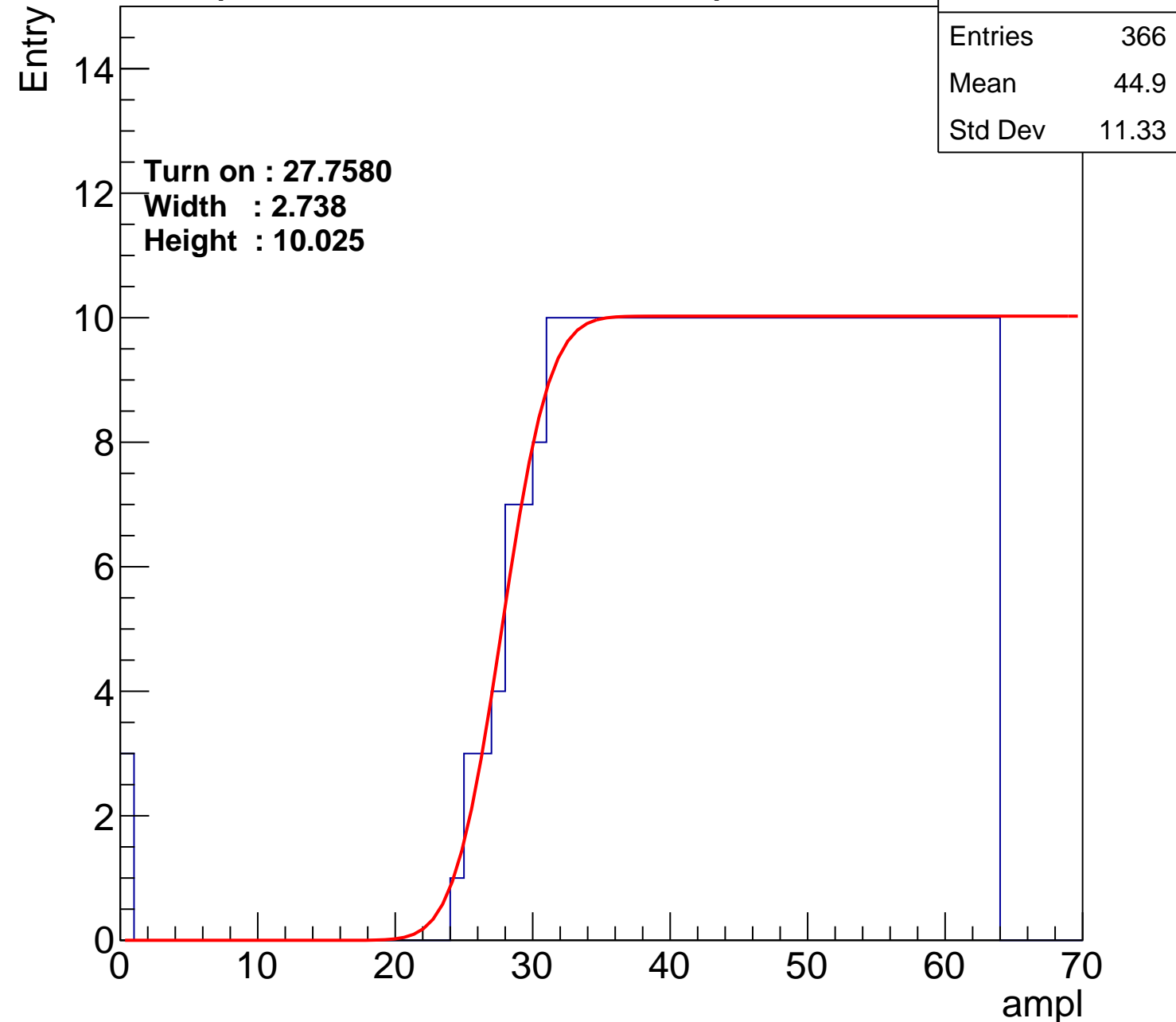
Width : 2.738

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch38

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 352 |
| Mean | 45.74 |
| Std Dev | 10.53 |

Turn on : 29.0528

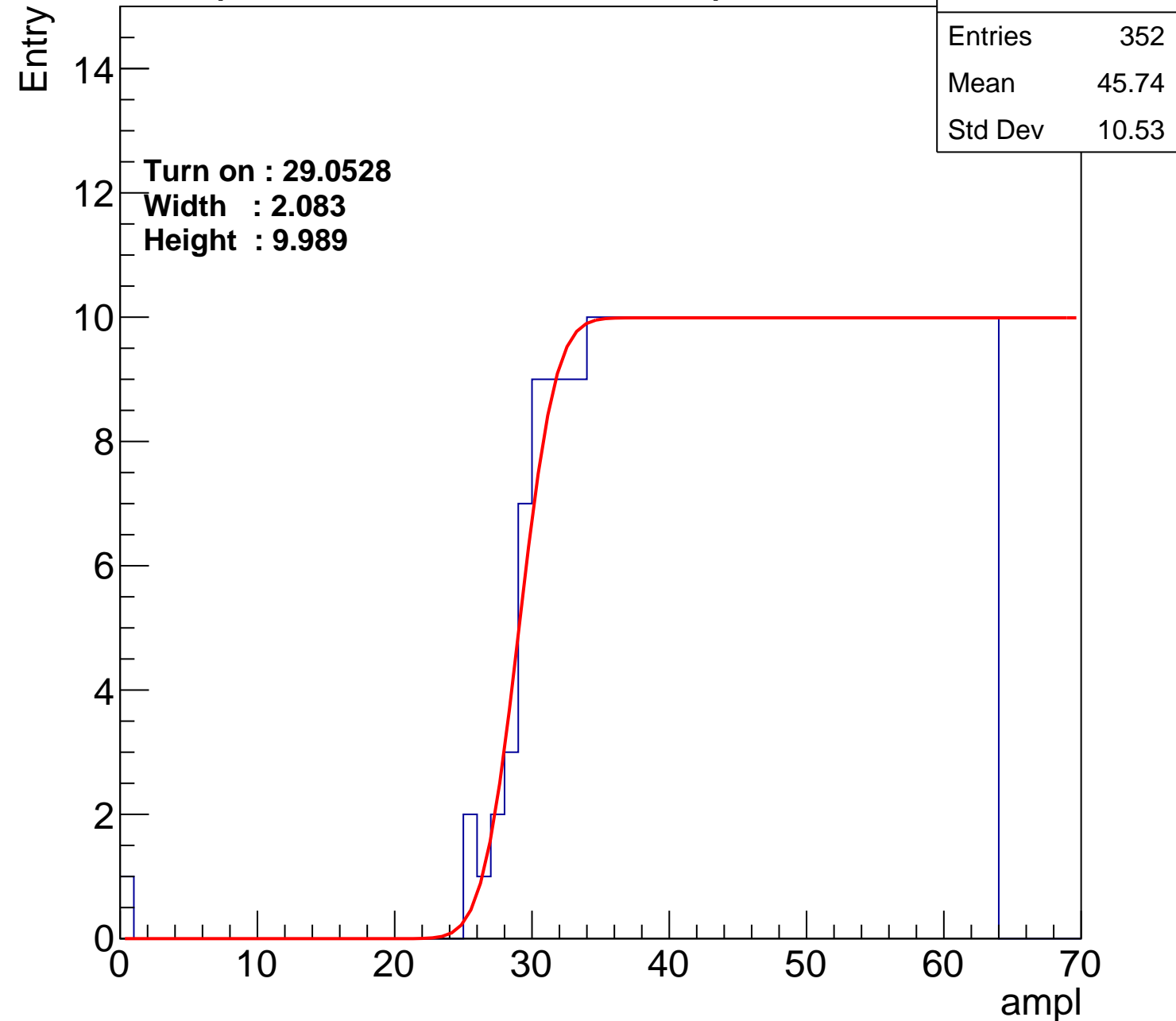
Width : 2.083

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch39

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.39 |
| Std Dev | 11.73 |

Turn on : 26.9489

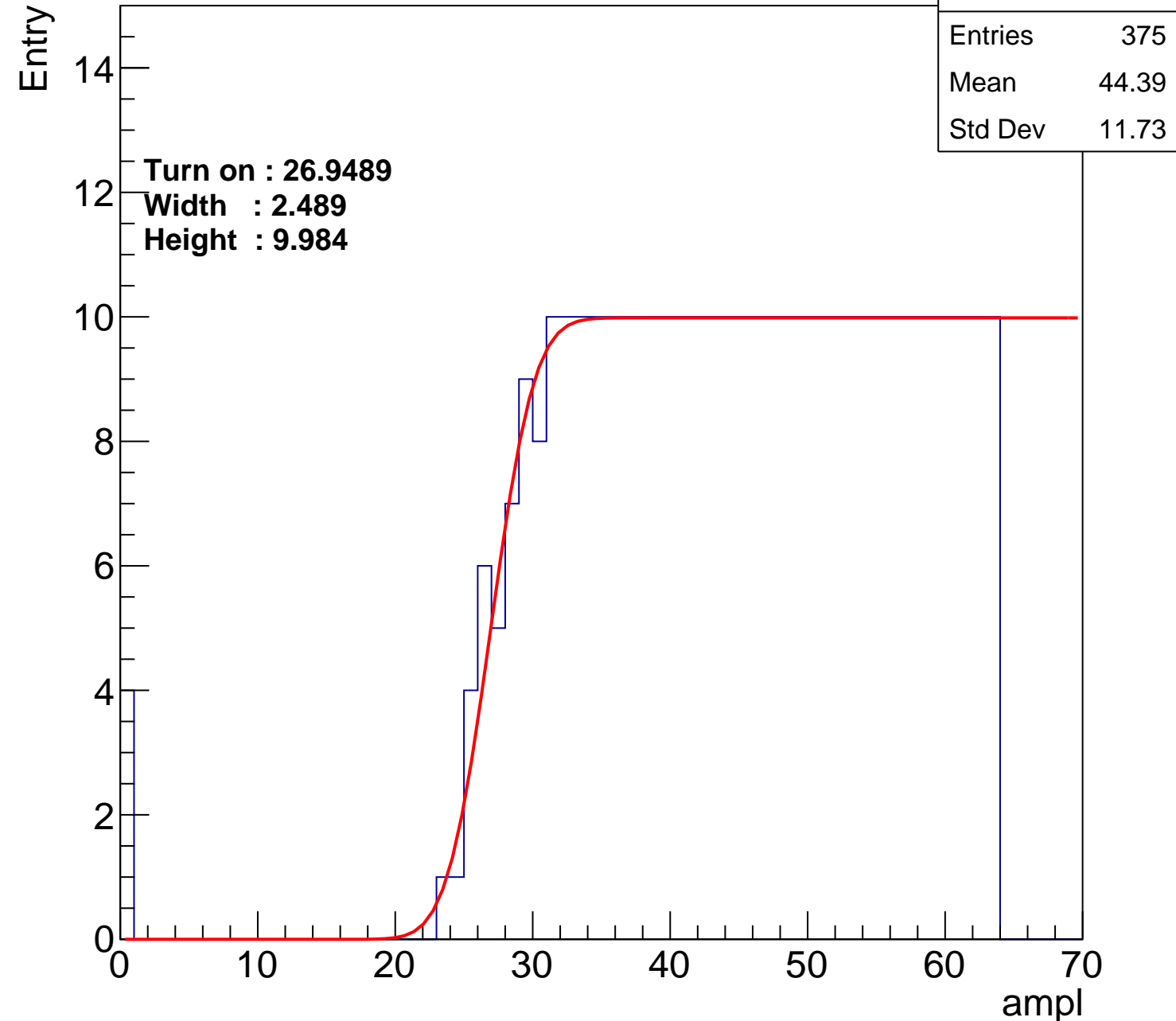
Width : 2.489

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch40

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.21 |
| Std Dev | 10.87 |

Turn on : 28.3893

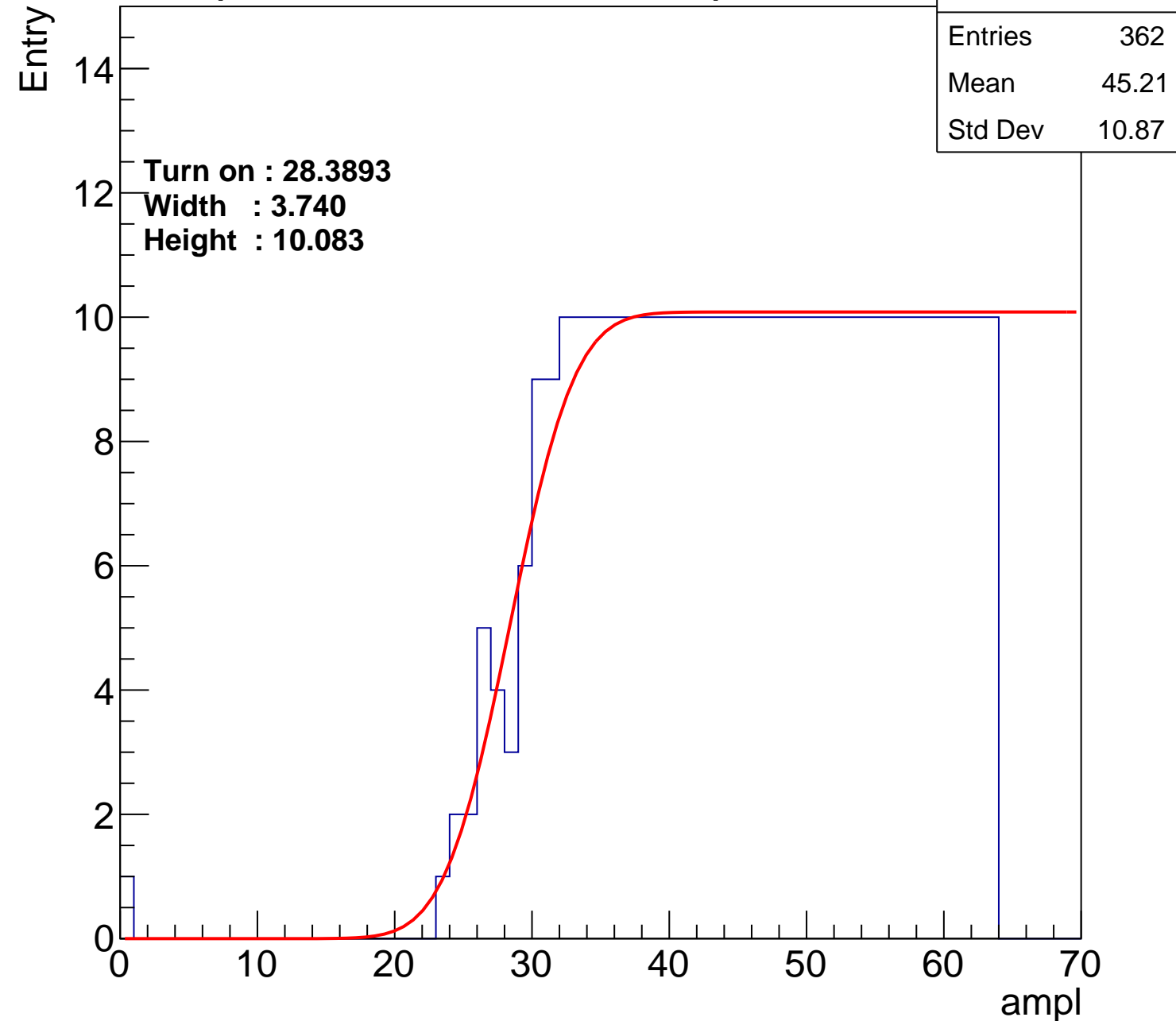
Width : 3.740

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch41

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.08 |
| Std Dev | 11.11 |

Turn on : 28.4864

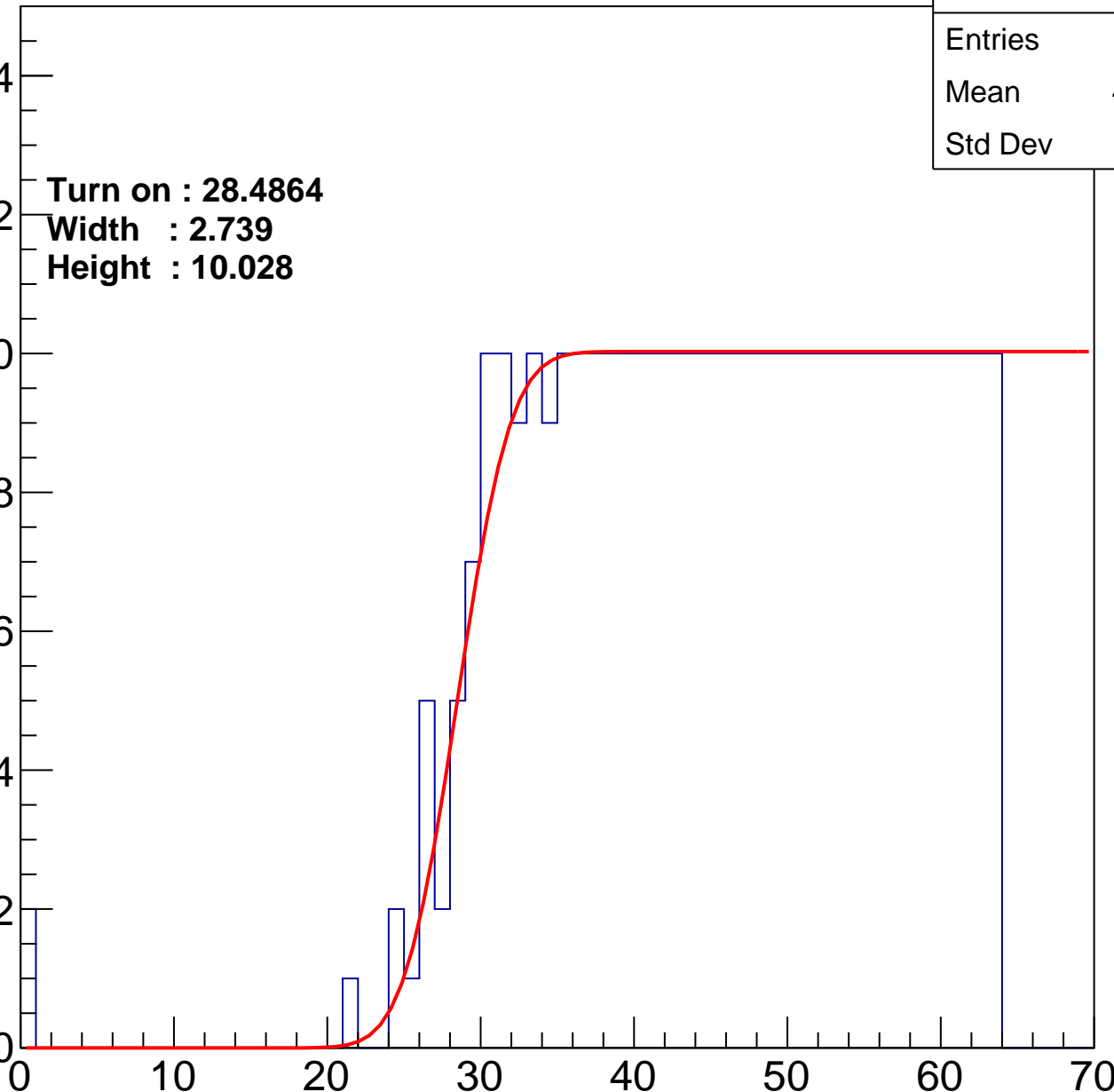
Width : 2.739

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch42

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.26 |
| Std Dev | 11.69 |

Turn on : 26.2850

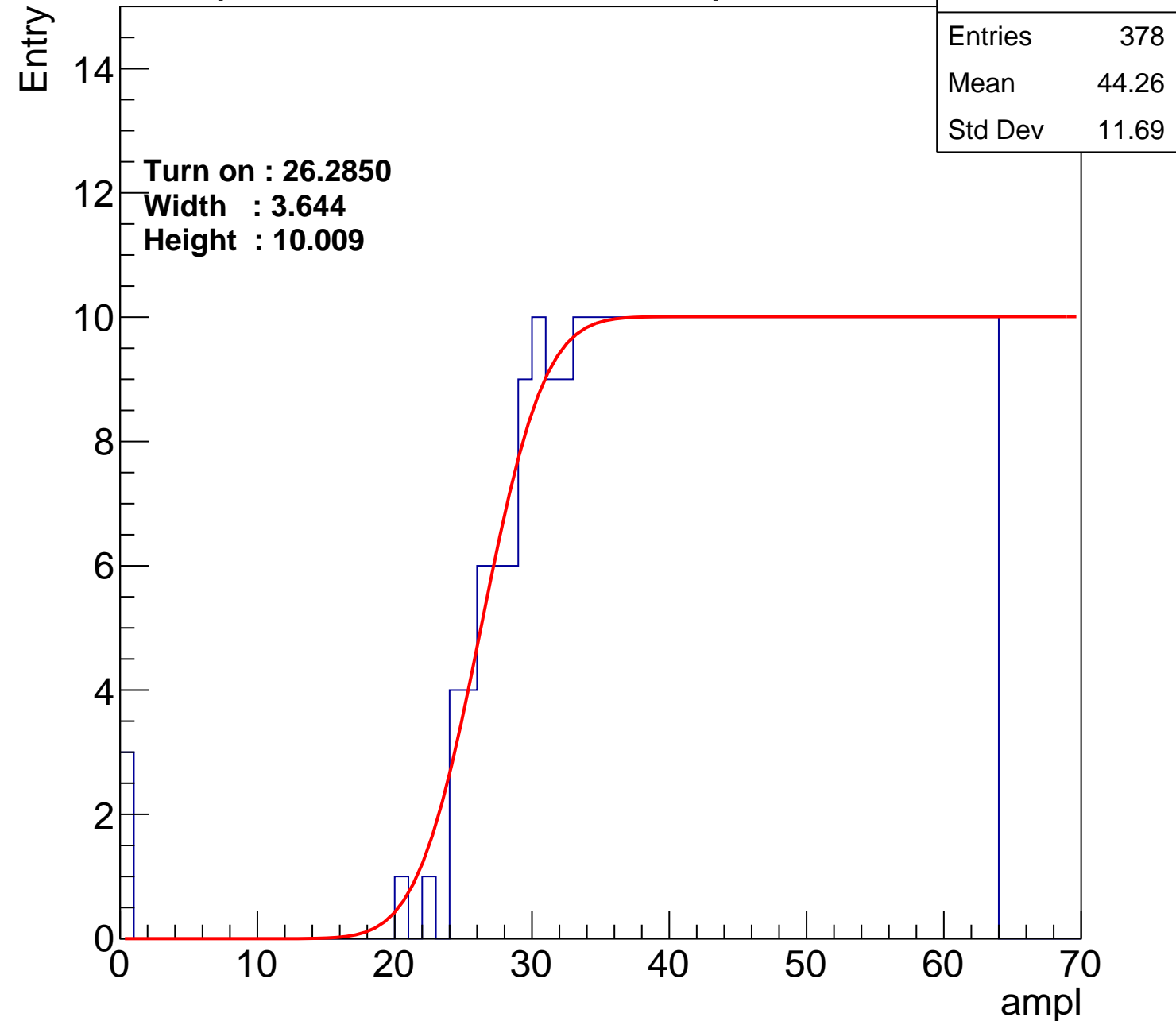
Width : 3.644

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch43

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.94 |
| Std Dev | 11.18 |

Turn on : 27.6347

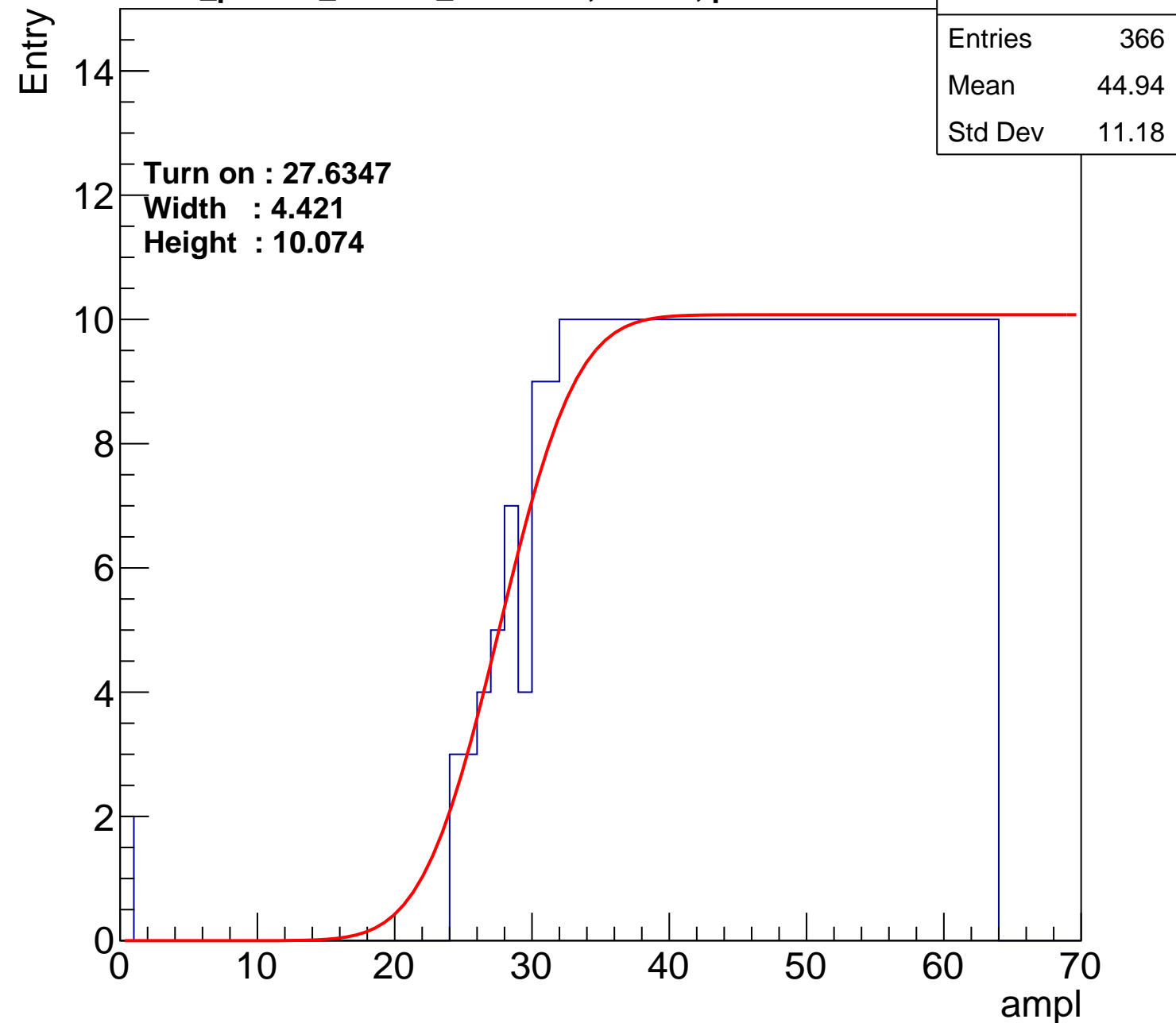
Width : 4.421

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch44

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.37 |
| Std Dev | 11.07 |

Turn on : 28.7587

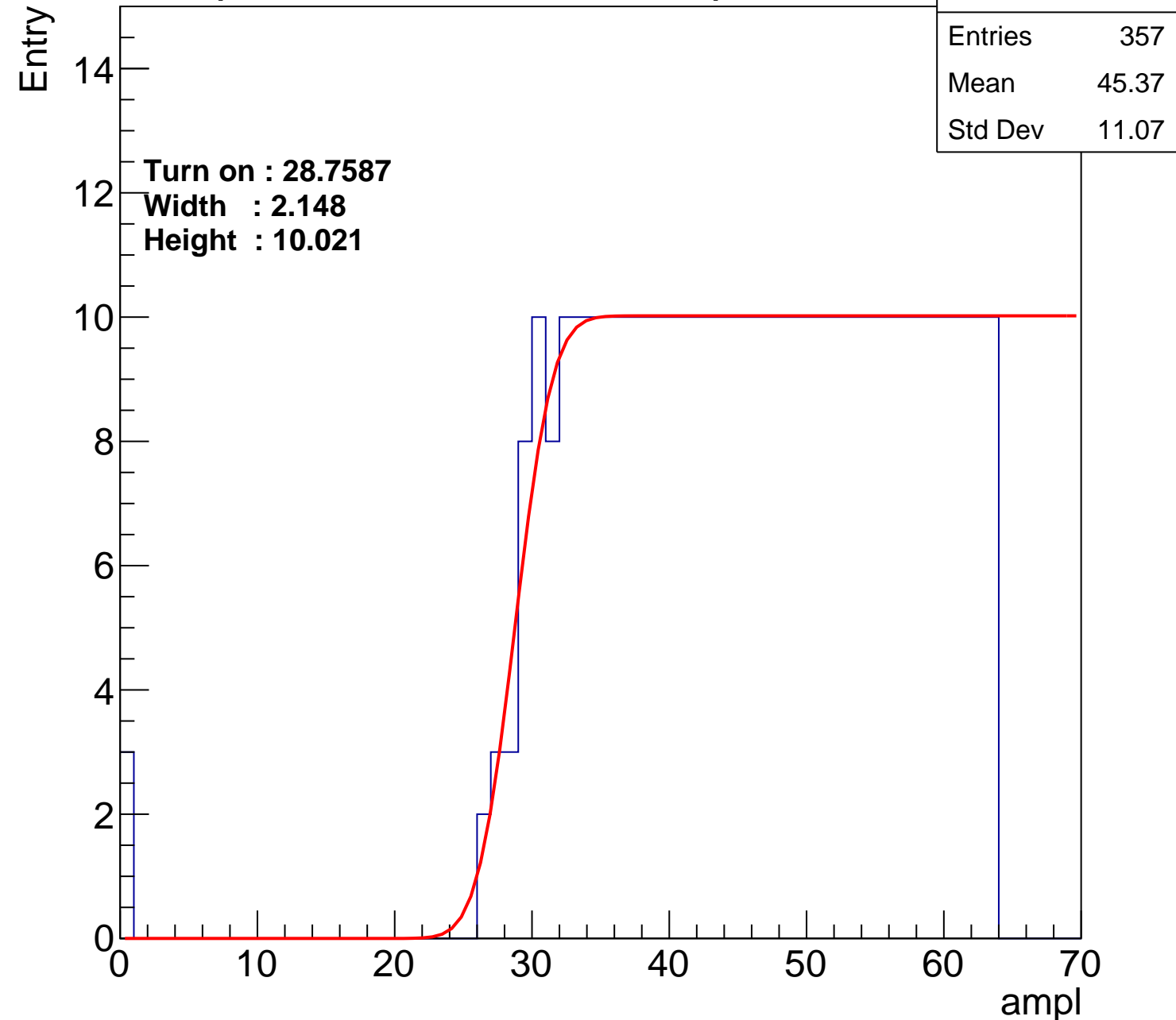
Width : 2.148

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch45

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.57 |
| Std Dev | 11.84 |

Turn on : 27.4466

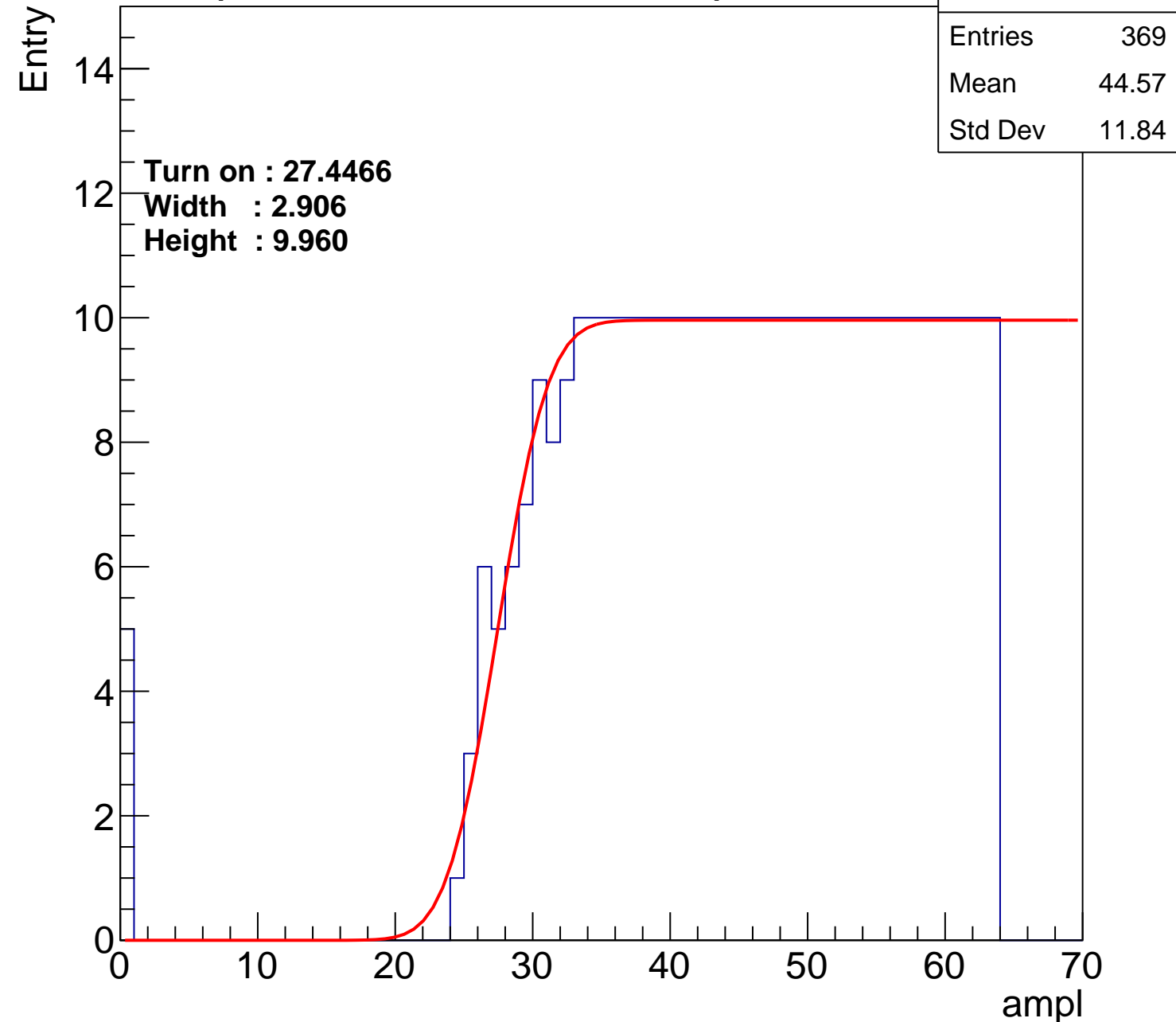
Width : 2.906

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch46

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|------|
| Entries | 364 |
| Mean | 45 |
| Std Dev | 11.2 |

Turn on : 28.1898

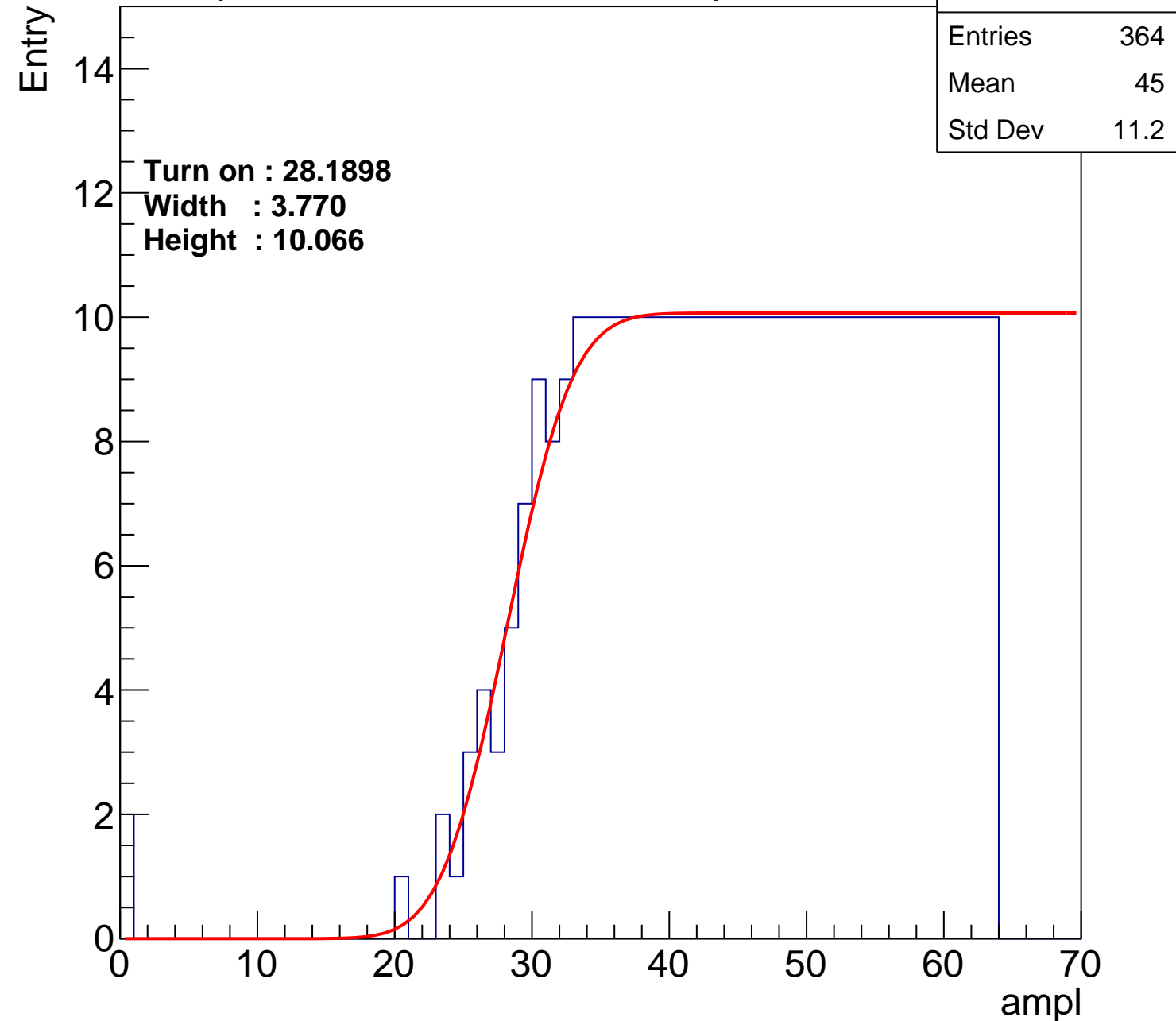
Width : 3.770

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch47

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.1 |
| Std Dev | 11.45 |

Turn on : 25.9330

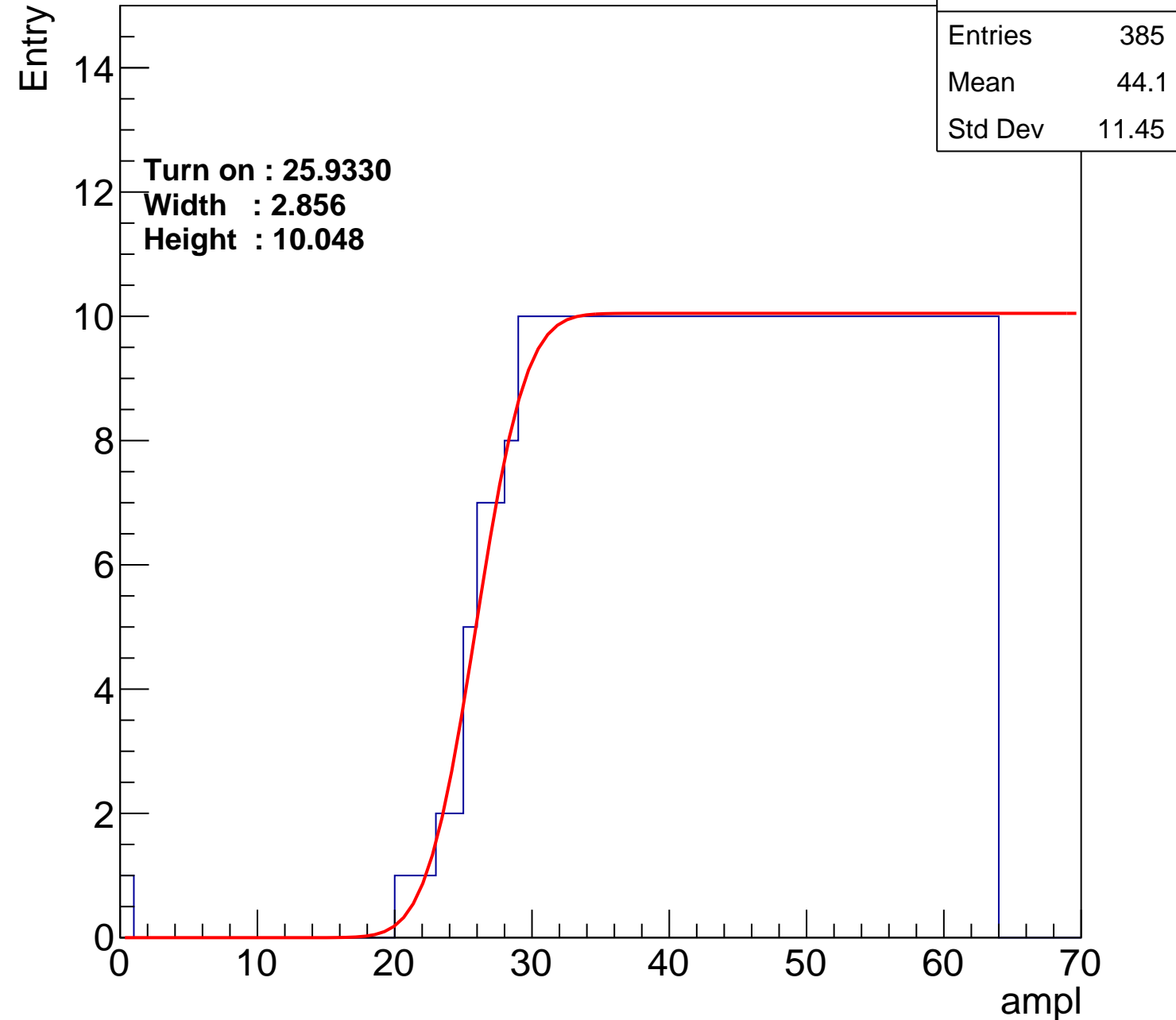
Width : 2.856

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch48

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.39 |
| Std Dev | 11.65 |

Turn on : 27.1890

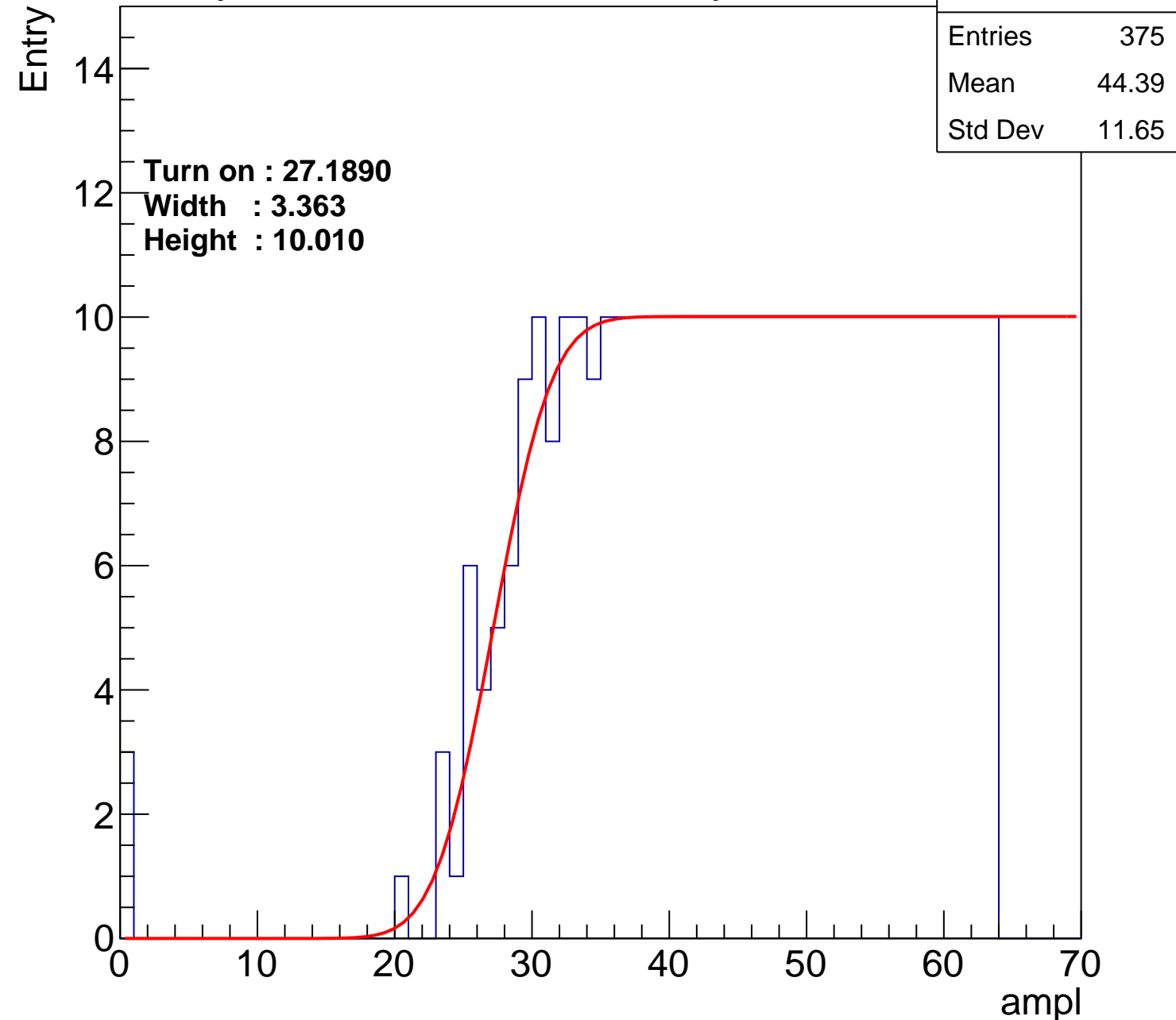
Width : 3.363

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch49

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.27 |
| Std Dev | 10.97 |

Turn on : 28.6244

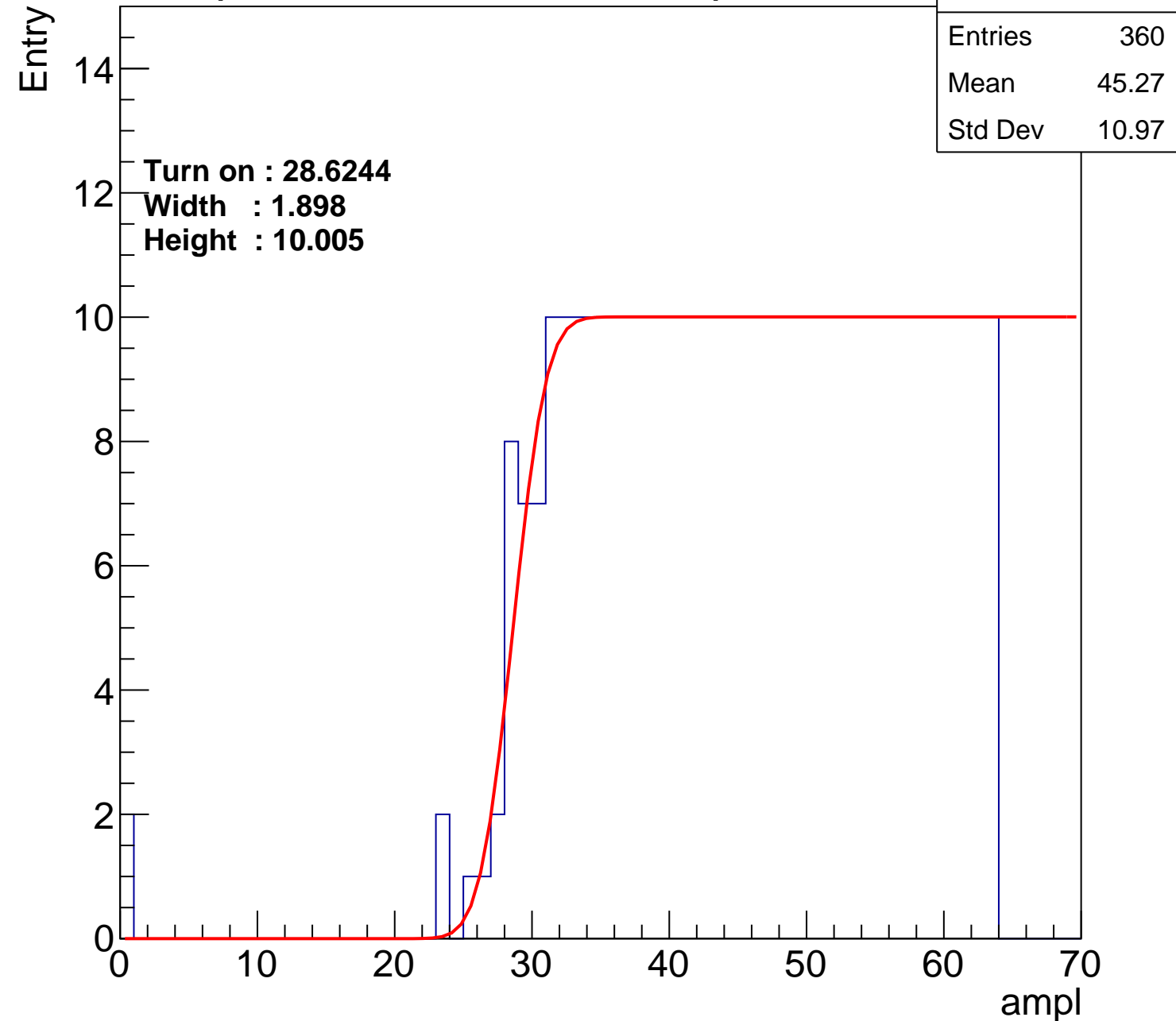
Width : 1.898

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch50

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.07 |
| Std Dev | 11.07 |

Turn on : 27.5745

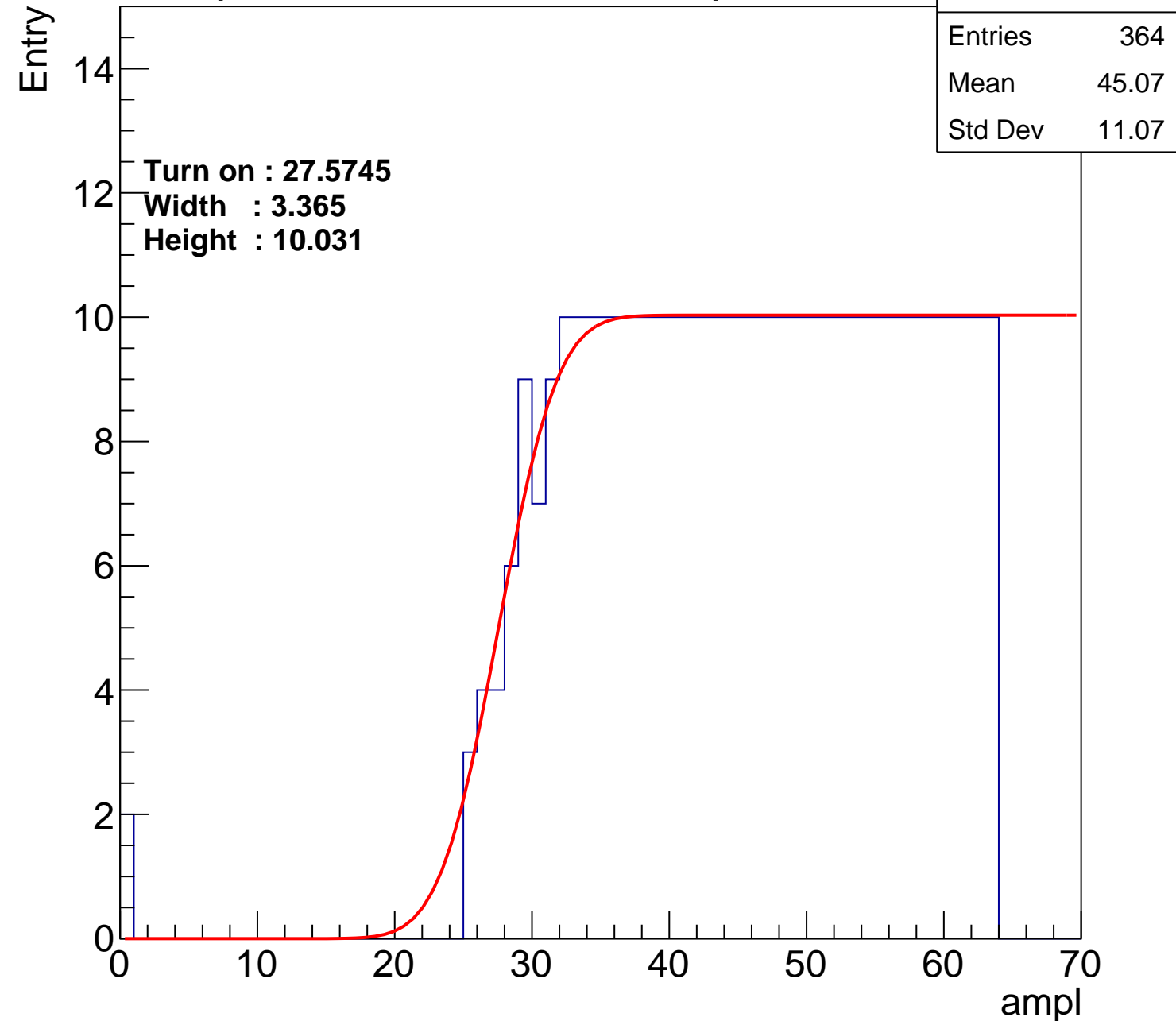
Width : 3.365

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch51

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.18 |
| Std Dev | 12.03 |

Turn on : 27.4146

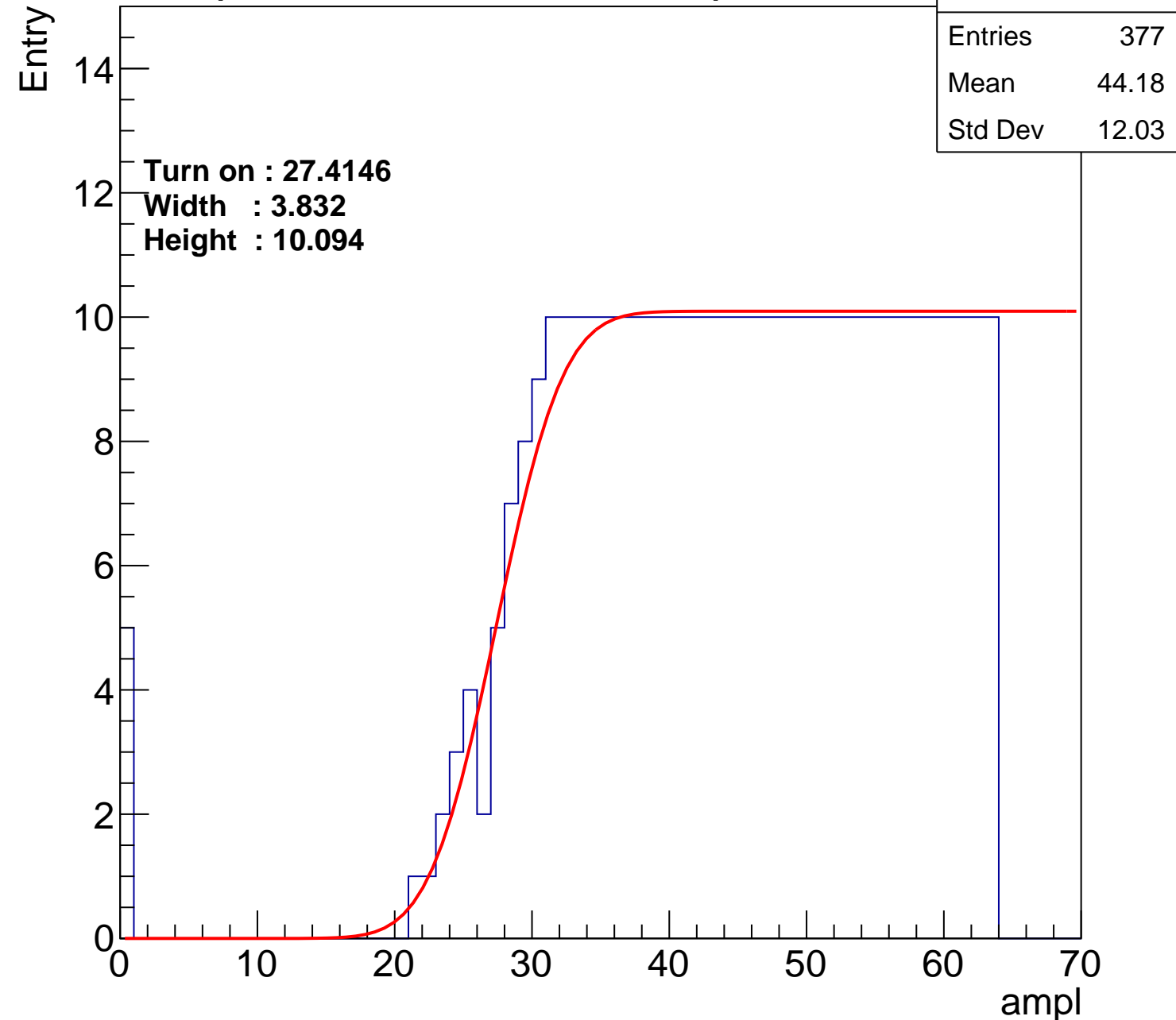
Width : 3.832

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch52

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.45 |
| Std Dev | 10.69 |

Turn on : 28.6354

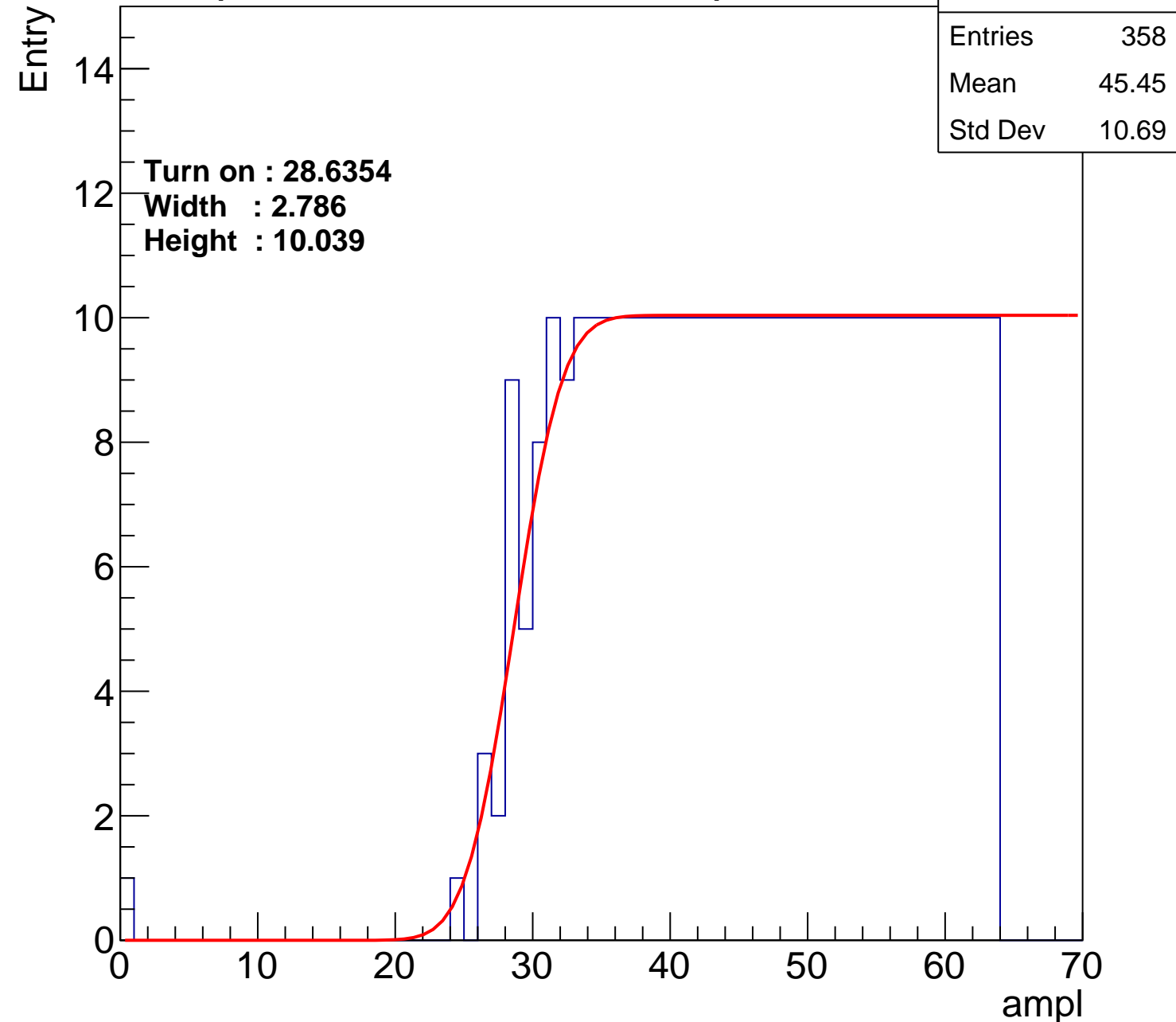
Width : 2.786

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch53

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.24 |
| Std Dev | 11.63 |

Turn on : 26.4203

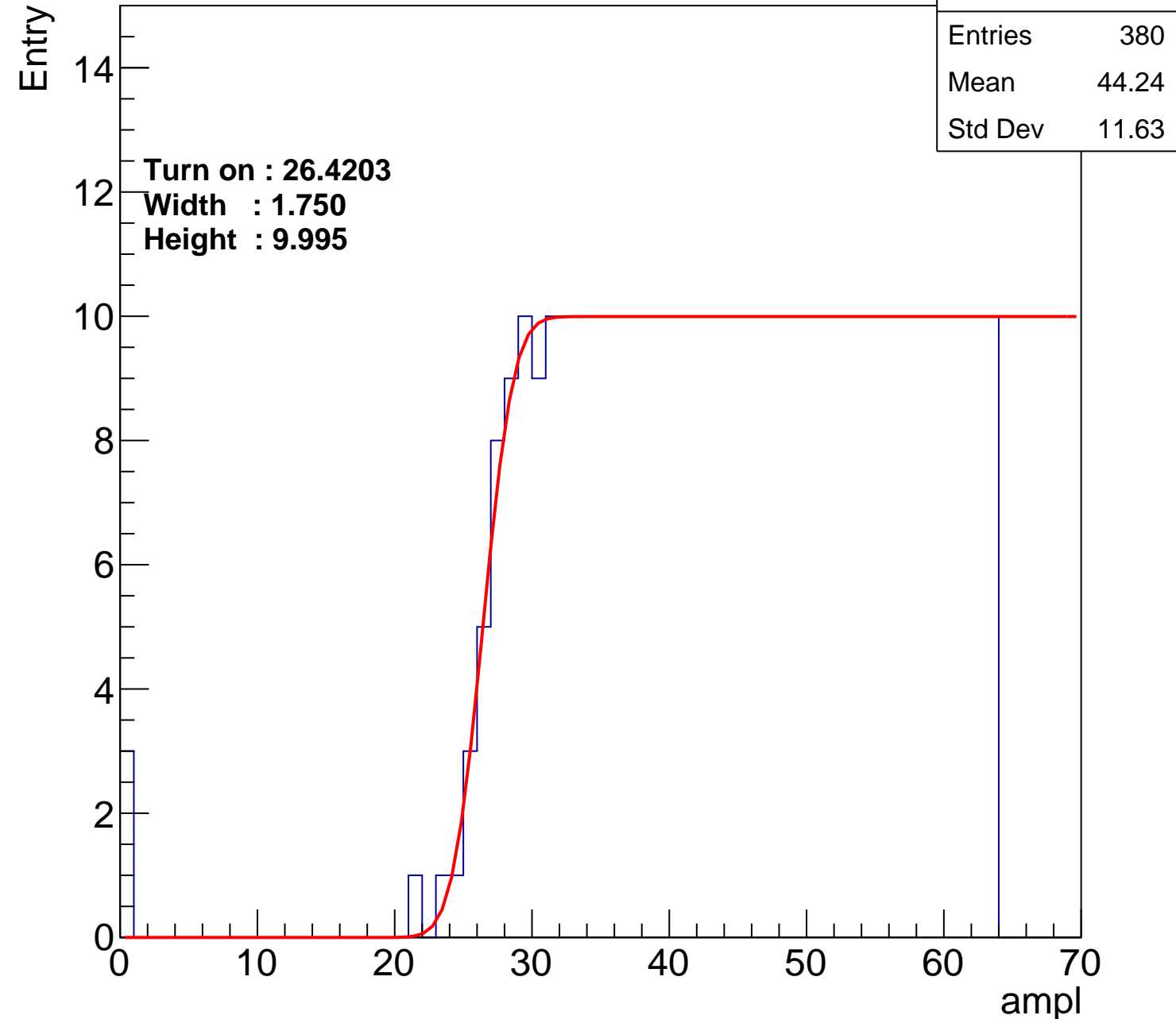
Width : 1.750

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch54

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.77 |
| Std Dev | 11.23 |

Turn on : 27.3707

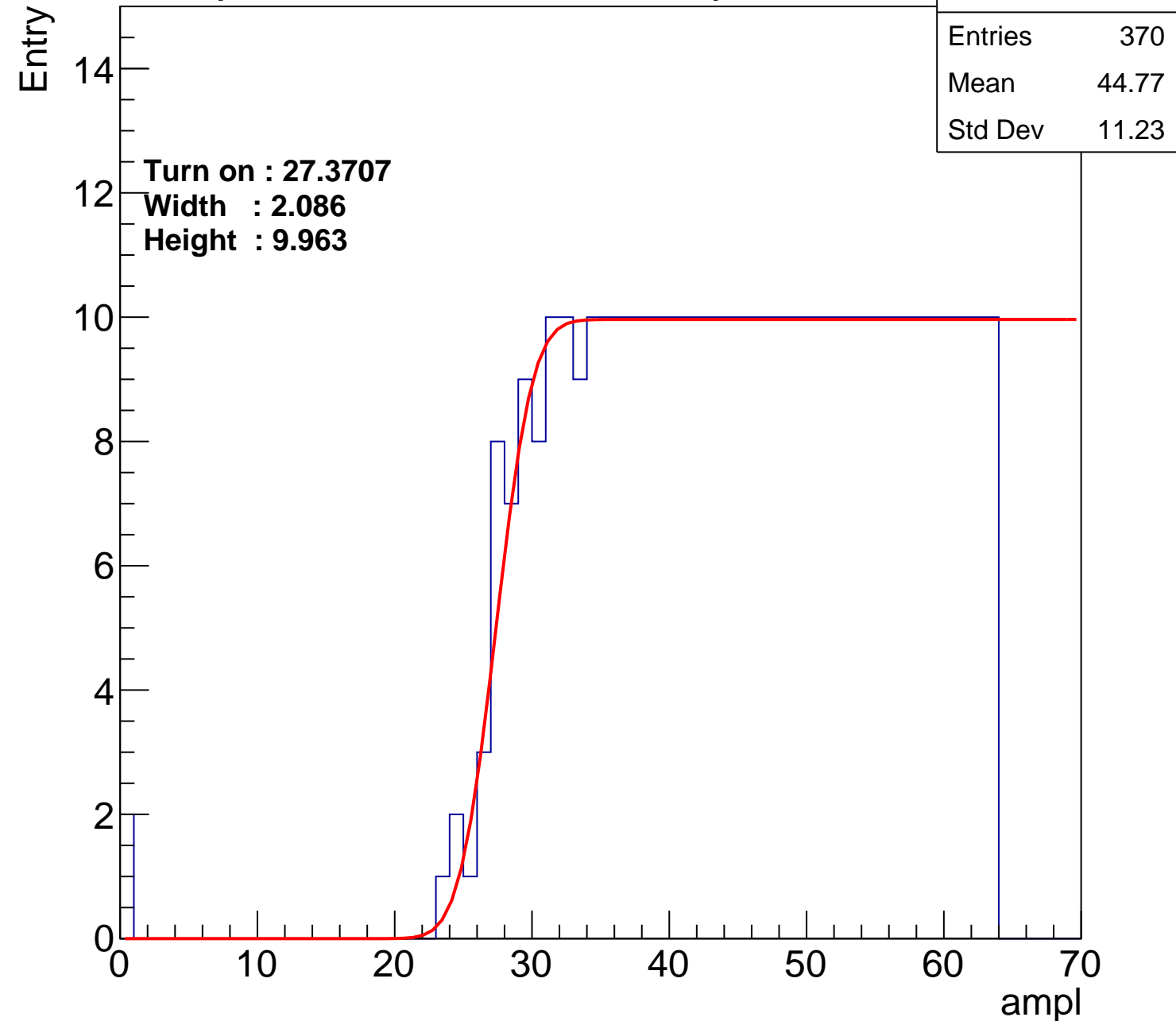
Width : 2.086

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch55

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.04 |
| Std Dev | 11.12 |

Turn on : 27.9222

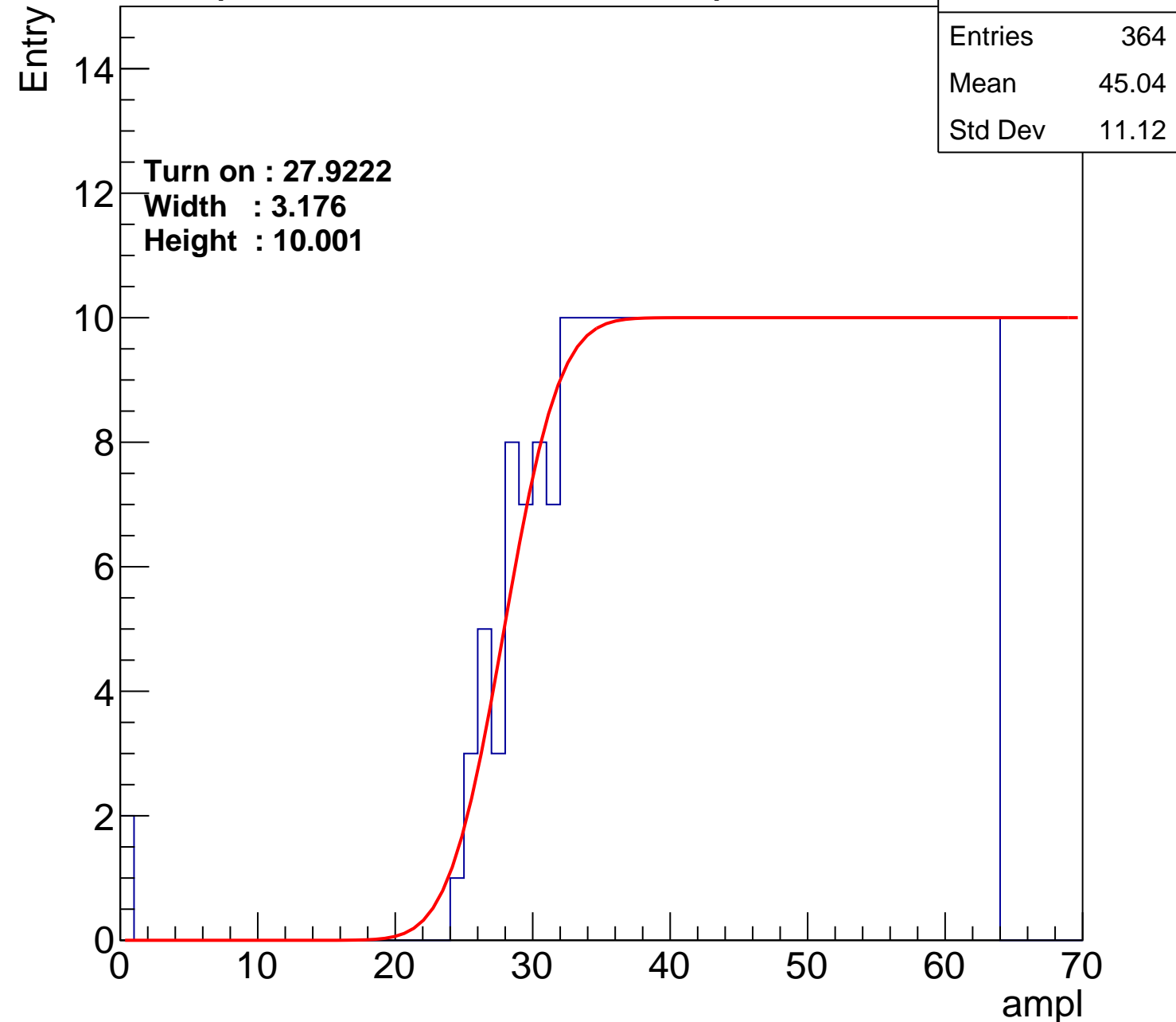
Width : 3.176

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch56

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.02 |
| Std Dev | 11.76 |

Turn on : 25.7719

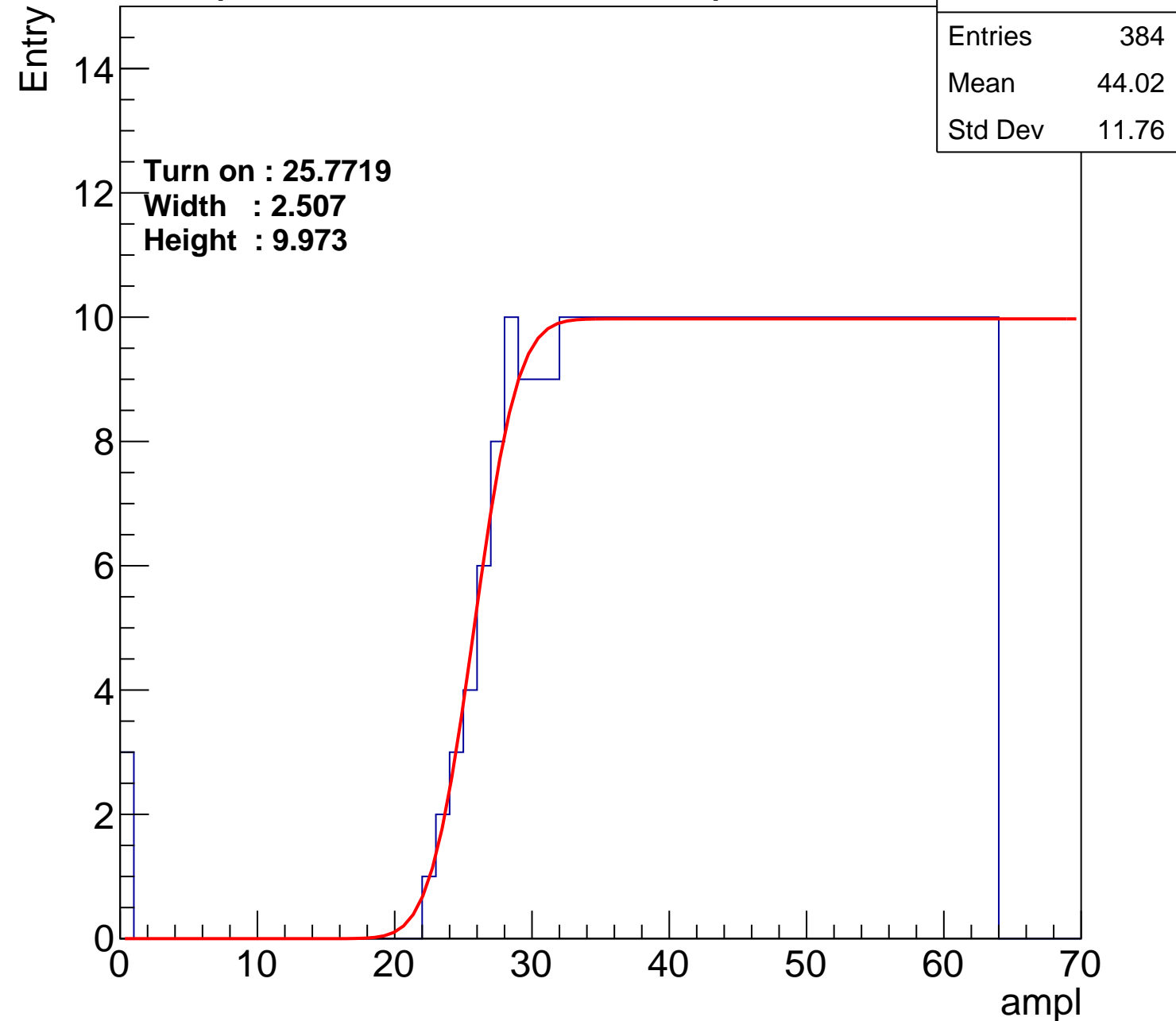
Width : 2.507

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch57

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.84 |
| Std Dev | 11.38 |

Turn on : 27.9880

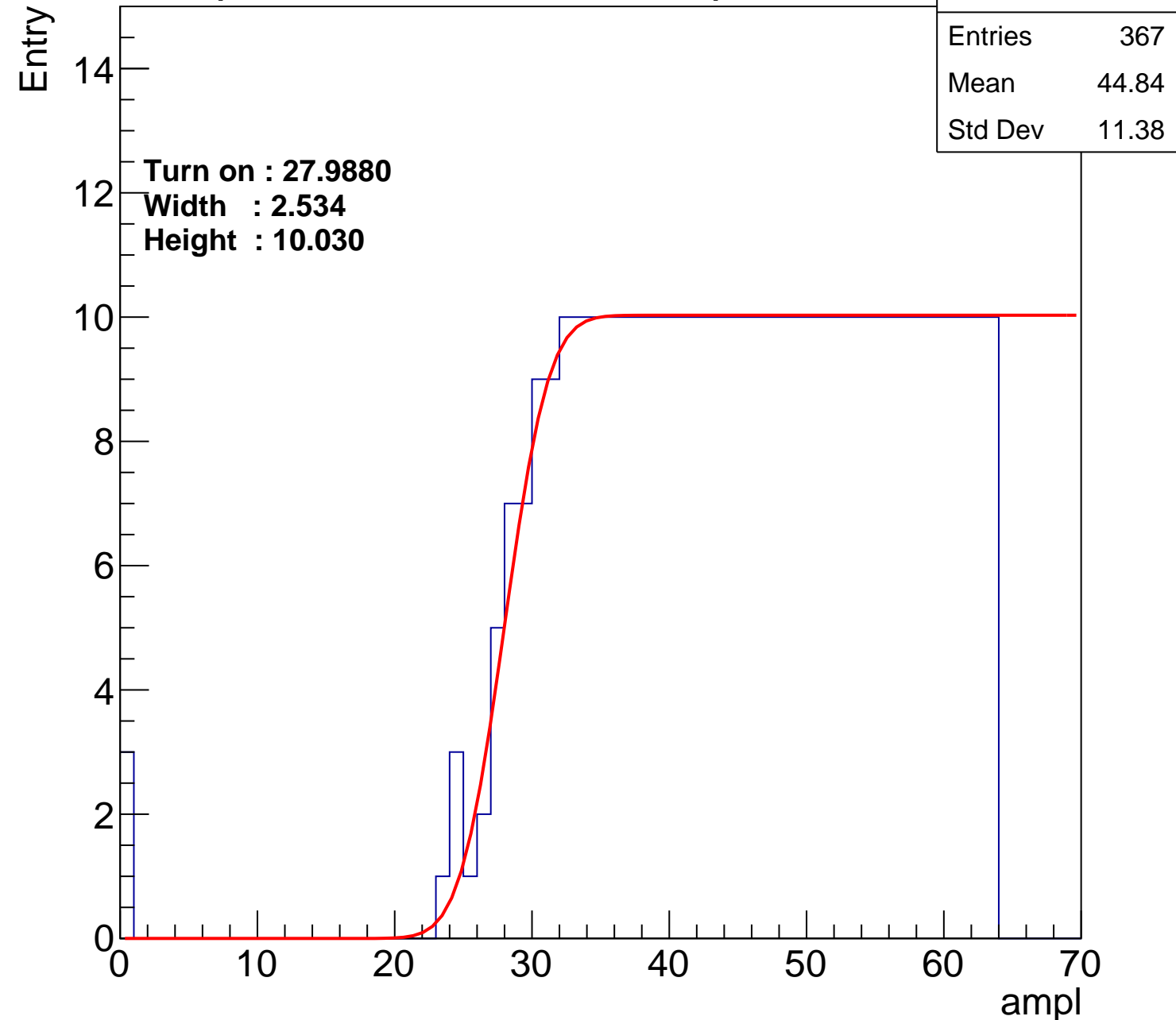
Width : 2.534

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch58

calib_packv5_042523_0143.root, FC#13, port D2

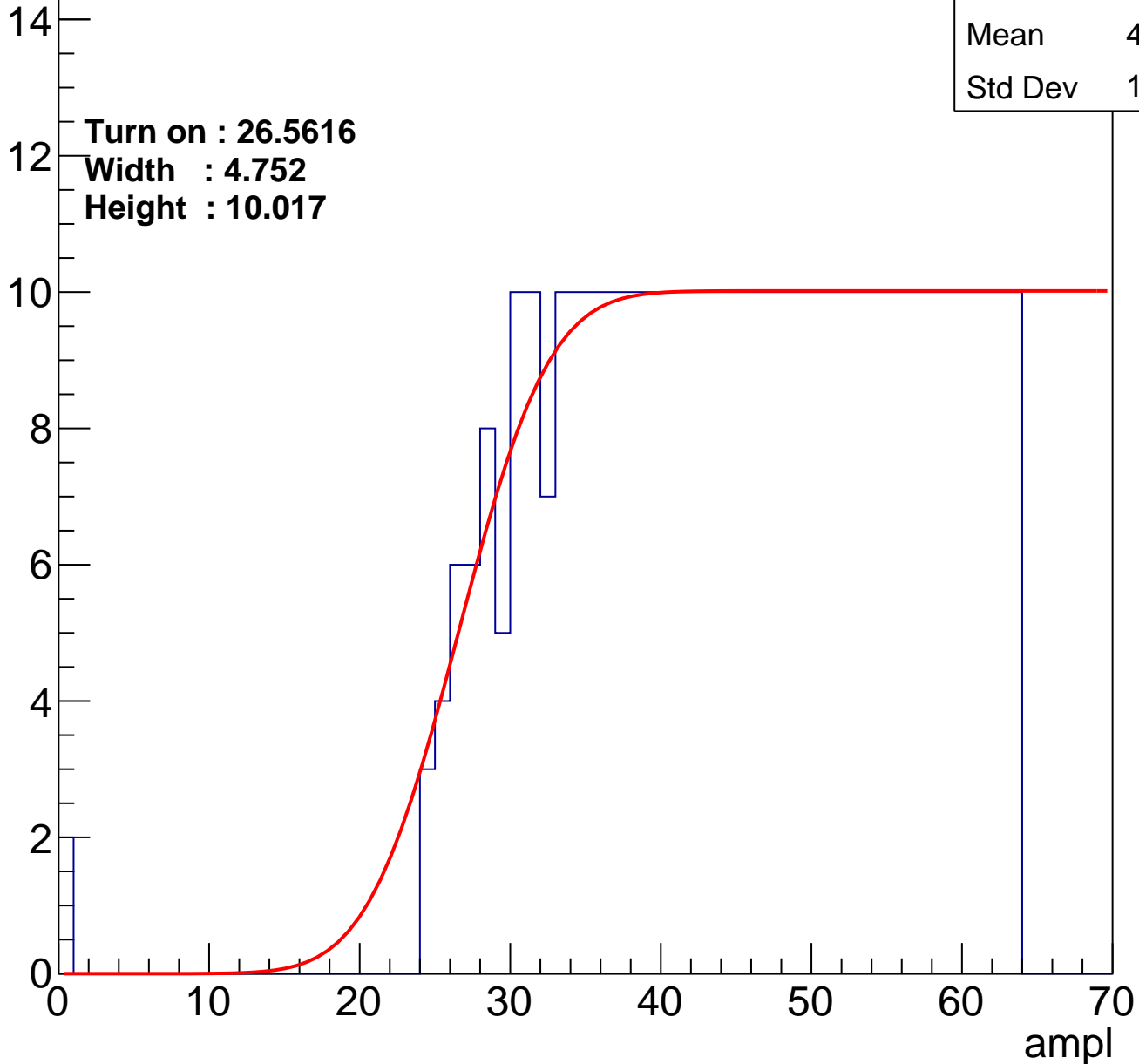
Entry

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.67 |
| Std Dev | 11.33 |

Turn on : 26.5616

Width : 4.752

Height : 10.017



B1L003S, U8-ch59

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 345 |
| Mean | 45.78 |
| Std Dev | 11.16 |

Turn on : 30.2440

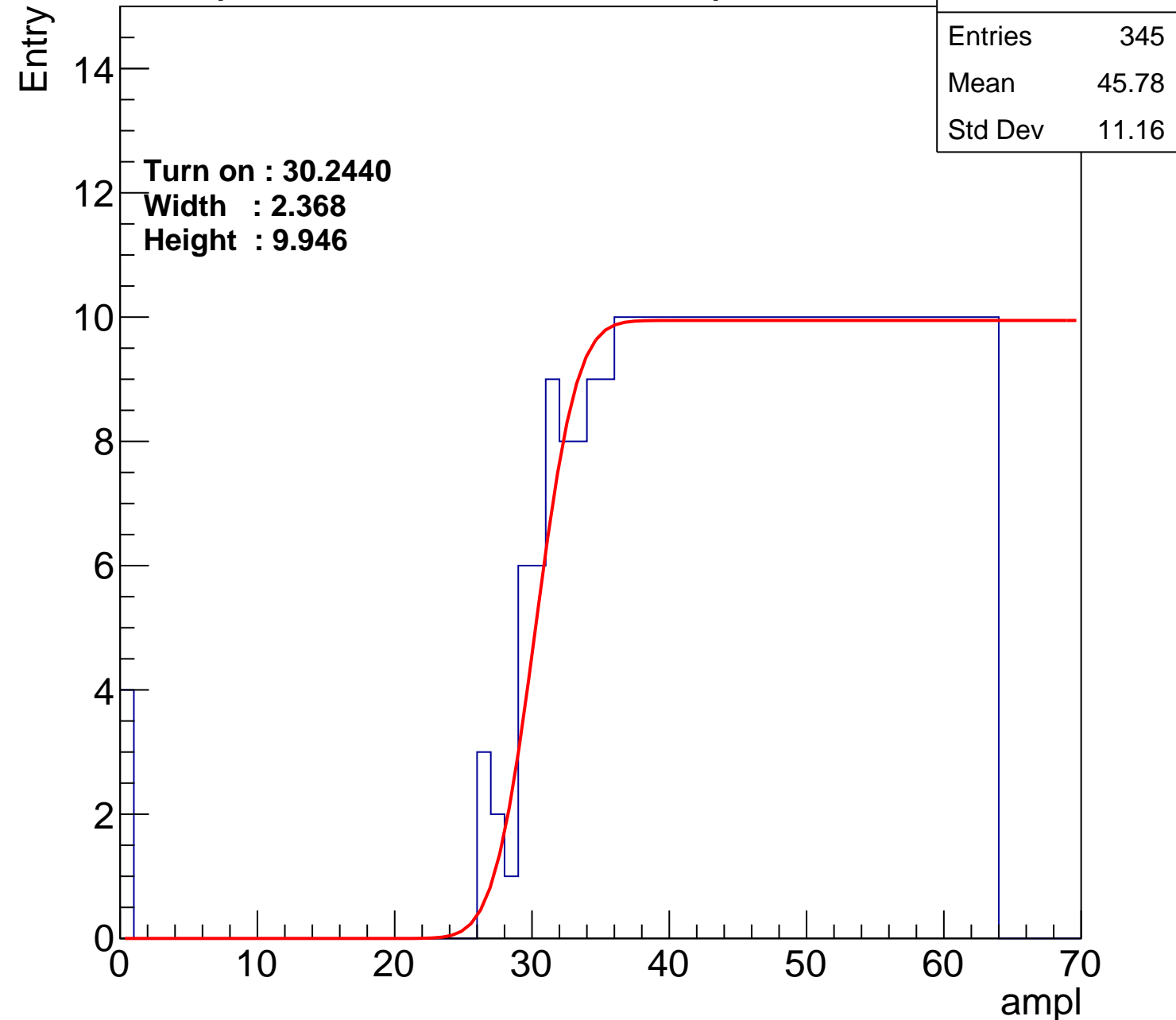
Width : 2.368

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch60

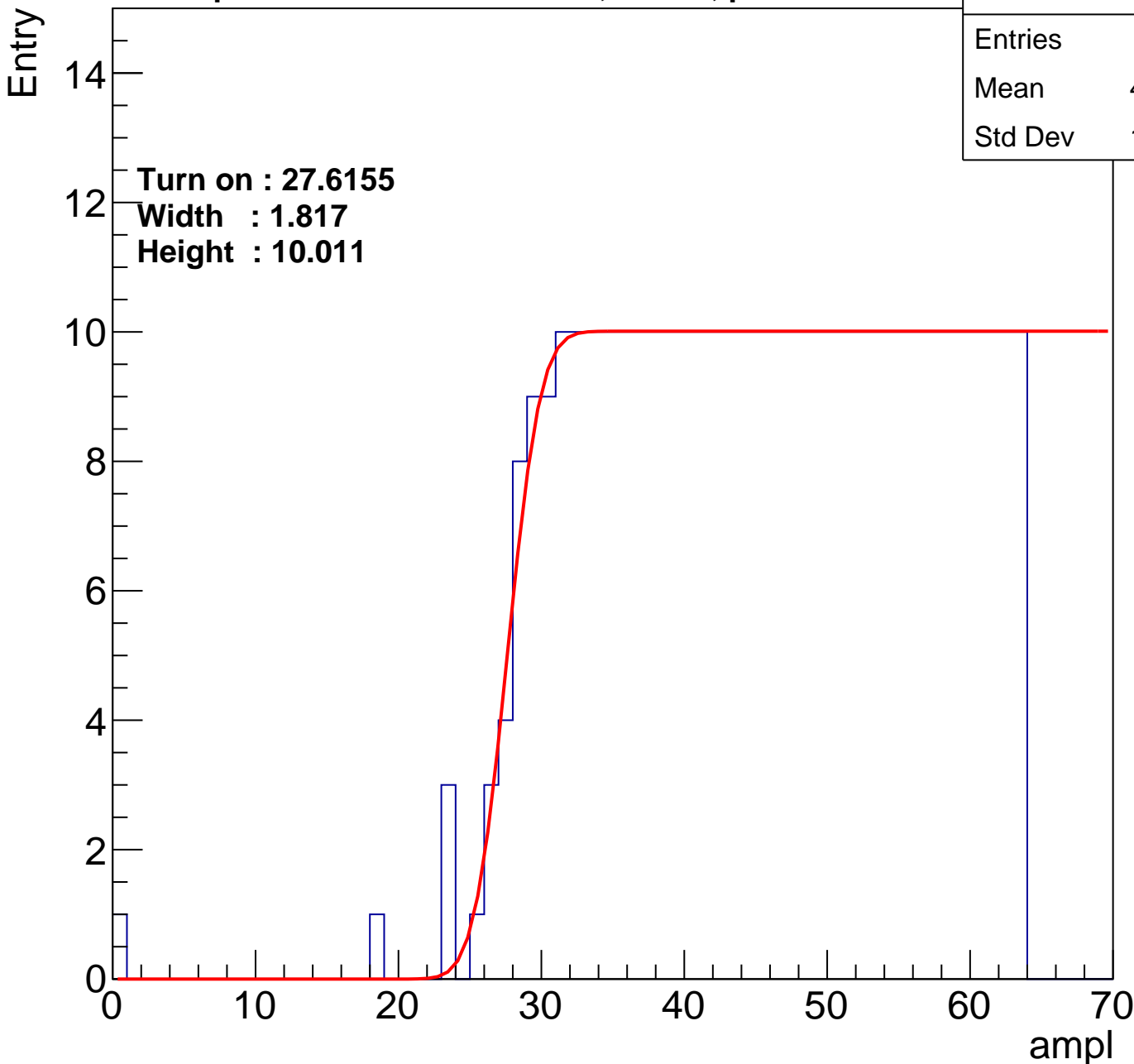
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 27.6155

Width : 1.817

Height : 10.011

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.89 |
| Std Dev | 11.03 |



B1L003S, U8-ch61

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.04 |
| Std Dev | 11.07 |

Turn on : 27.5951

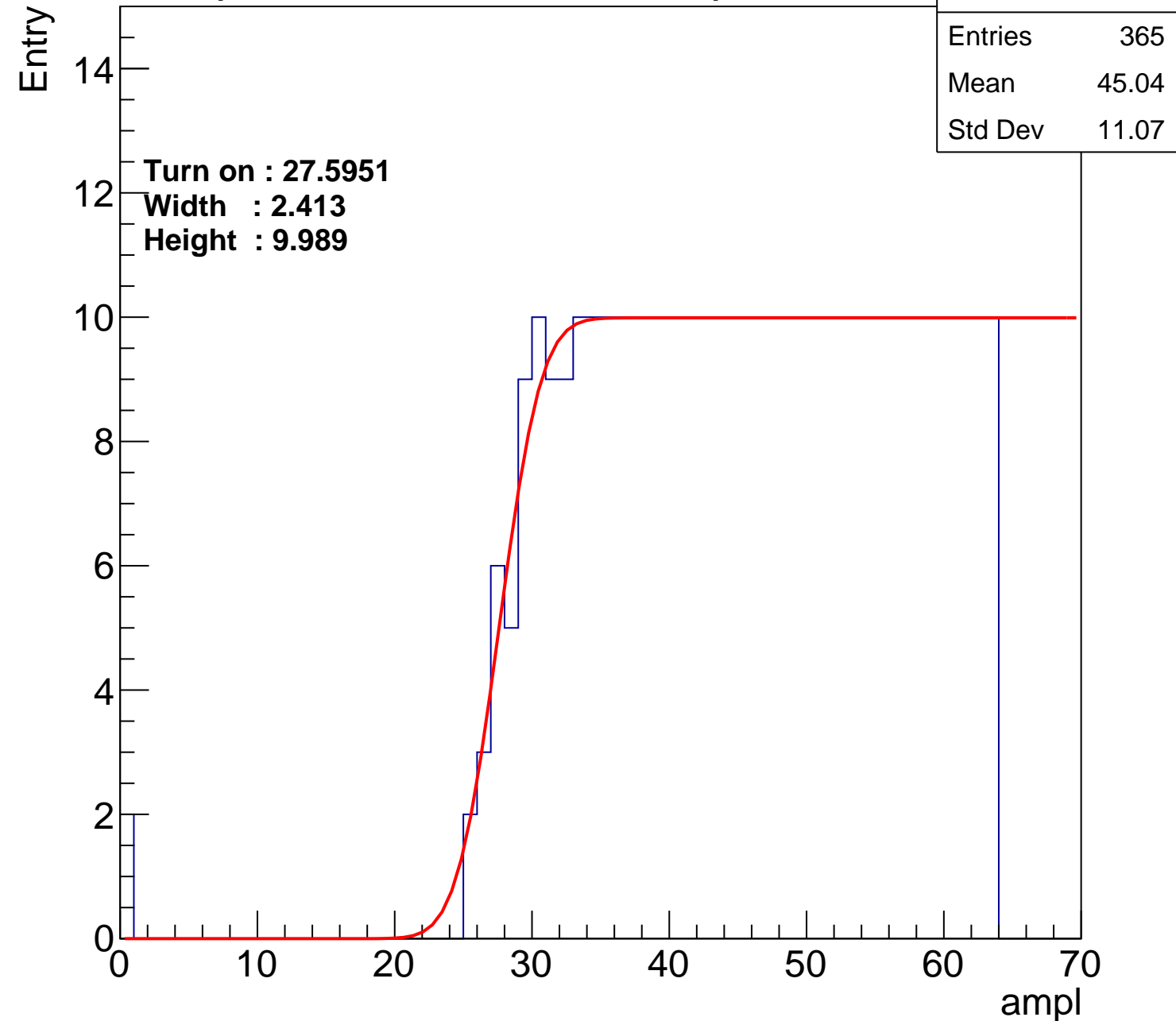
Width : 2.413

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch62

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.98 |
| Std Dev | 11.77 |

Turn on : 26.1617

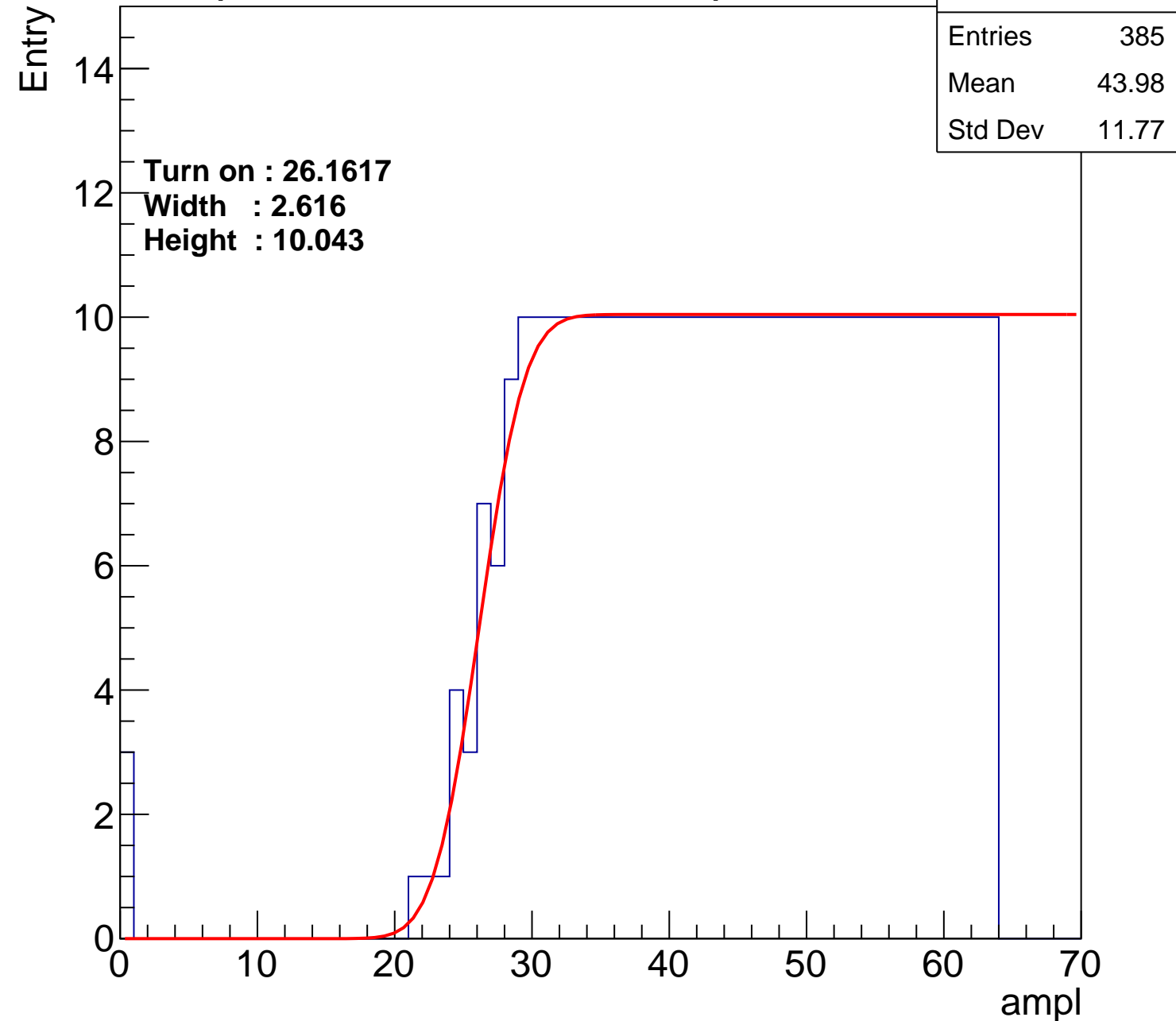
Width : 2.616

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch63

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.36 |
| Std Dev | 11.6 |

Turn on : 26.7419

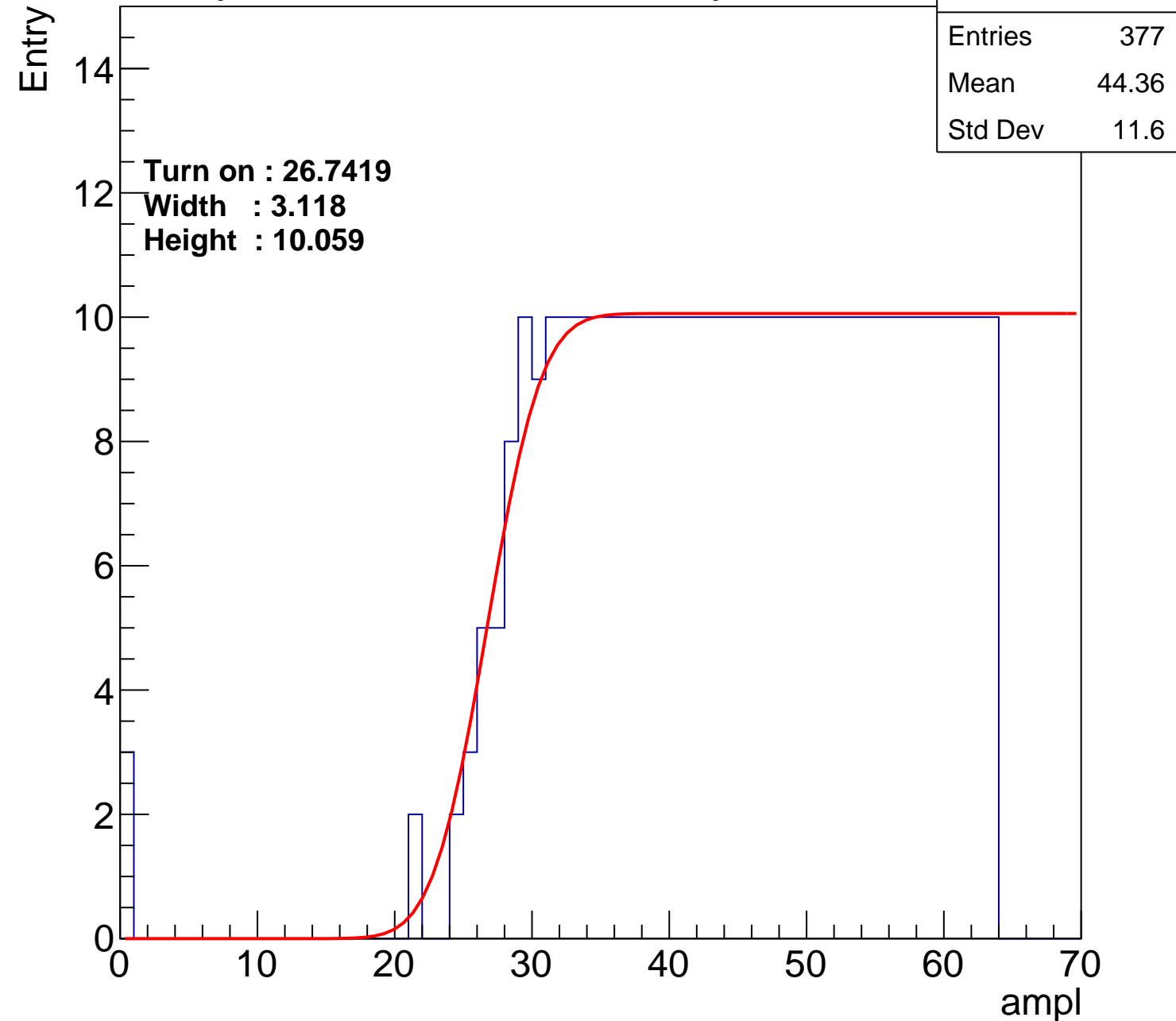
Width : 3.118

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch64

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 353 |
| Mean | 45.5 |
| Std Dev | 11.06 |

Turn on : 28.7994

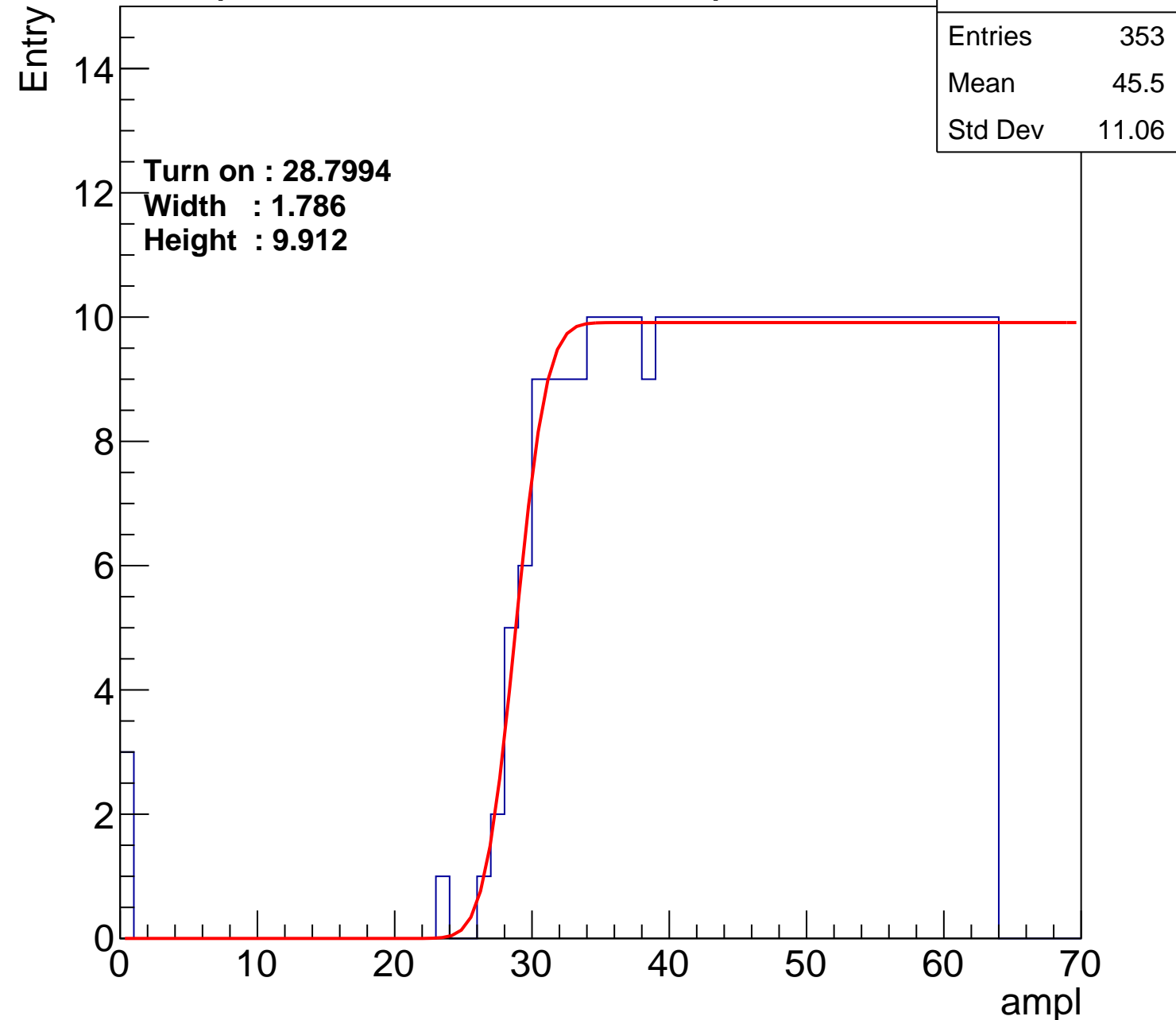
Width : 1.786

Height : 9.912

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch65

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.55 |
| Std Dev | 11.51 |

Turn on : 27.2550

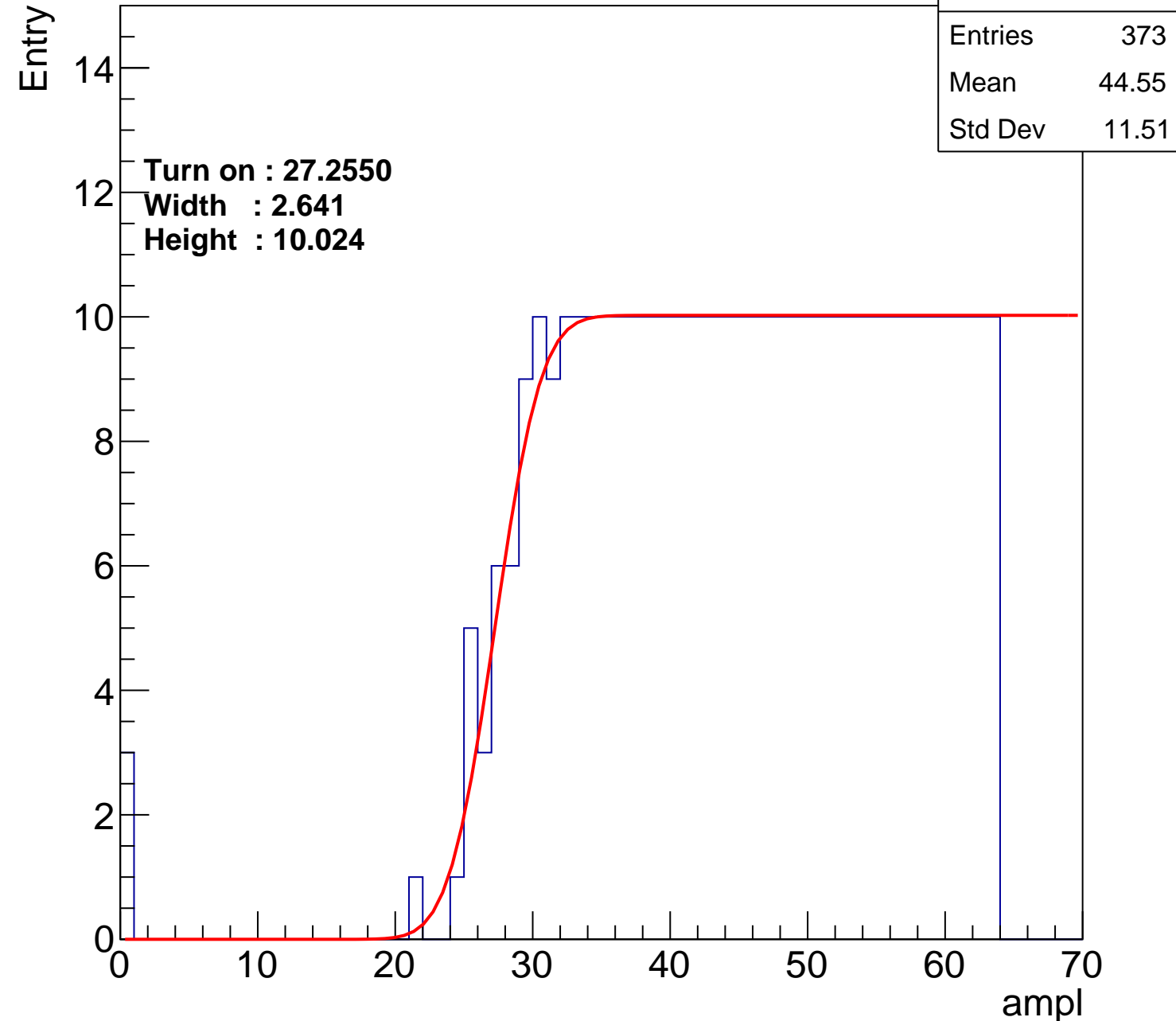
Width : 2.641

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch66

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.13 |
| Std Dev | 10.88 |

Turn on : 27.6079

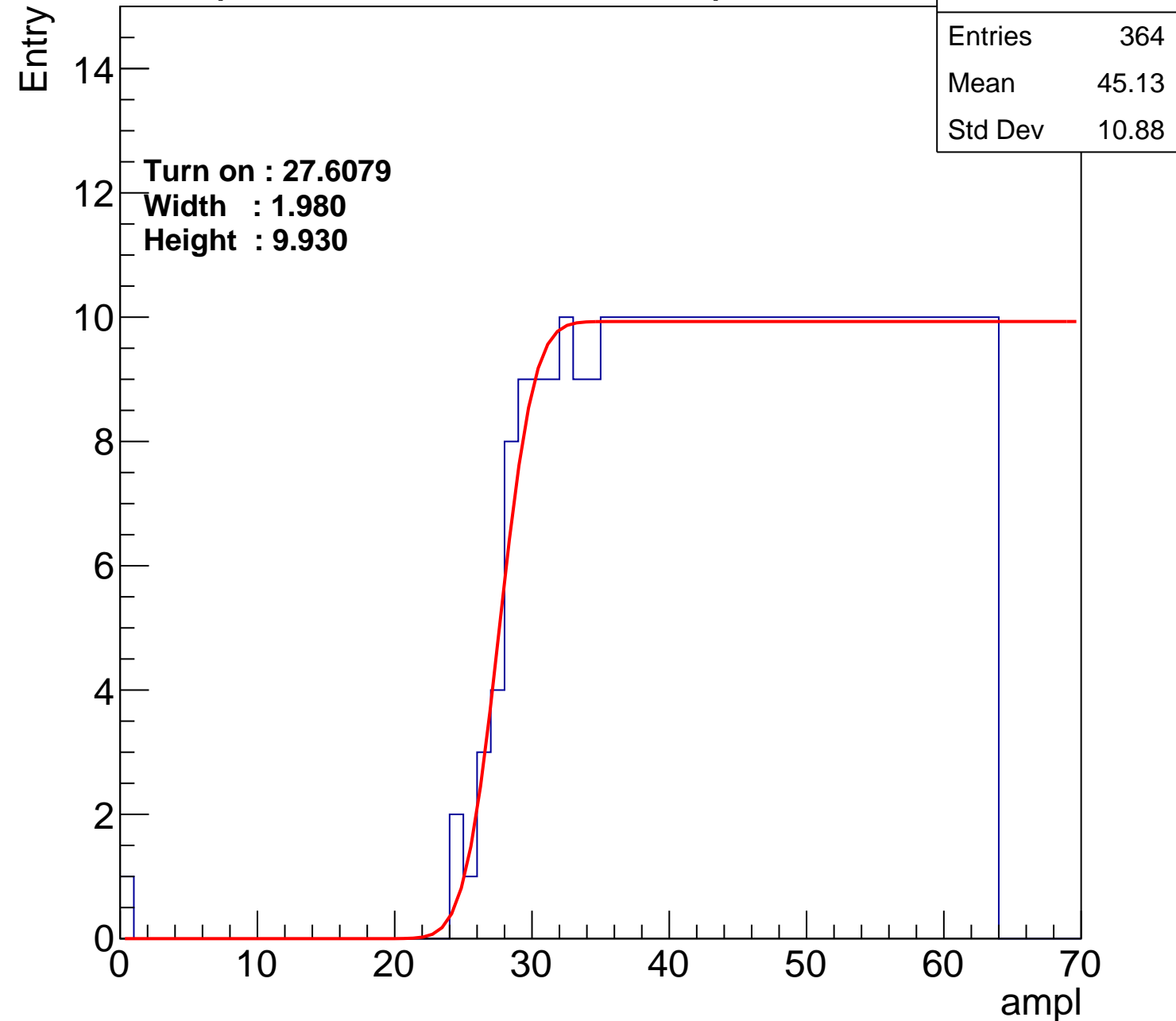
Width : 1.980

Height : 9.930

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch67

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.75 |
| Std Dev | 11.58 |

Turn on : 27.2892

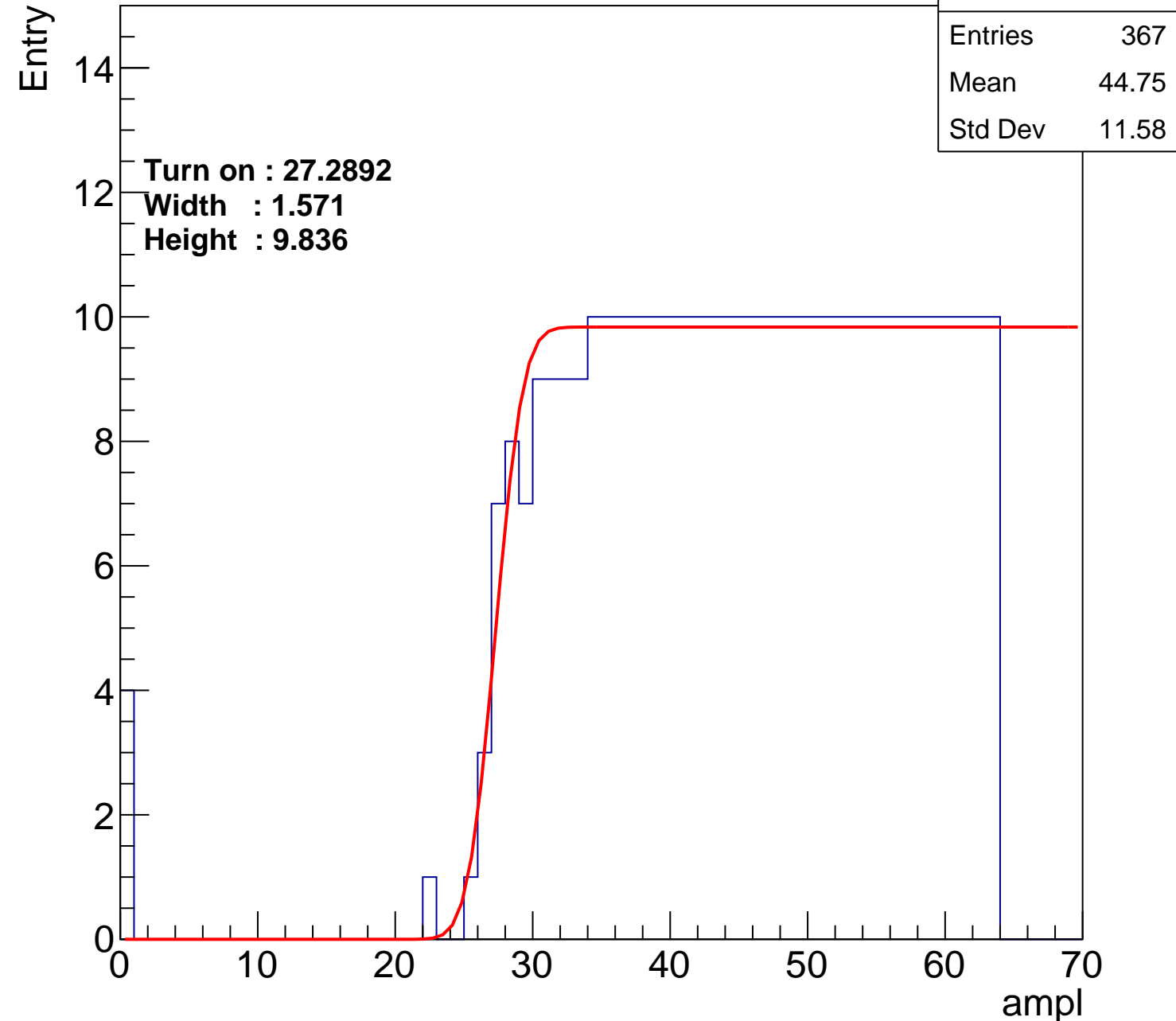
Width : 1.571

Height : 9.836

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch68

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.5 |
| Std Dev | 11.67 |

Turn on : 27.9023

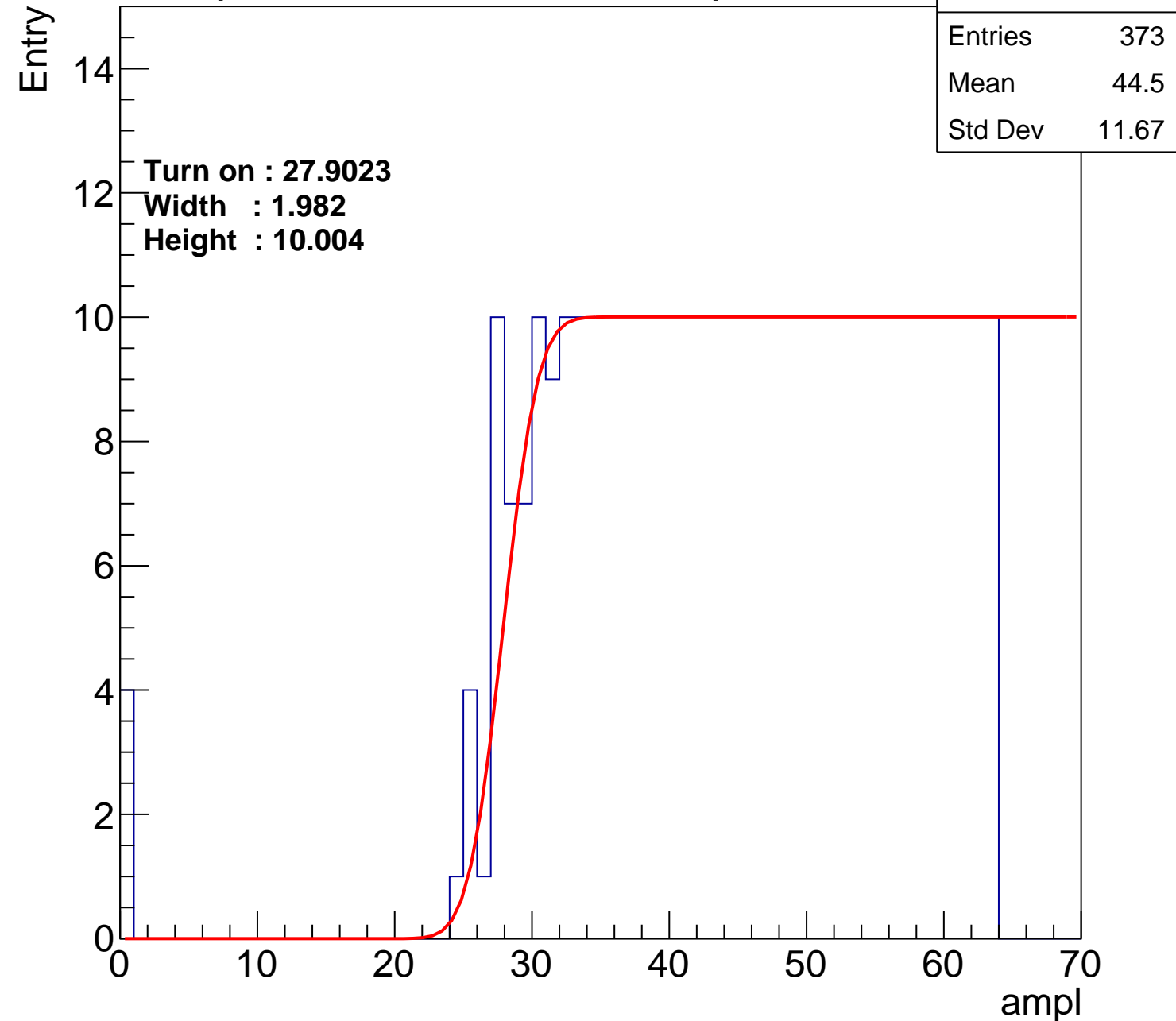
Width : 1.982

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch69

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.51 |
| Std Dev | 11.34 |

Turn on : 27.0514

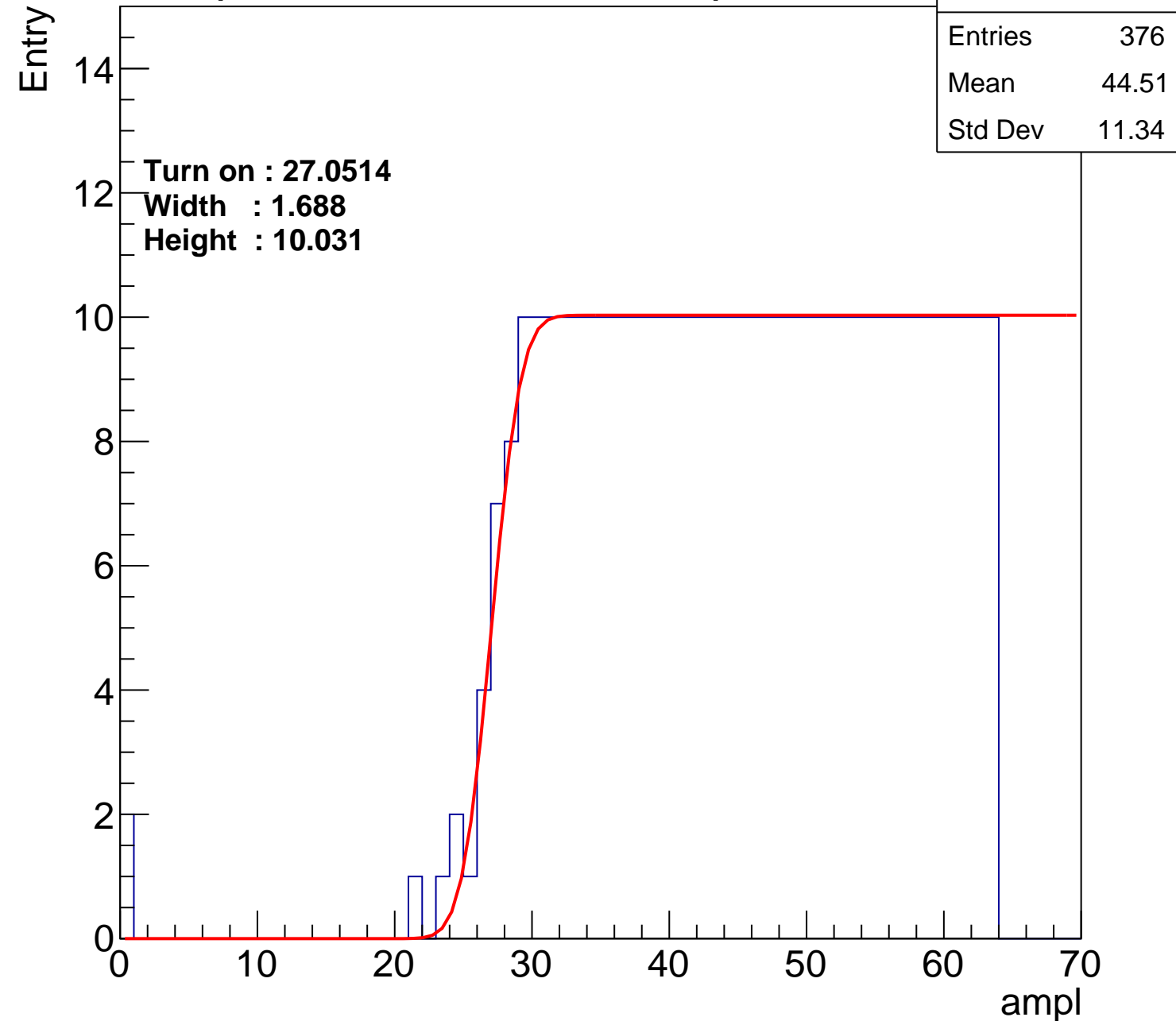
Width : 1.688

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch70

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.17 |
| Std Dev | 11.73 |

Turn on : 26.6944

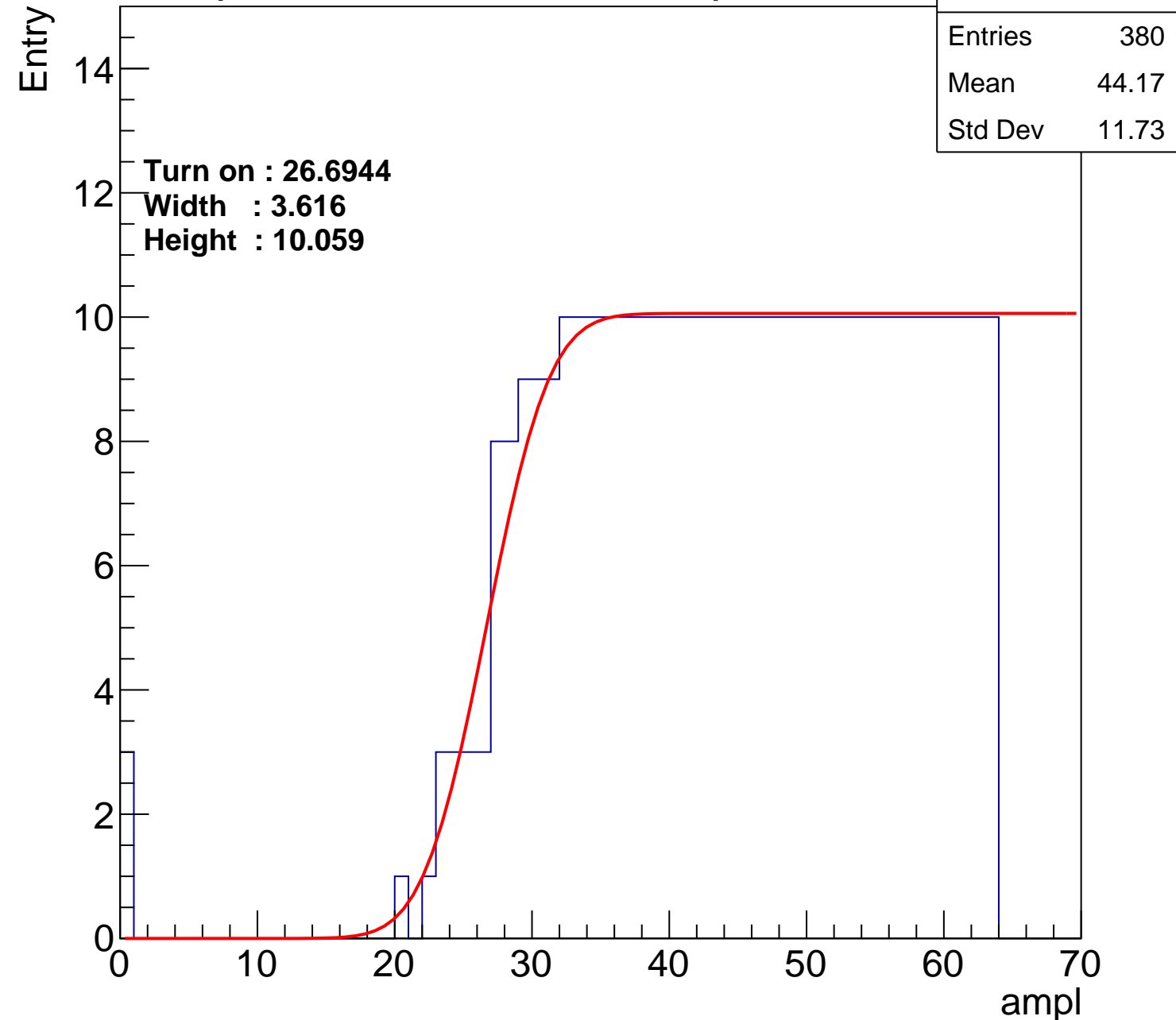
Width : 3.616

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch71

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 347 |
| Mean | 45.9 |
| Std Dev | 10.65 |

Turn on : 29.6203

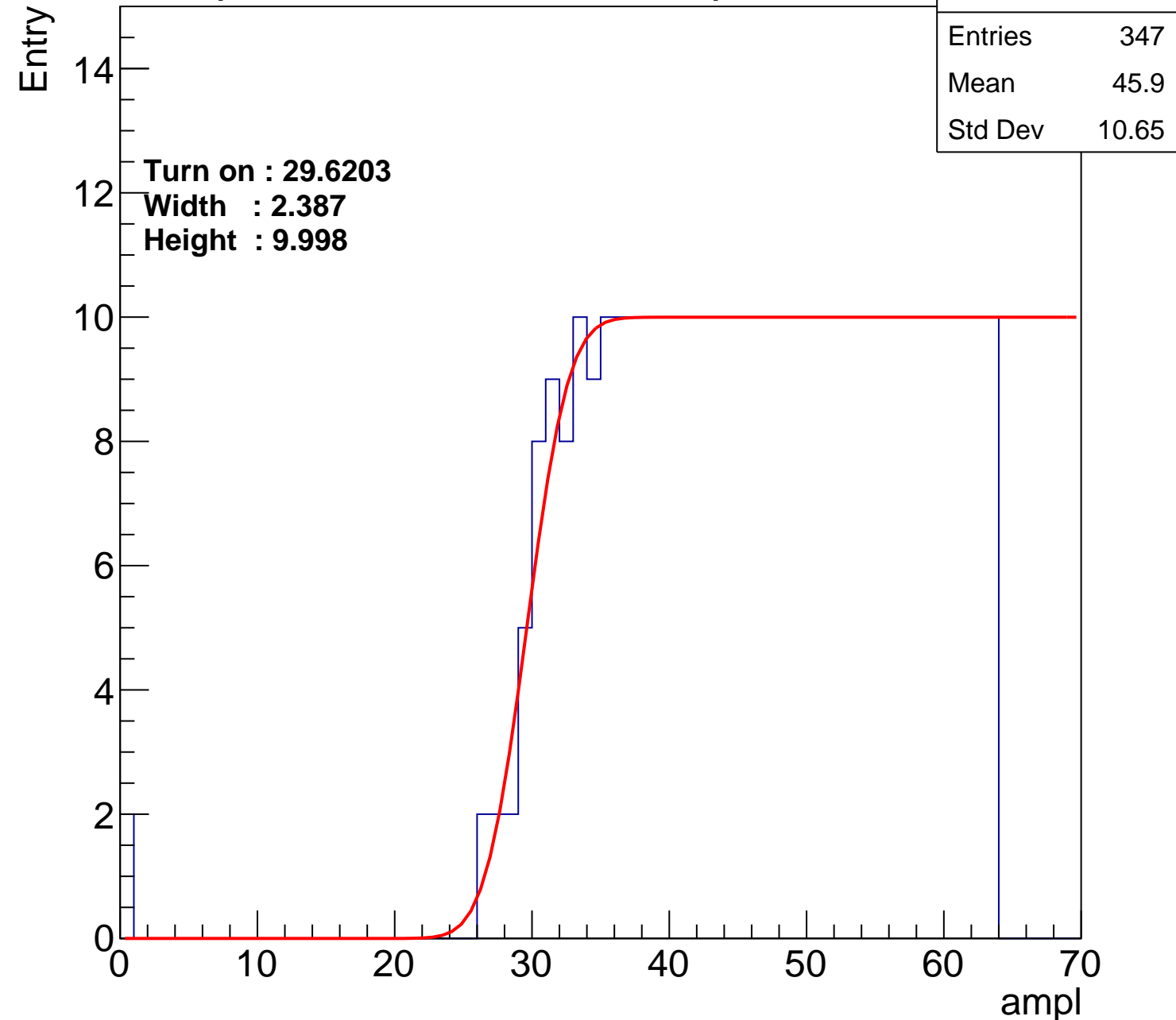
Width : 2.387

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch72

calib_packv5_042523_0143.root, FC#13, port D2

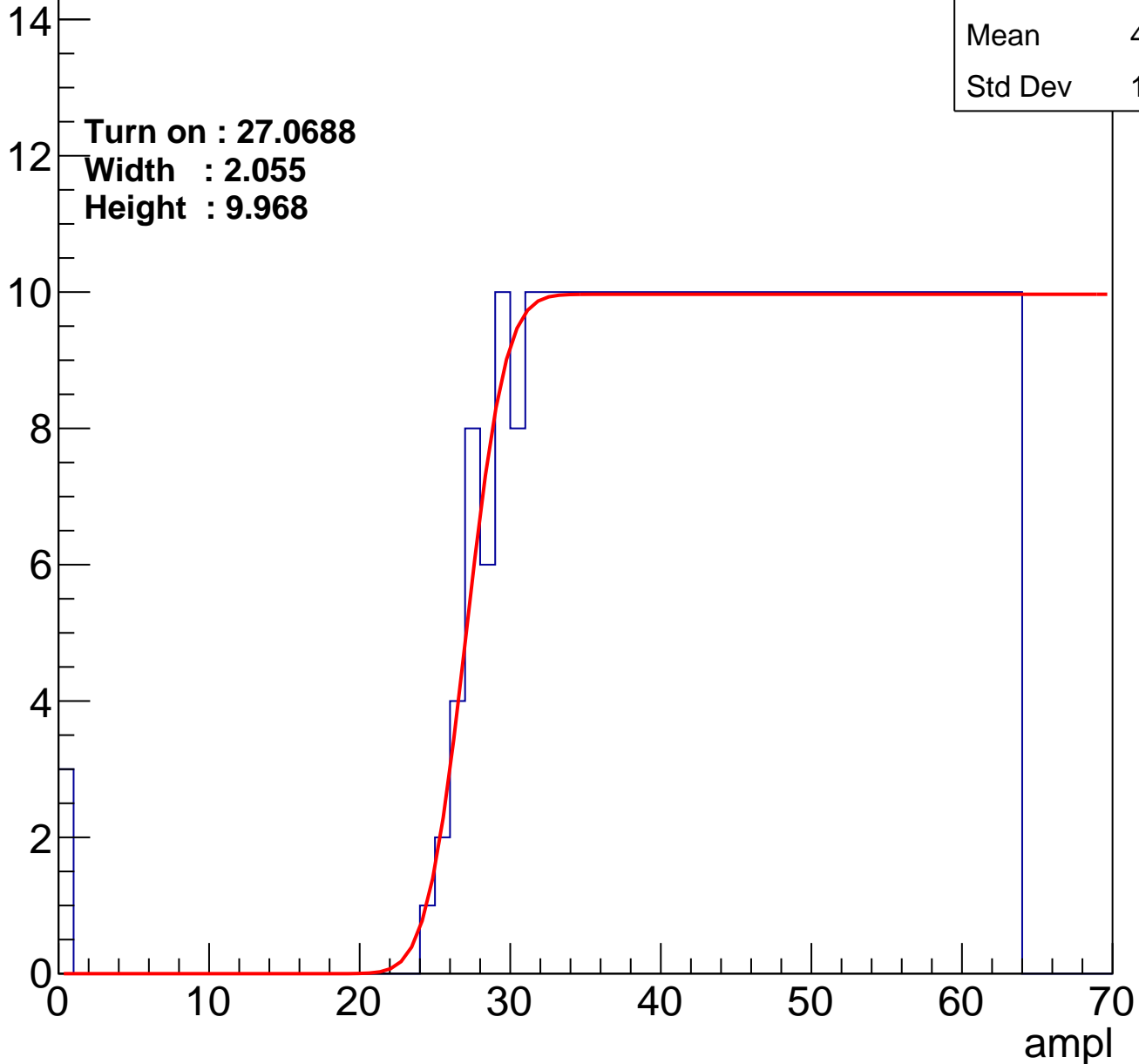
| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.63 |
| Std Dev | 11.43 |

Turn on : 27.0688

Width : 2.055

Height : 9.968

Entry



B1L003S, U8-ch73

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.13 |
| Std Dev | 11.03 |

Turn on : 27.7546

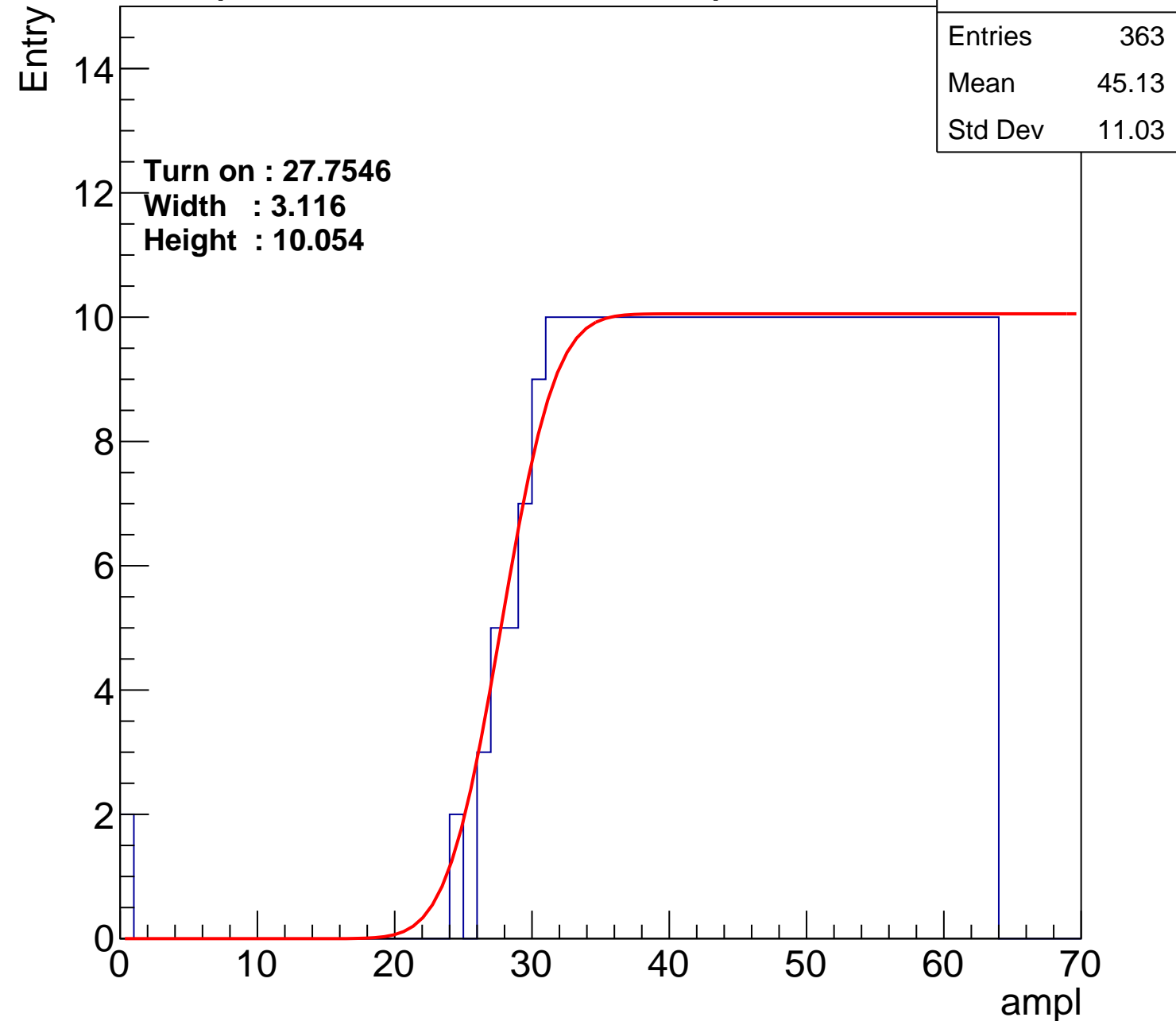
Width : 3.116

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch74

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 44.72 |
| Std Dev | 11.96 |

Turn on : 28.4125

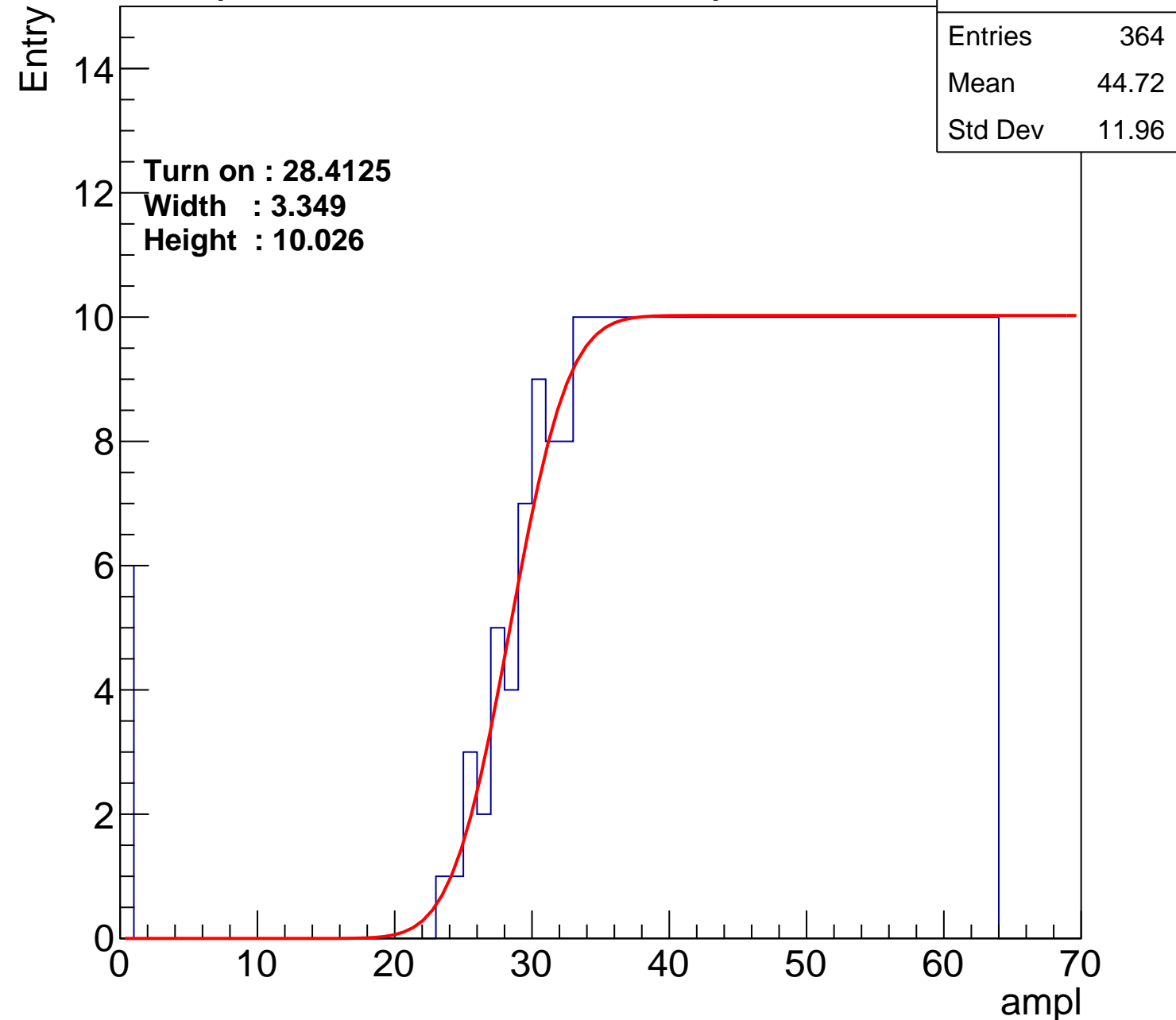
Width : 3.349

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch75

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.23 |
| Std Dev | 12.25 |

Turn on : 27.3350

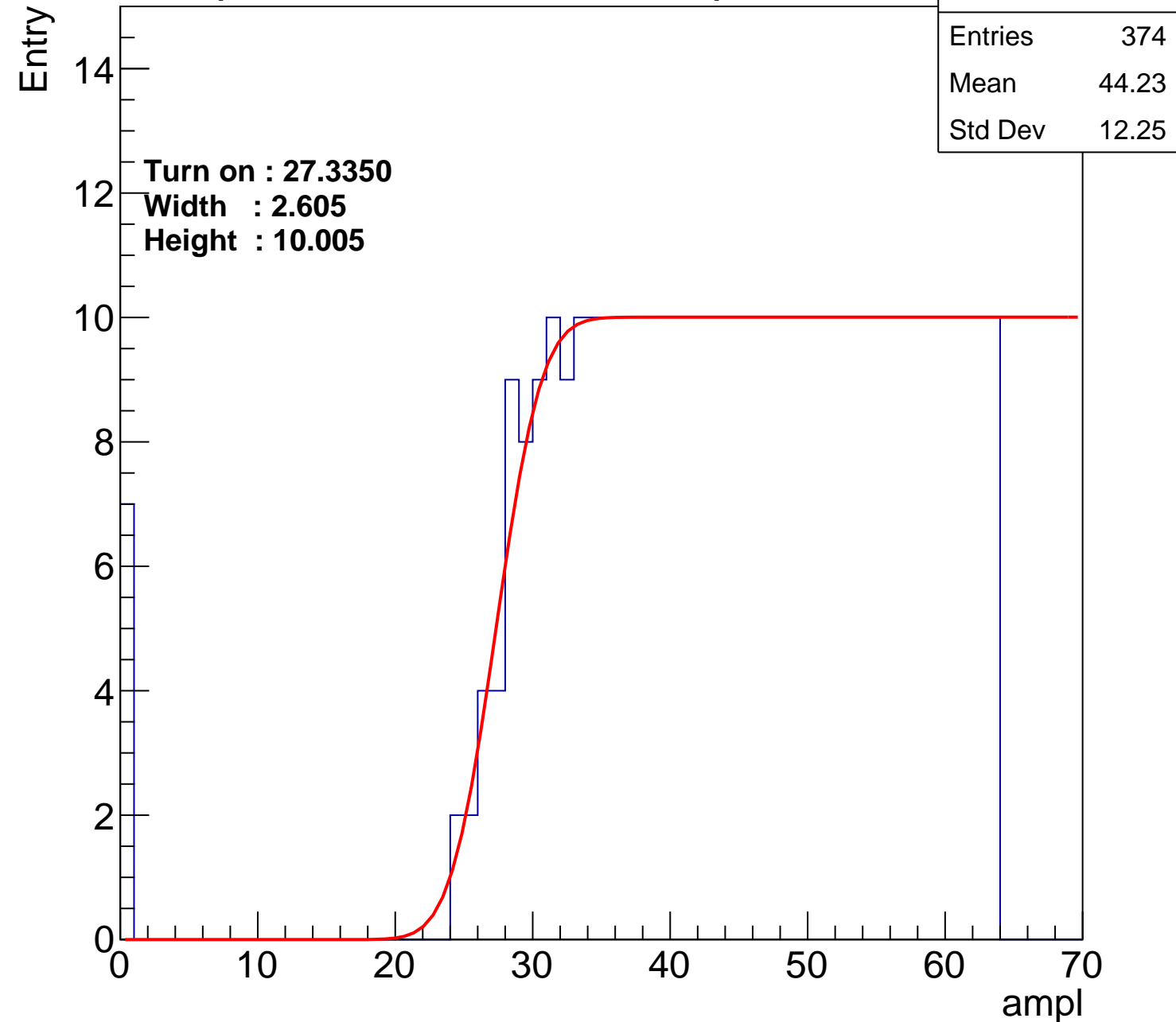
Width : 2.605

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch76

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.9 |
| Std Dev | 11.32 |

Turn on : 27.9001

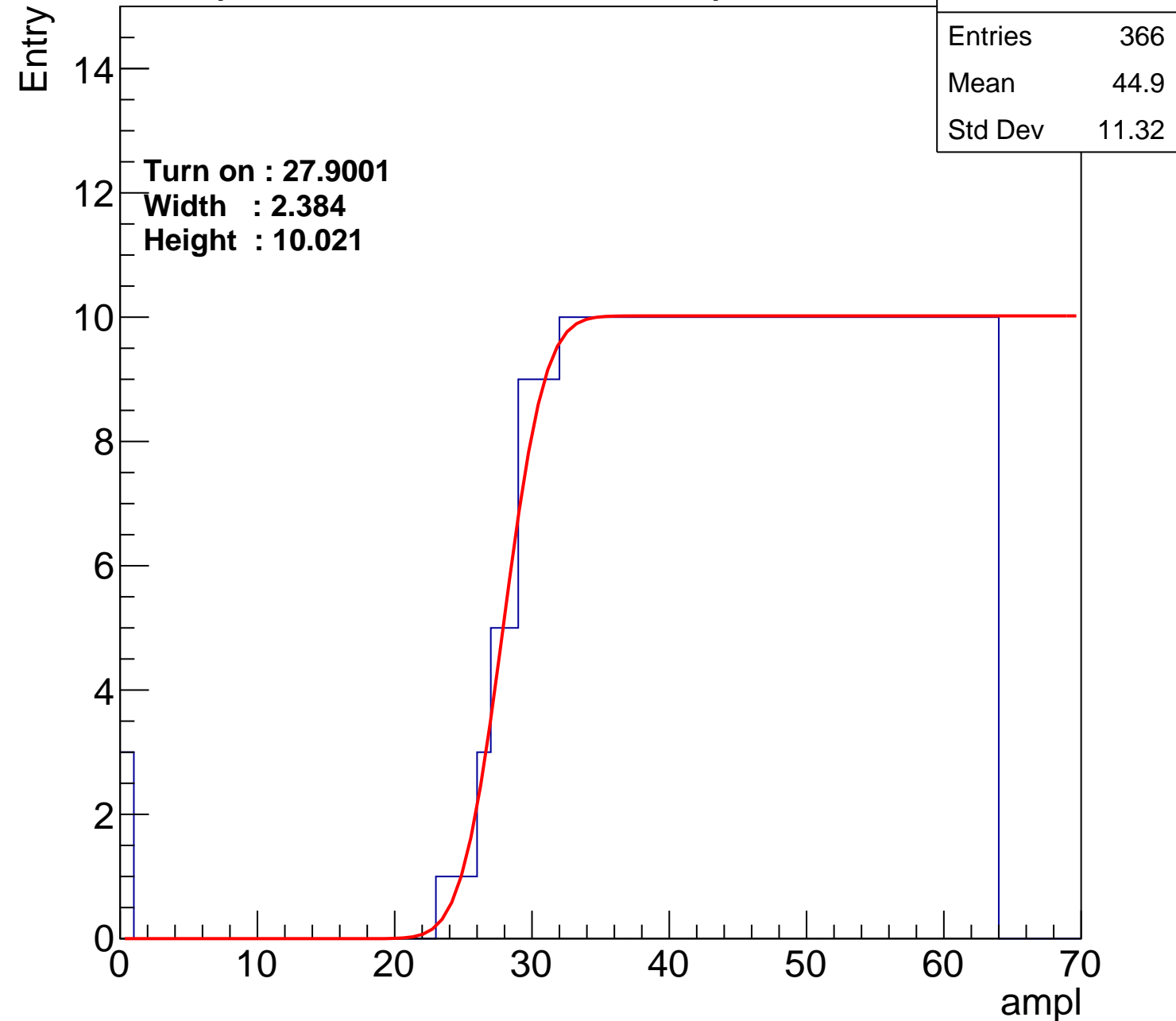
Width : 2.384

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch77

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.09 |
| Std Dev | 11.45 |

Turn on : 25.9033

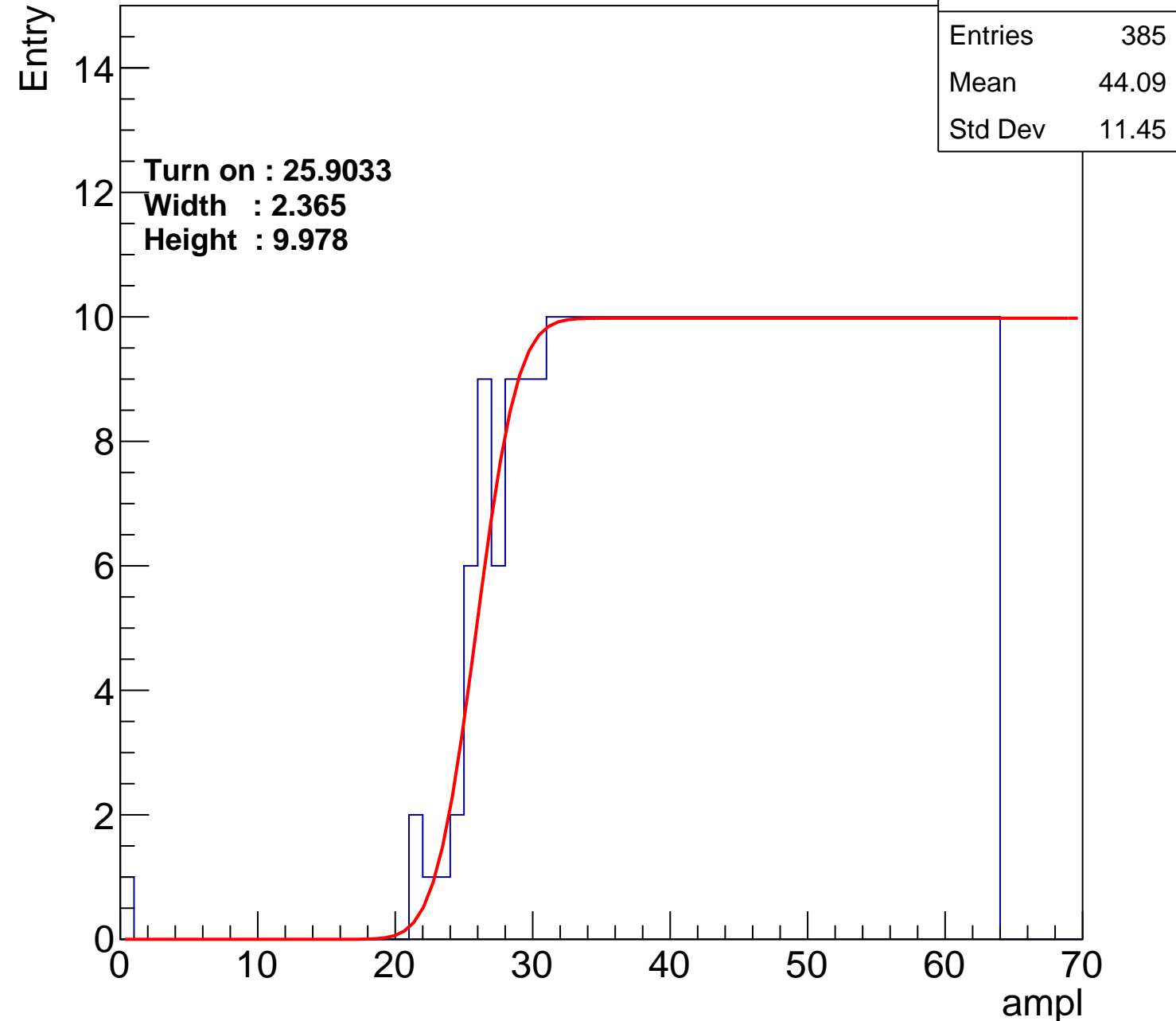
Width : 2.365

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch78

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.34 |
| Std Dev | 11.62 |

Turn on : 26.5138

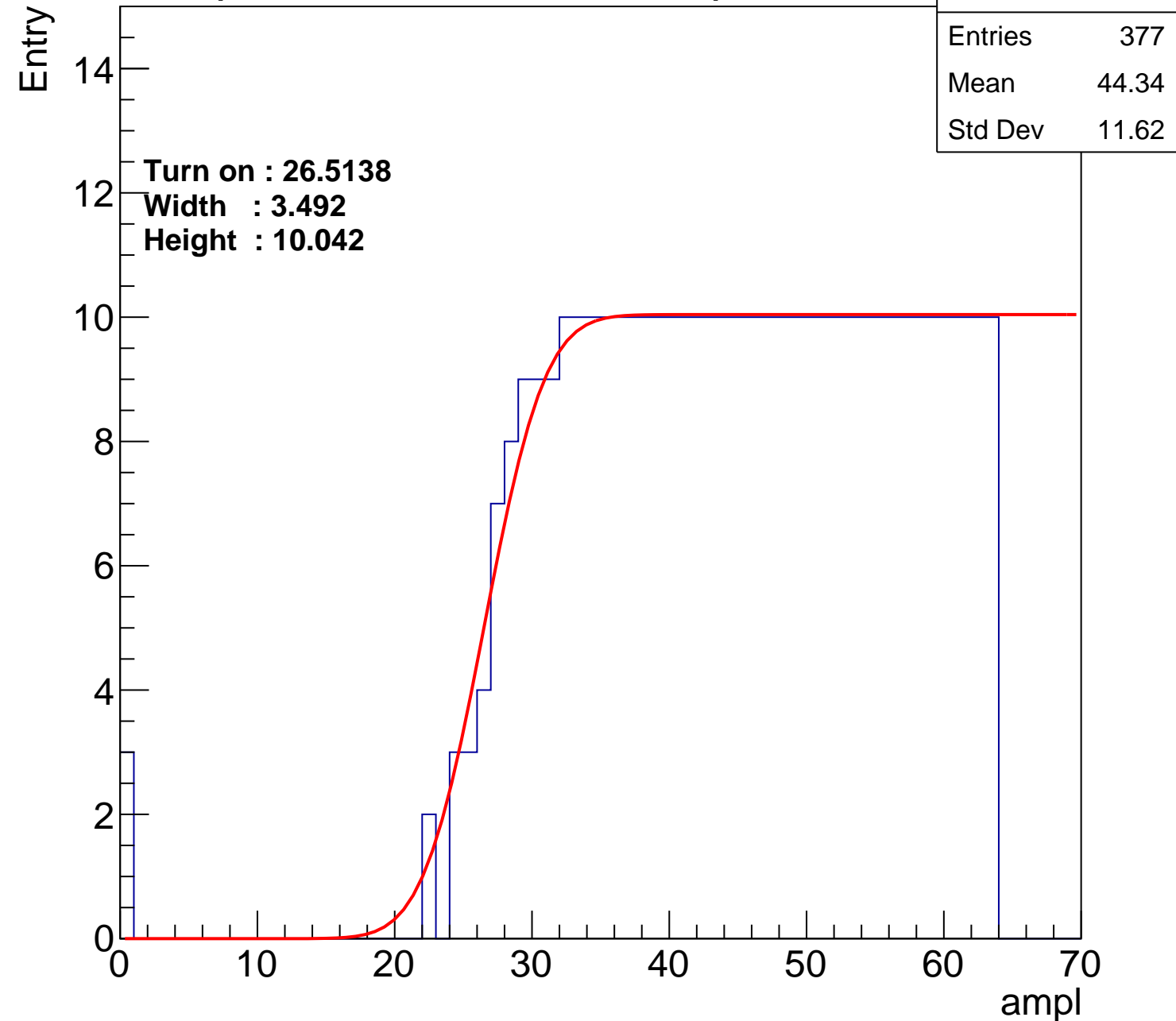
Width : 3.492

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch79

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.24 |
| Std Dev | 11.94 |

Turn on : 27.0497

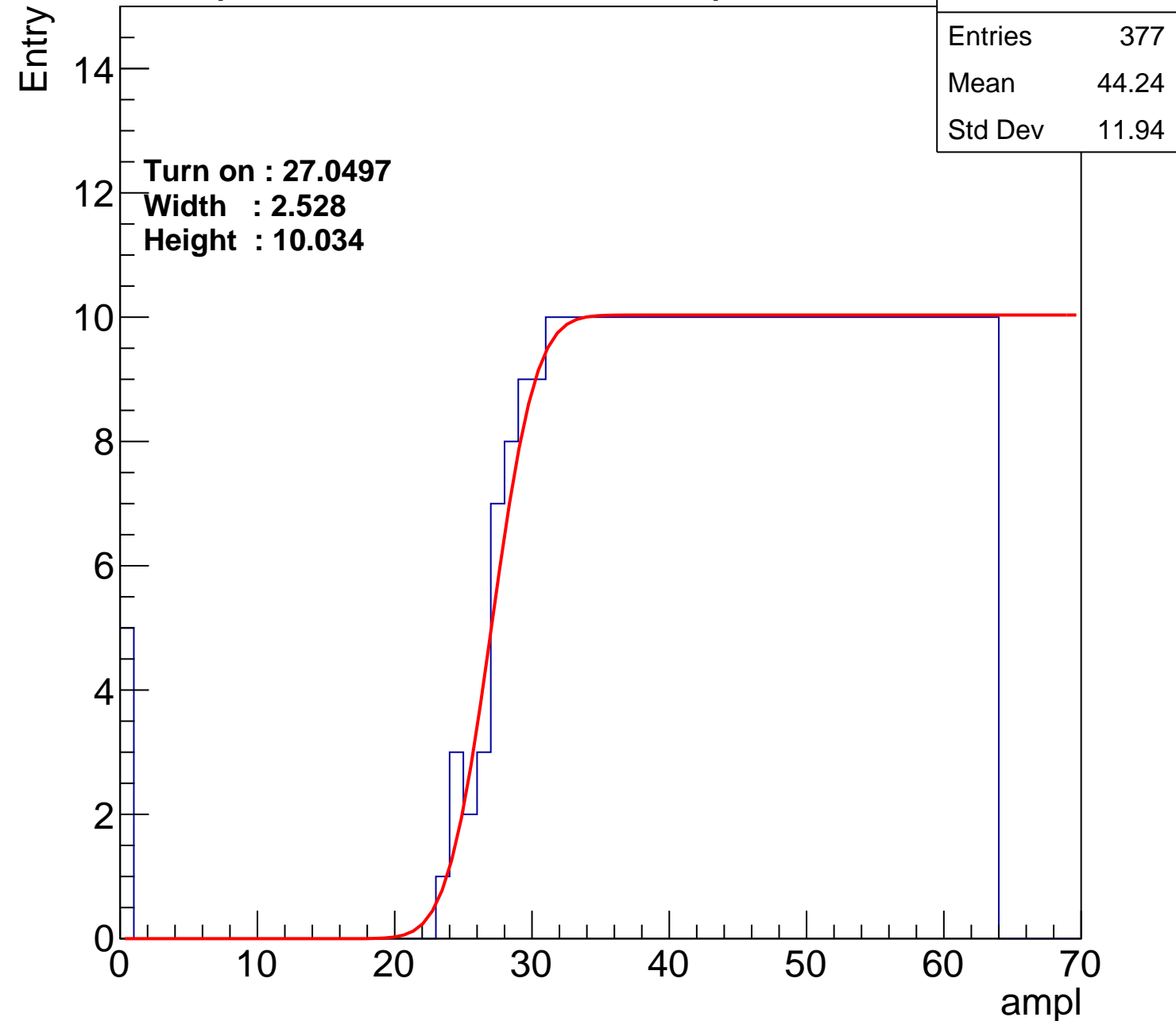
Width : 2.528

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch80

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.07 |
| Std Dev | 11.27 |

Turn on : 28.4081

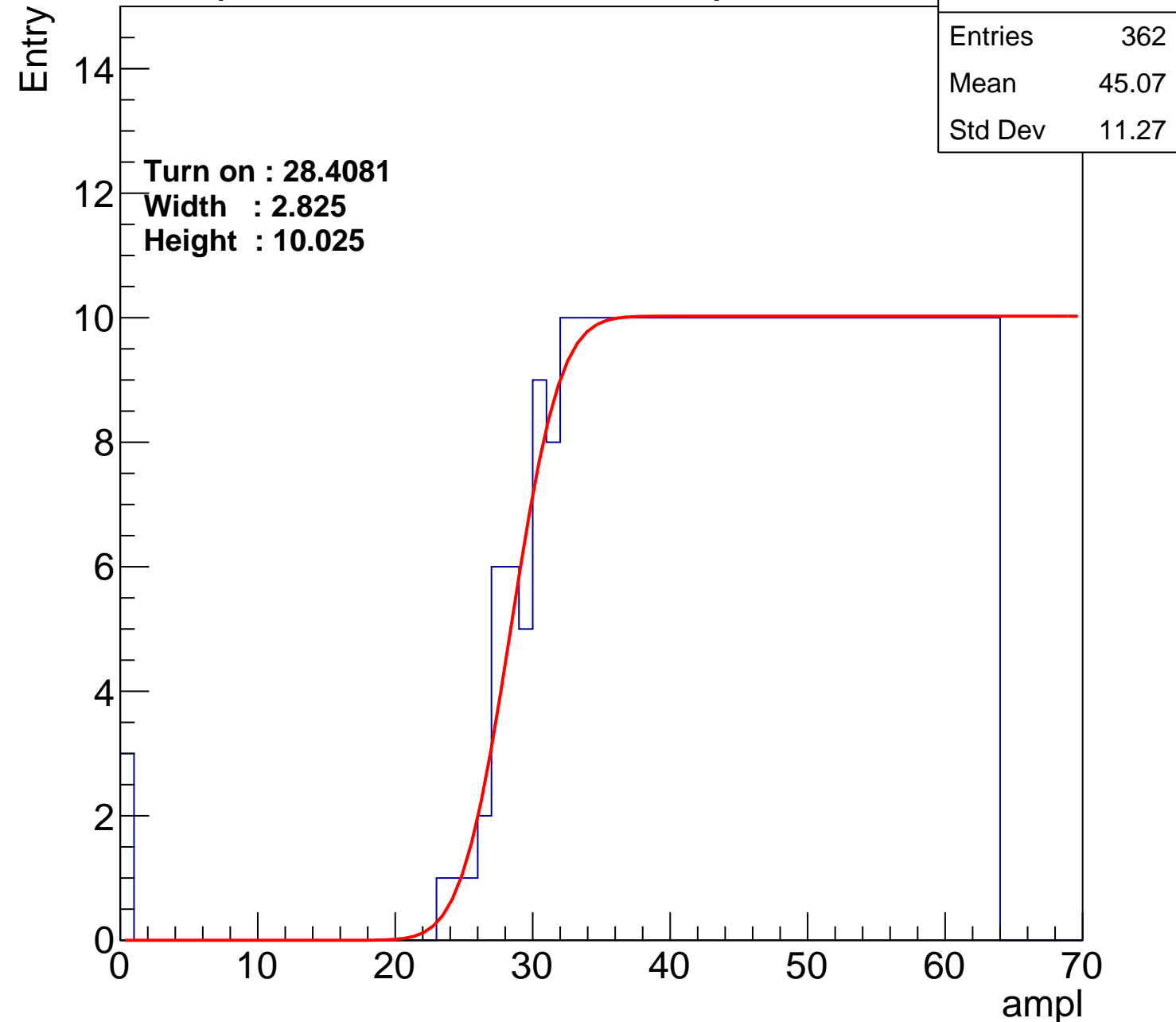
Width : 2.825

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch81

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.15 |
| Std Dev | 11.06 |

Turn on : 28.7091

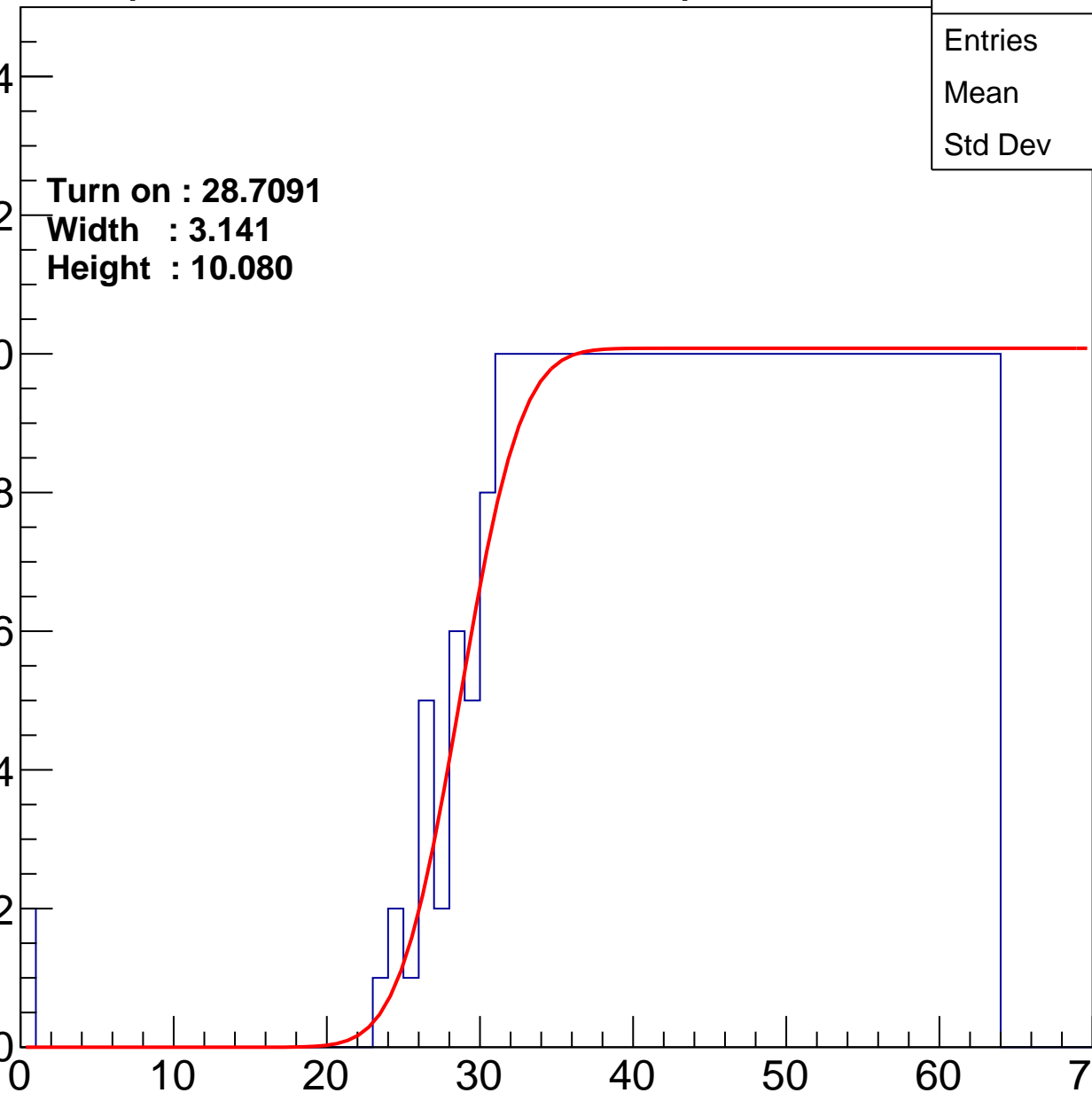
Width : 3.141

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch82

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.16 |
| Std Dev | 11.7 |

Turn on : 26.4663

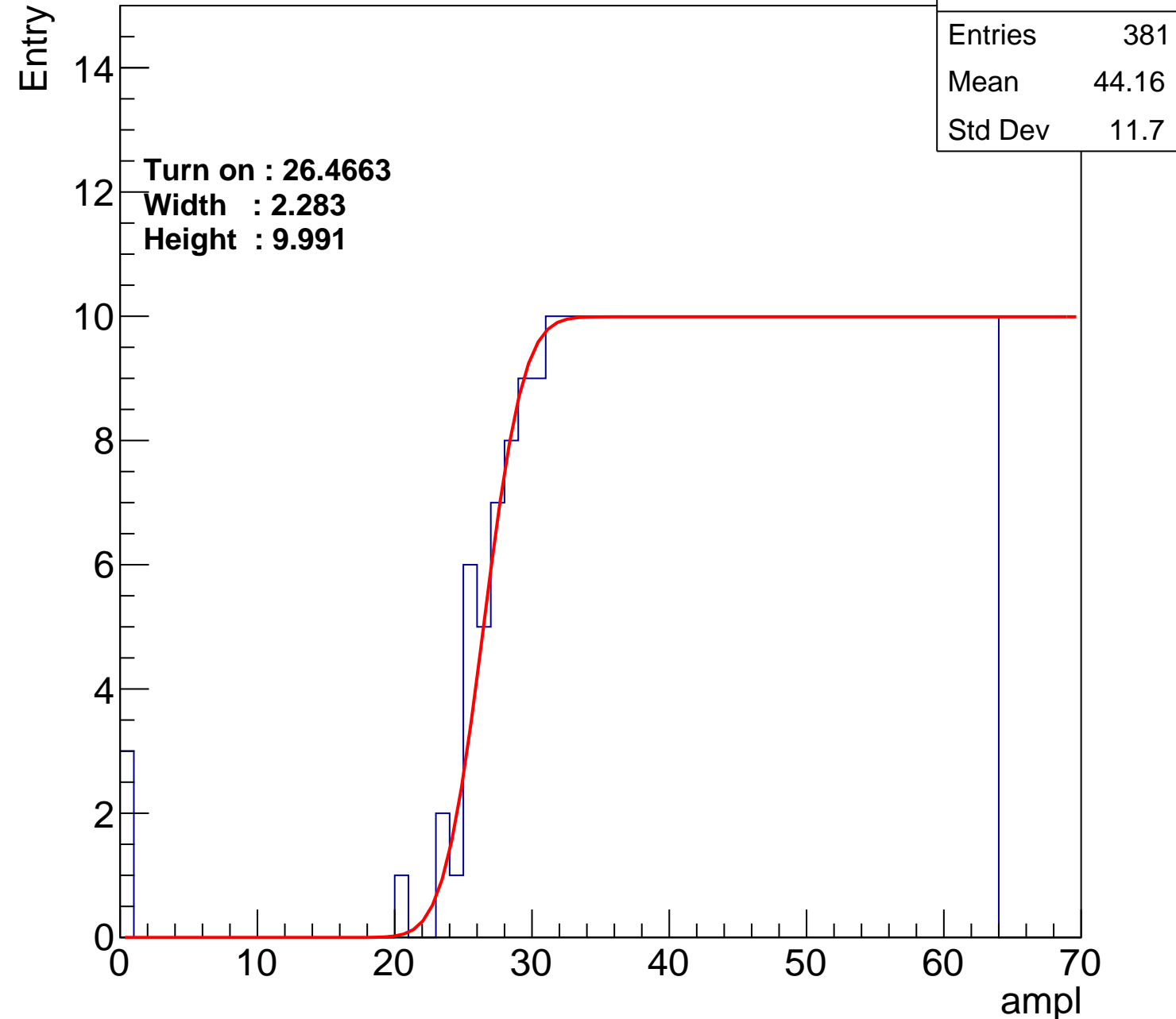
Width : 2.283

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch83

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 44.98 |
| Std Dev | 11.31 |

Turn on : 28.0480

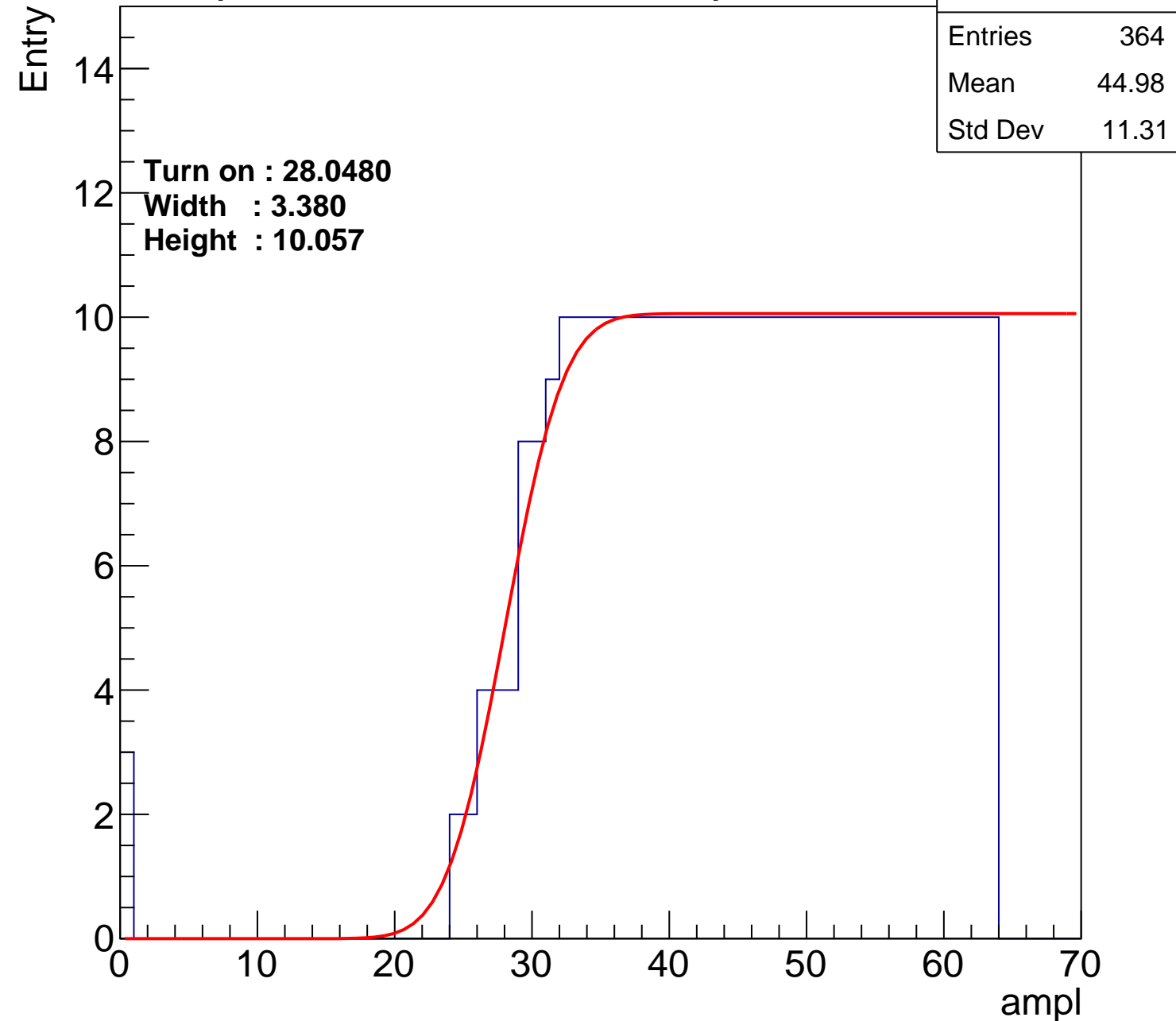
Width : 3.380

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch84

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.04 |
| Std Dev | 11.53 |

Turn on : 25.7715

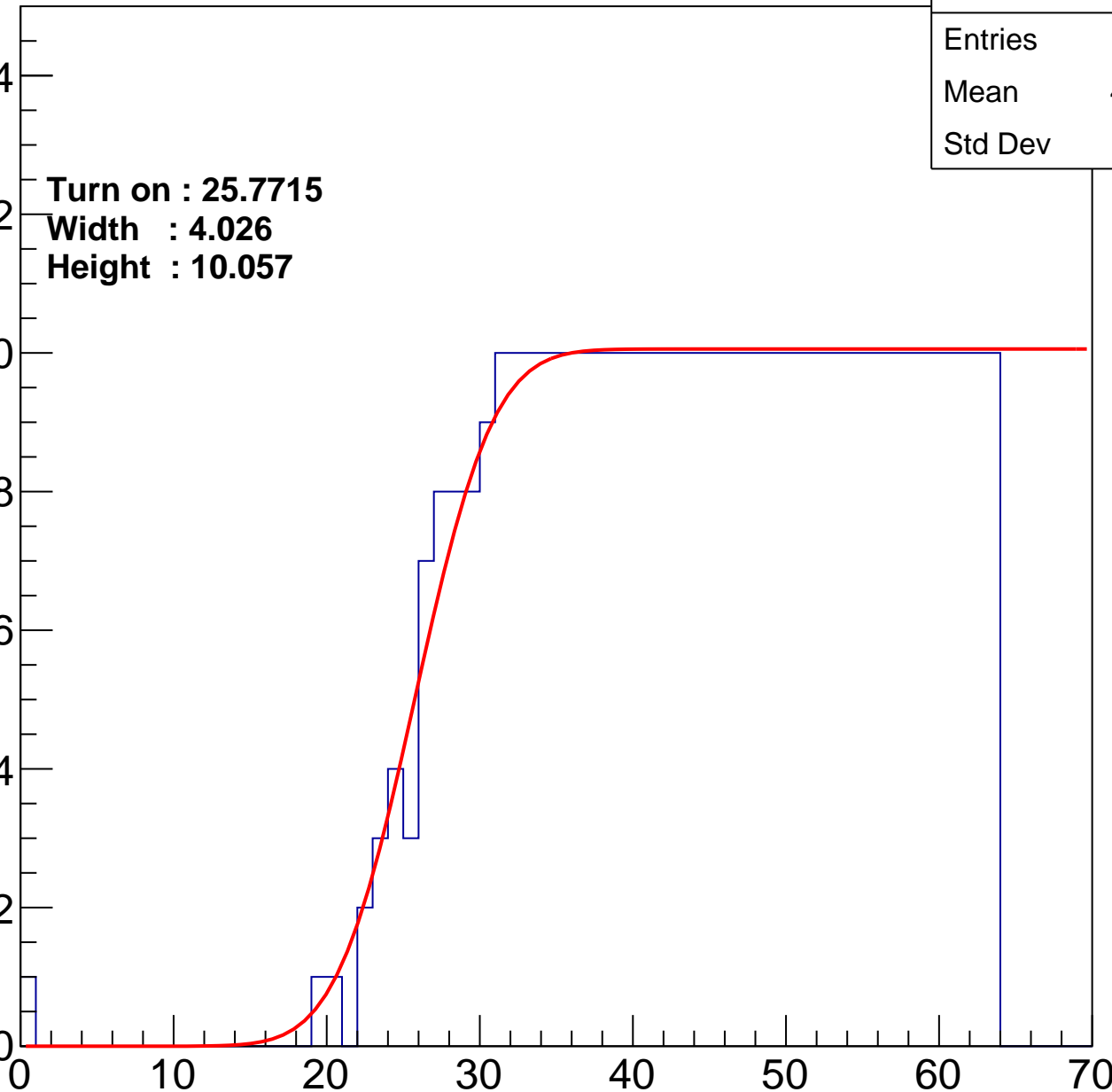
Width : 4.026

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch85

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.45 |
| Std Dev | 10.73 |

Turn on : 28.4043

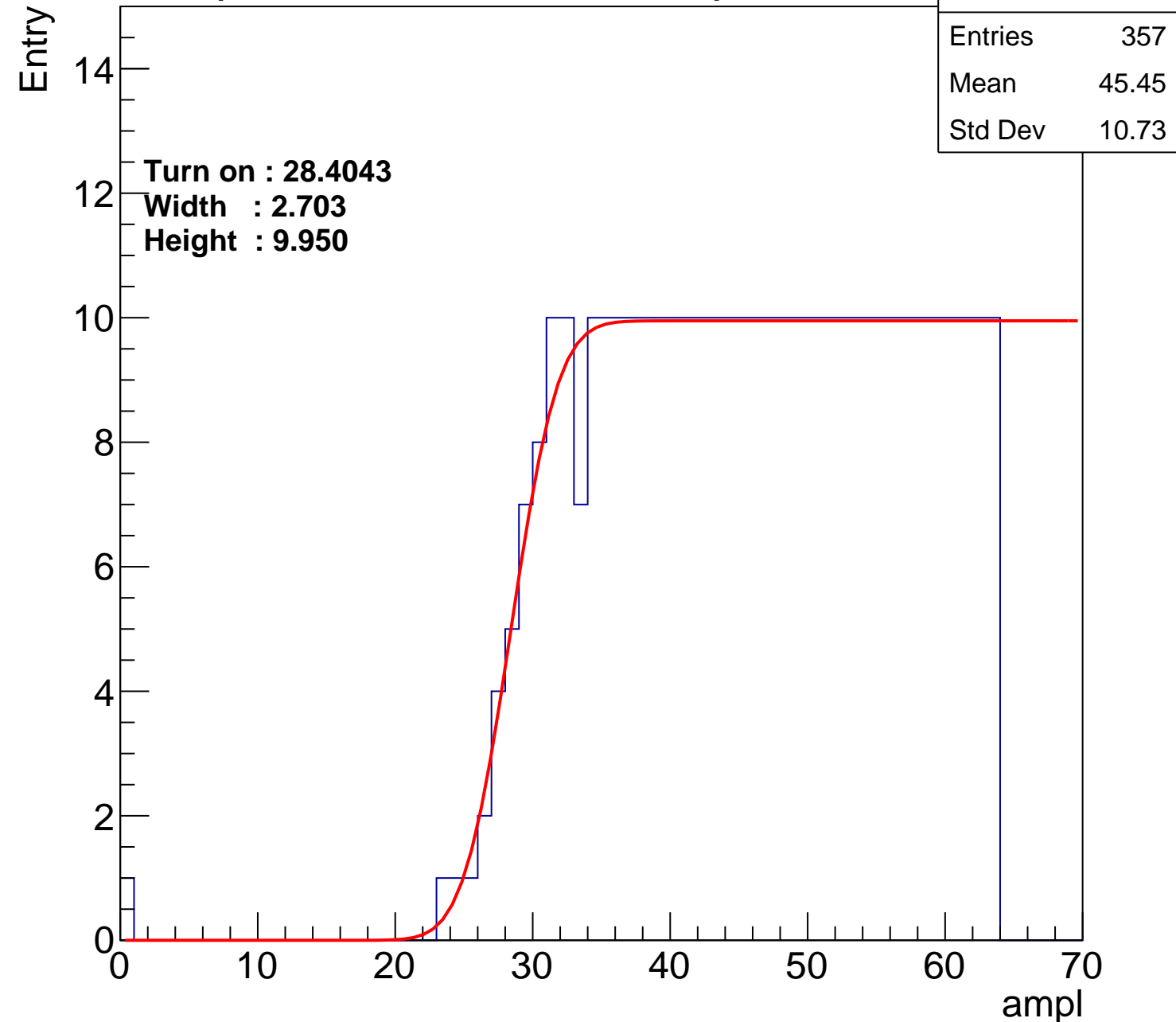
Width : 2.703

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch86

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.18 |
| Std Dev | 11.21 |

Turn on : 28.5420

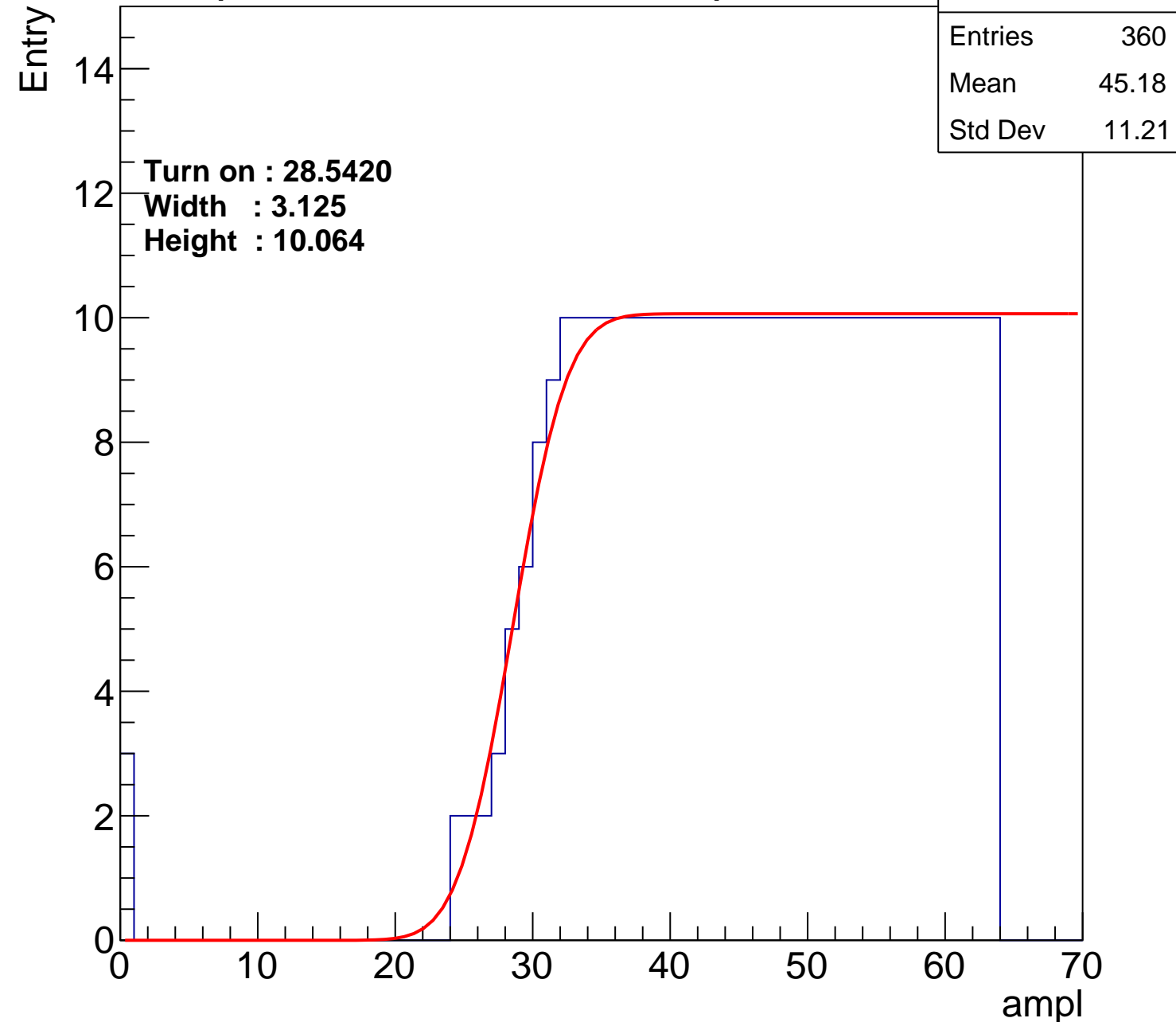
Width : 3.125

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch87

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.63 |
| Std Dev | 11.17 |

Turn on : 26.7413

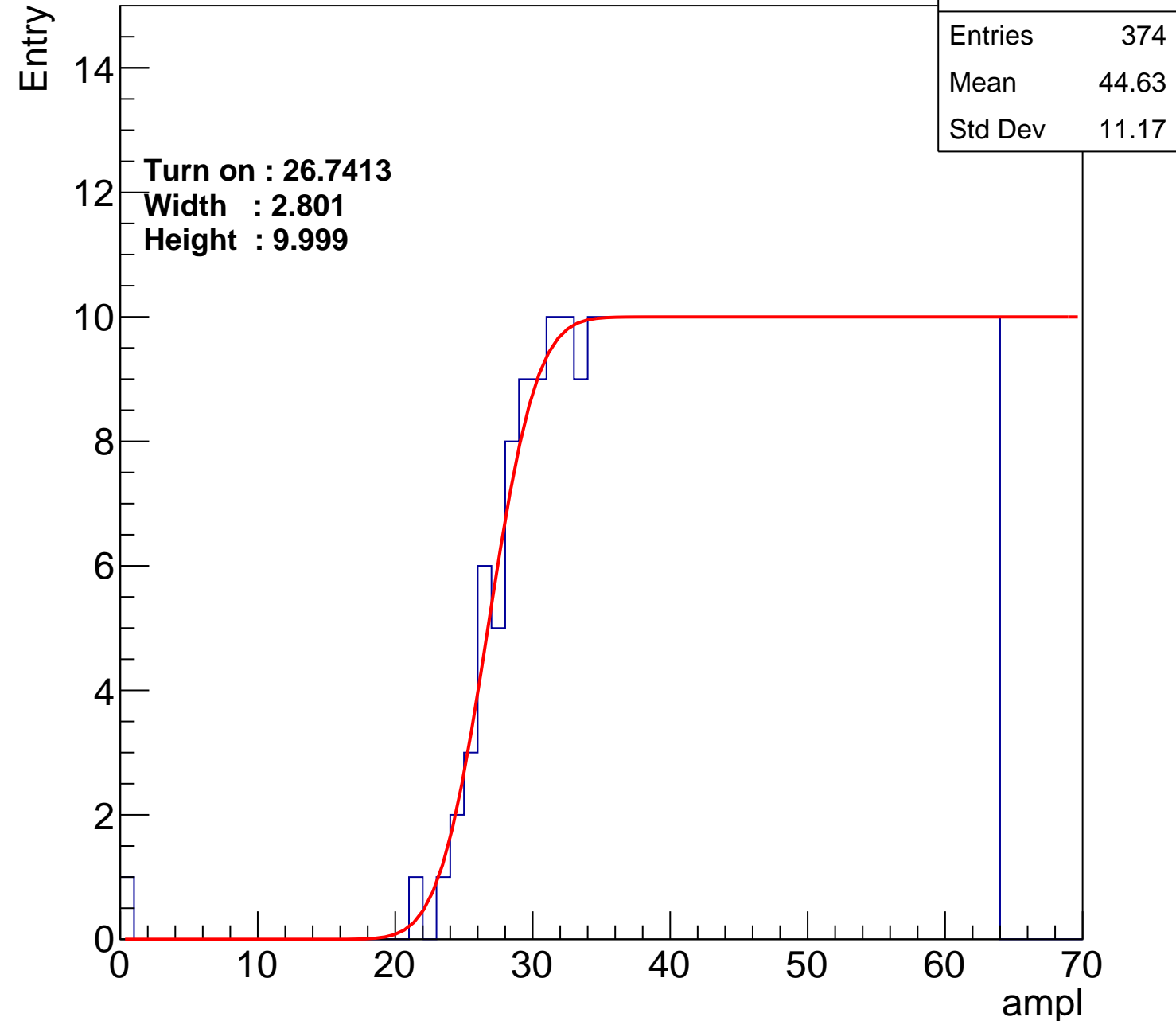
Width : 2.801

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch88

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.75 |
| Std Dev | 11.26 |

Turn on : 27.1958

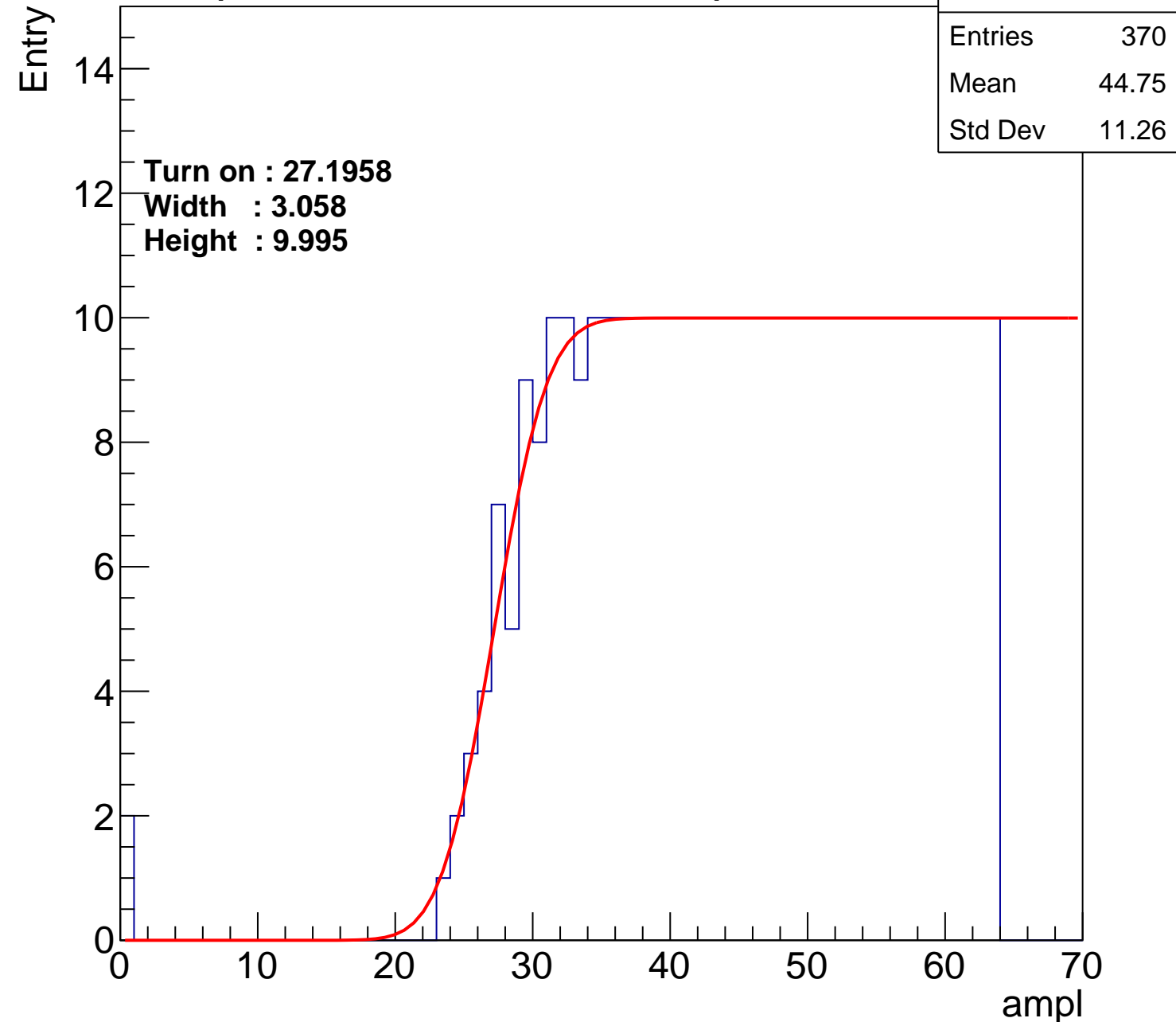
Width : 3.058

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch89

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 355 |
| Mean | 45.54 |
| Std Dev | 10.8 |

Turn on : 28.5168

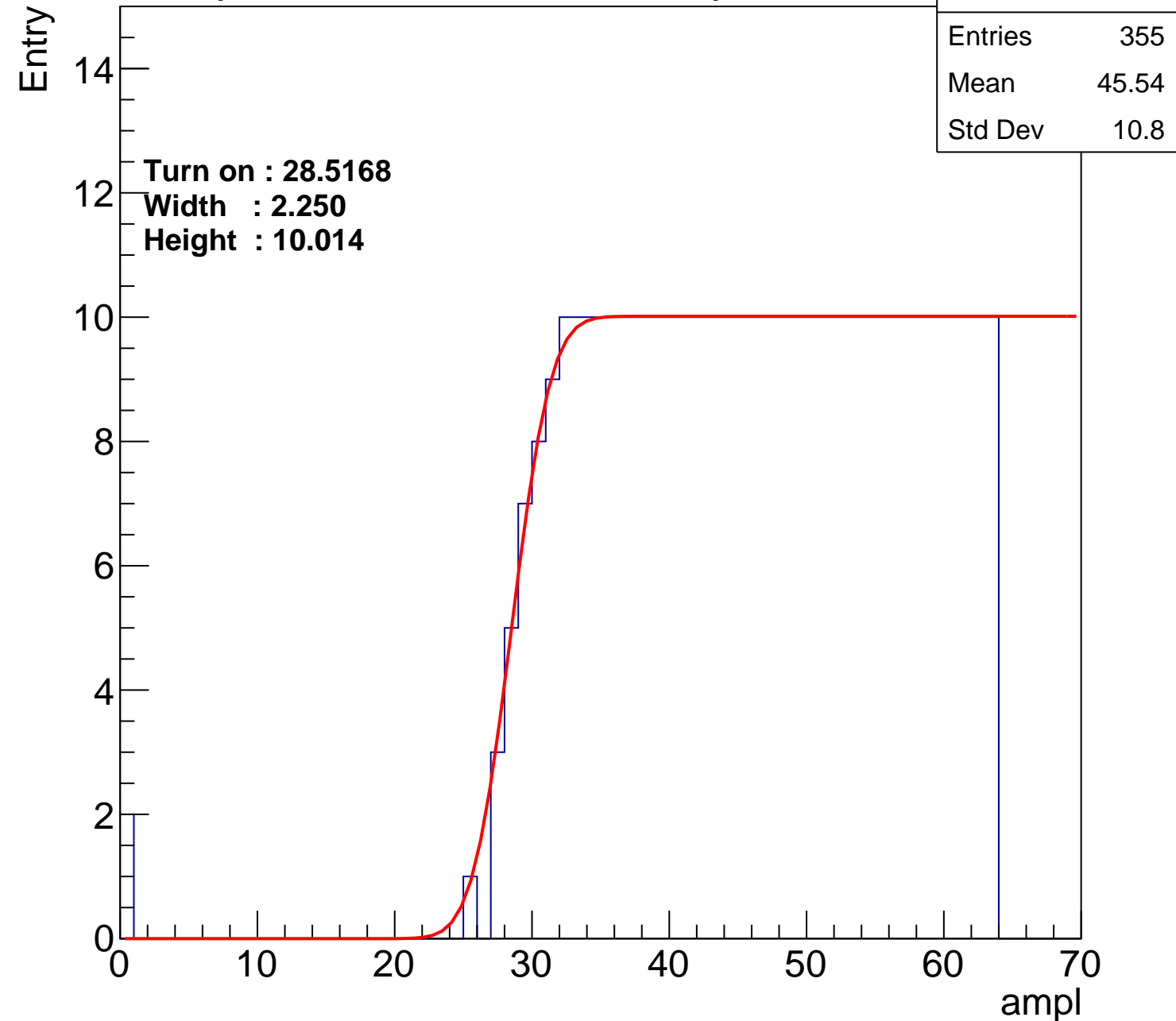
Width : 2.250

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch90

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.45 |
| Std Dev | 11.74 |

Turn on : 27.3956

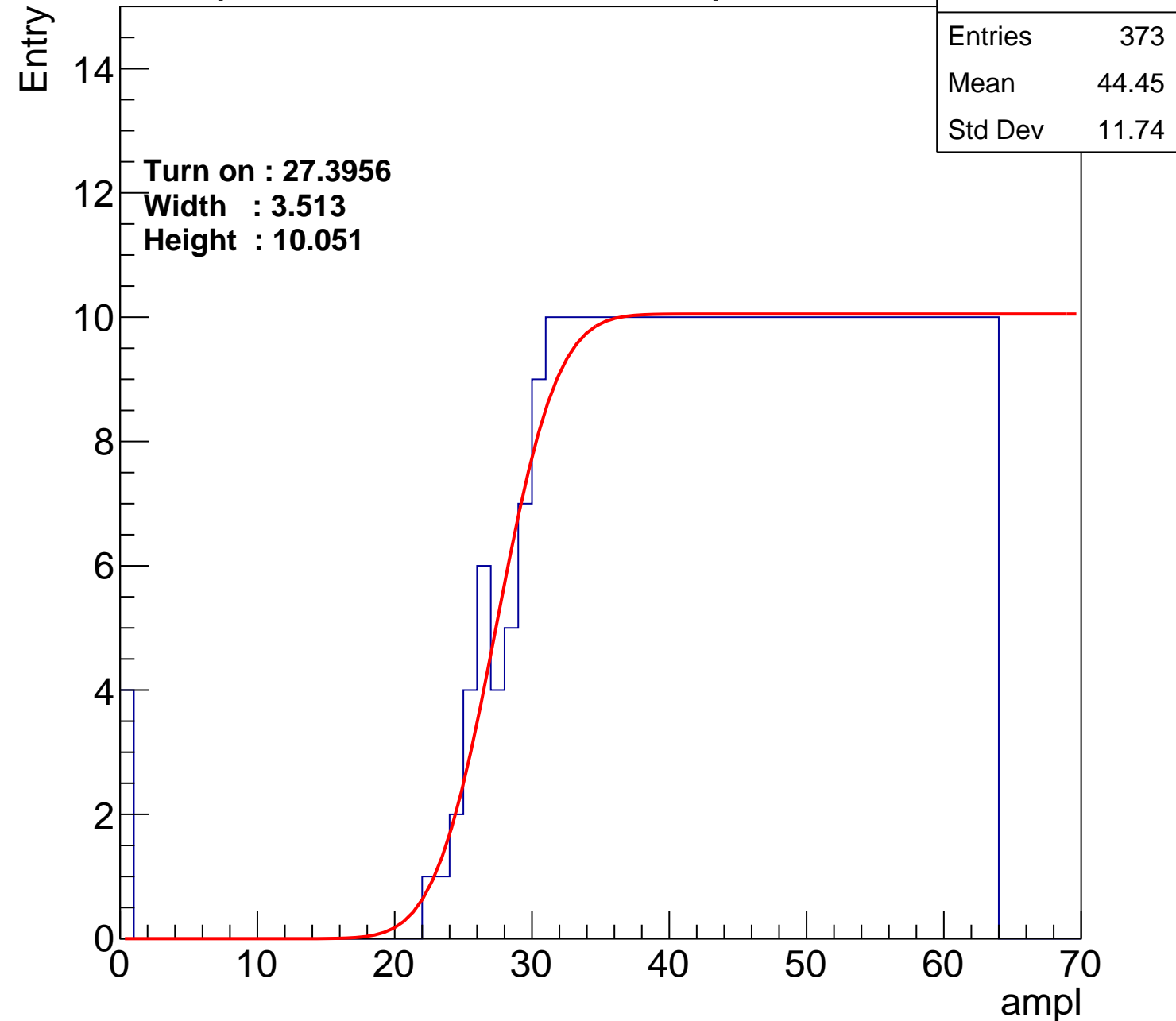
Width : 3.513

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch91

calib_packv5_042523_0143.root, FC#13, port D2

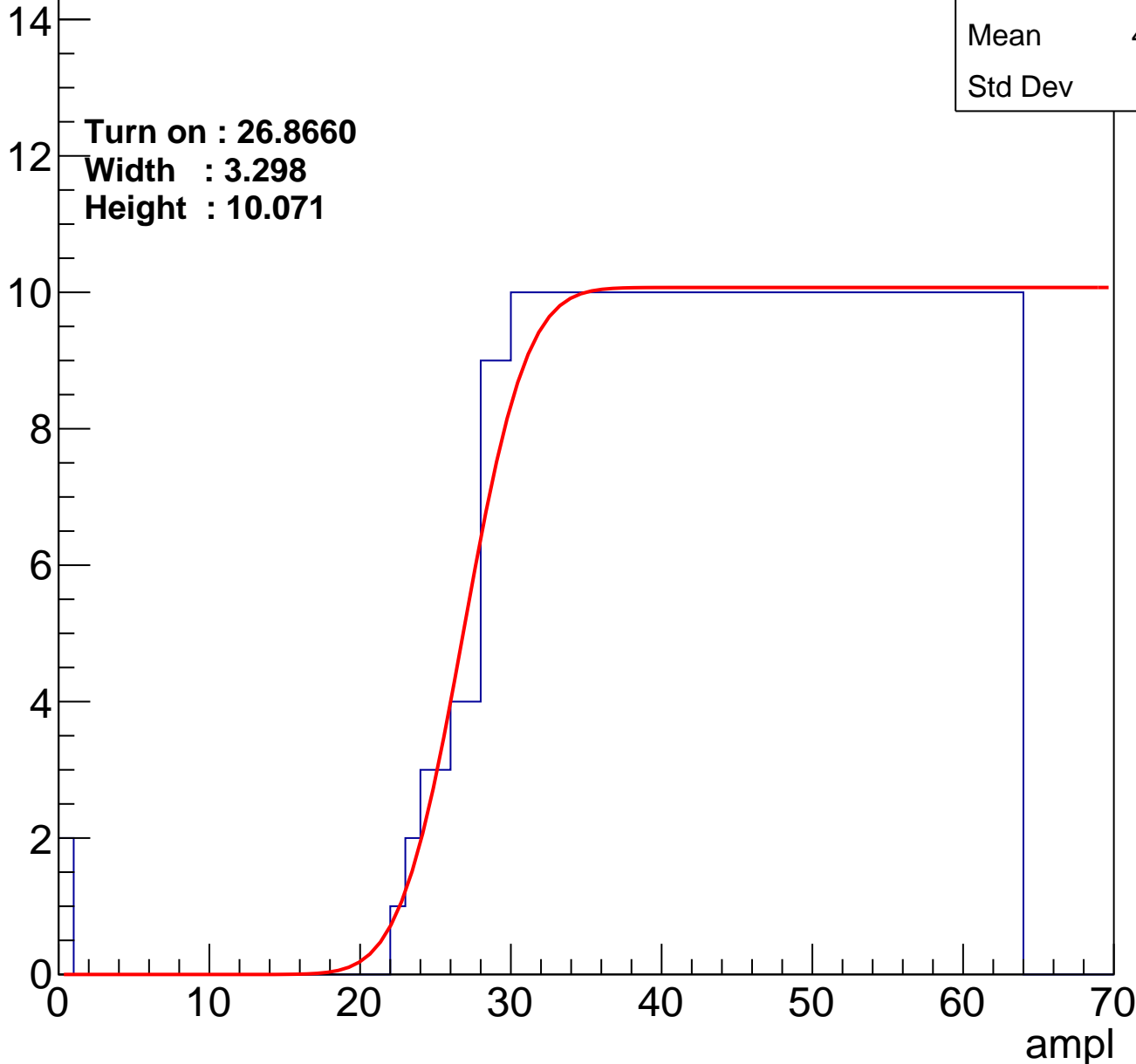
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.43 |
| Std Dev | 11.41 |

Turn on : 26.8660

Width : 3.298

Height : 10.071

Entry



B1L003S, U8-ch92

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.13 |
| Std Dev | 10.89 |

Turn on : 27.8185

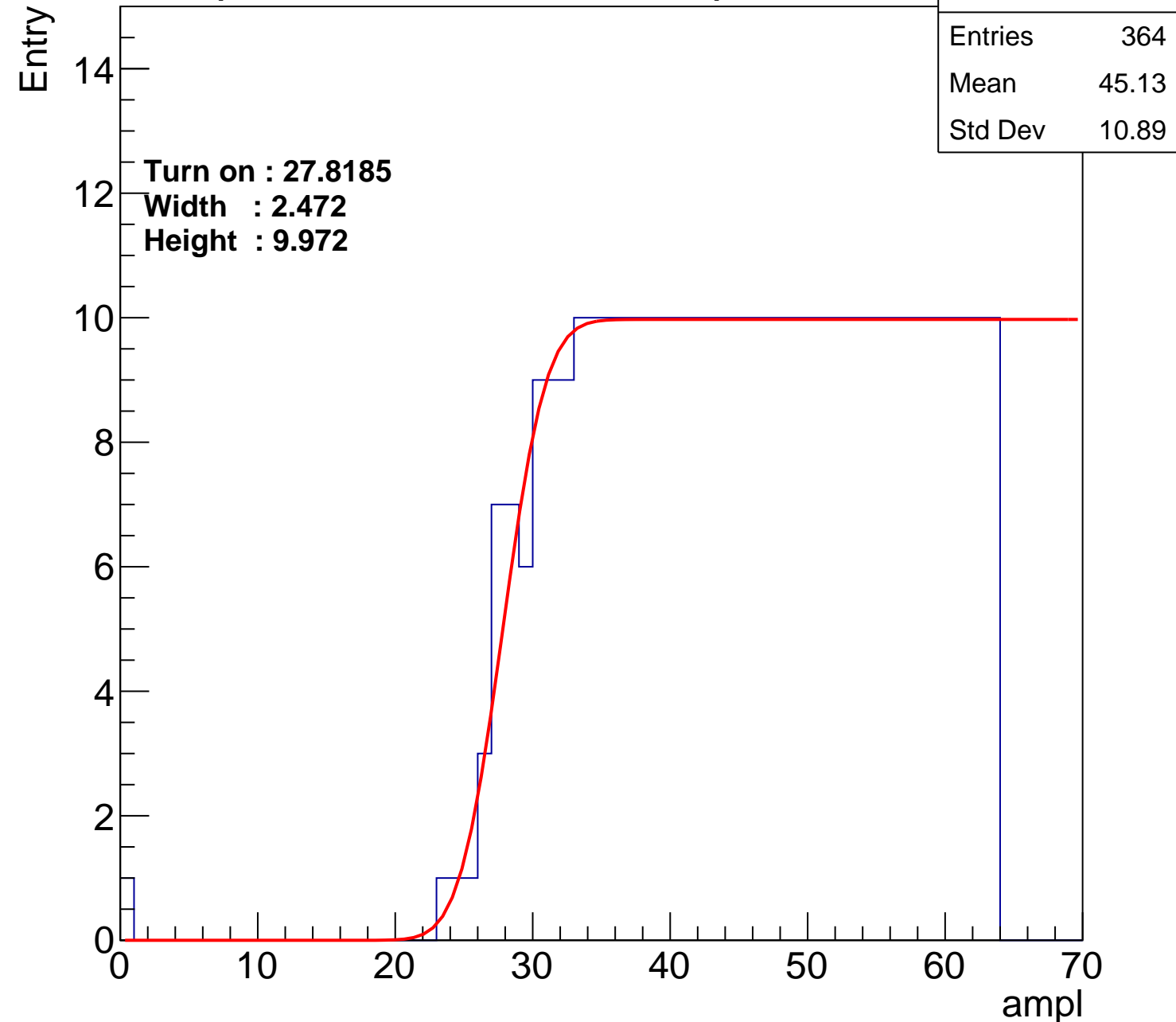
Width : 2.472

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch93

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.04 |
| Std Dev | 11.65 |

Turn on : 26.1628

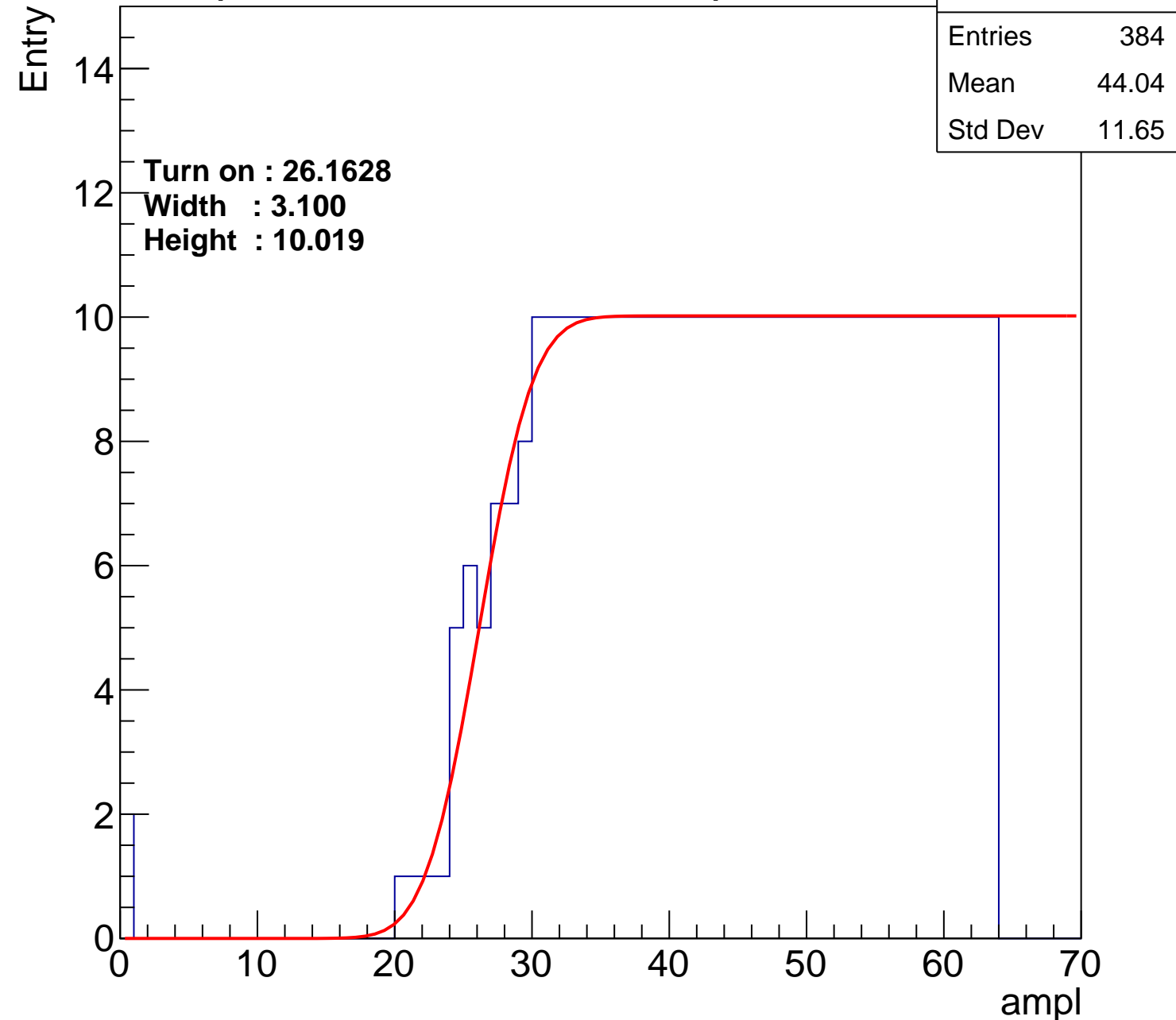
Width : 3.100

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch94

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.65 |
| Std Dev | 11.91 |

Turn on : 28.0854

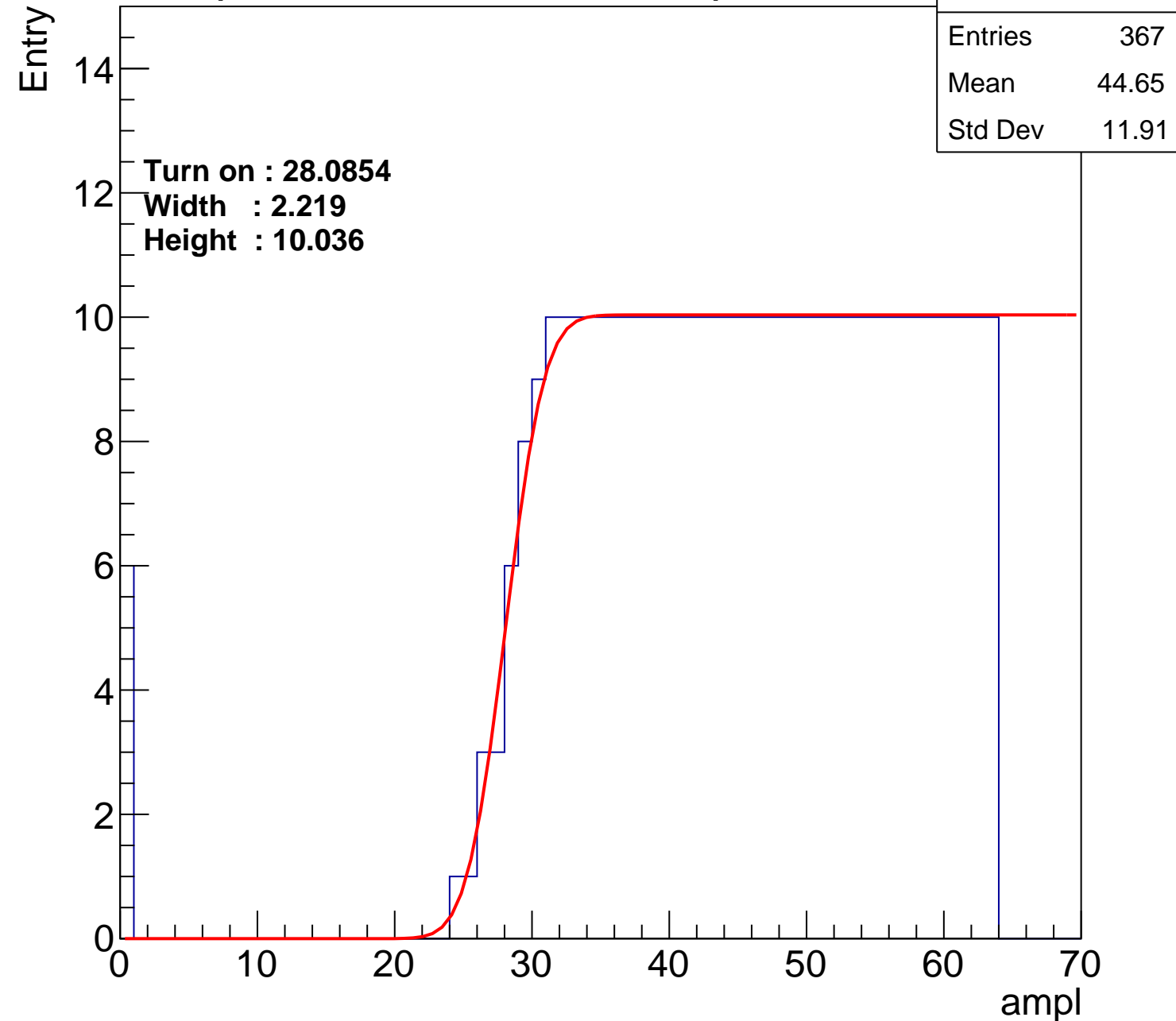
Width : 2.219

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch95

calib_packv5_042523_0143.root, FC#13, port D2

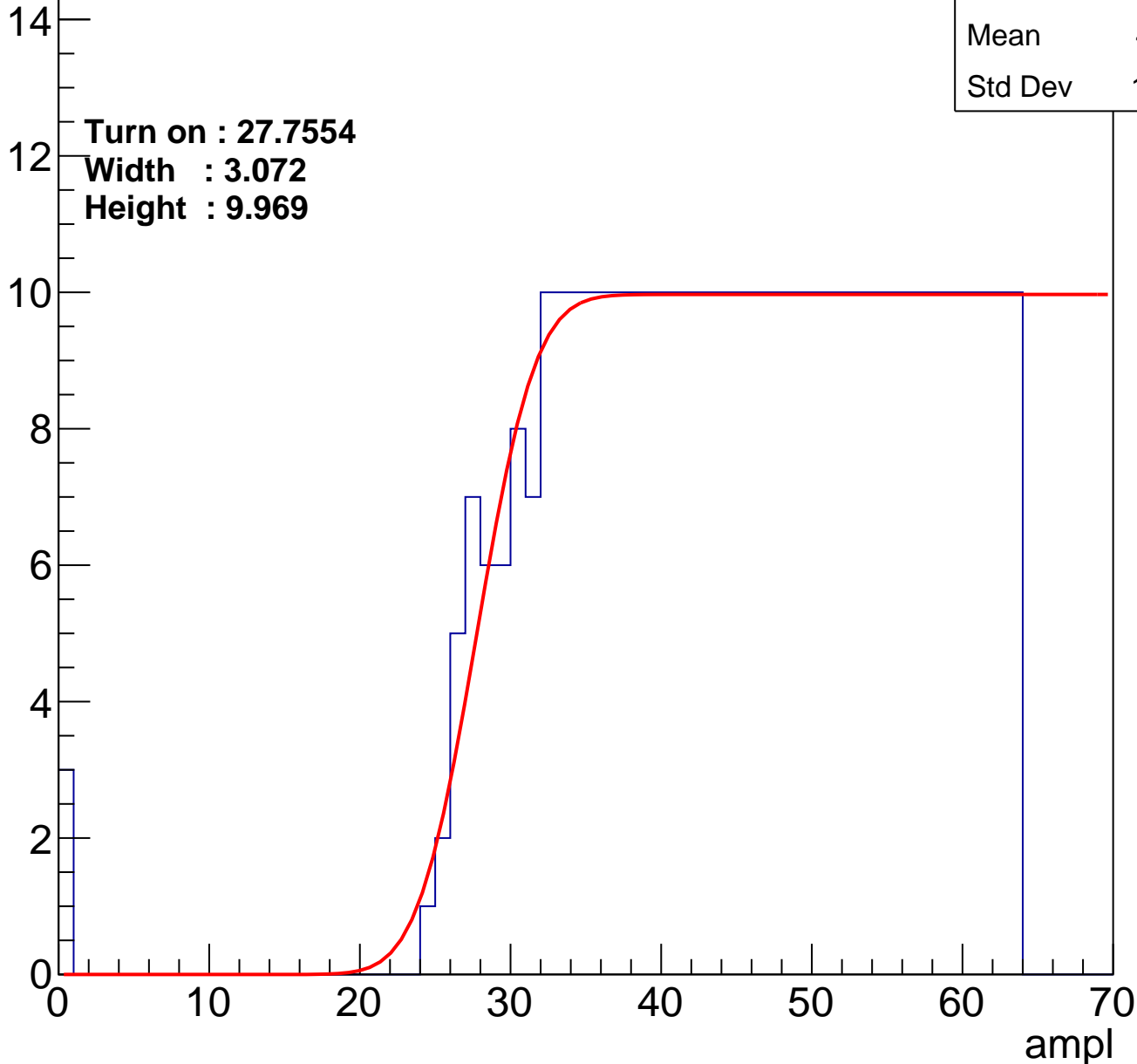
Entry

| | |
|---------|-------|
| Entries | 365 |
| Mean | 44.91 |
| Std Dev | 11.36 |

Turn on : 27.7554

Width : 3.072

Height : 9.969



B1L003S, U8-ch96

calib_packv5_042523_0143.root, FC#13, port D2

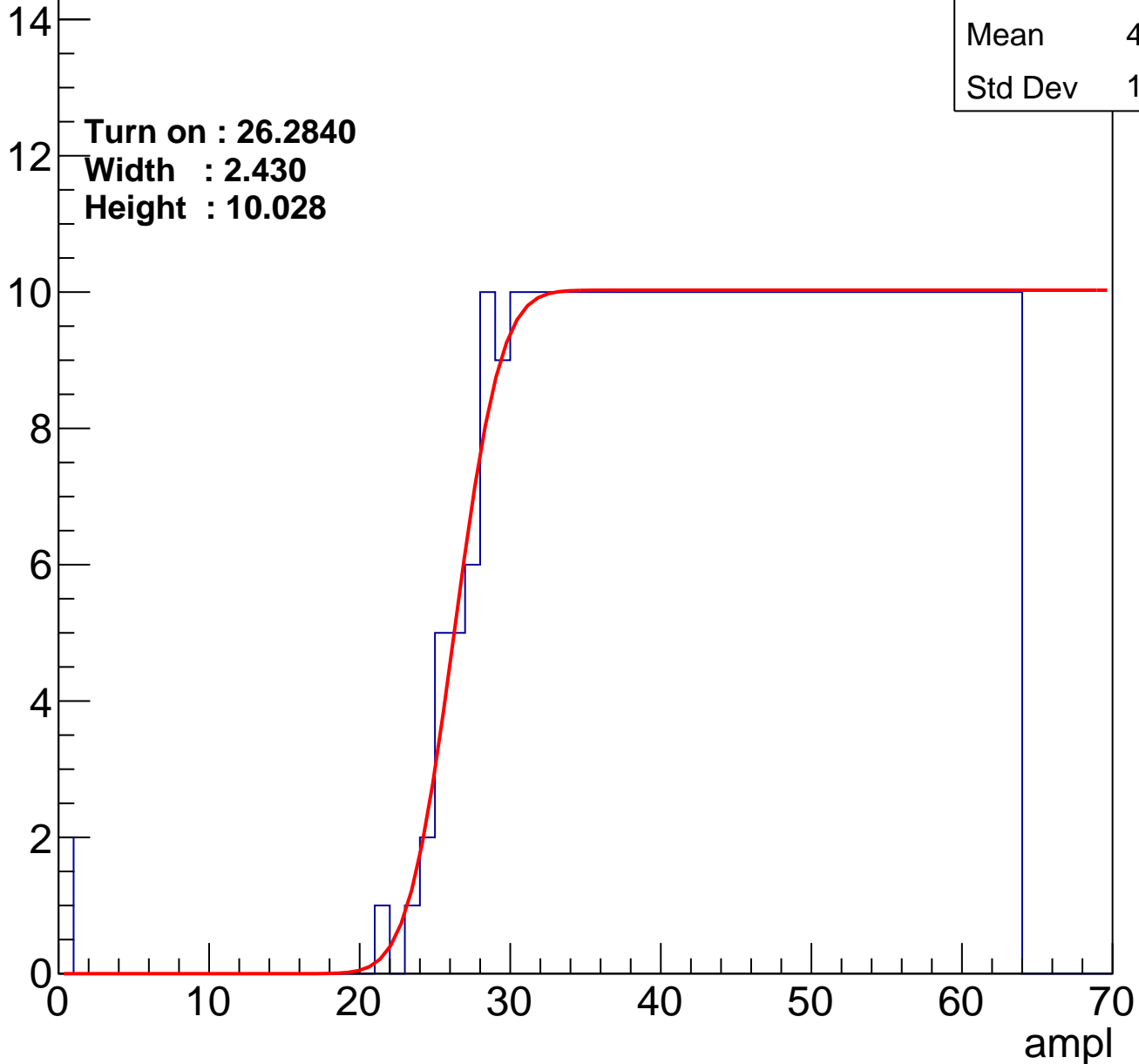
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.25 |
| Std Dev | 11.48 |

Turn on : 26.2840

Width : 2.430

Height : 10.028

Entry



B1L003S, U8-ch97

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.74 |
| Std Dev | 11.42 |

Turn on : 27.4112

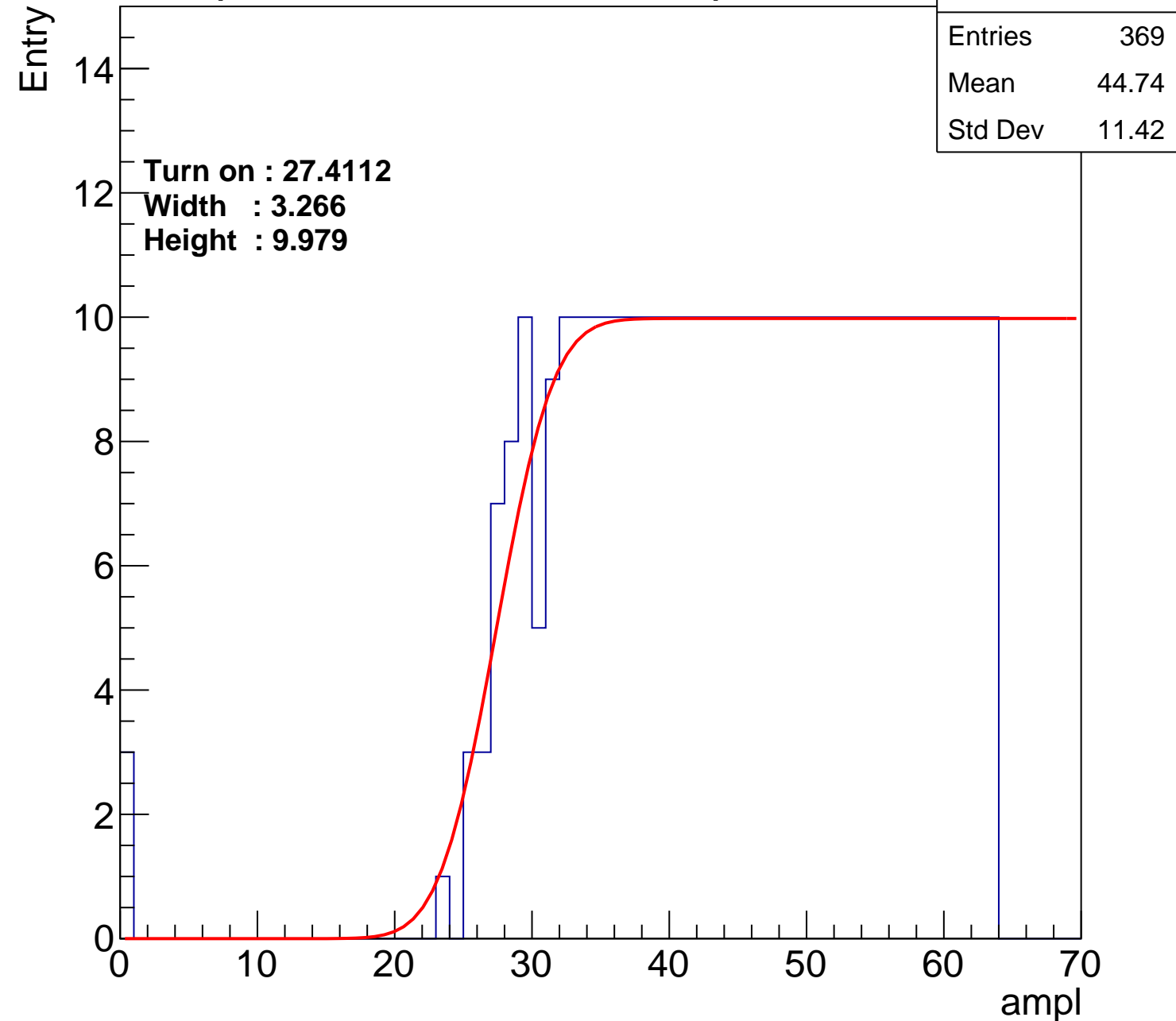
Width : 3.266

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch98

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.4 |
| Std Dev | 12.38 |

Turn on : 25.0242

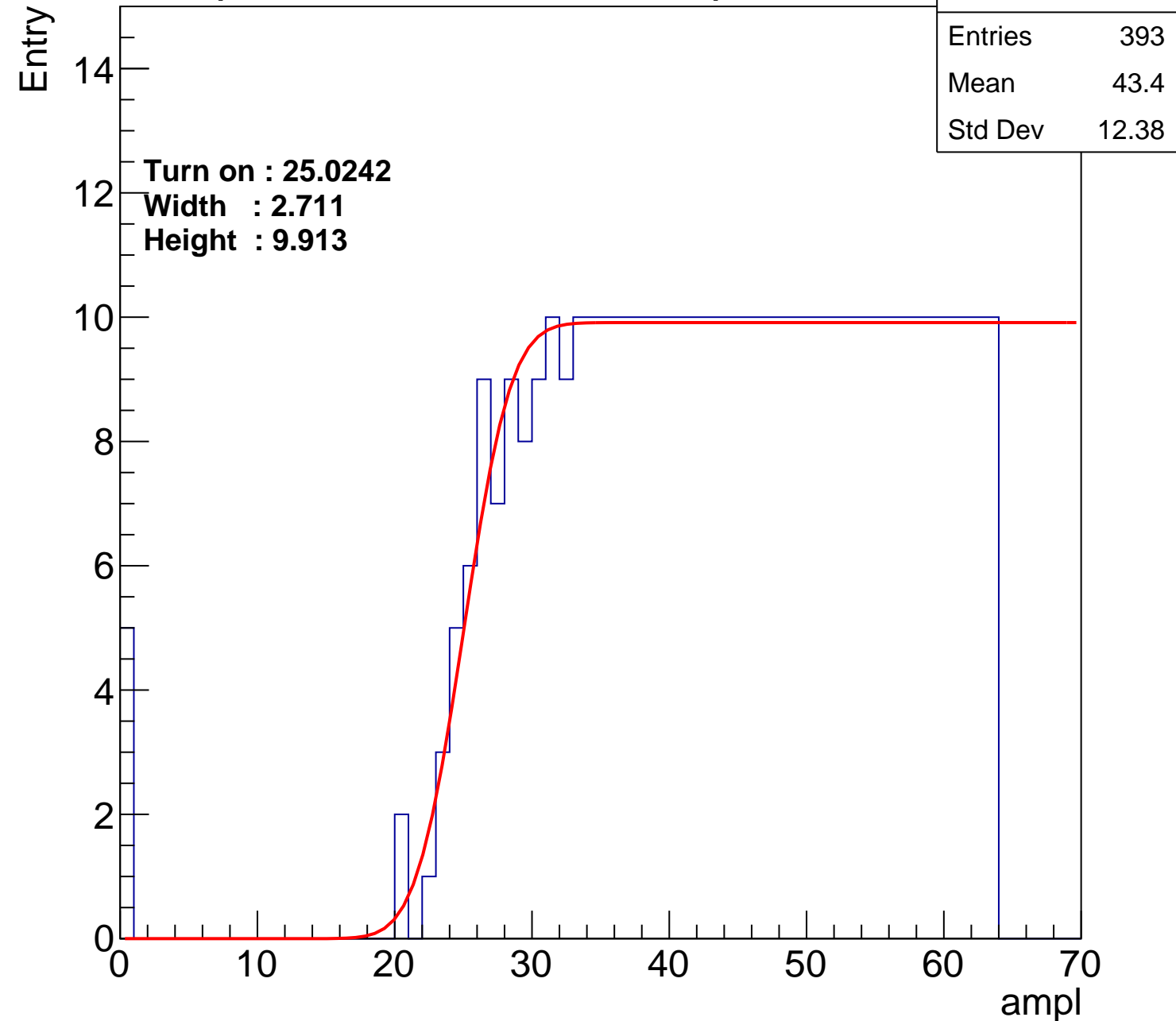
Width : 2.711

Height : 9.913

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch99

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.34 |
| Std Dev | 11.76 |

Turn on : 26.6481

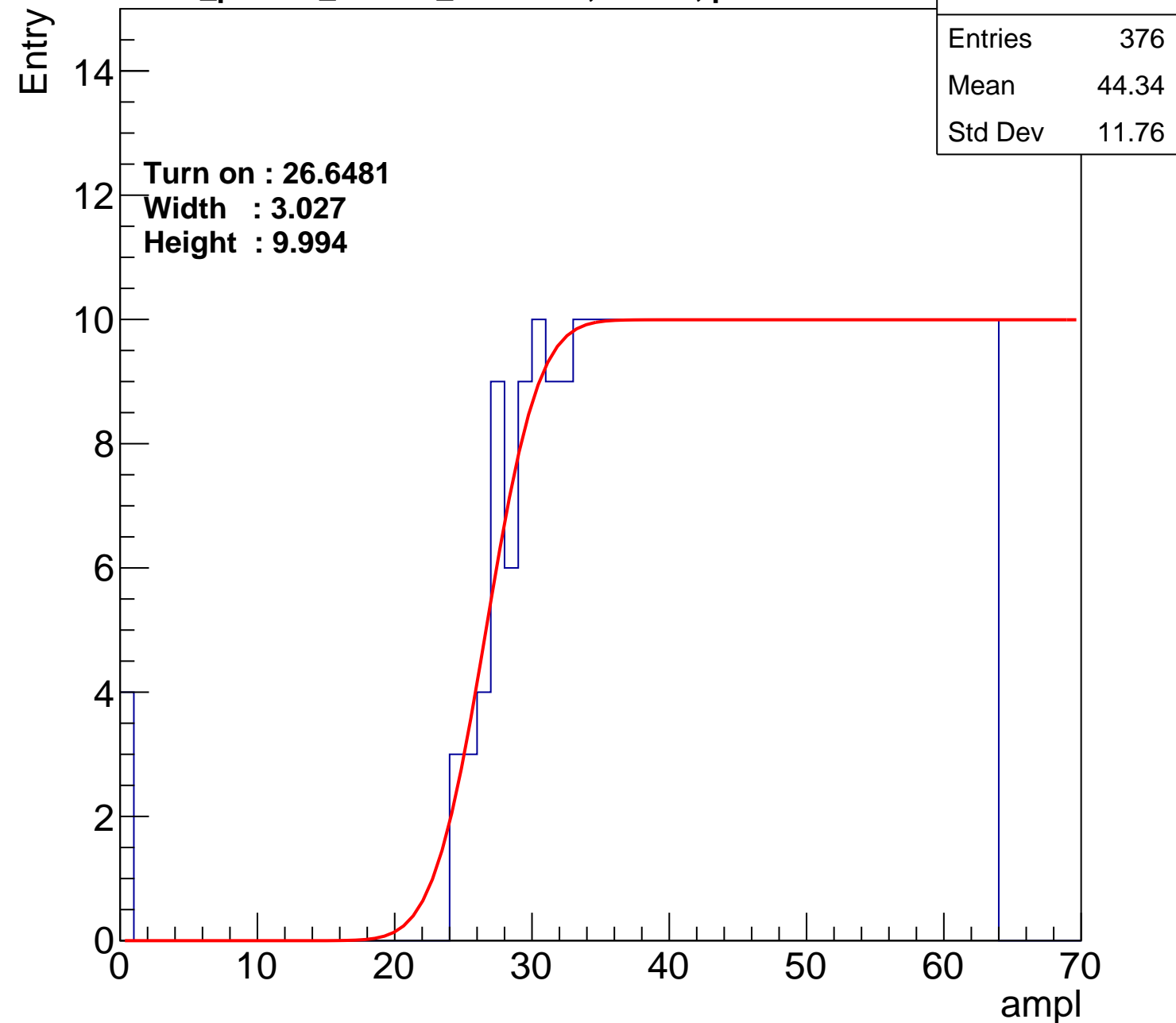
Width : 3.027

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch100

calib_packv5_042523_0143.root, FC#13, port D2

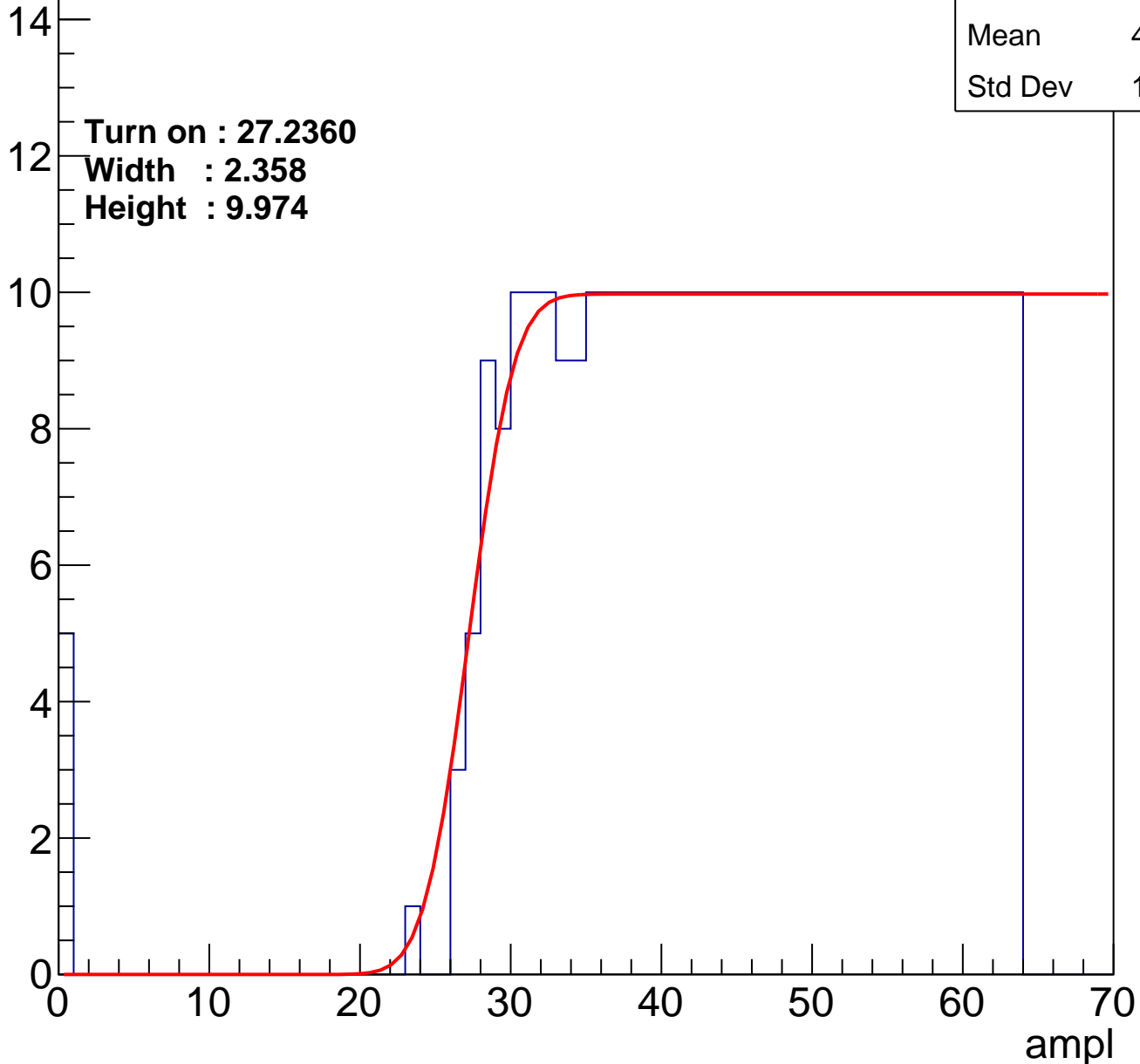
| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.62 |
| Std Dev | 11.77 |

Turn on : 27.2360

Width : 2.358

Height : 9.974

Entry



B1L003S, U8-ch101

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 344 |
| Mean | 46.1 |
| Std Dev | 10.39 |

Turn on : 30.2111

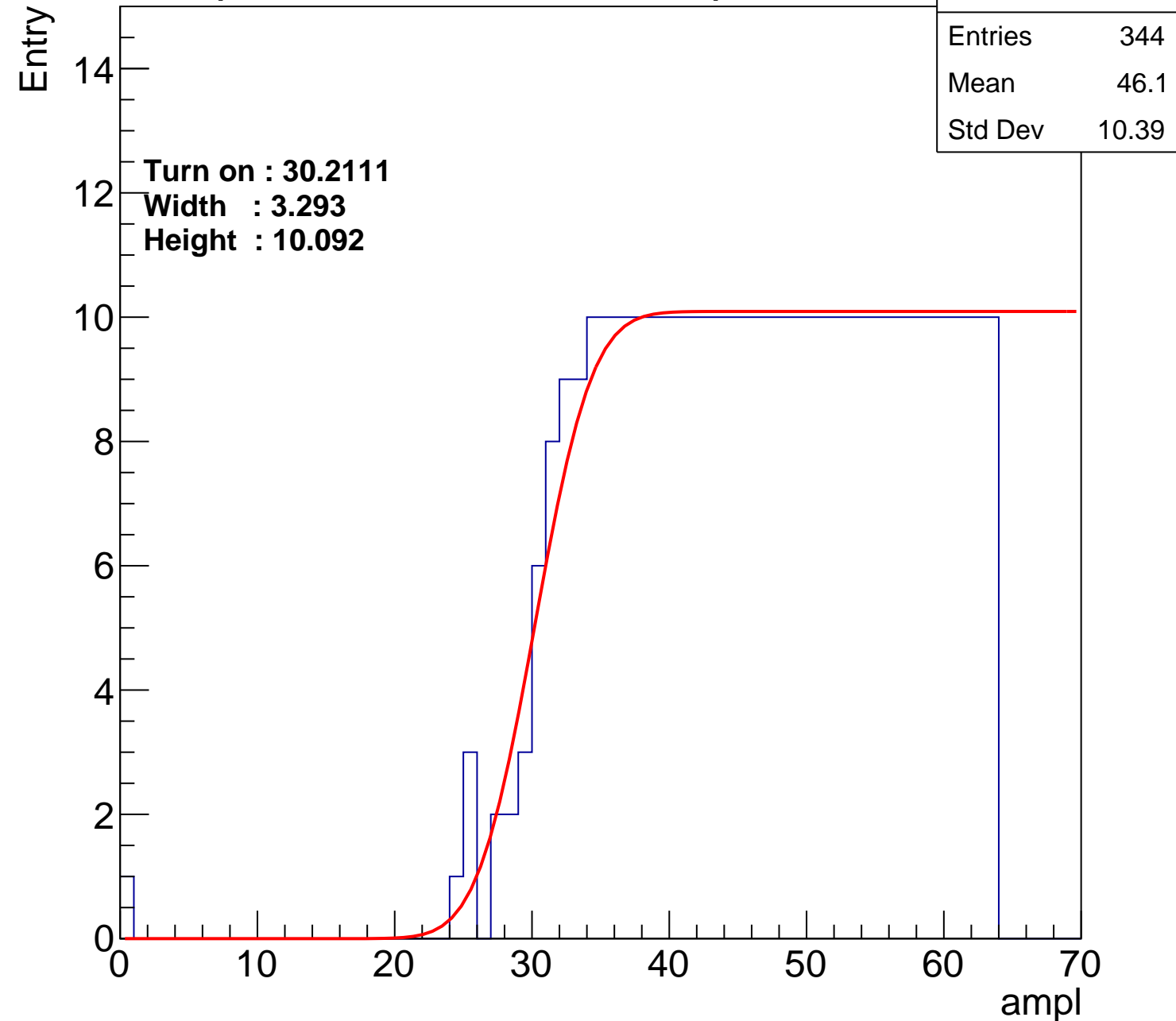
Width : 3.293

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch102

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 44.89 |
| Std Dev | 11.73 |

Turn on : 29.3240

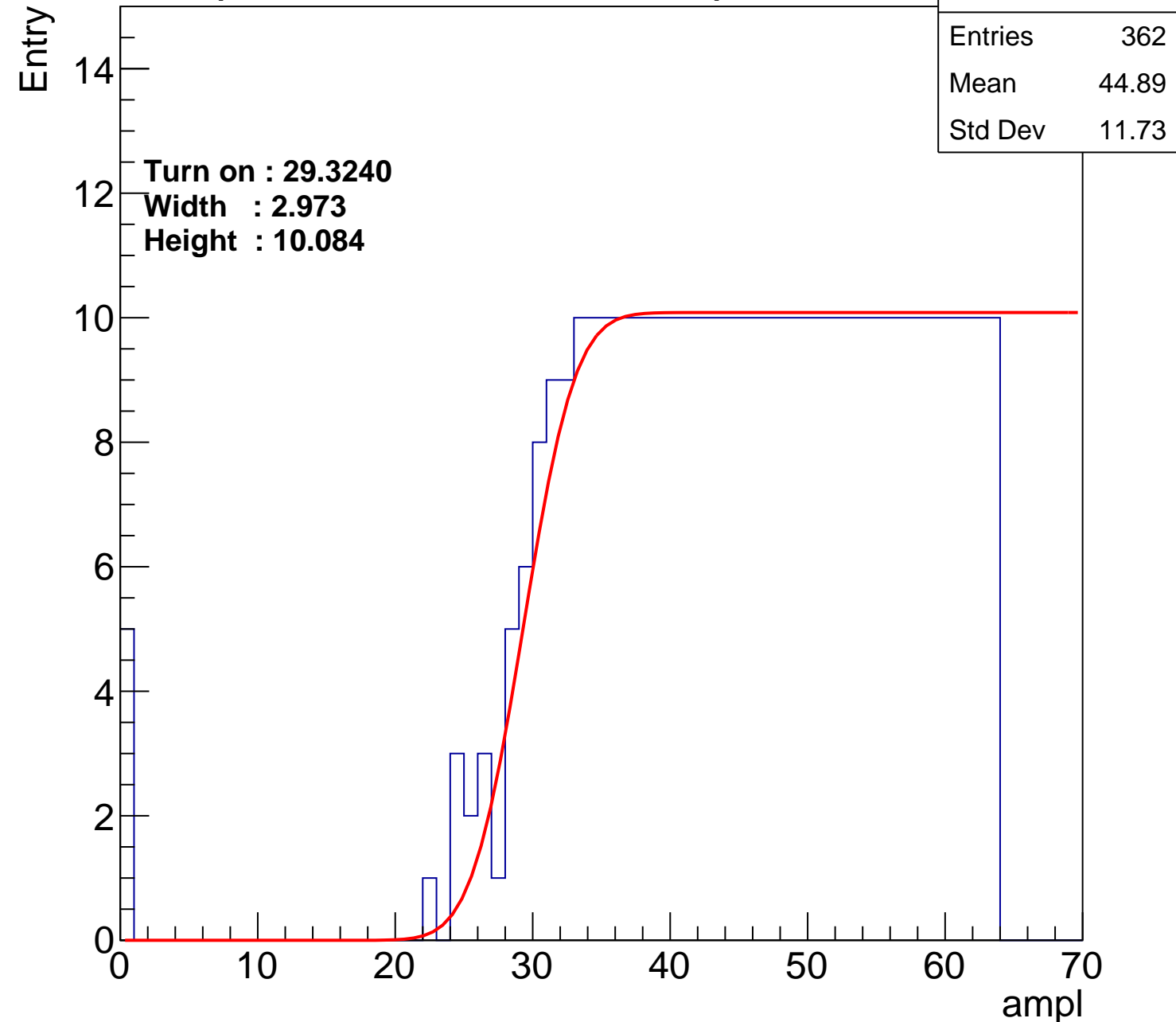
Width : 2.973

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch103

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 352 |
| Mean | 45.66 |
| Std Dev | 10.67 |

Turn on : 29.8501

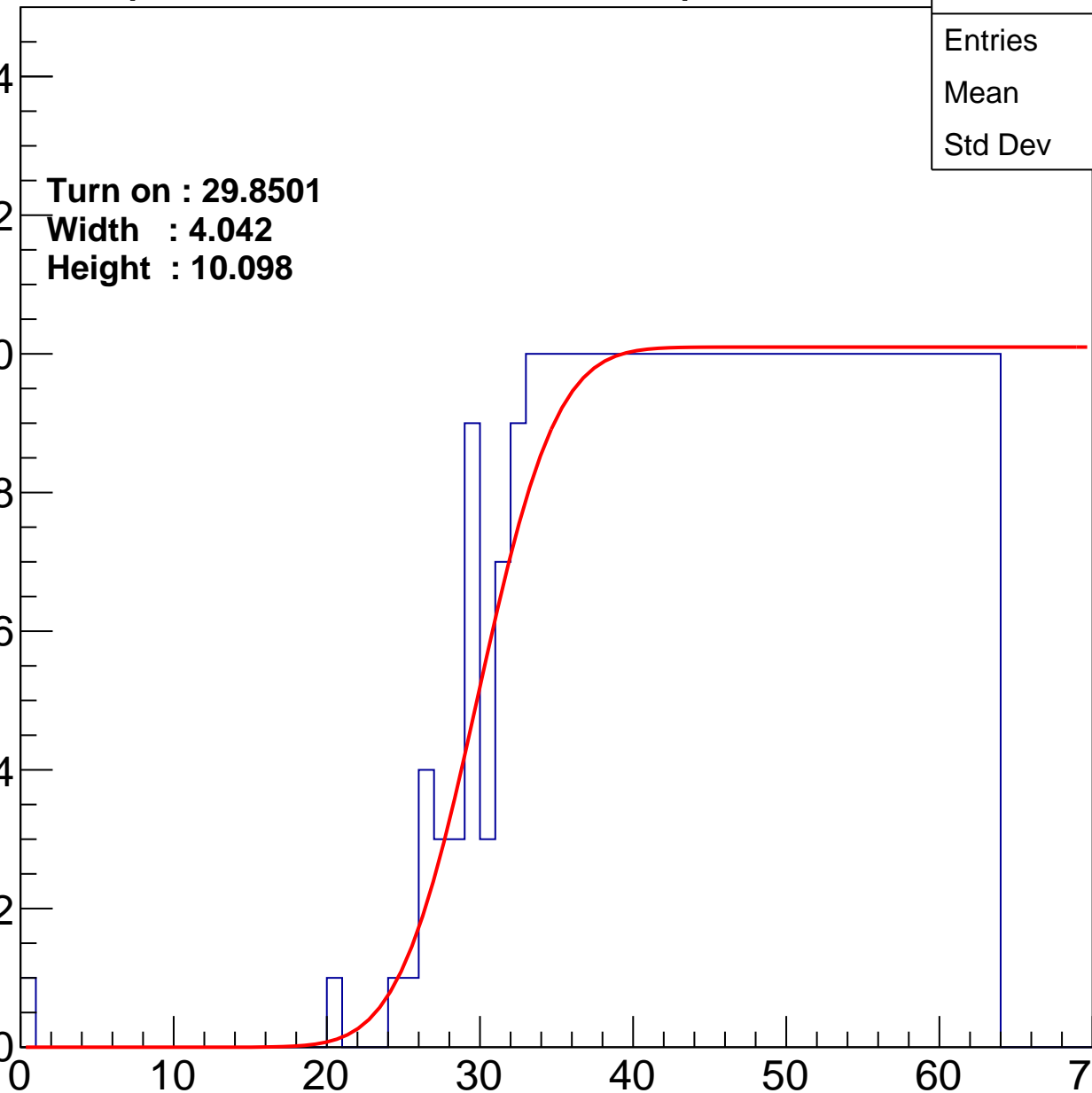
Width : 4.042

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch104

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45 |
| Std Dev | 11.47 |

Turn on : 27.0341

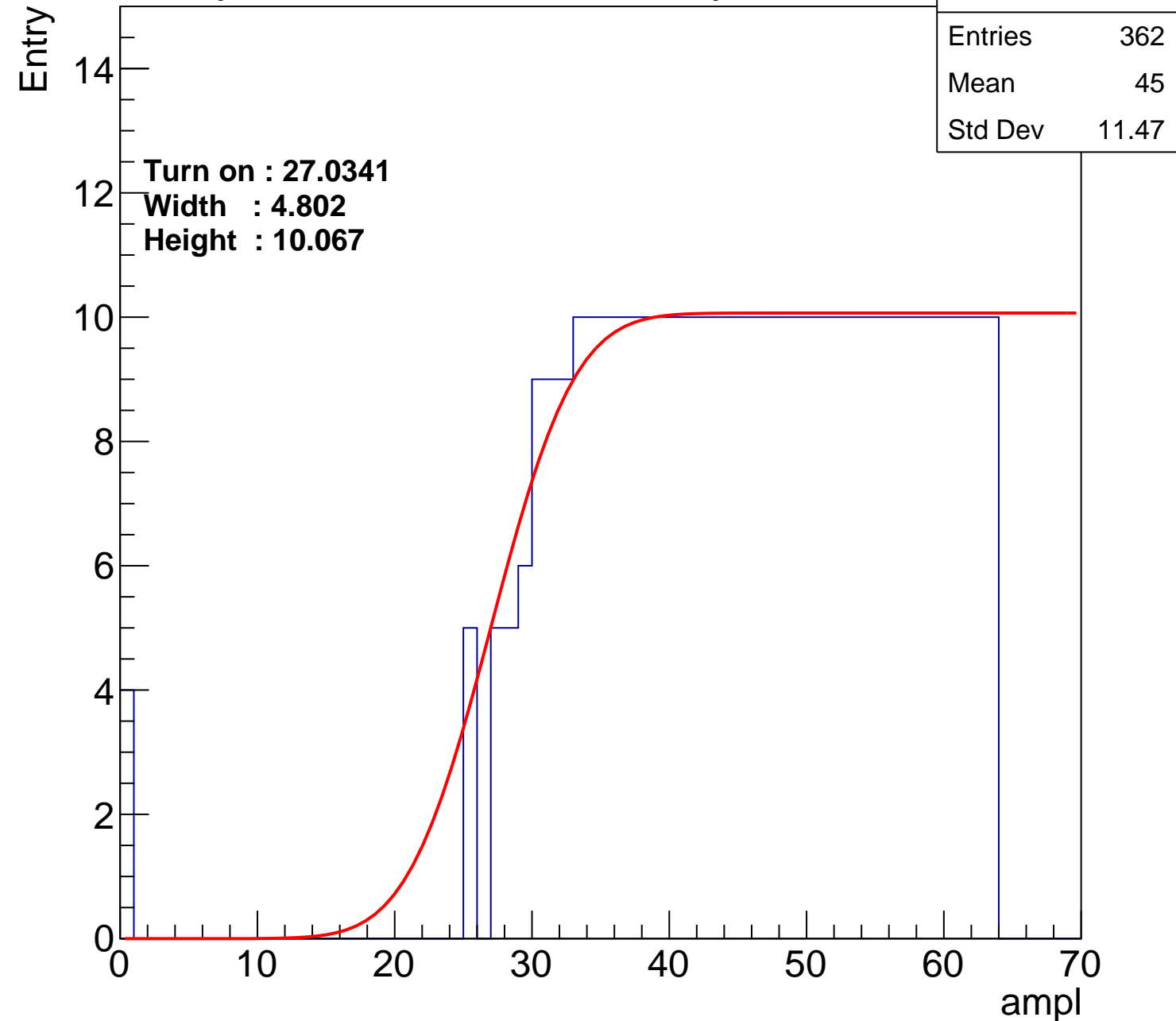
Width : 4.802

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch105

calib_packv5_042523_0143.root, FC#13, port D2

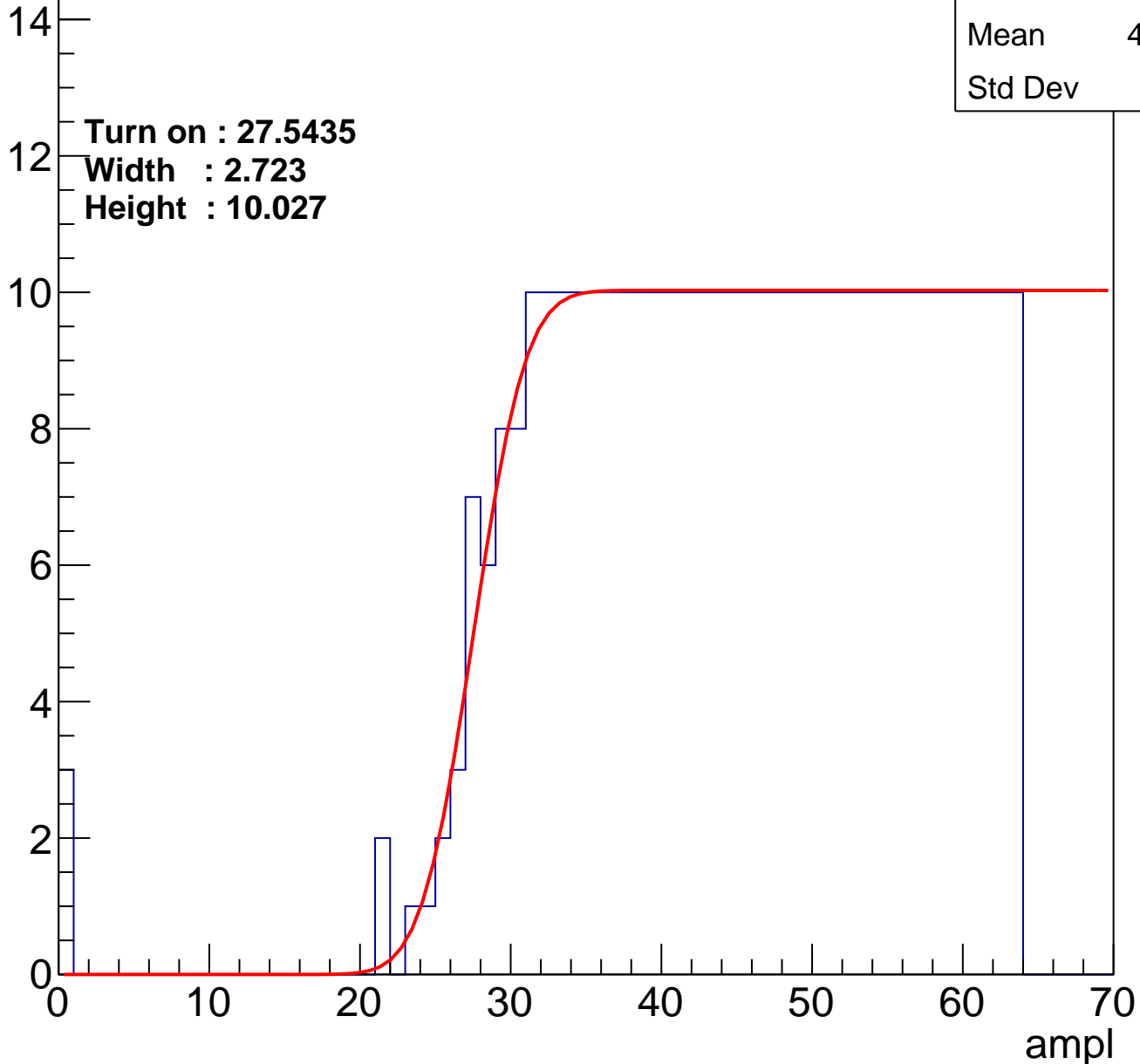
| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.63 |
| Std Dev | 11.5 |

Turn on : 27.5435

Width : 2.723

Height : 10.027

Entry



B1L003S, U8-ch106

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.1 |
| Std Dev | 11.87 |

Turn on : 26.5461

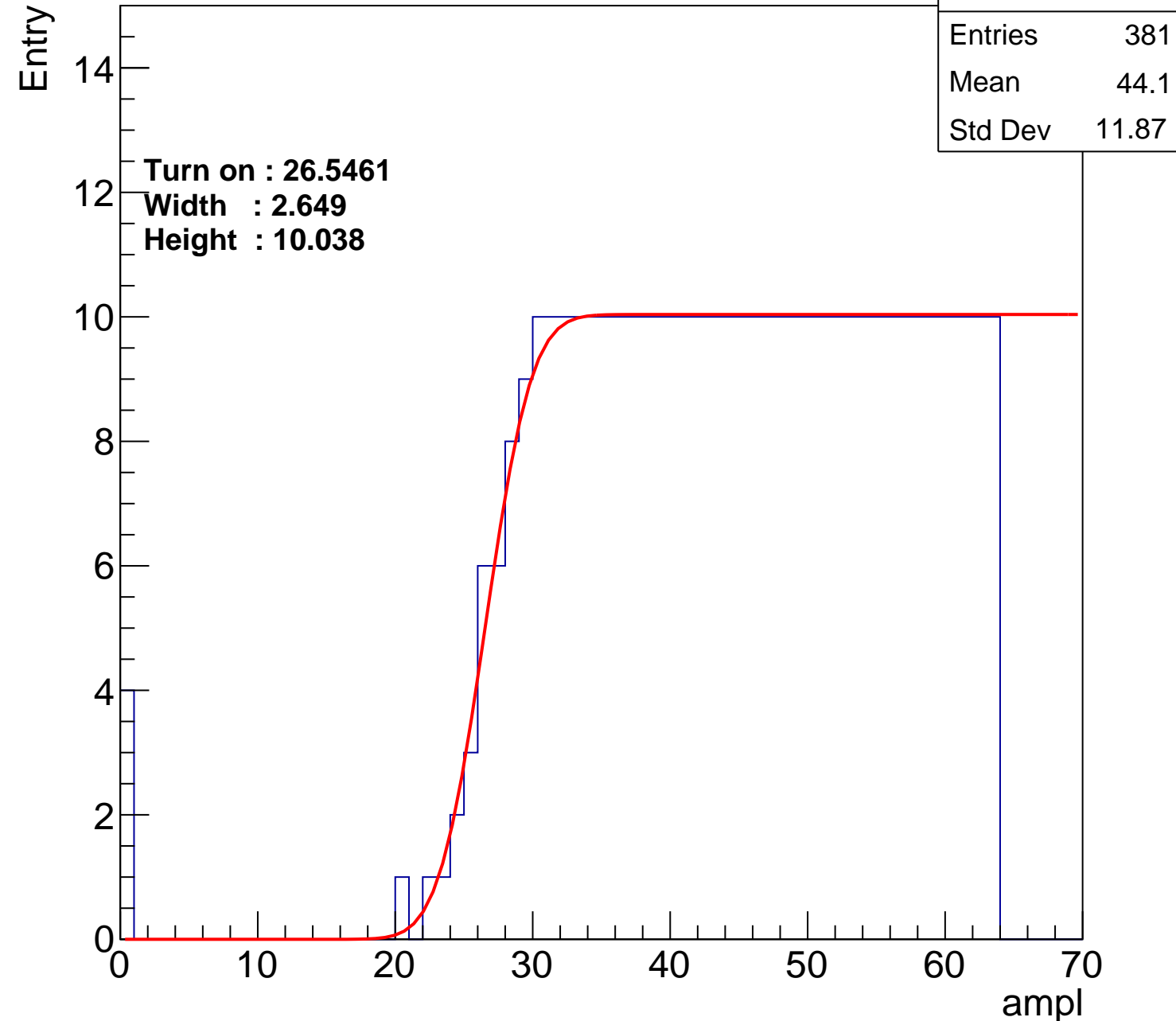
Width : 2.649

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch107

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.85 |
| Std Dev | 11.26 |

Turn on : 28.6574

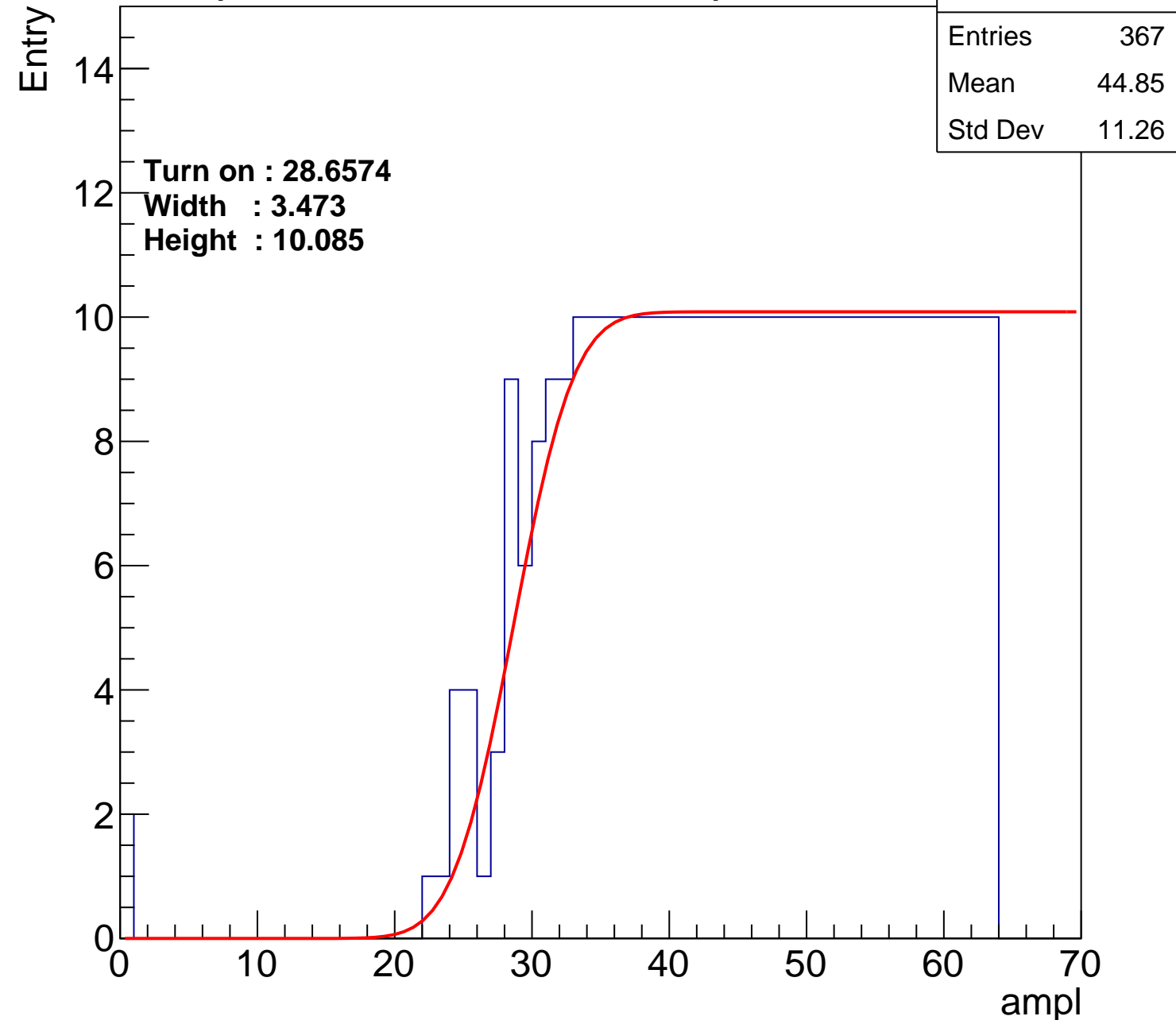
Width : 3.473

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch108

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.17 |
| Std Dev | 11.61 |

Turn on : 26.3188

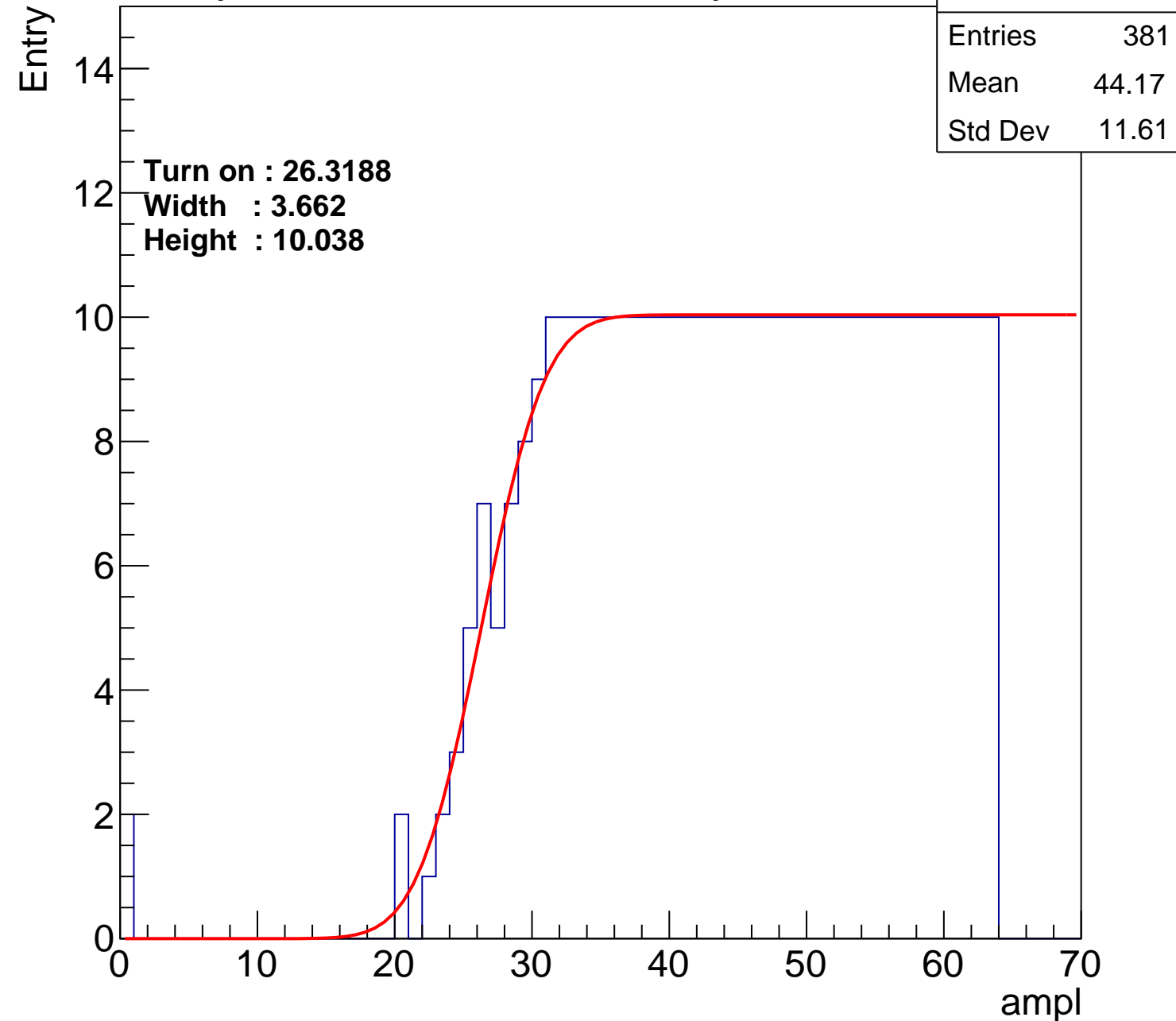
Width : 3.662

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch109

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.44 |
| Std Dev | 11.44 |

Turn on : 26.3892

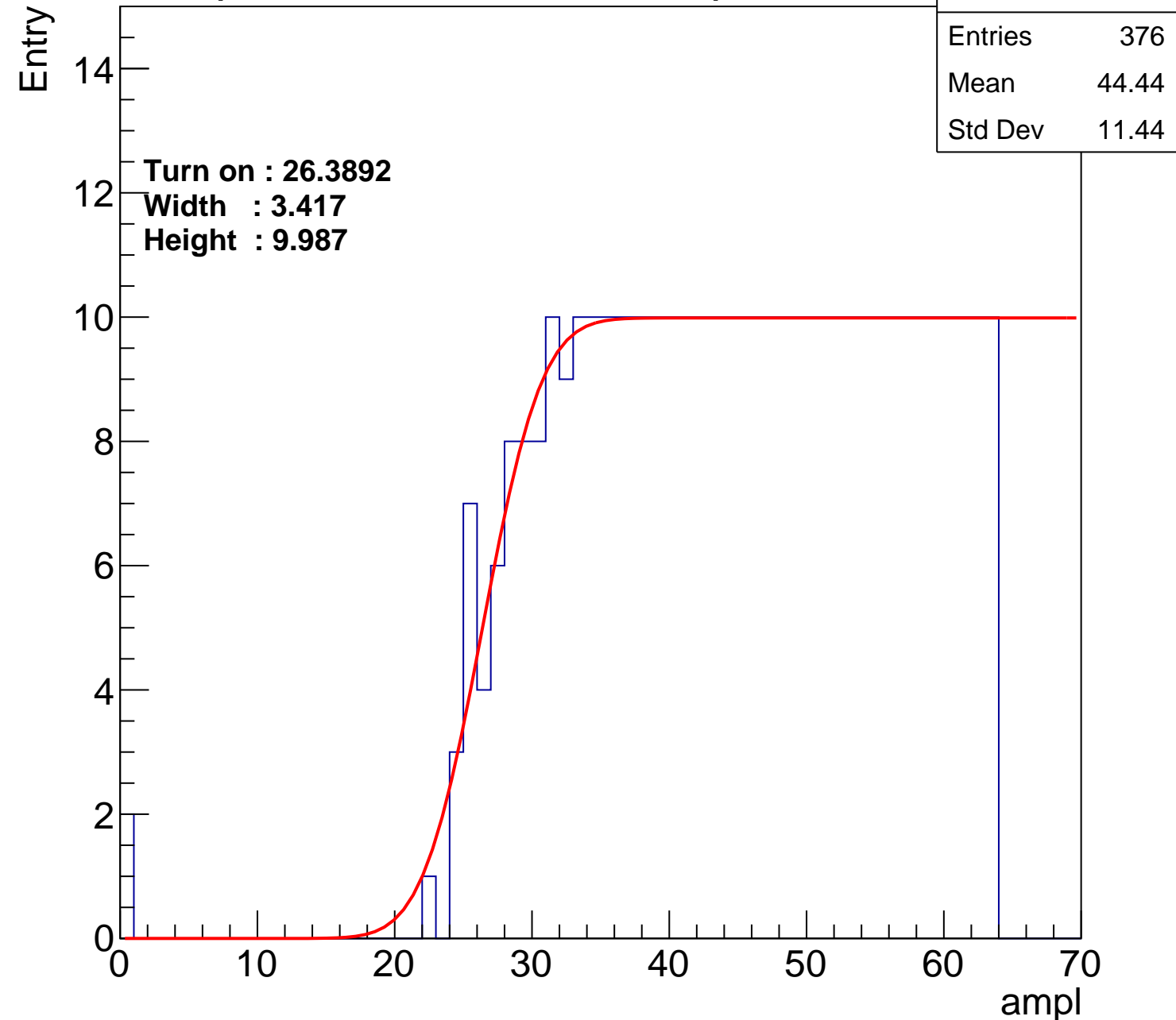
Width : 3.417

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch110

calib_packv5_042523_0143.root, FC#13, port D2

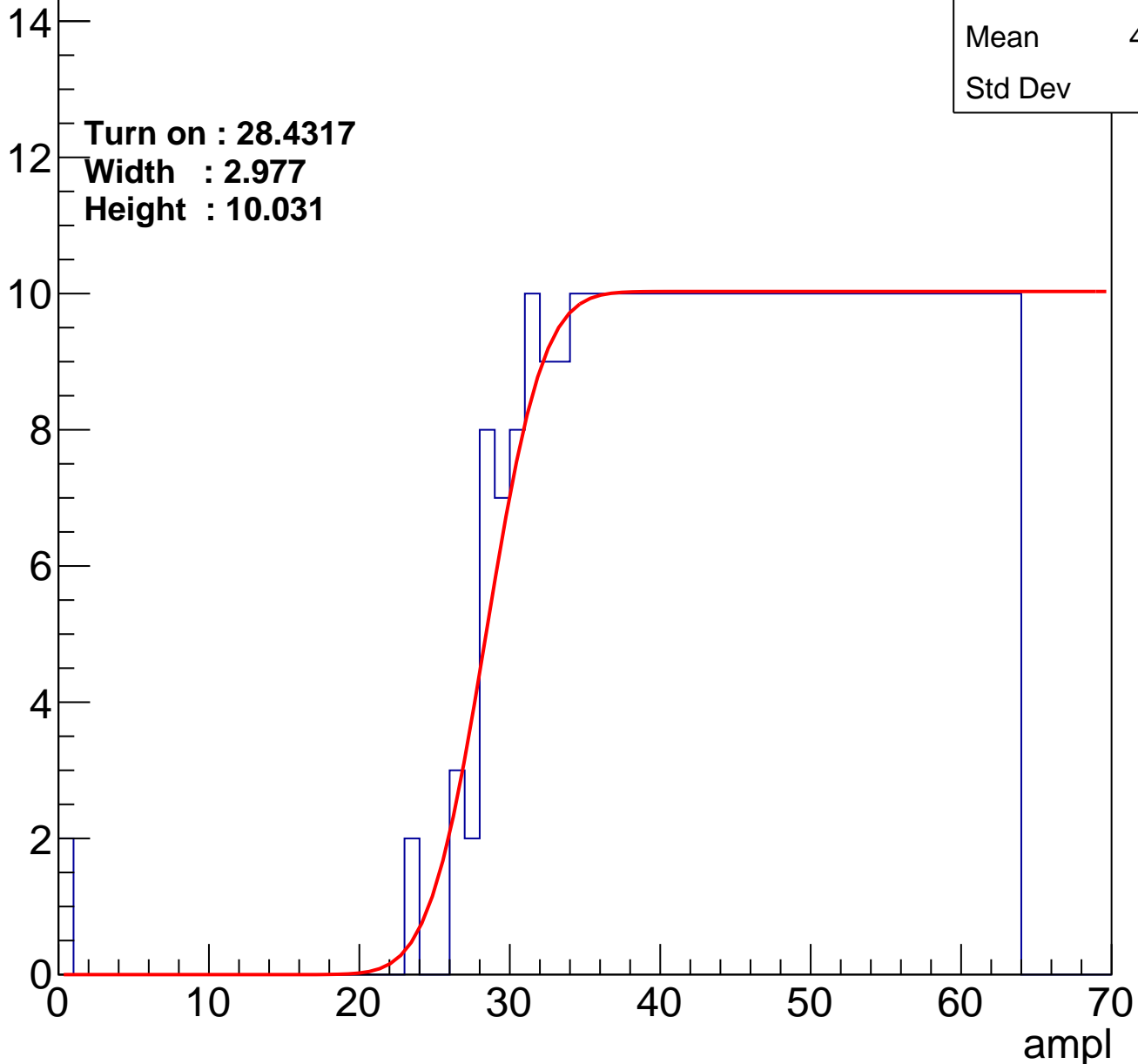
| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.25 |
| Std Dev | 11 |

Turn on : 28.4317

Width : 2.977

Height : 10.031

Entry



B1L003S, U8-ch111

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 344 |
| Mean | 46.09 |
| Std Dev | 10.41 |

Turn on : 29.9999

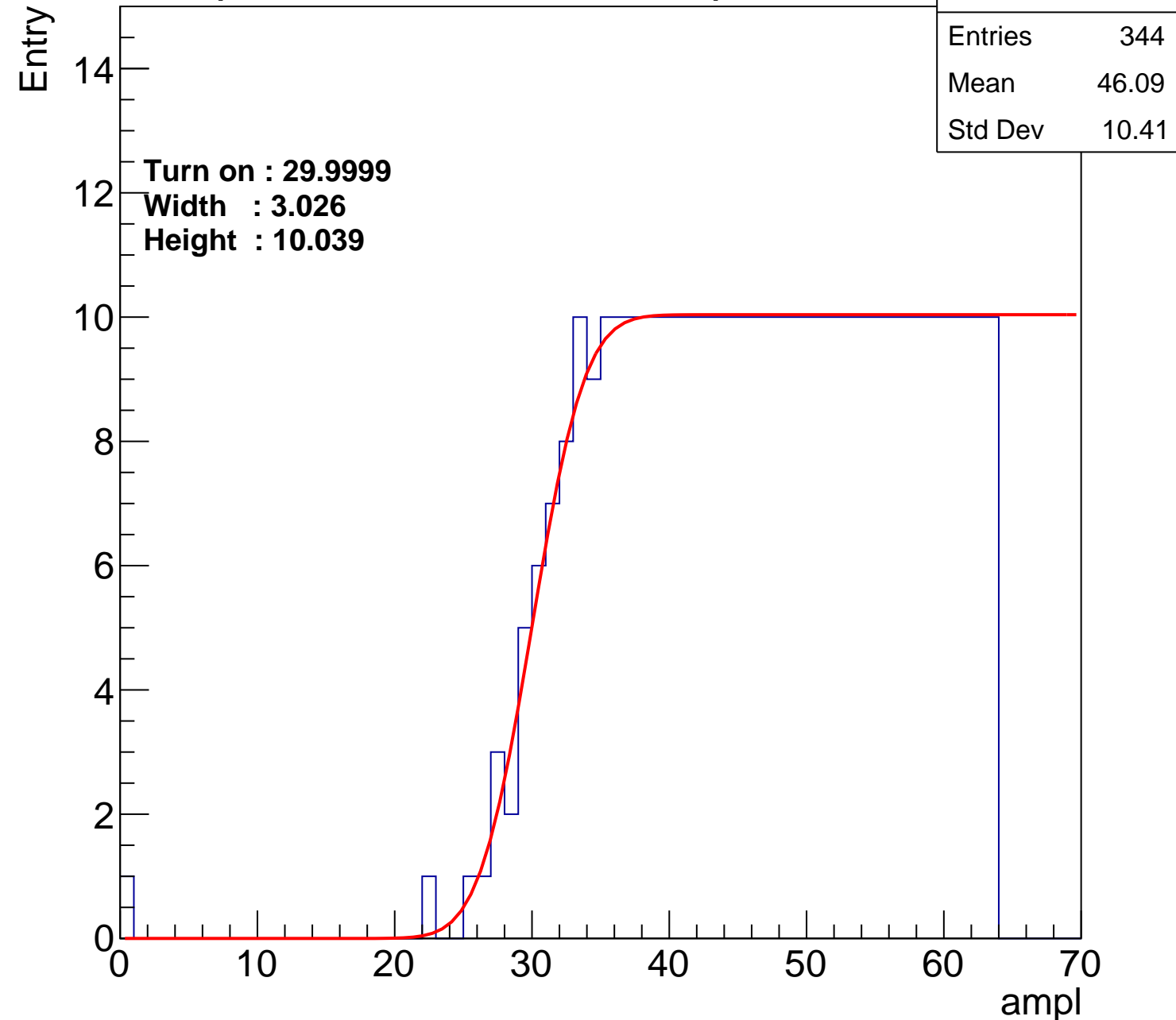
Width : 3.026

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch112

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 380 |
| Mean | 43.99 |
| Std Dev | 12.16 |

Turn on : 27.2422

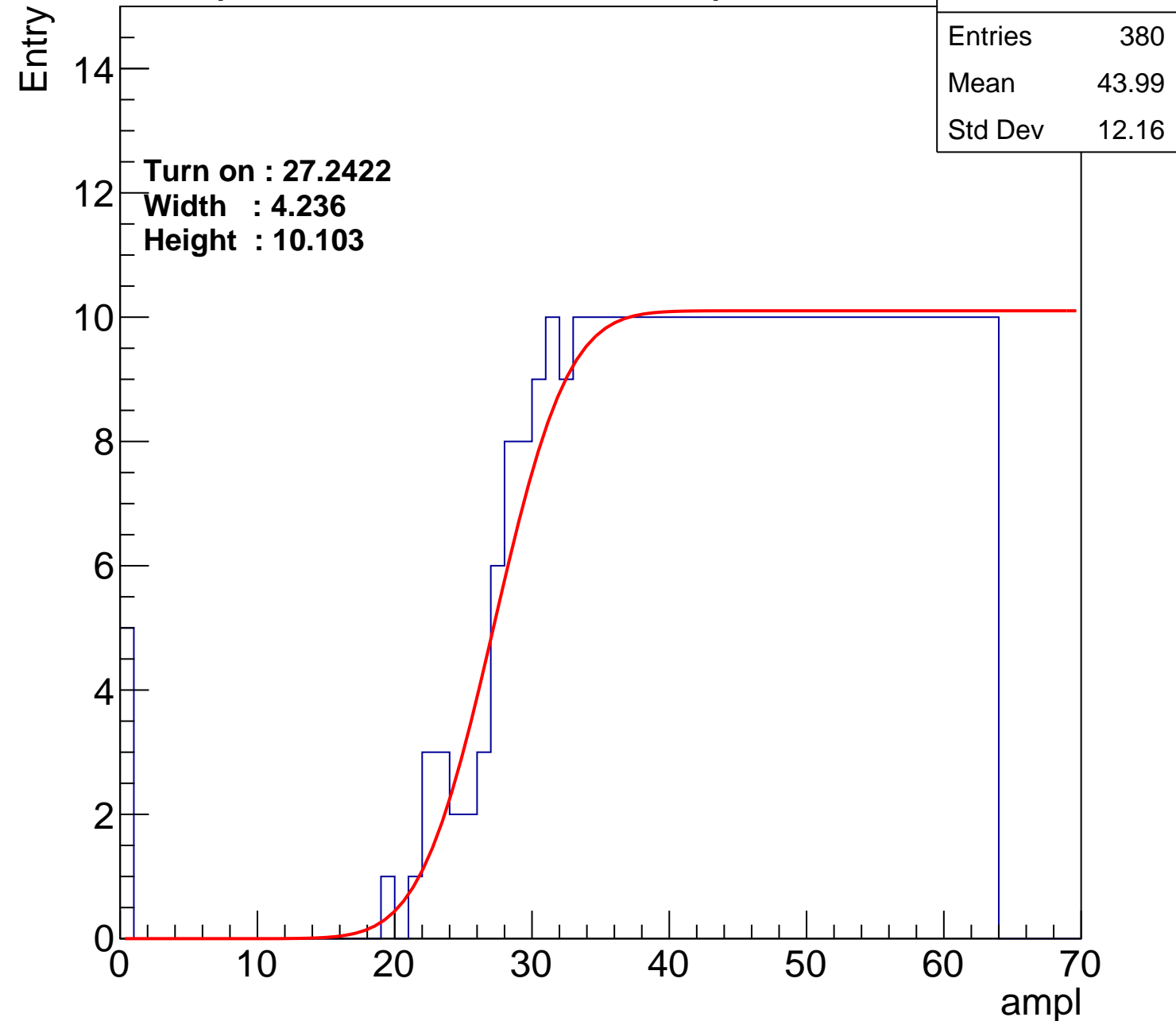
Width : 4.236

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch113

calib_packv5_042523_0143.root, FC#13, port D2

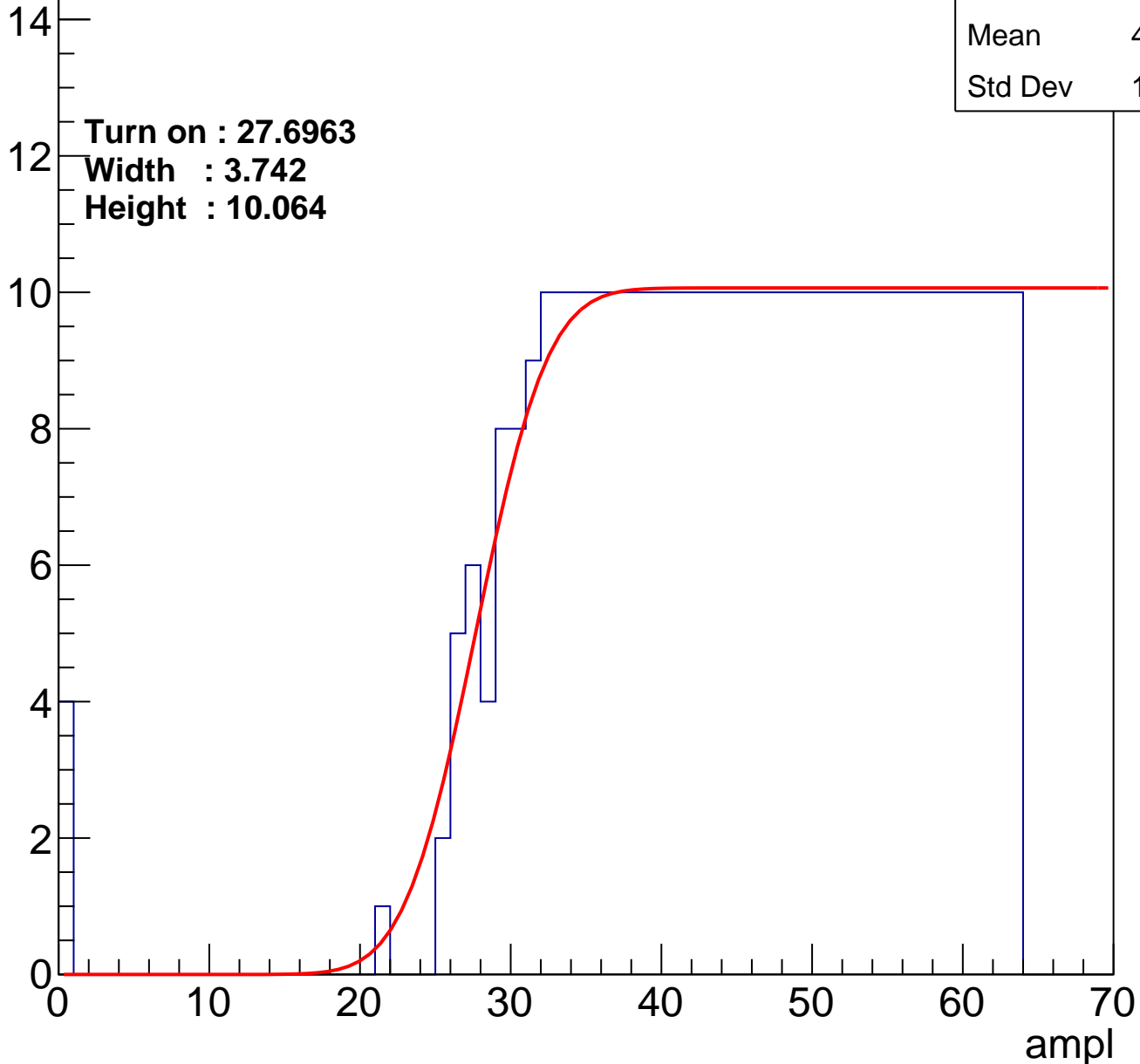
| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.76 |
| Std Dev | 11.58 |

Turn on : 27.6963

Width : 3.742

Height : 10.064

Entry



B1L003S, U8-ch114

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.62 |
| Std Dev | 11.17 |

Turn on : 26.4419

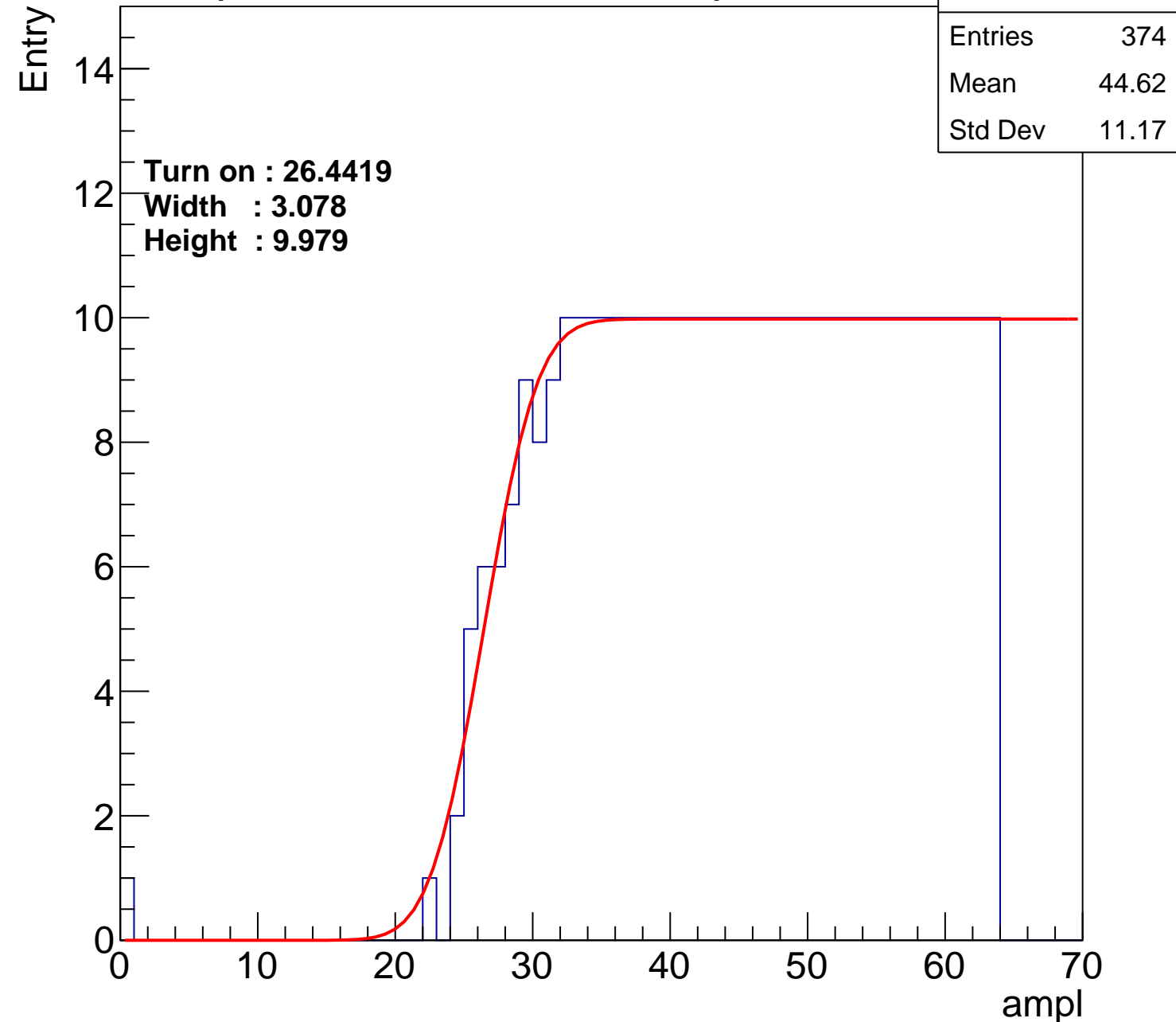
Width : 3.078

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch115

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.02 |
| Std Dev | 11.1 |

Turn on : 28.3683

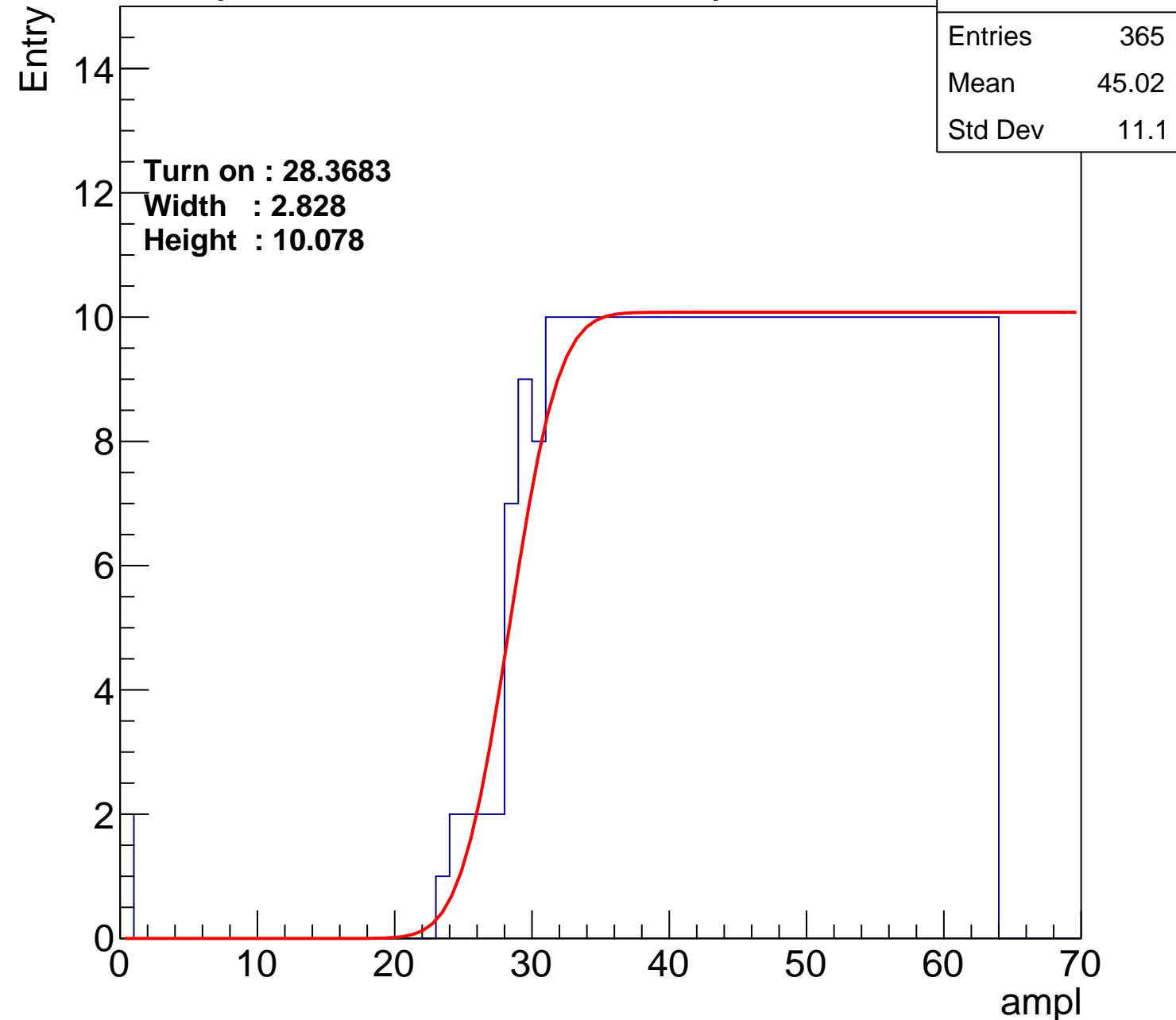
Width : 2.828

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch116

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.53 |
| Std Dev | 11.67 |

Turn on : 27.4164

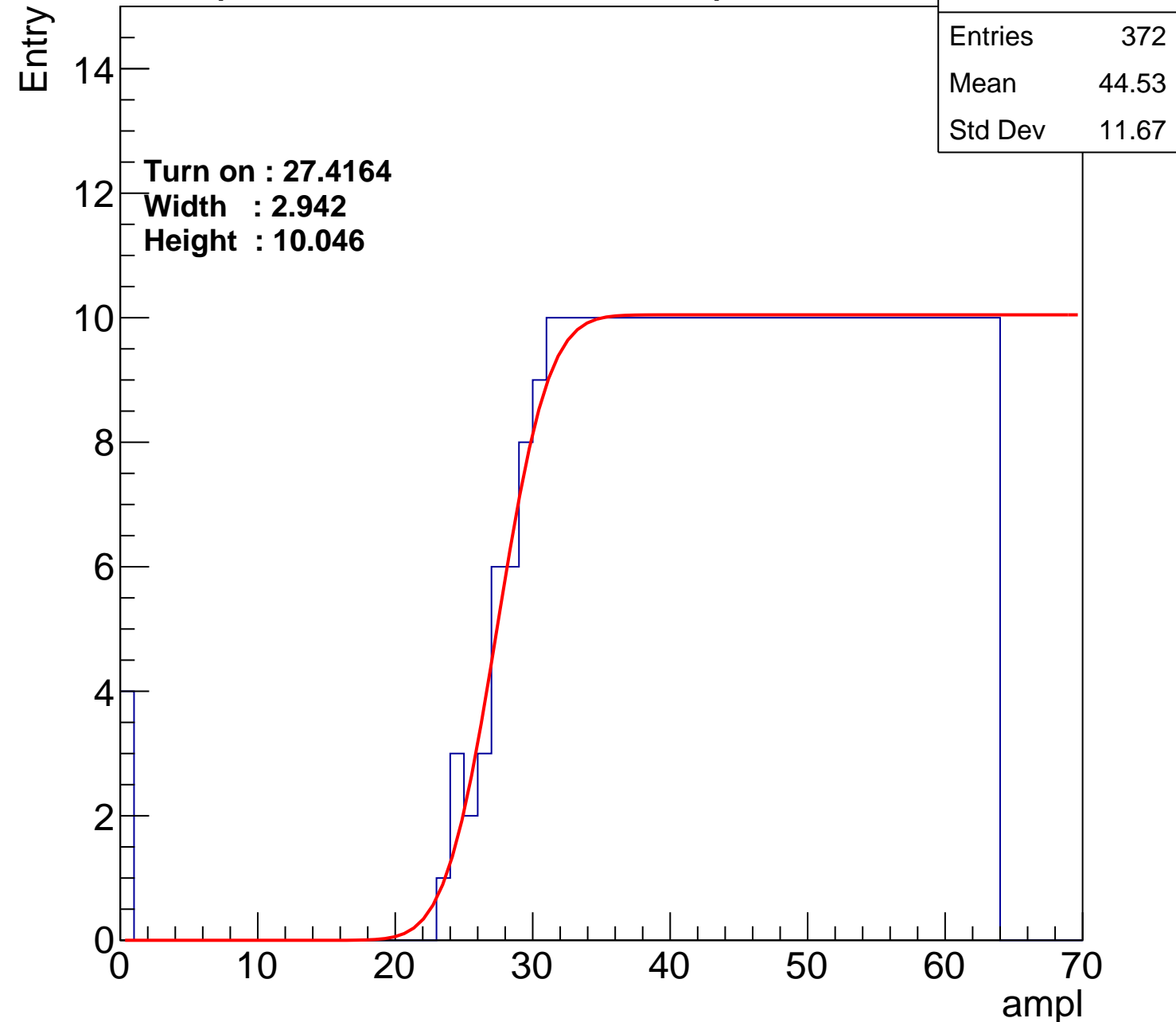
Width : 2.942

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch117

calib_packv5_042523_0143.root, FC#13, port D2

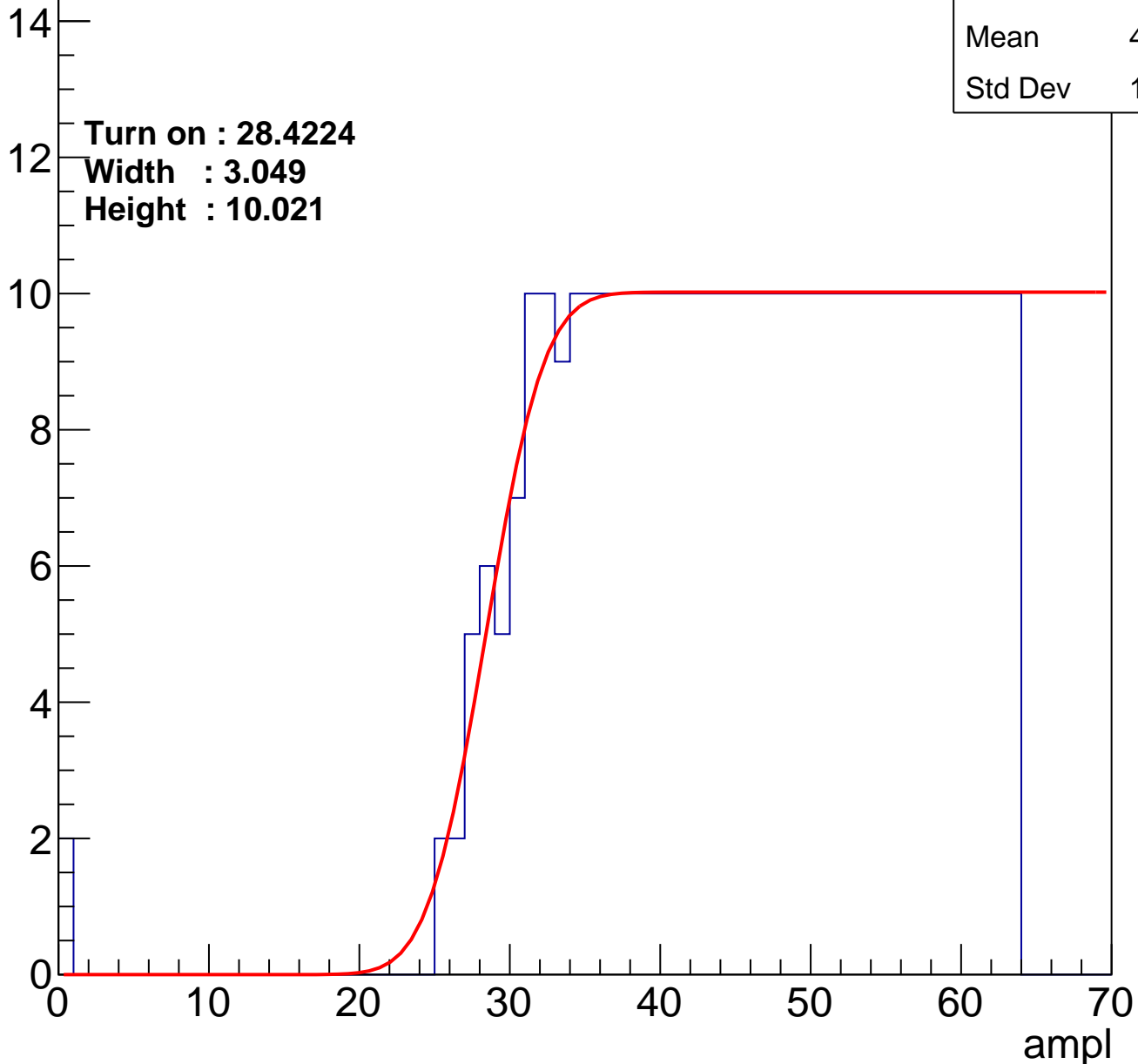
| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.35 |
| Std Dev | 10.94 |

Turn on : 28.4224

Width : 3.049

Height : 10.021

Entry



B1L003S, U8-ch118

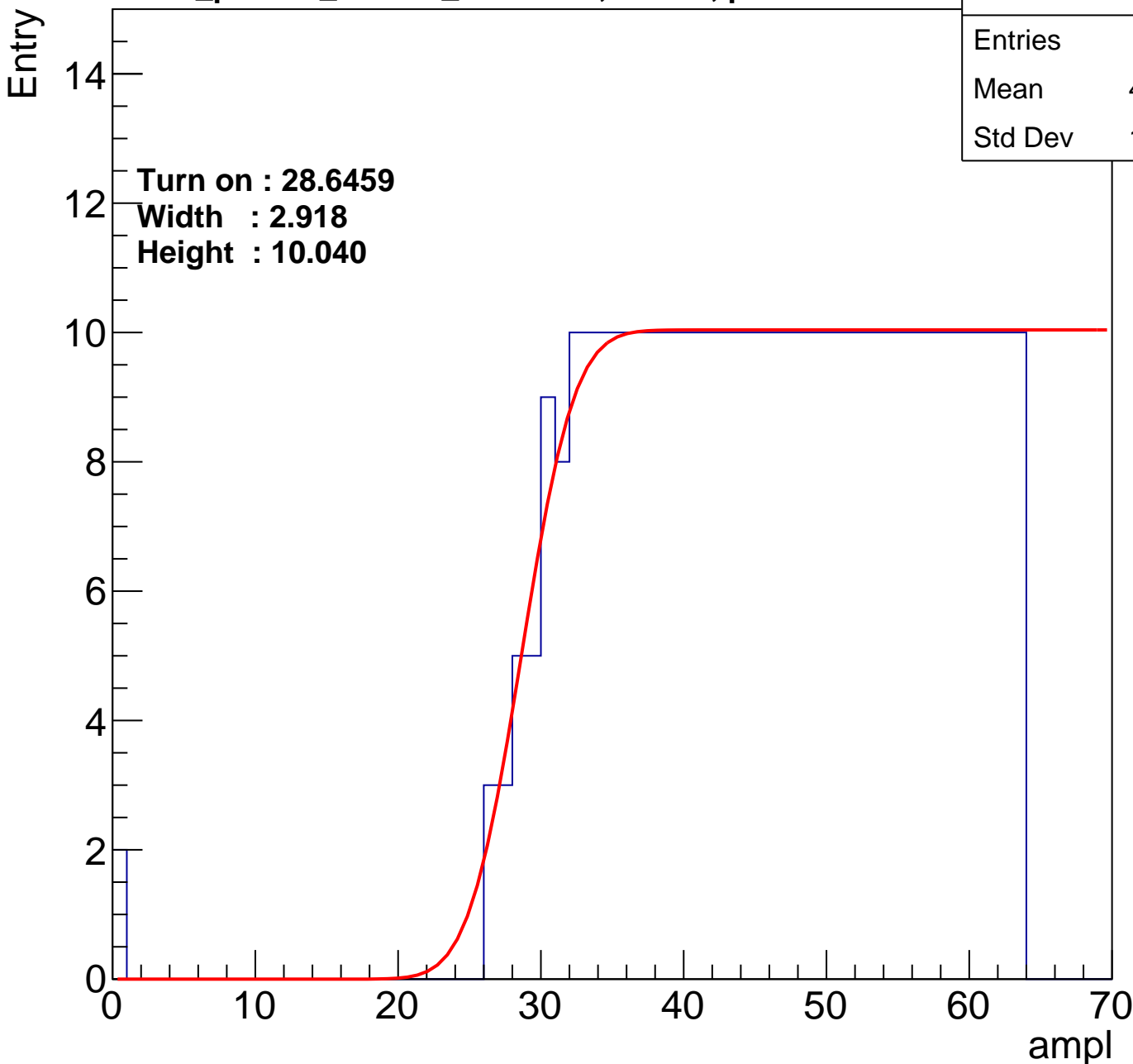
calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 355 |
| Mean | 45.53 |
| Std Dev | 10.83 |

Turn on : 28.6459

Width : 2.918

Height : 10.040



B1L003S, U8-ch119

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.73 |
| Std Dev | 11.11 |

Turn on : 26.8120

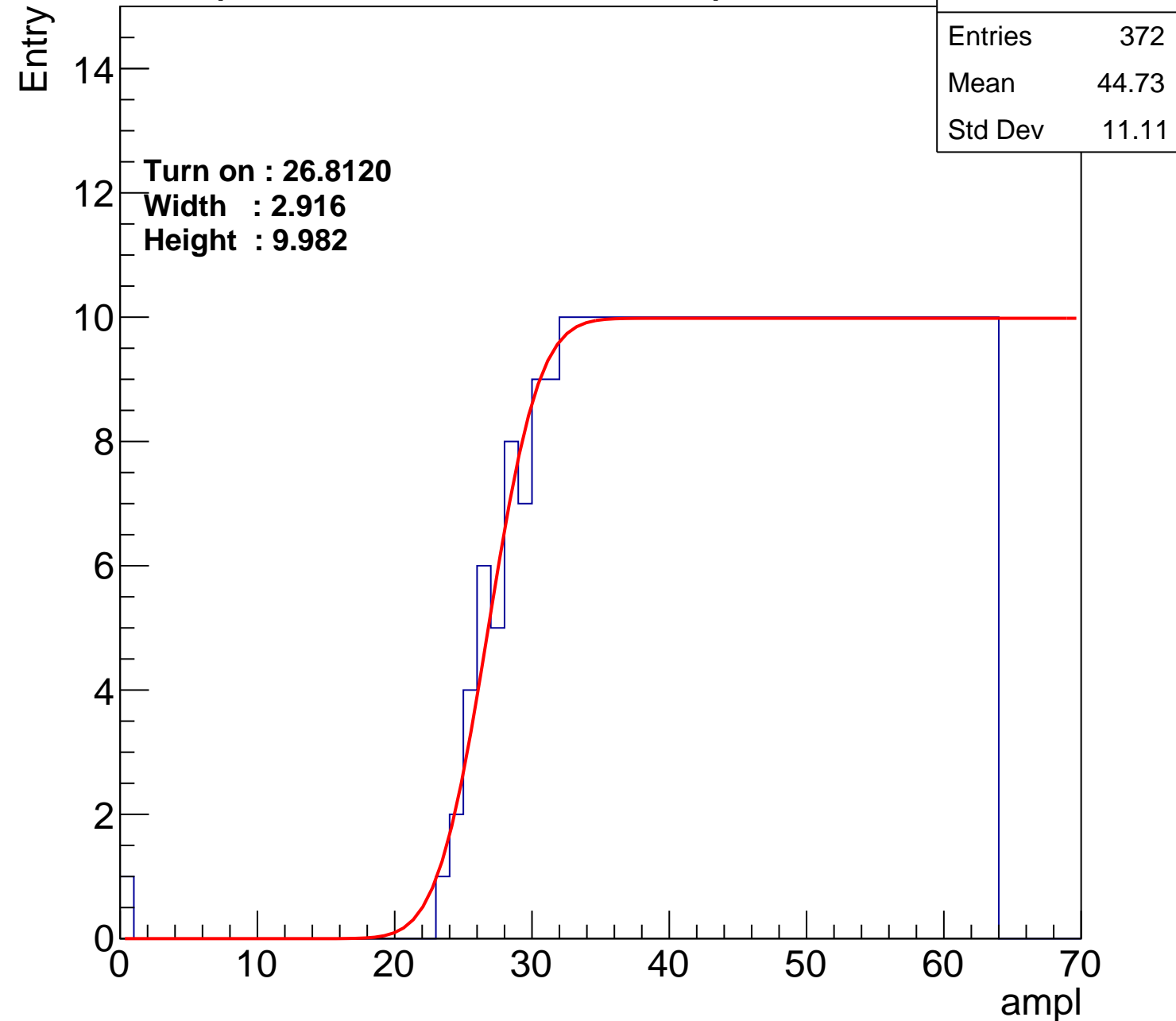
Width : 2.916

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch120

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.7 |
| Std Dev | 11.75 |

Turn on : 28.0328

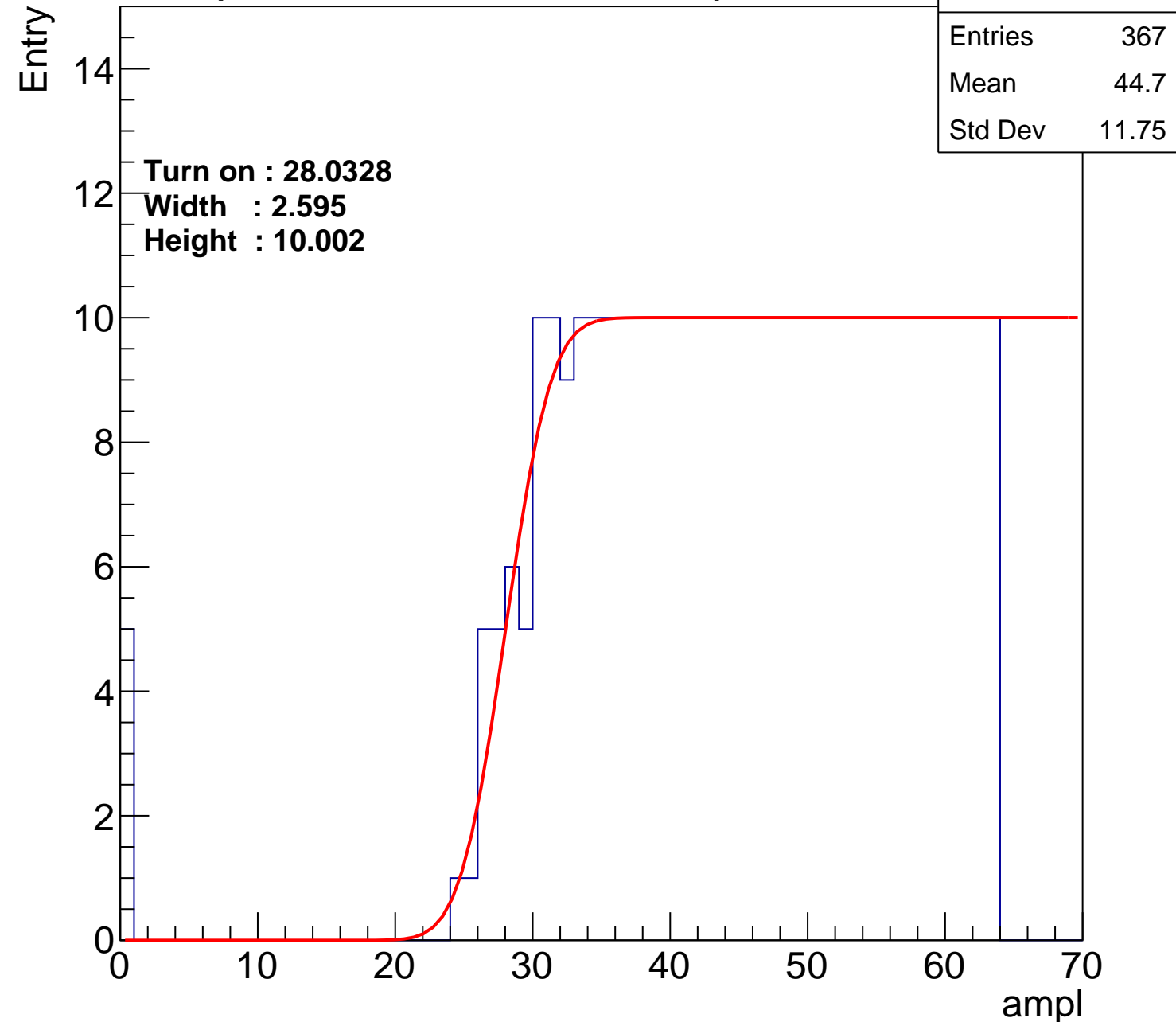
Width : 2.595

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch121

calib_packv5_042523_0143.root, FC#13, port D2

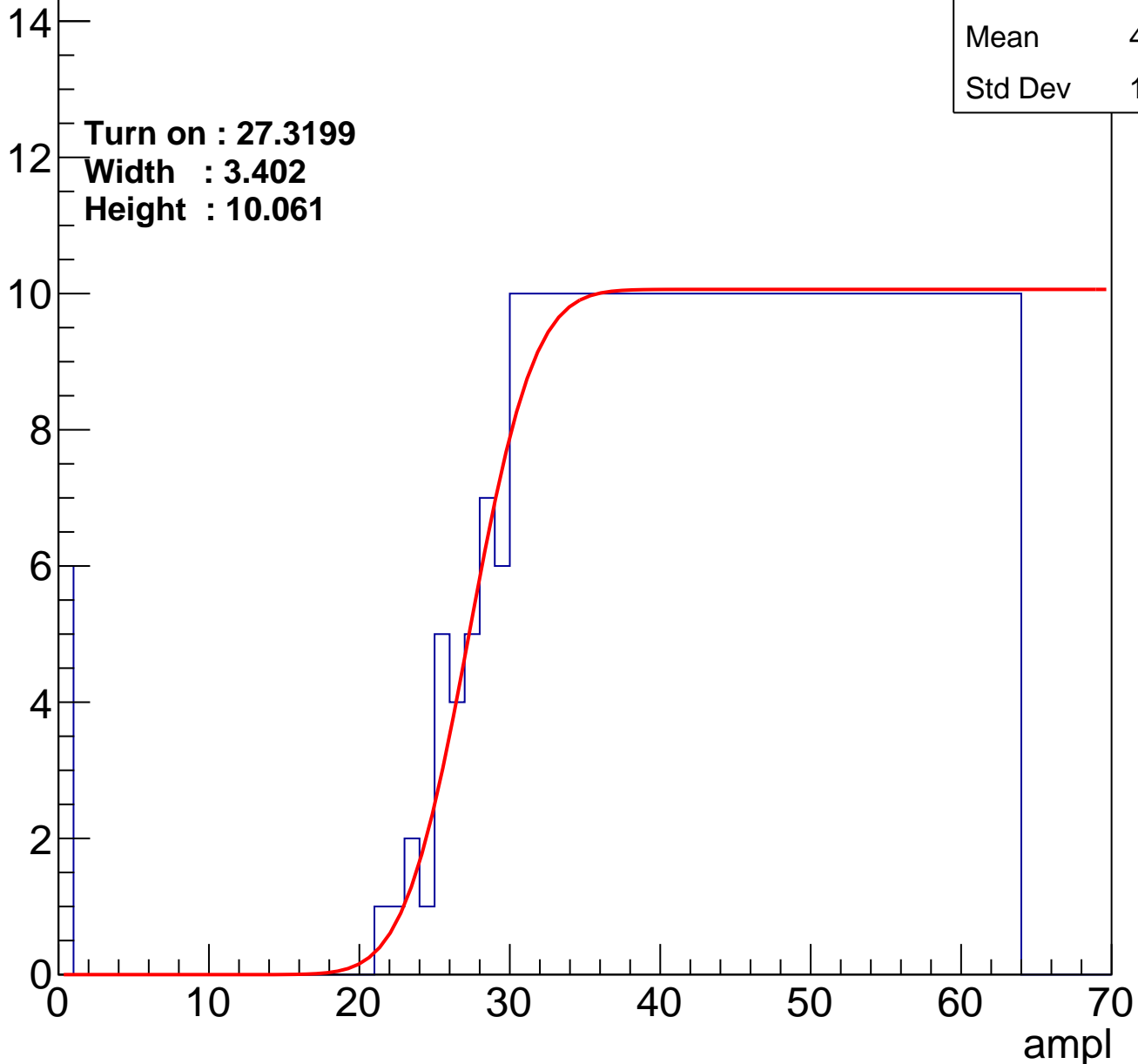
| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.07 |
| Std Dev | 12.22 |

Turn on : 27.3199

Width : 3.402

Height : 10.061

Entry



B1L003S, U8-ch122

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 364 |
| Mean | 44.92 |
| Std Dev | 11.49 |

Turn on : 28.1514

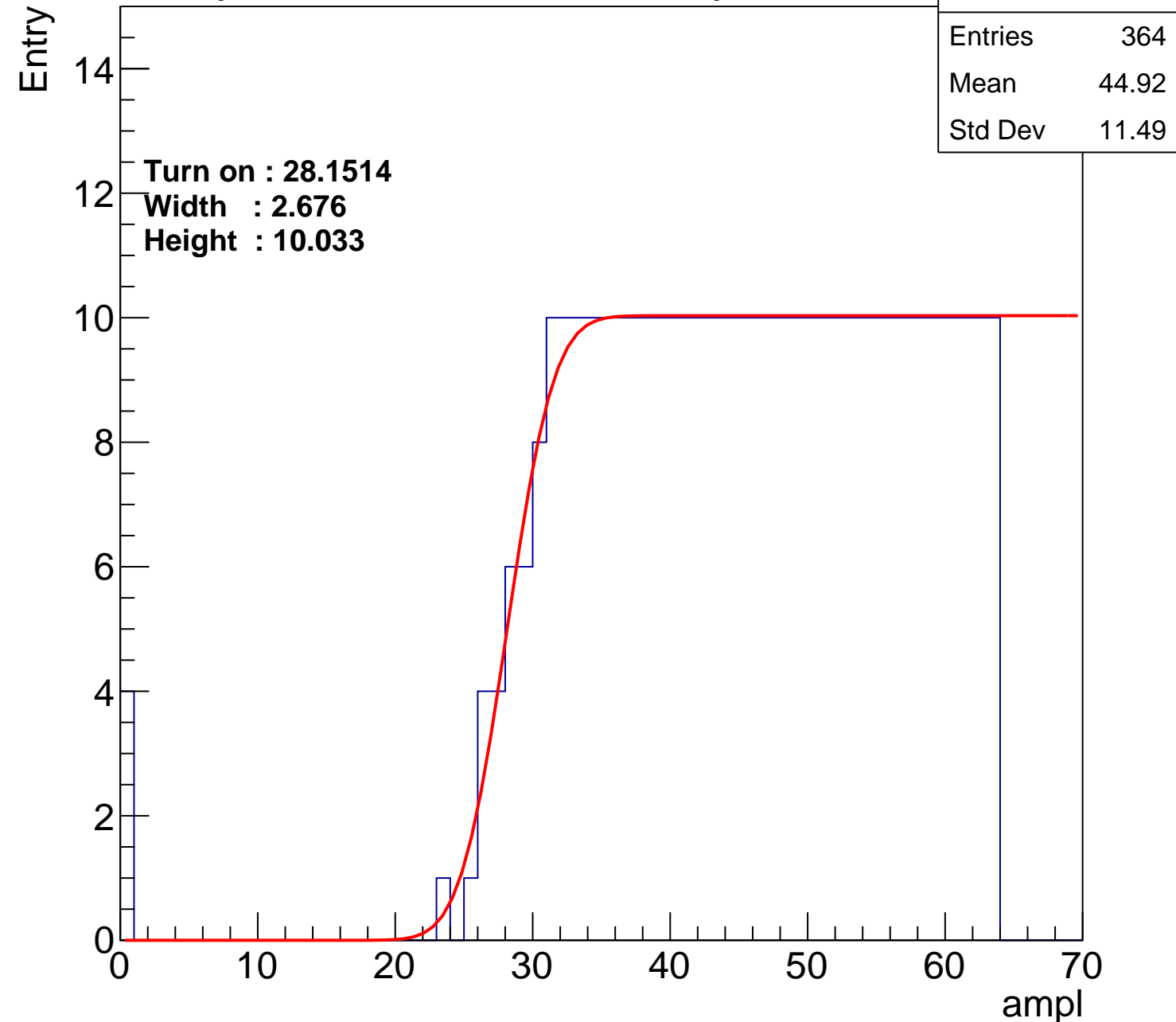
Width : 2.676

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch123

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 365 |
| Mean | 44.95 |
| Std Dev | 11.23 |

Turn on : 28.5620

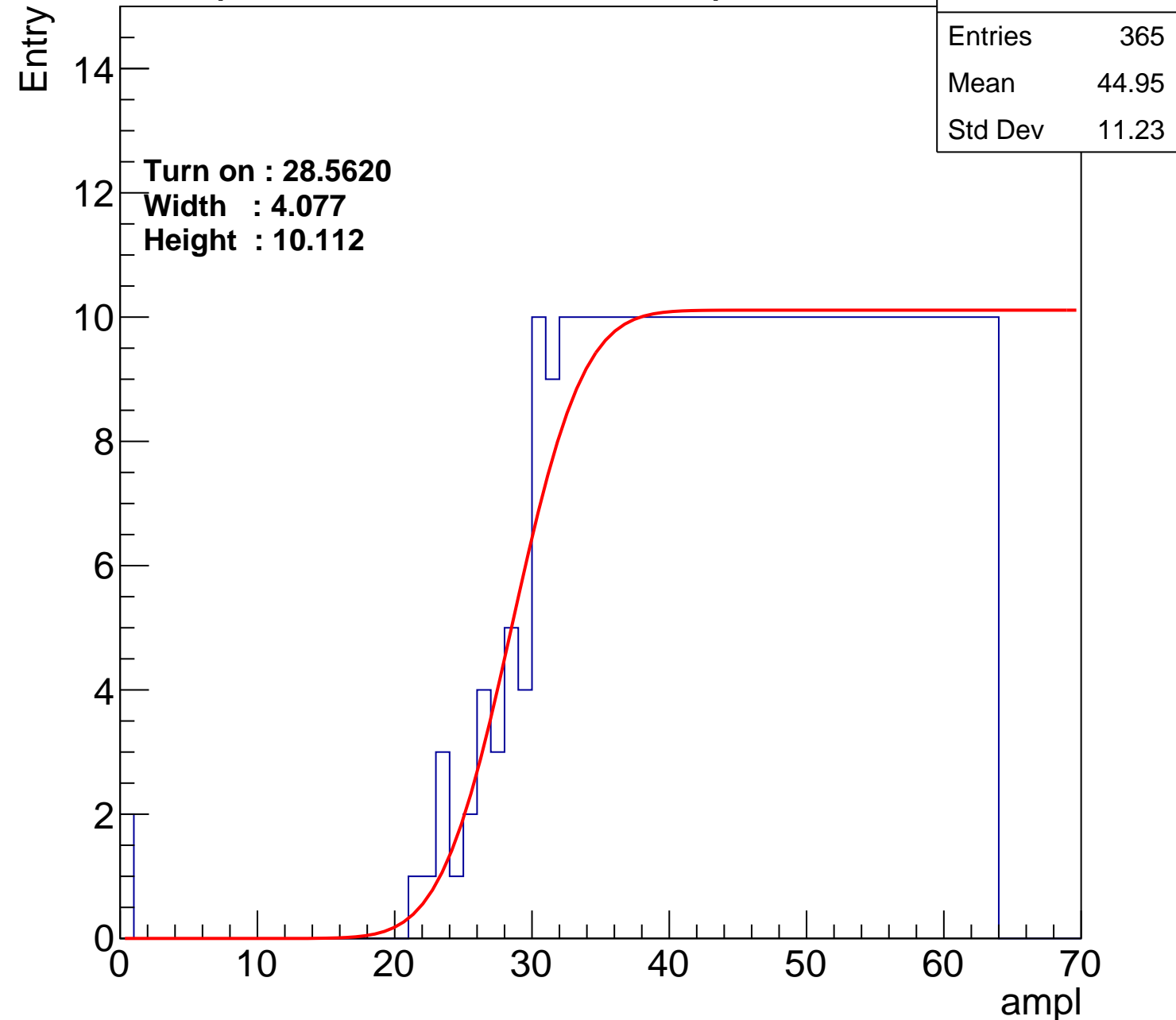
Width : 4.077

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch124

calib_packv5_042523_0143.root, FC#13, port D2

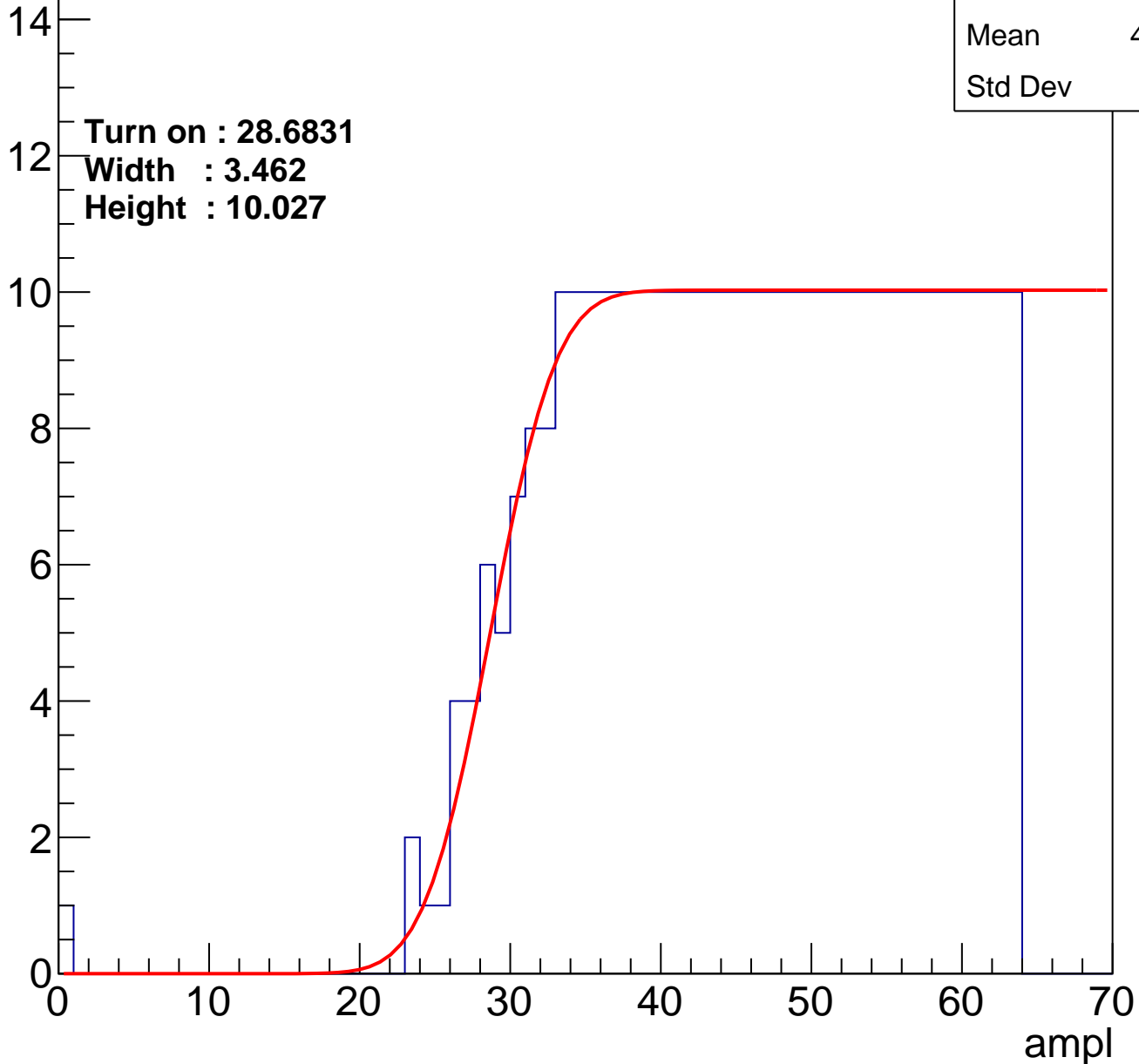
| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.42 |
| Std Dev | 10.8 |

Turn on : 28.6831

Width : 3.462

Height : 10.027

Entry



B1L003S, U8-ch125

calib_packv5_042523_0143.root, FC#13, port D2

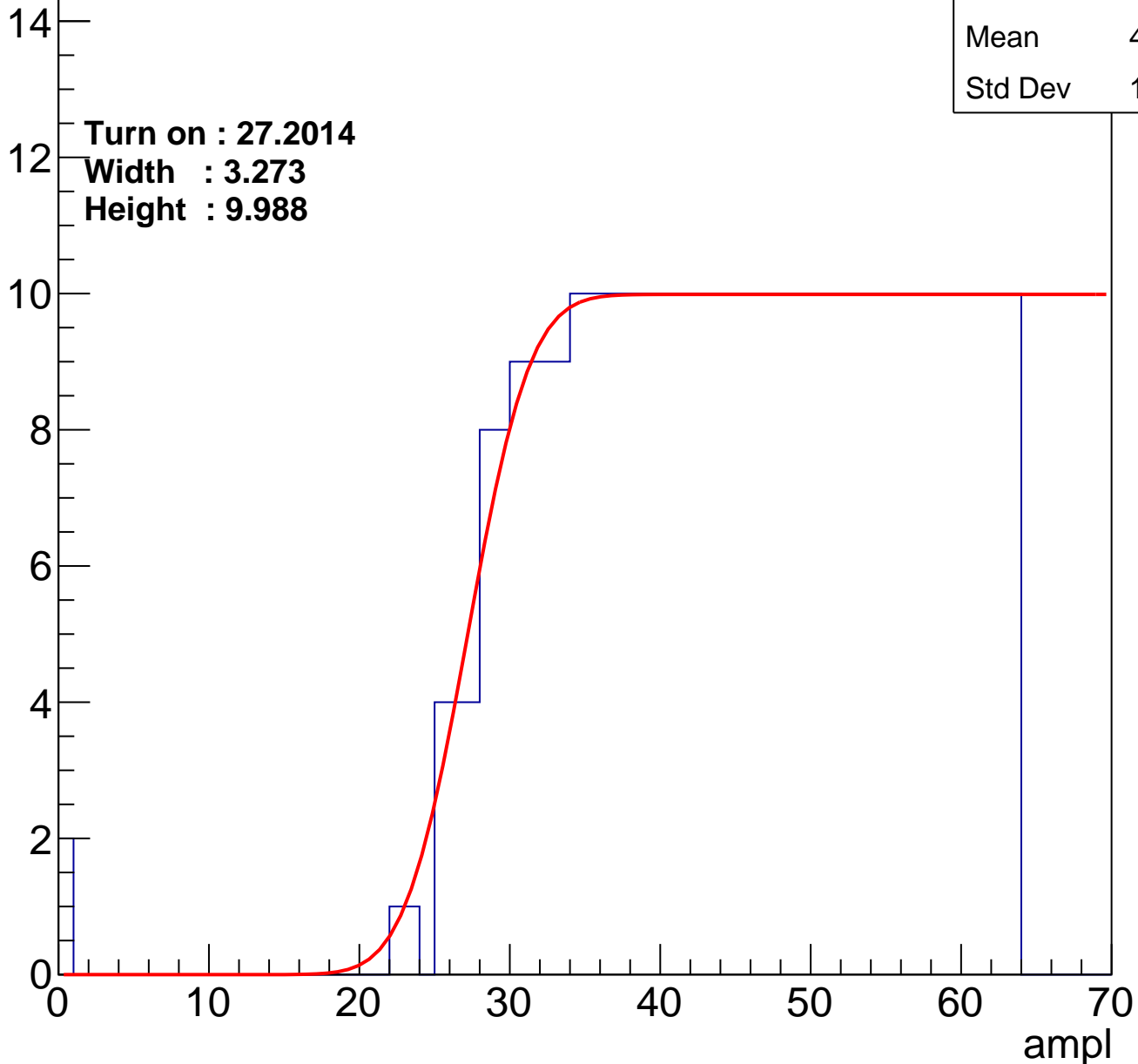
| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.83 |
| Std Dev | 11.24 |

Turn on : 27.2014

Width : 3.273

Height : 9.988

Entry



B1L003S, U8-ch126

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.14 |
| Std Dev | 11.87 |

Turn on : 26.8224

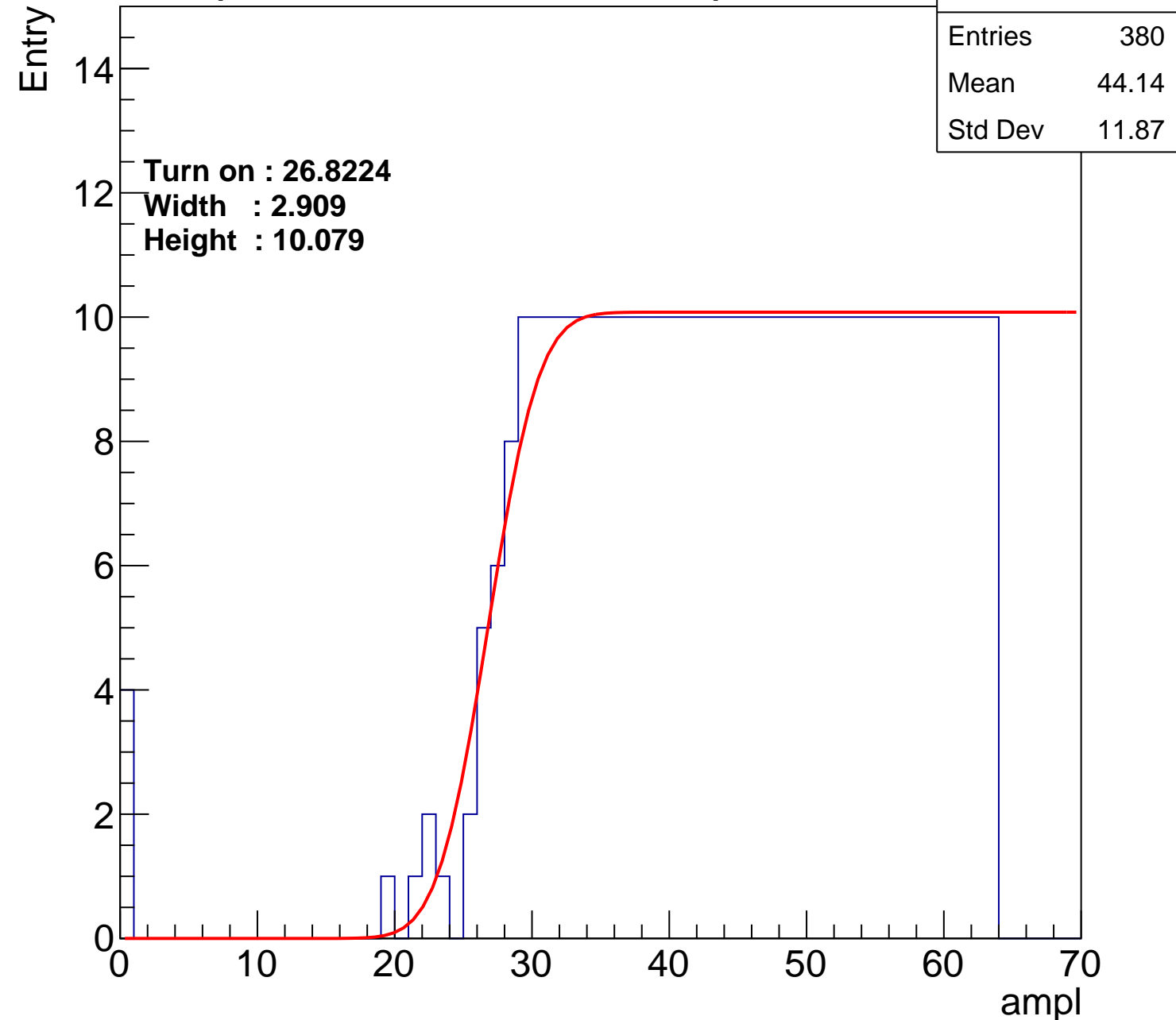
Width : 2.909

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch127

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.37 |
| Std Dev | 11.7 |

Turn on : 27.3271

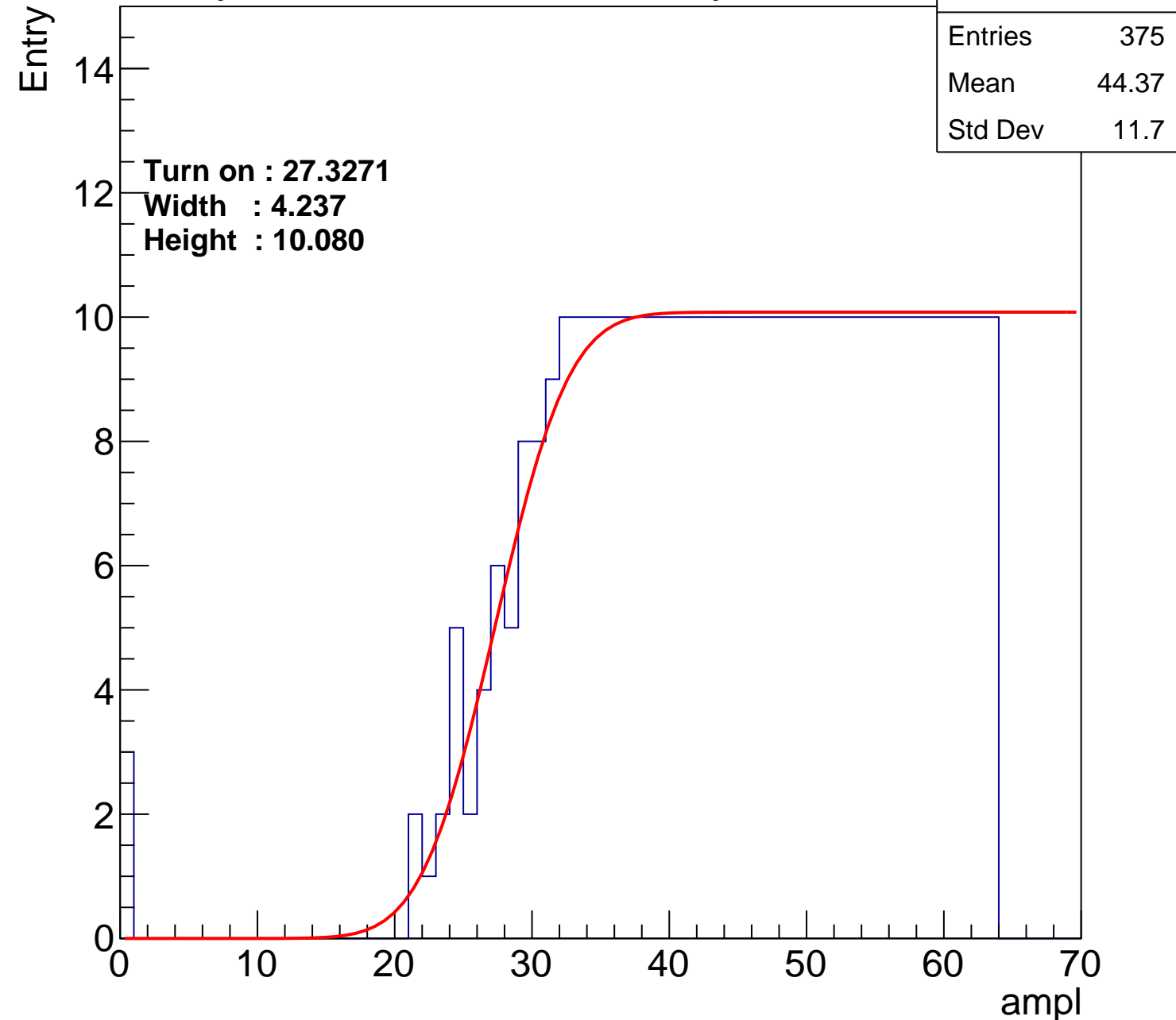
Width : 4.237

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U8-ch127

calib_packv5_042523_0143.root, FC#13, port D2

| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.37 |
| Std Dev | 11.7 |

Turn on : 27.3271

Width : 4.237

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl

