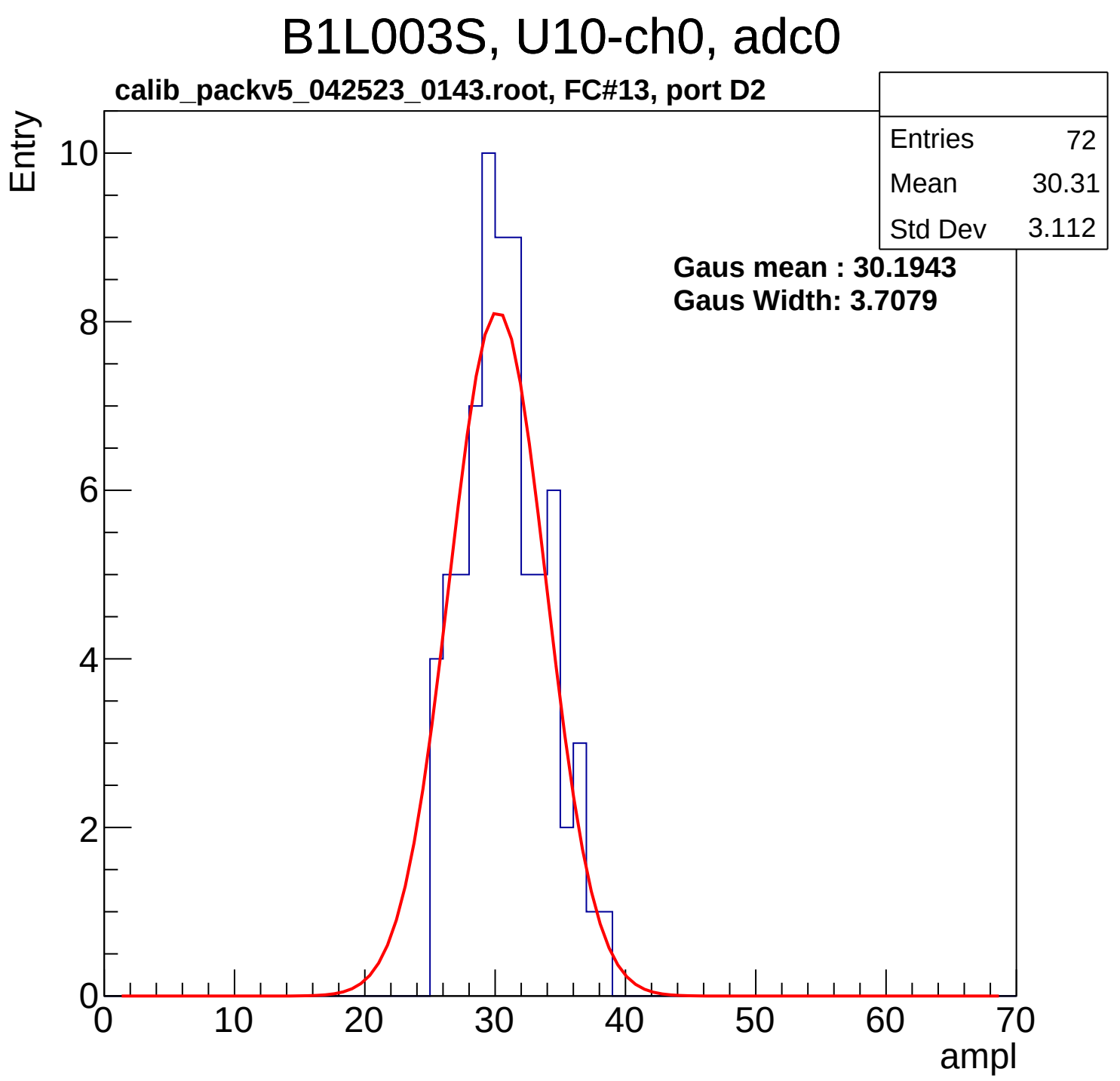
B1L003S, U10-ch0, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 30.31
Std Dev 3.112
Gaus mean : 30.1943
Gaus Width: 3.7079
Entry
ampl
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2
4
6
8
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0
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40
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60
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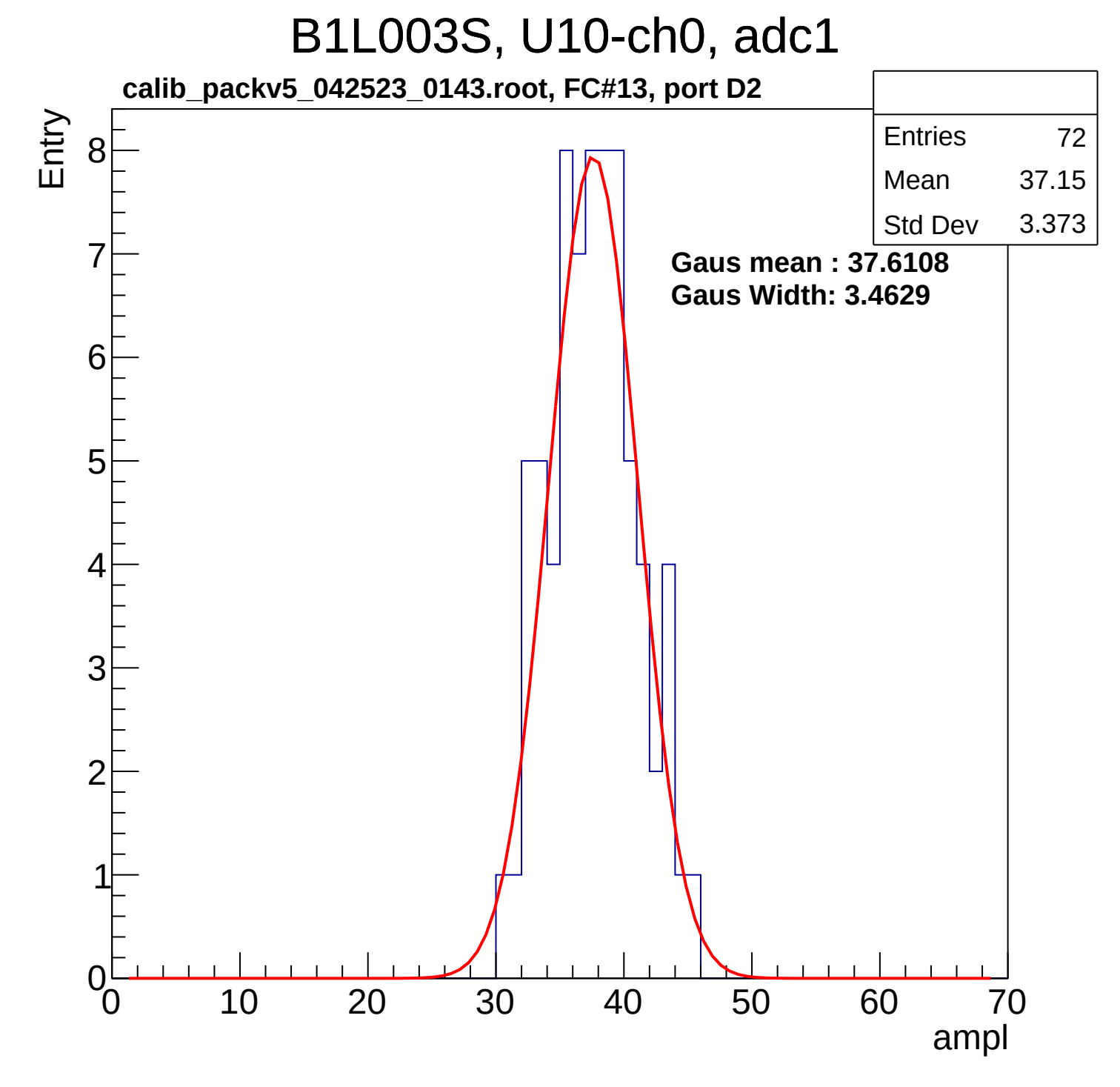
B1L003S, U10-ch0, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 37.15
Std Dev 3.373
Gaus mean : 37.6108
Gaus Width: 3.4629
Entry
ampl
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1
2
3
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5
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0
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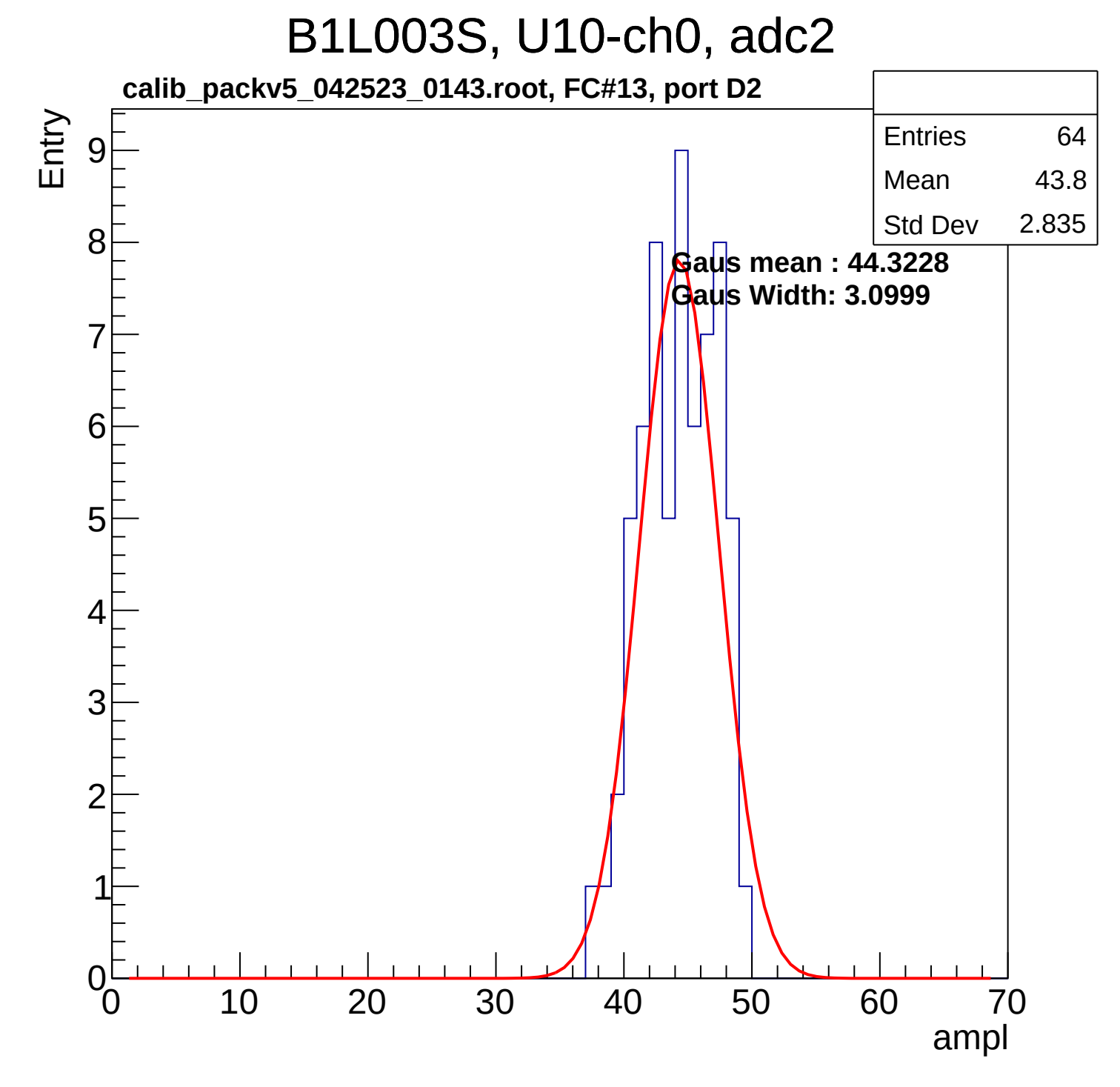
B1L003S, U10-ch0, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.8
Std Dev 2.835
Gaus mean : 44.3228
Gaus Width: 3.0999
Entry
ampl
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3
4
5
6
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0
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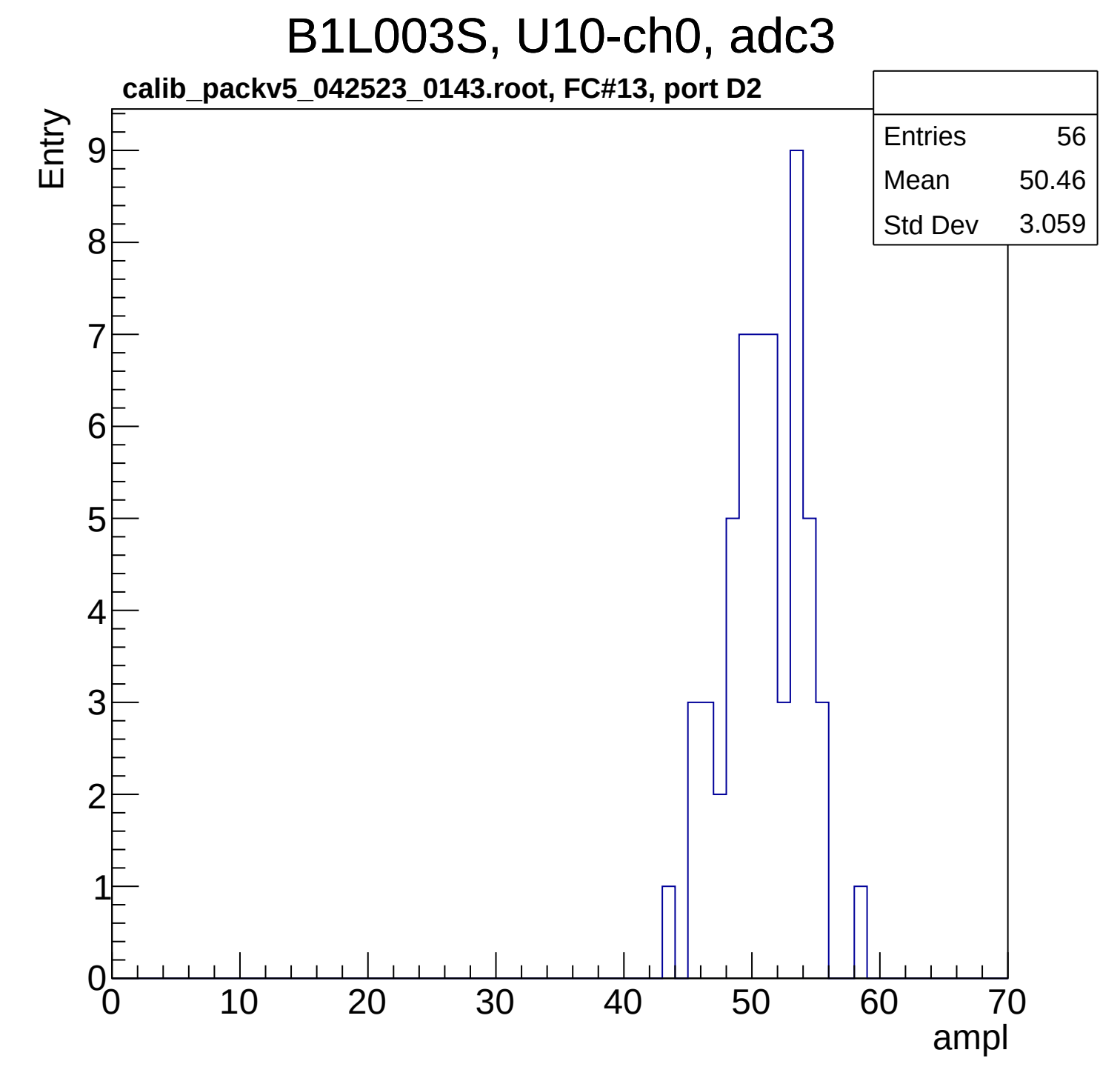
B1L003S, U10-ch0, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 50.46
Std Dev 3.059
Entry
ampl
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3
4
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6
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0
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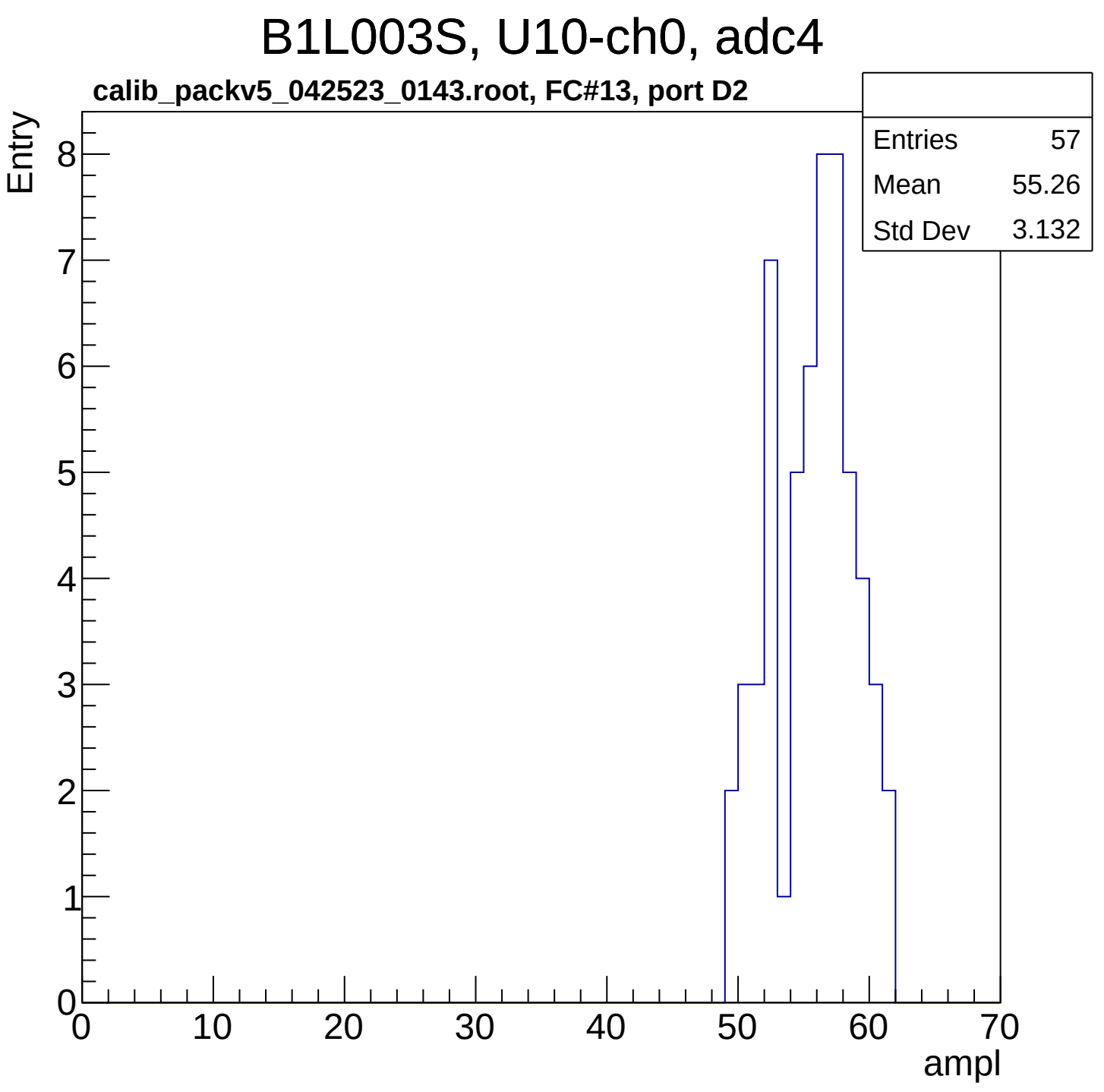
B1L003S, U10-ch0, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.26
Std Dev 3.132
Entry
ampl
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2
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4
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7
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0
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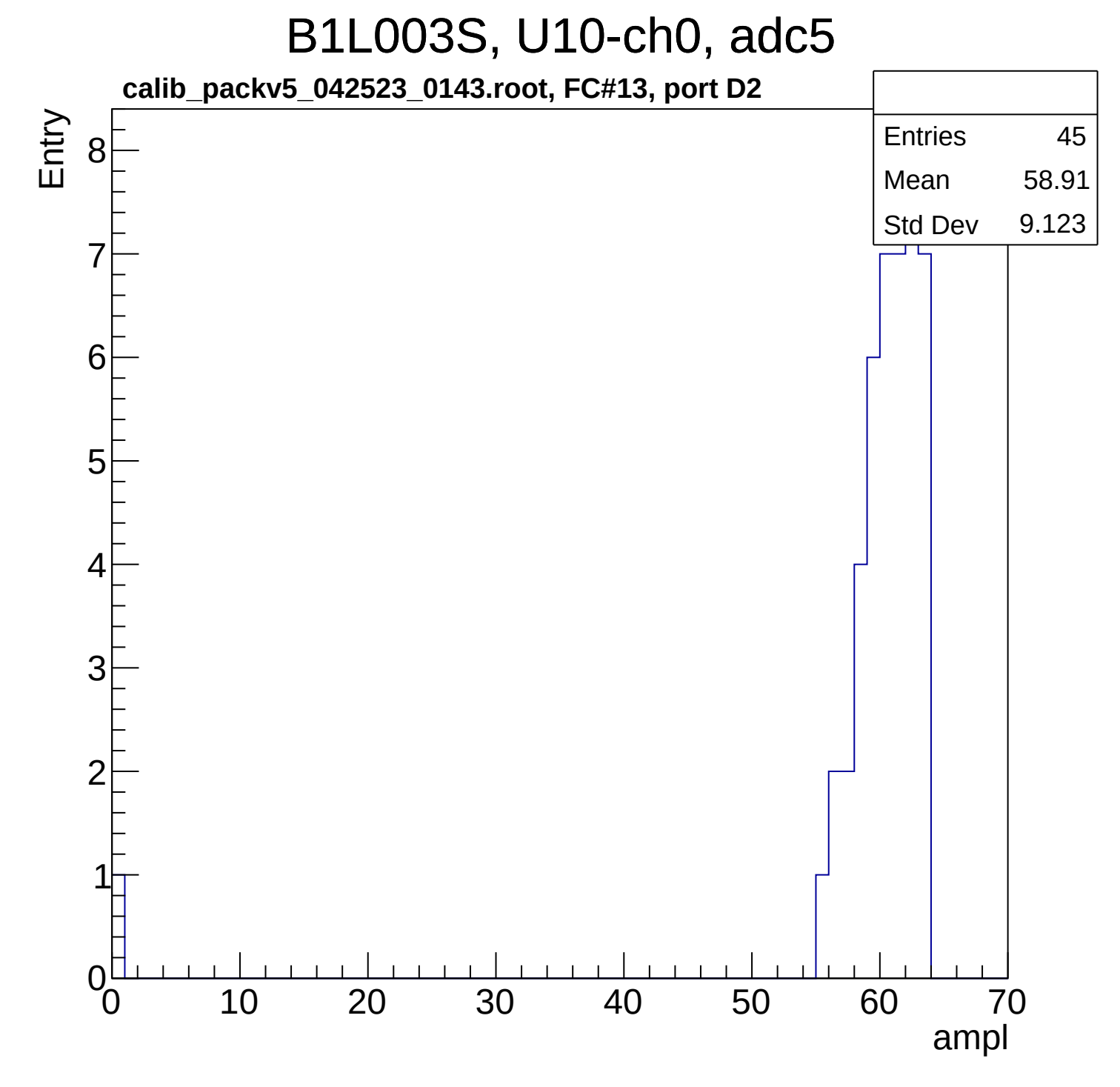
B1L003S, U10-ch0, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.91
Std Dev 9.123
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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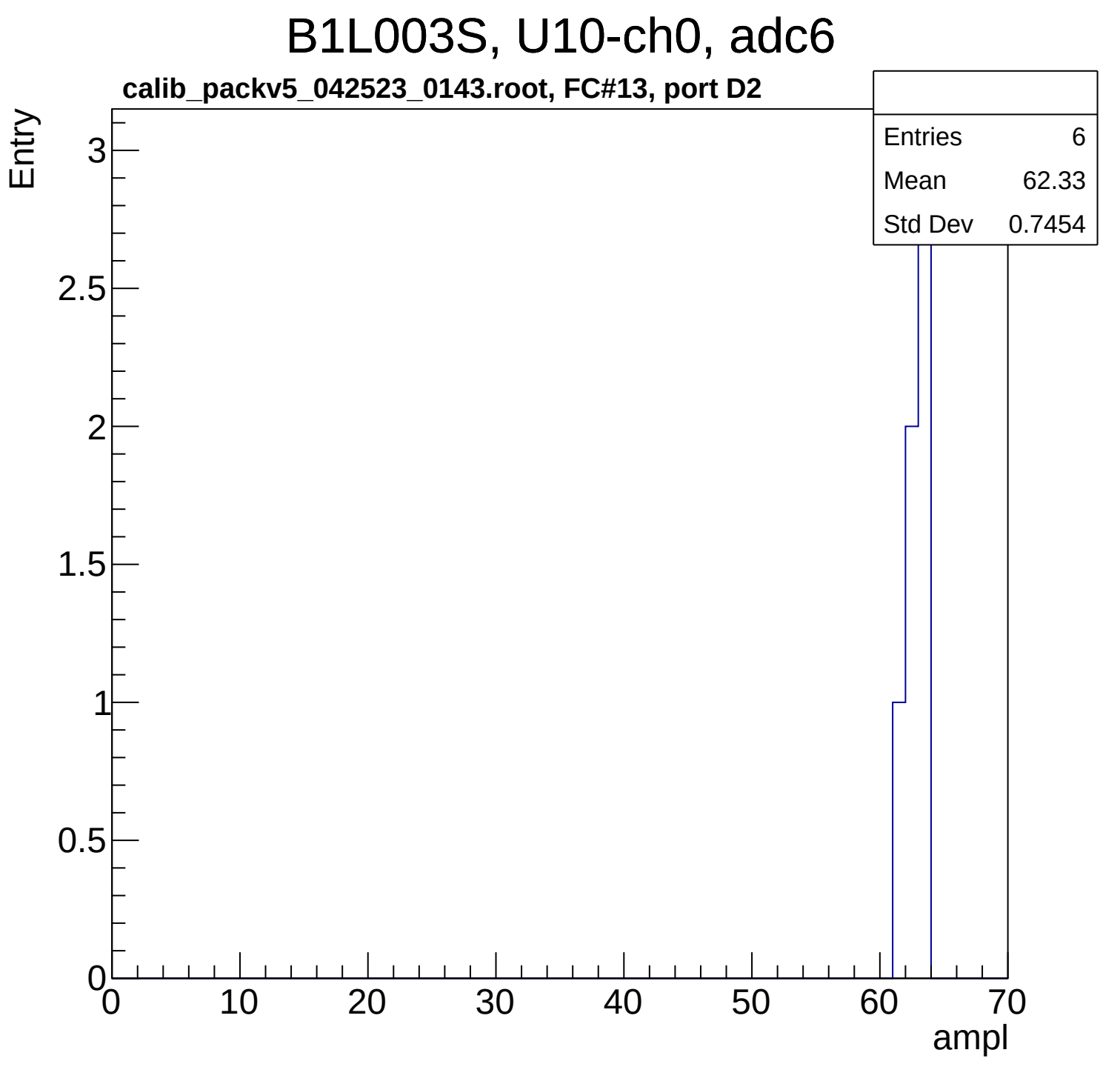
B1L003S, U10-ch0, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
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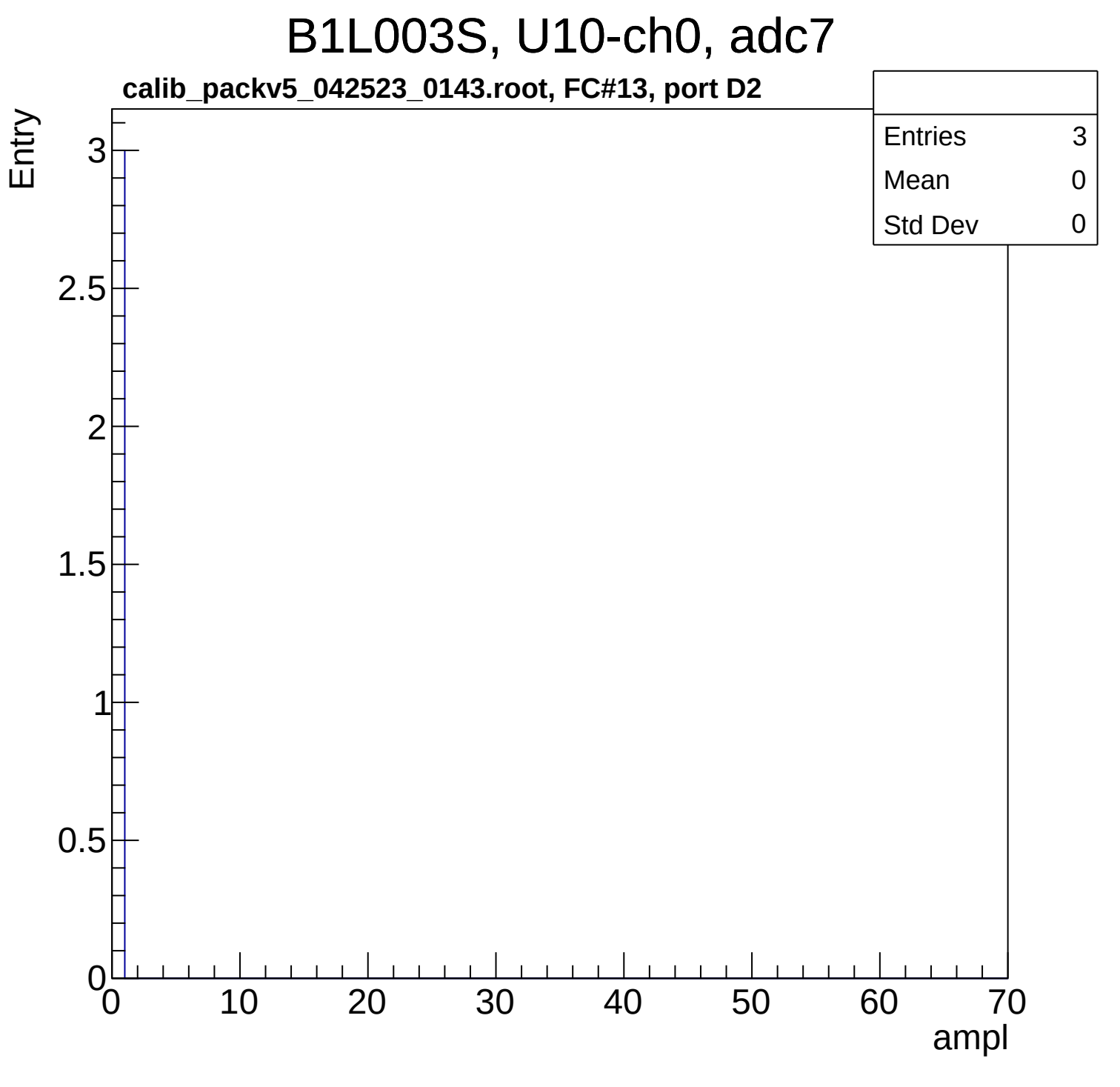
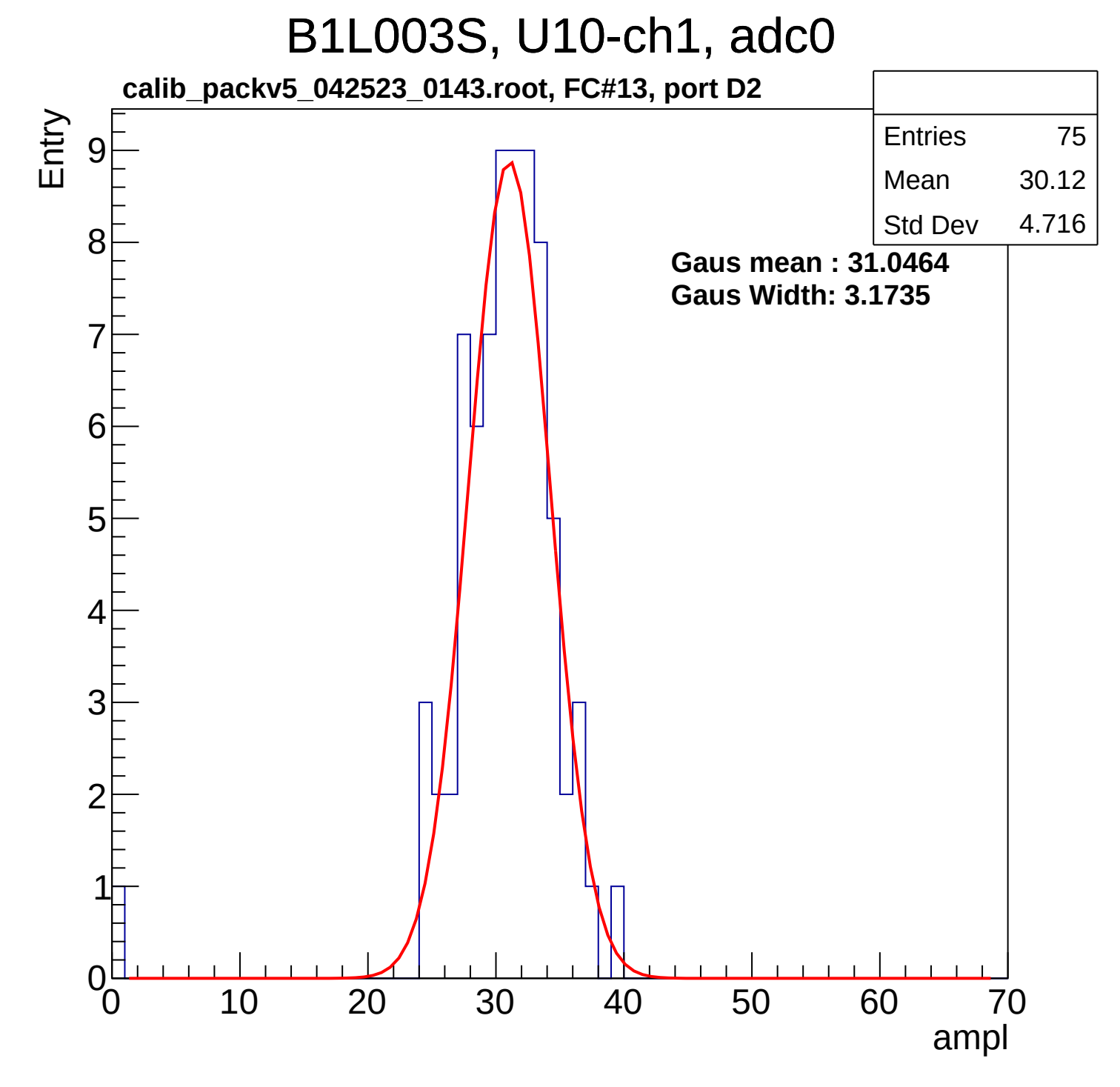
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch0, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch1, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 30.12
Std Dev 4.716
Gaus mean : 31.0464
Gaus Width: 3.1735
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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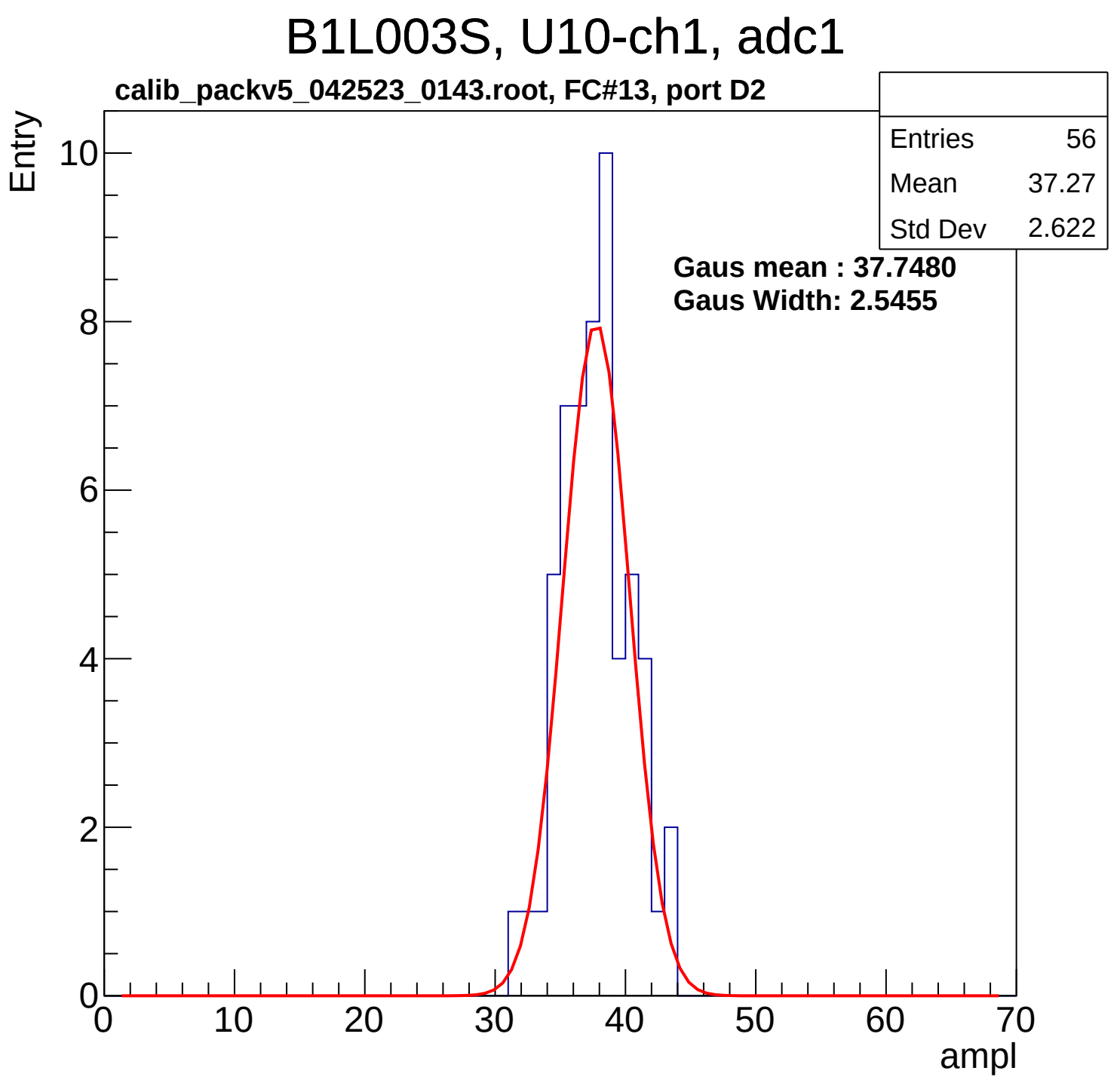
B1L003S, U10-ch1, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 37.27
Std Dev 2.622
Gaus mean : 37.7480
Gaus Width: 2.5455
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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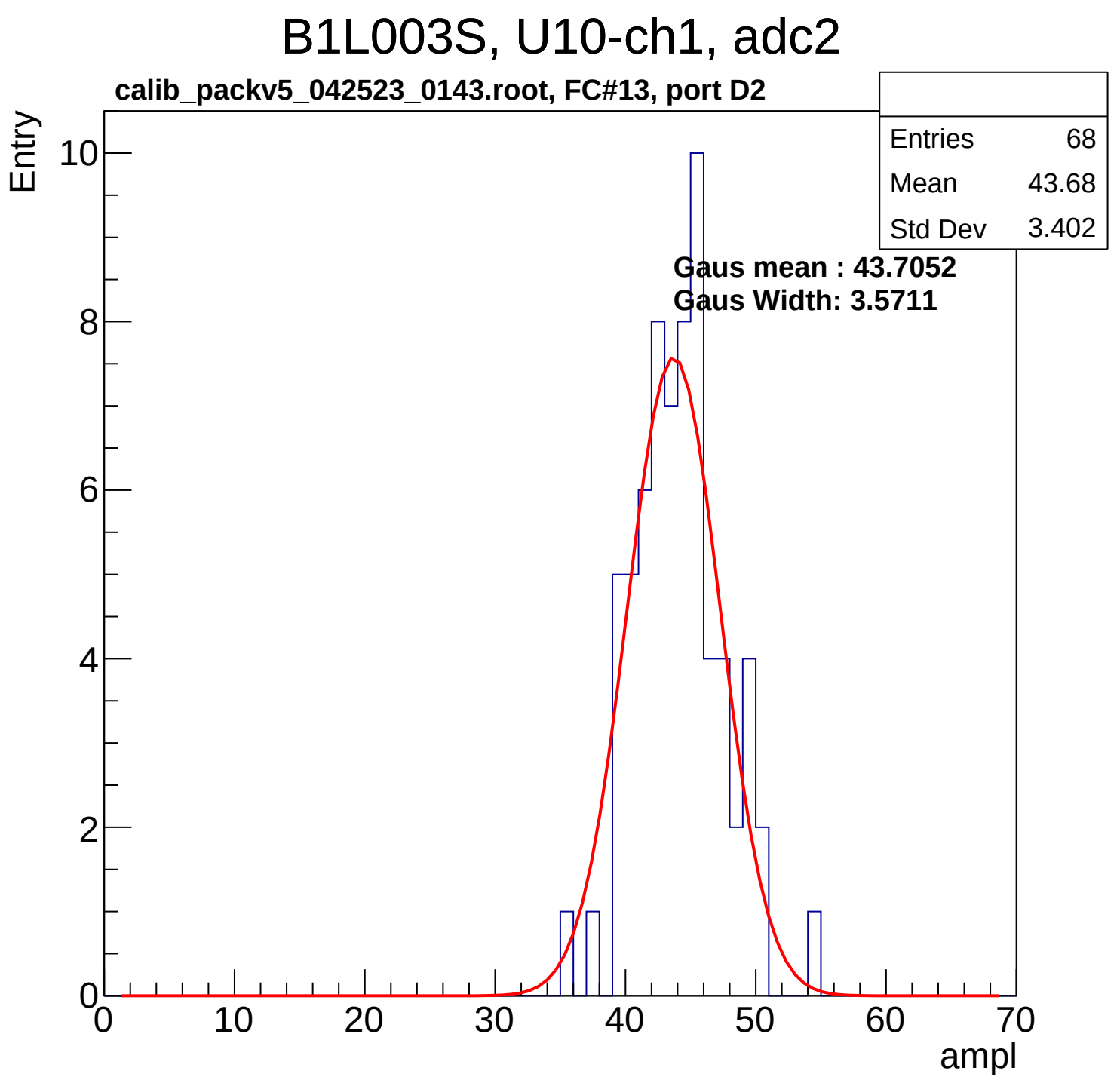
B1L003S, U10-ch1, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 43.68
Std Dev 3.402
Gaus mean : 43.7052
Gaus Width: 3.5711
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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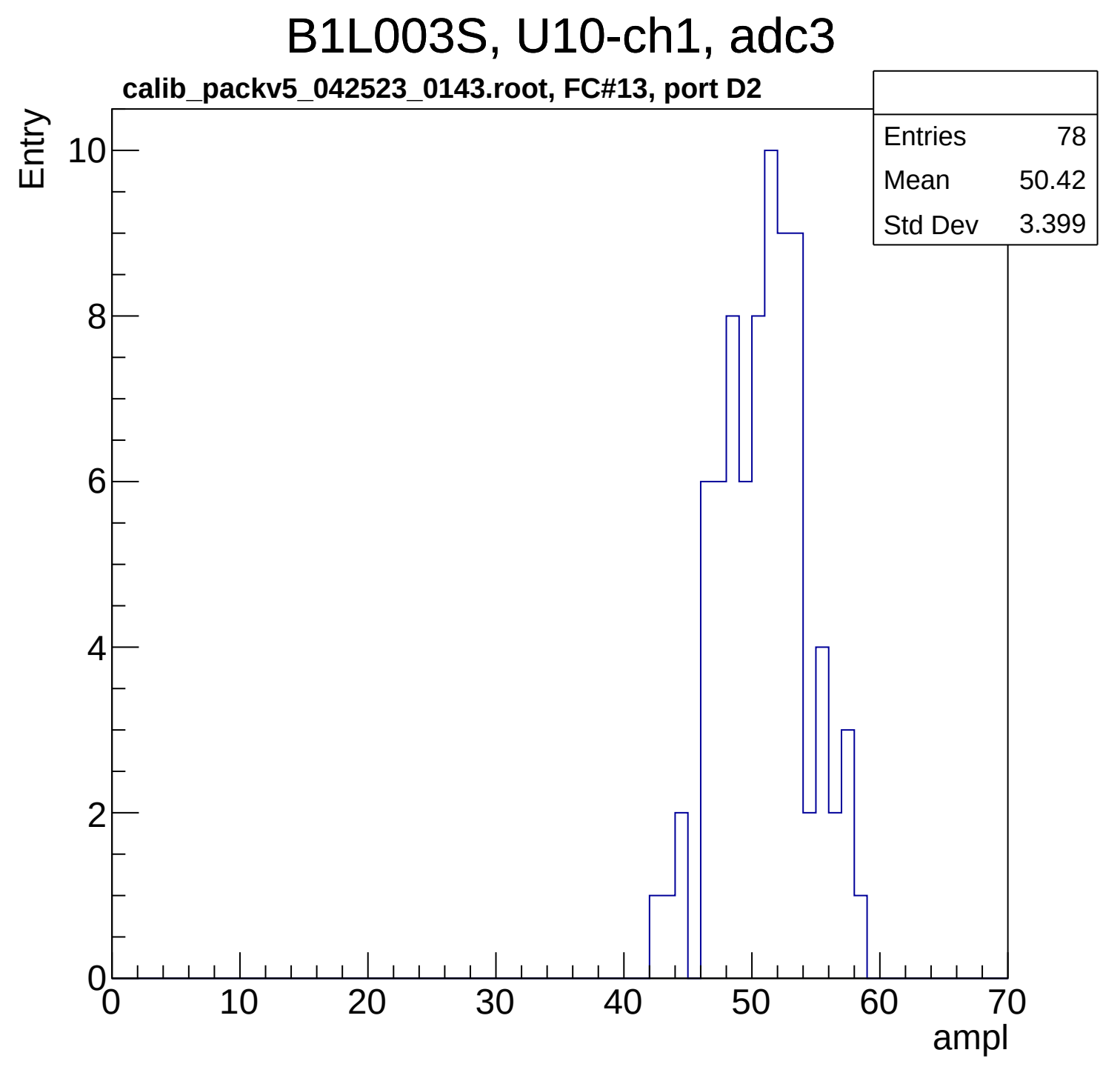
B1L003S, U10-ch1, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 50.42
Std Dev 3.399
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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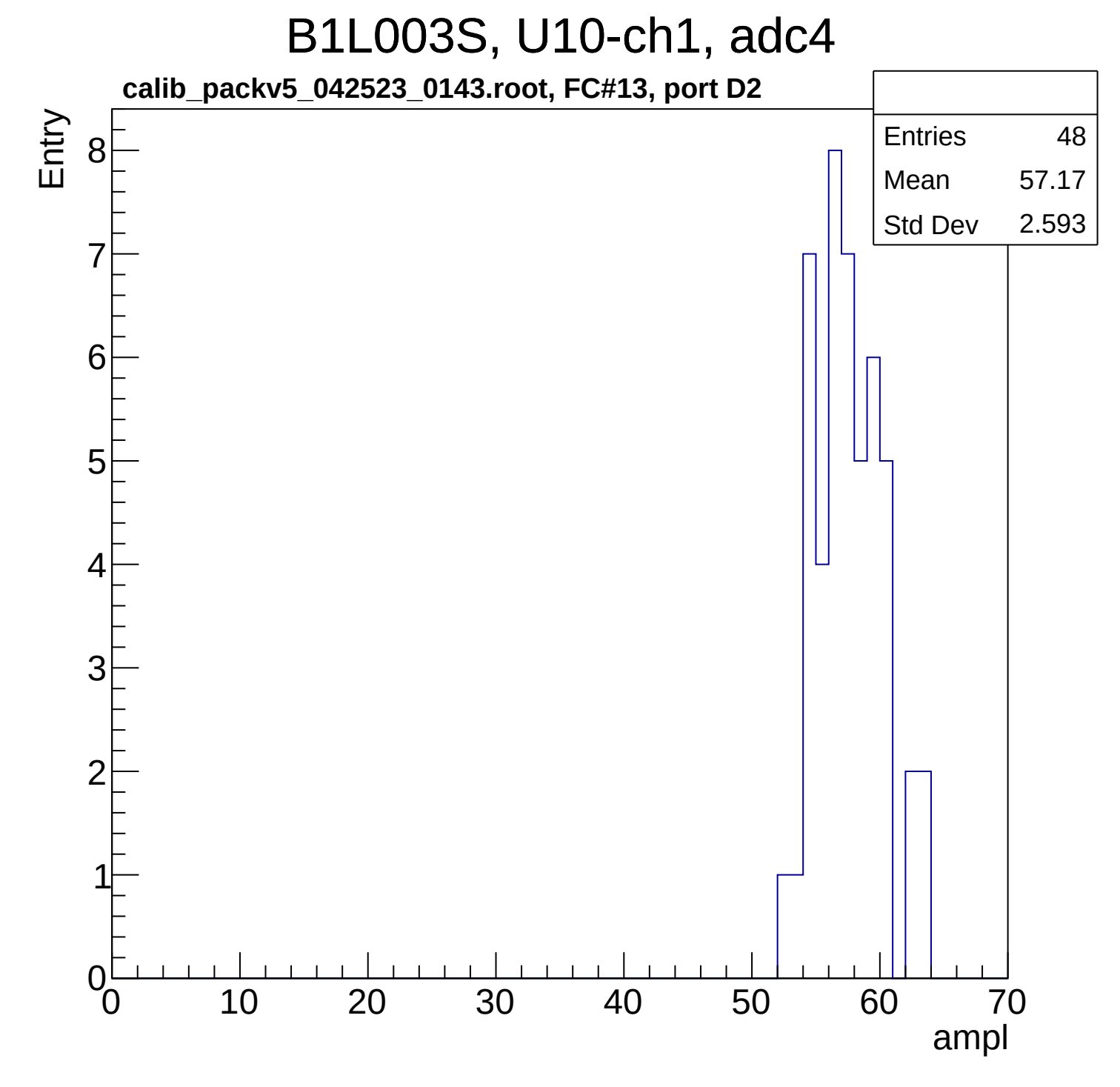
B1L003S, U10-ch1, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 57.17
Std Dev 2.593
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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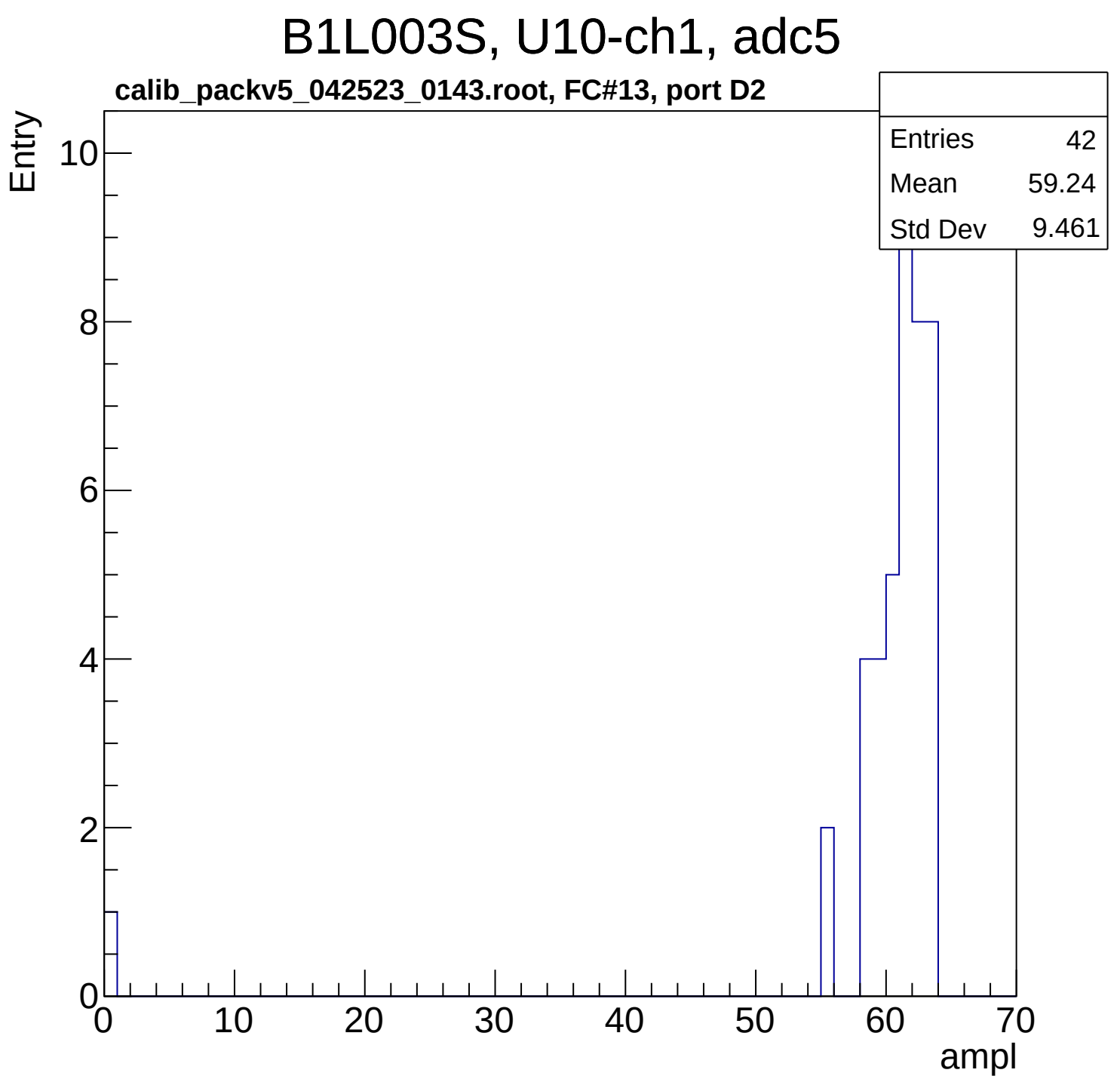
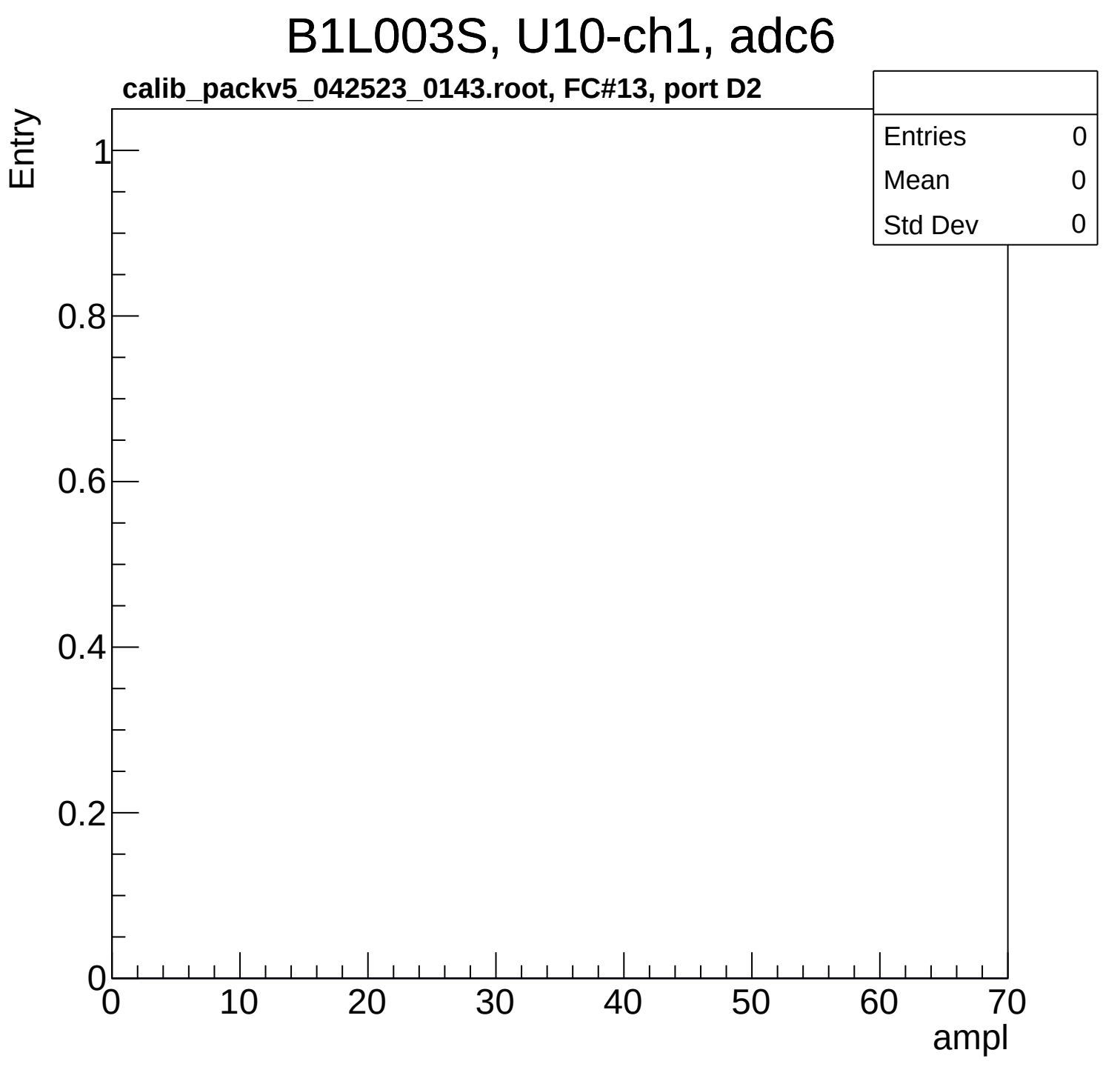
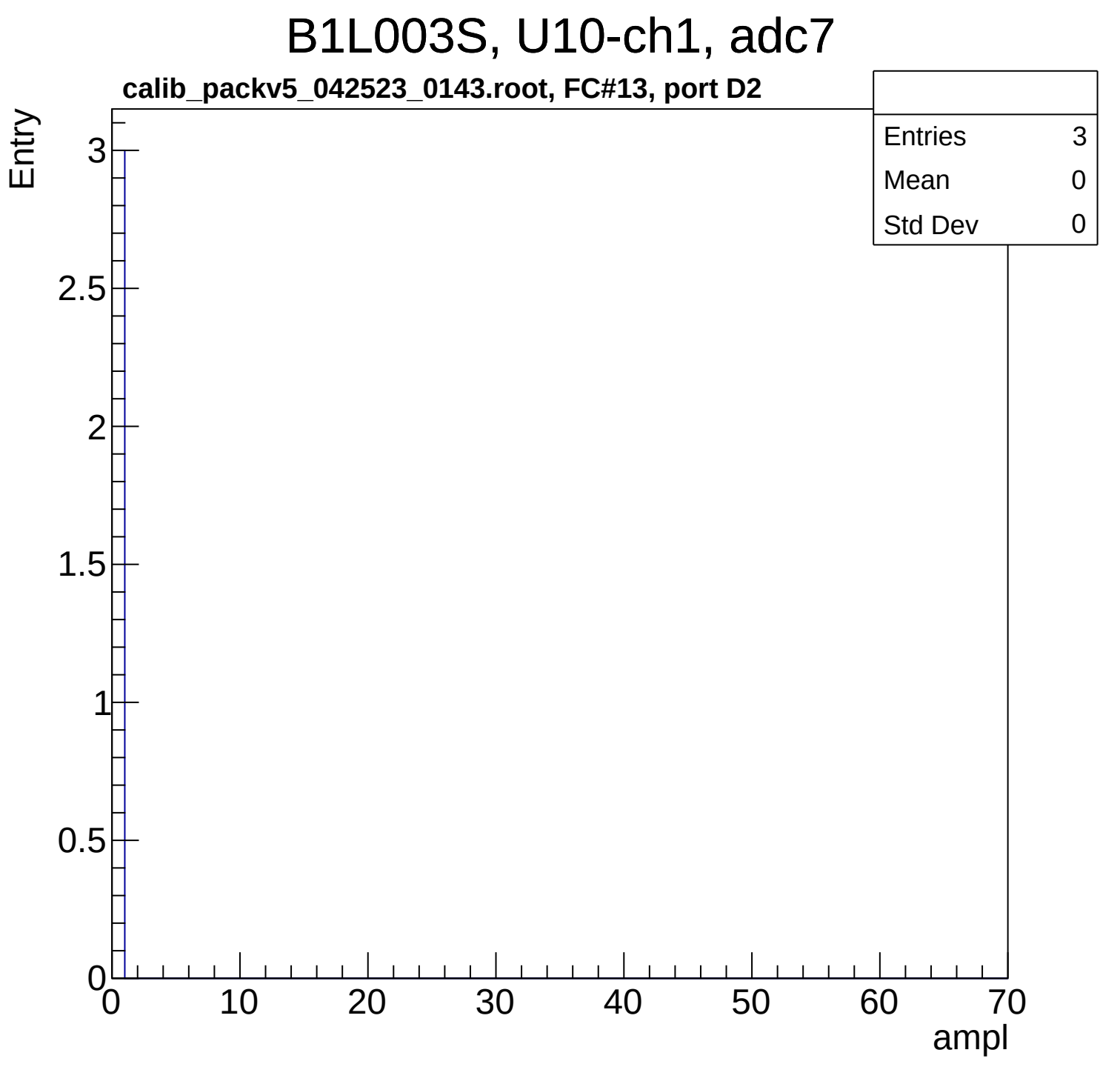
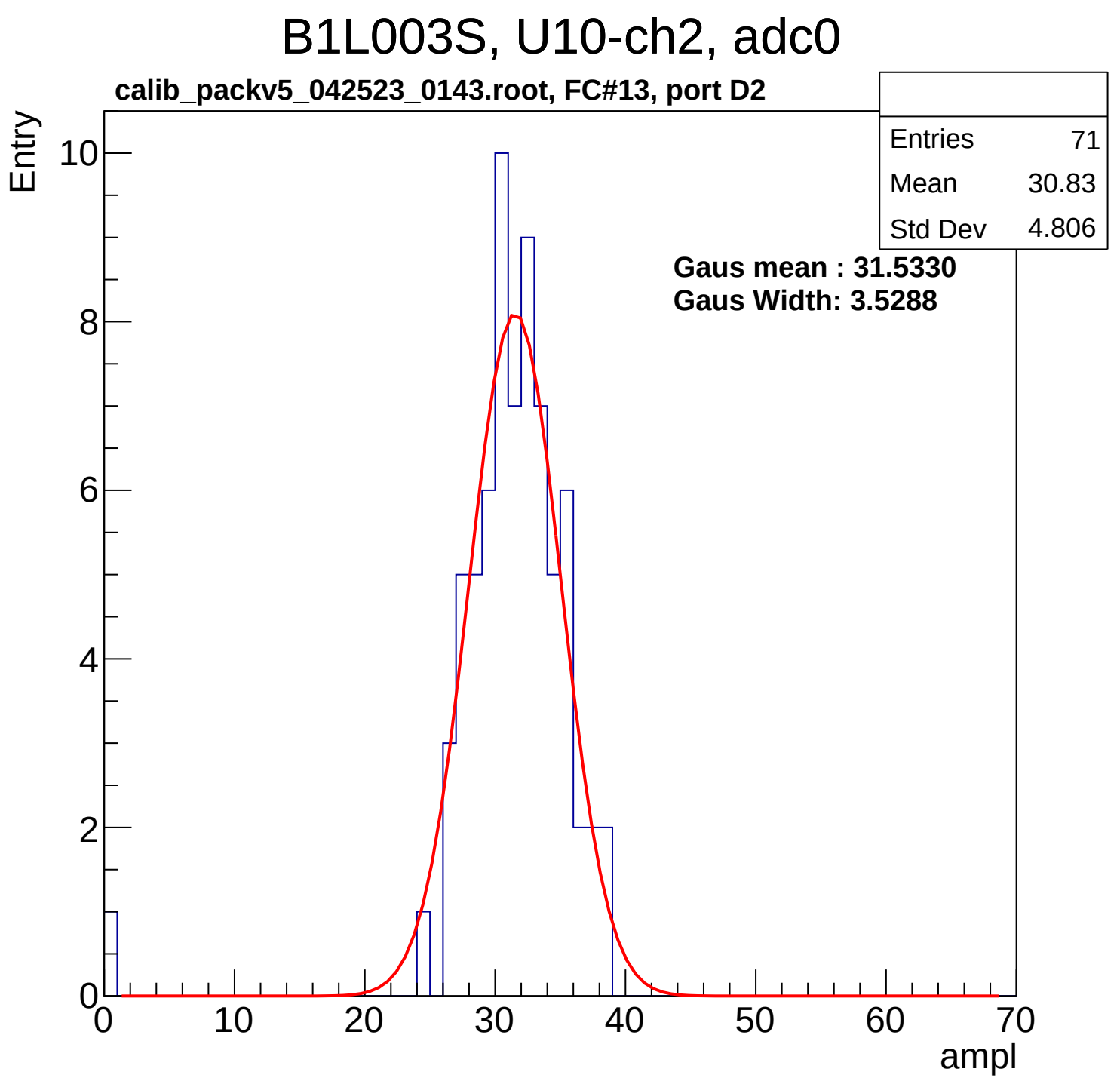

B1L003S, U10-ch1, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 59.24
Std Dev 9.461
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch1, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch1, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch2, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 30.83
Std Dev 4.806
Gaus mean : 31.5330
Gaus Width: 3.5288
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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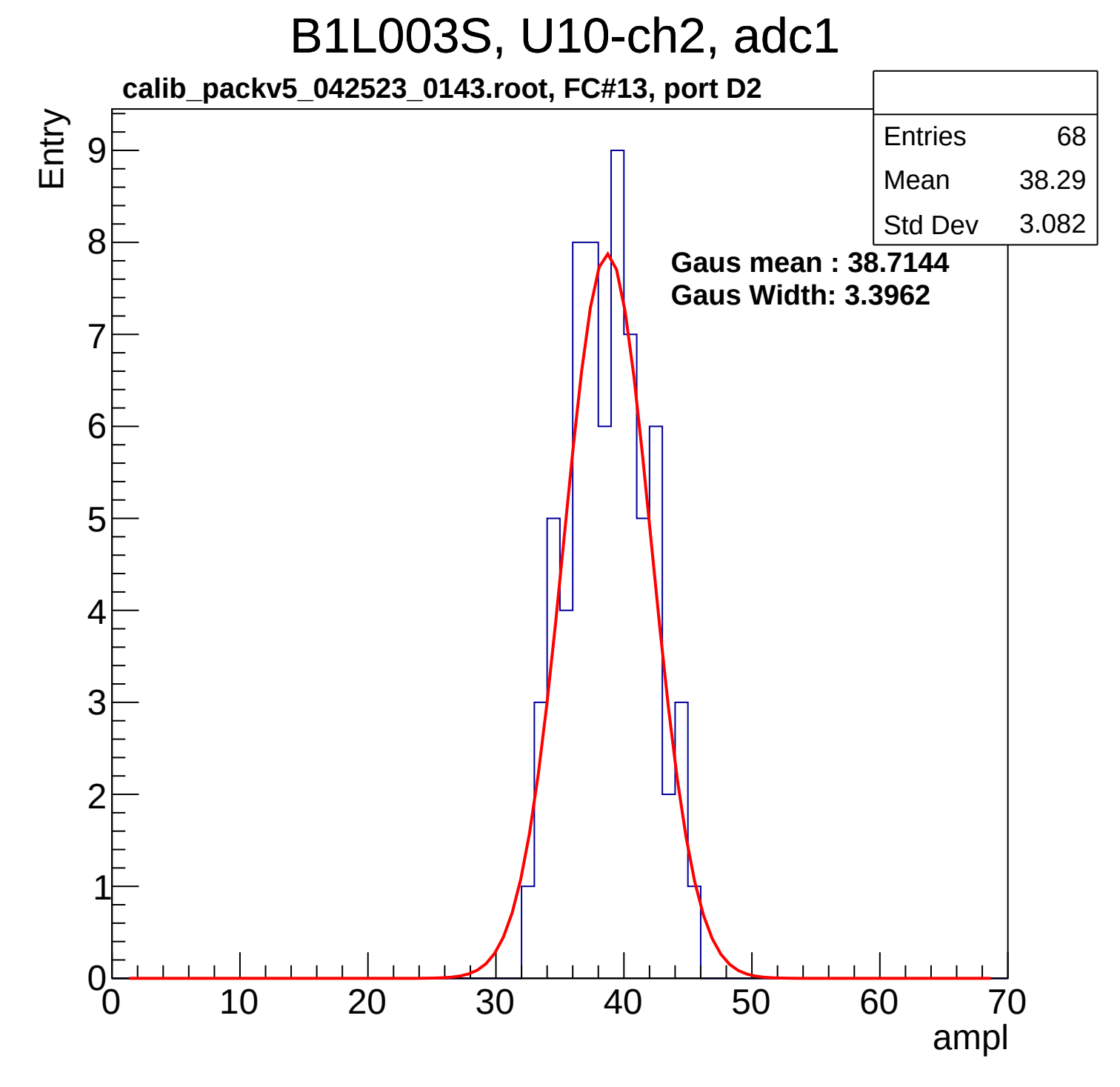
B1L003S, U10-ch2, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 38.29
Std Dev 3.082
Gaus mean : 38.7144
Gaus Width: 3.3962
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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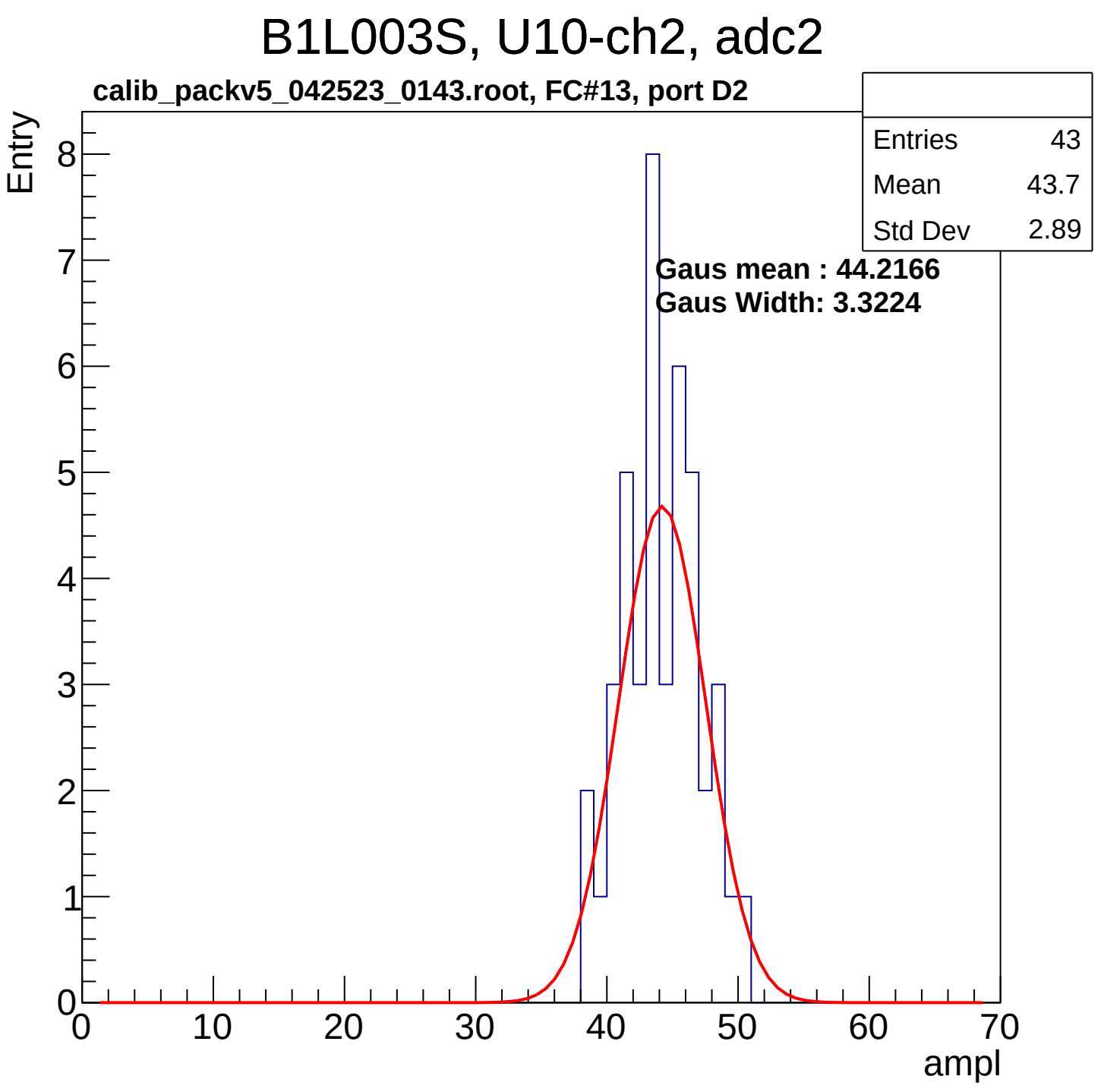
B1L003S, U10-ch2, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 43.7
Std Dev 2.89
Gaus mean : 44.2166
Gaus Width: 3.3224
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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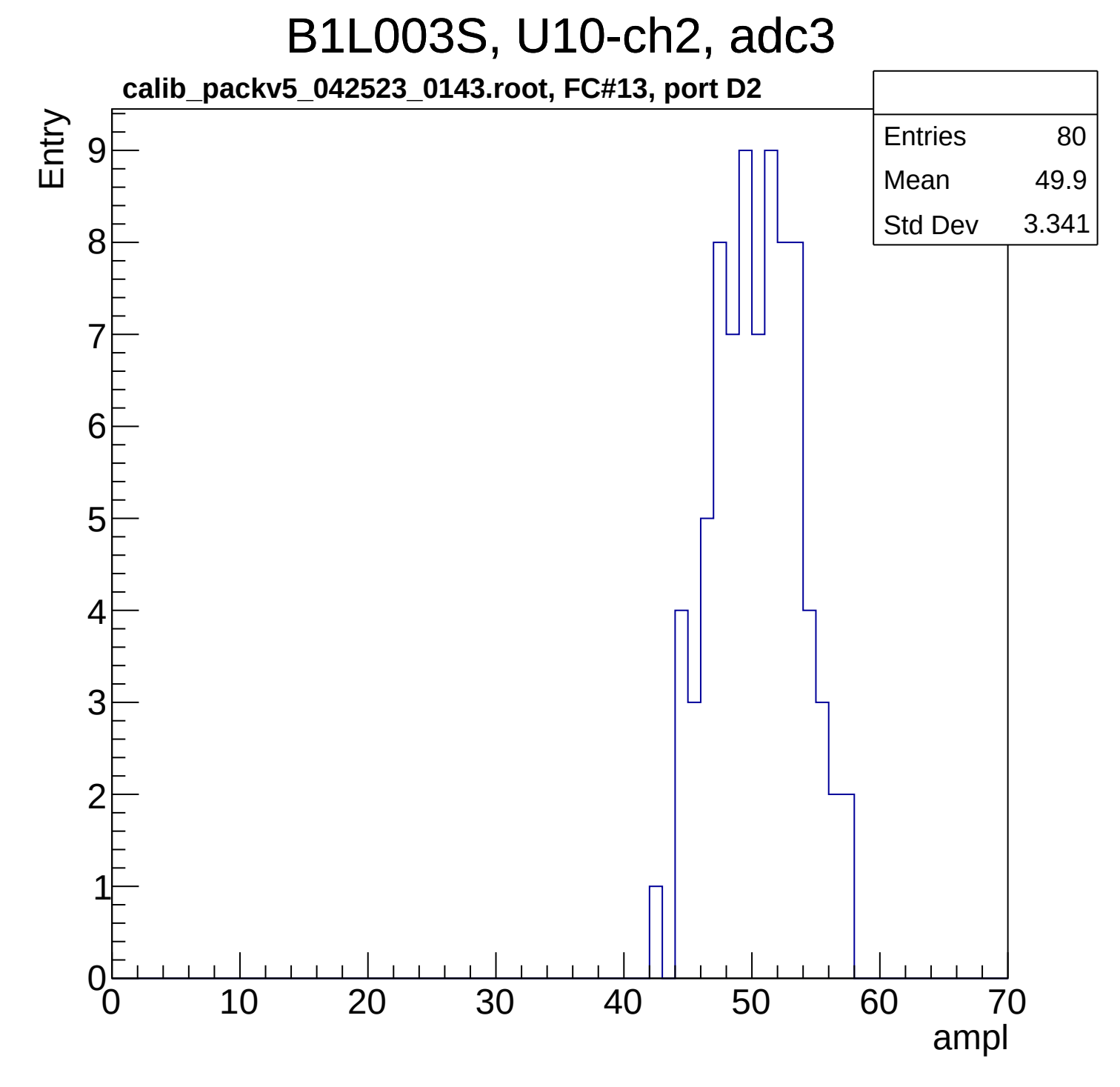
B1L003S, U10-ch2, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 49.9
Std Dev 3.341
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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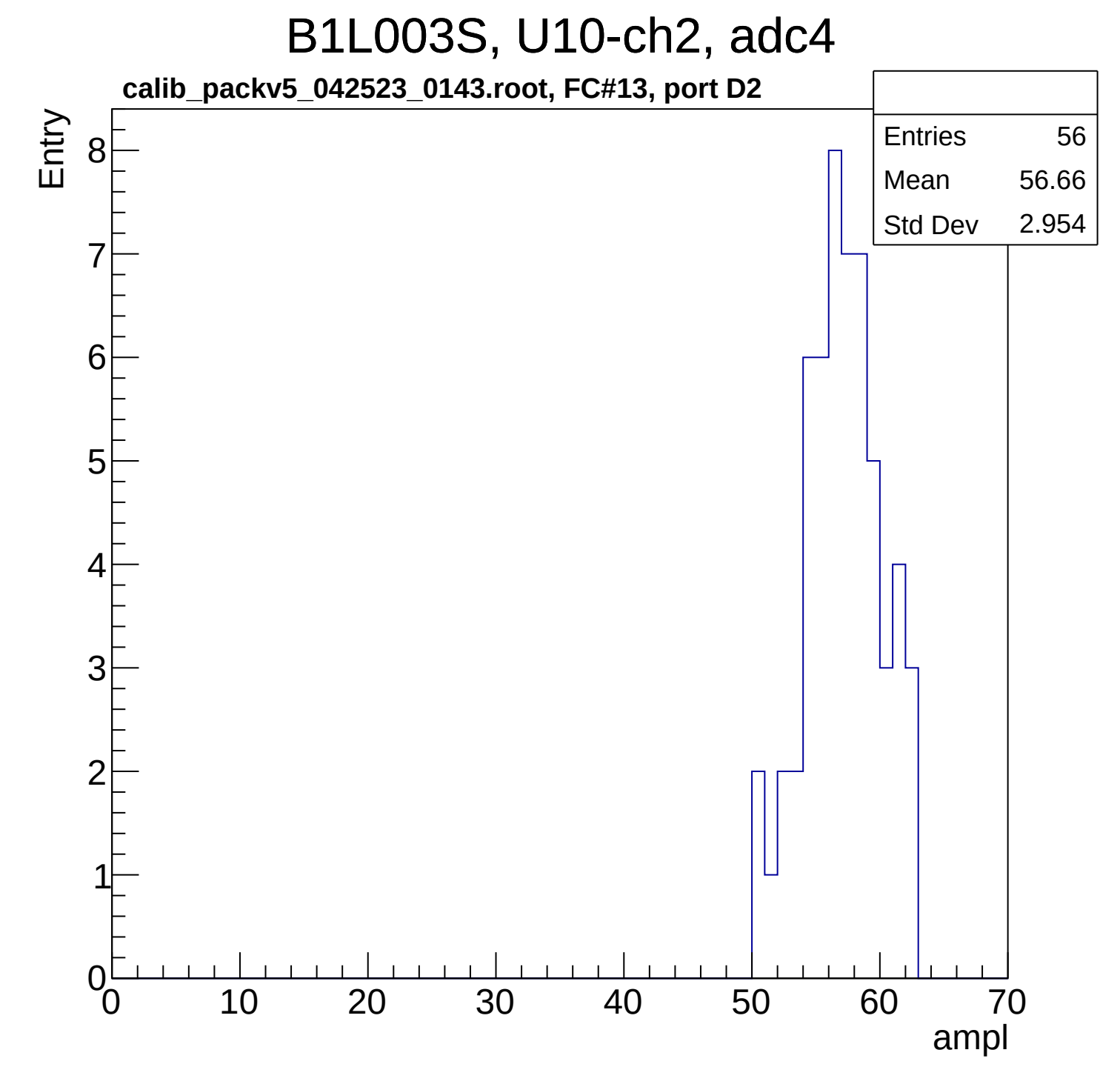
B1L003S, U10-ch2, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 56.66
Std Dev 2.954
Entry
ampl
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4
5
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0
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60
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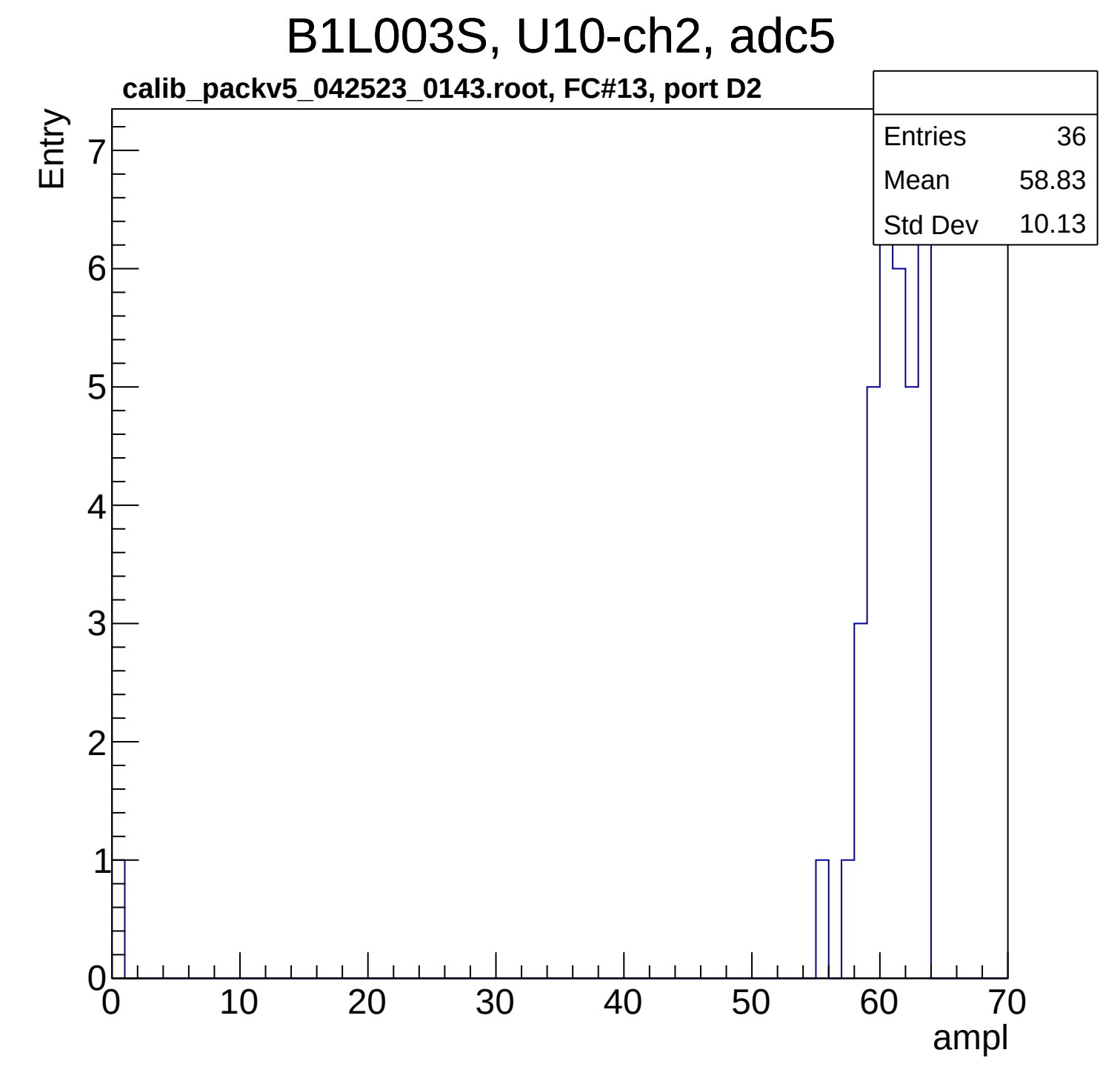
B1L003S, U10-ch2, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.83
Std Dev 10.13
Entry
ampl
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1
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3
4
5
6
7
0
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20
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40
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60
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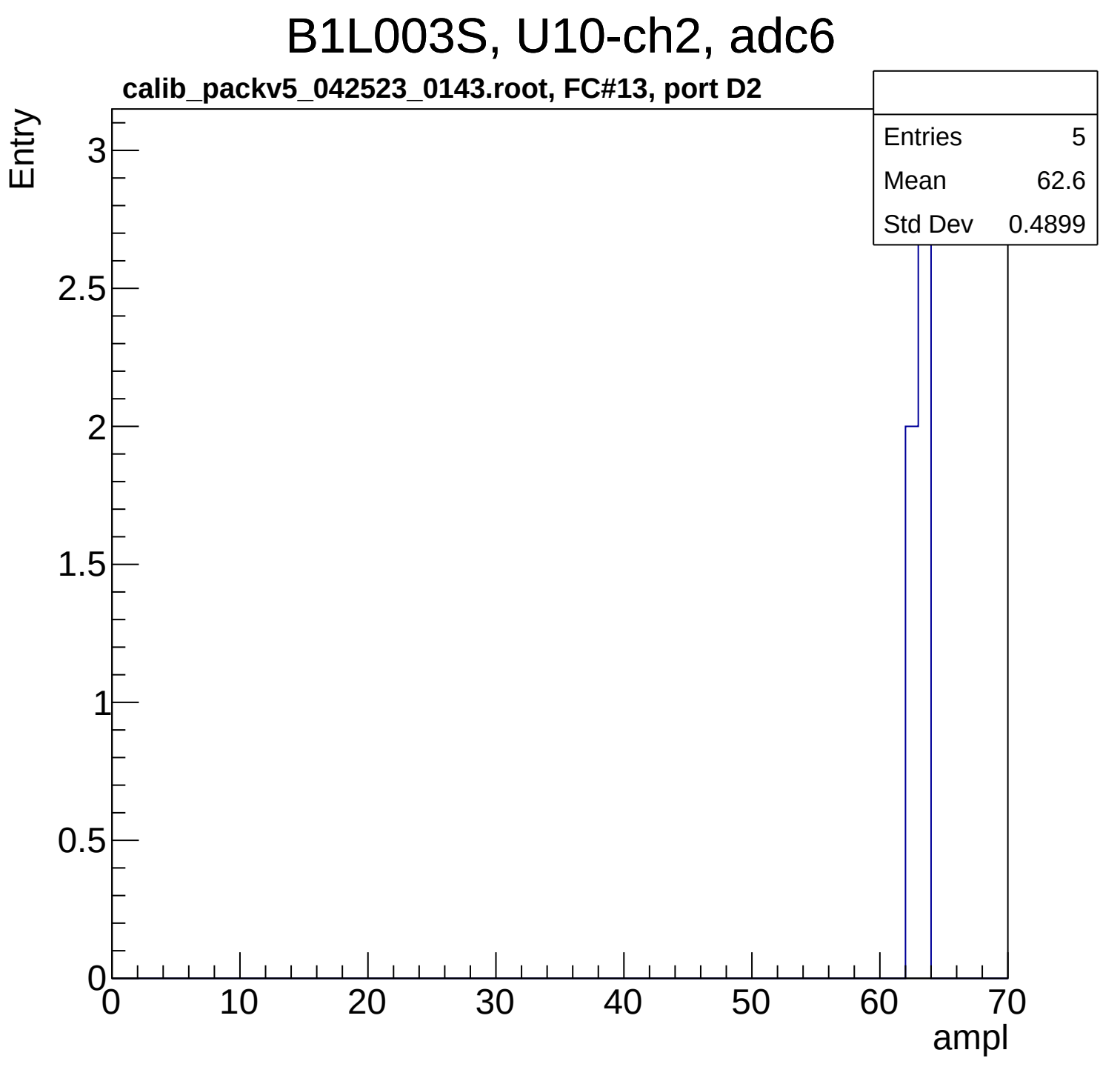
B1L003S, U10-ch2, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
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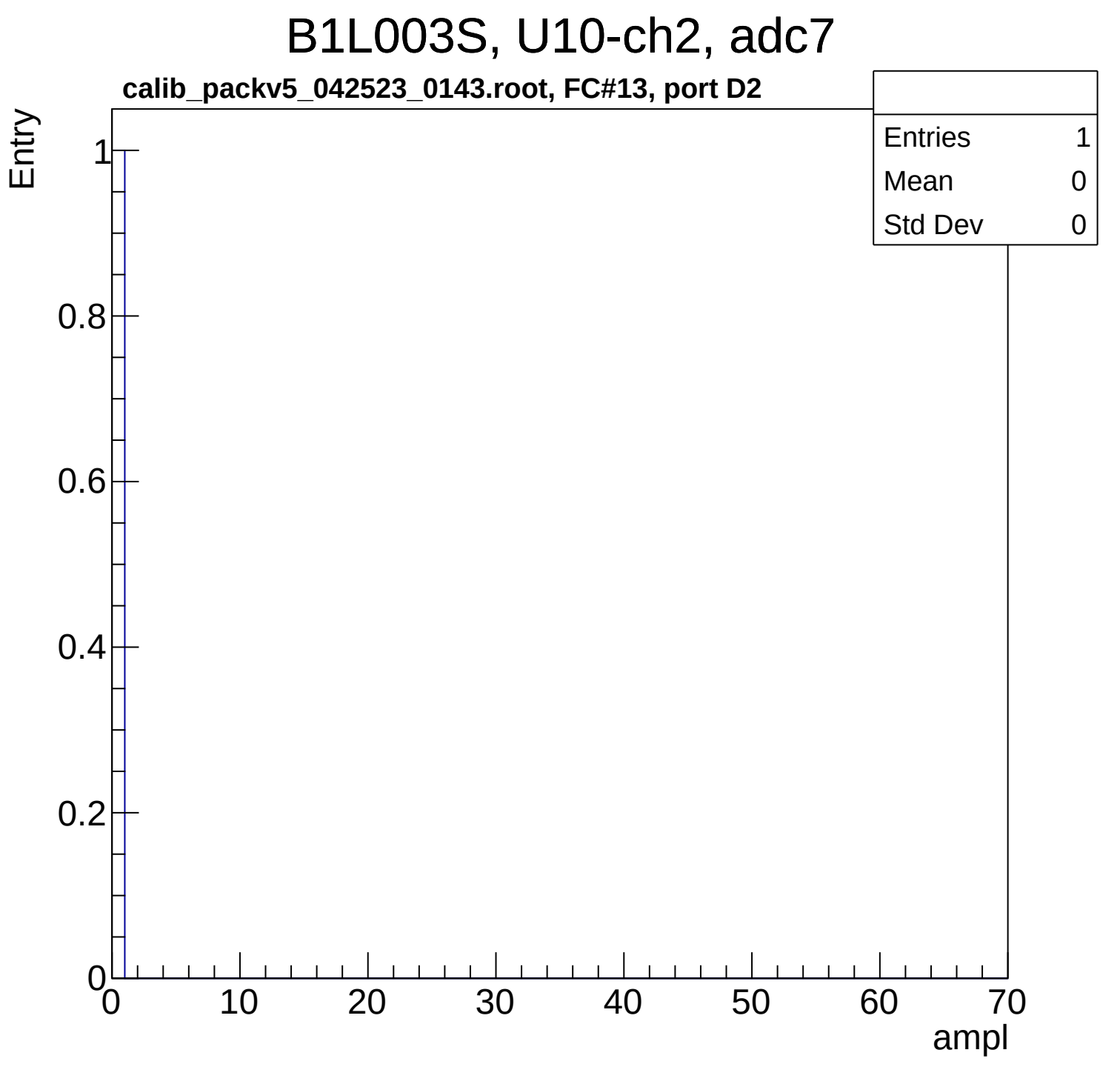
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch2, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
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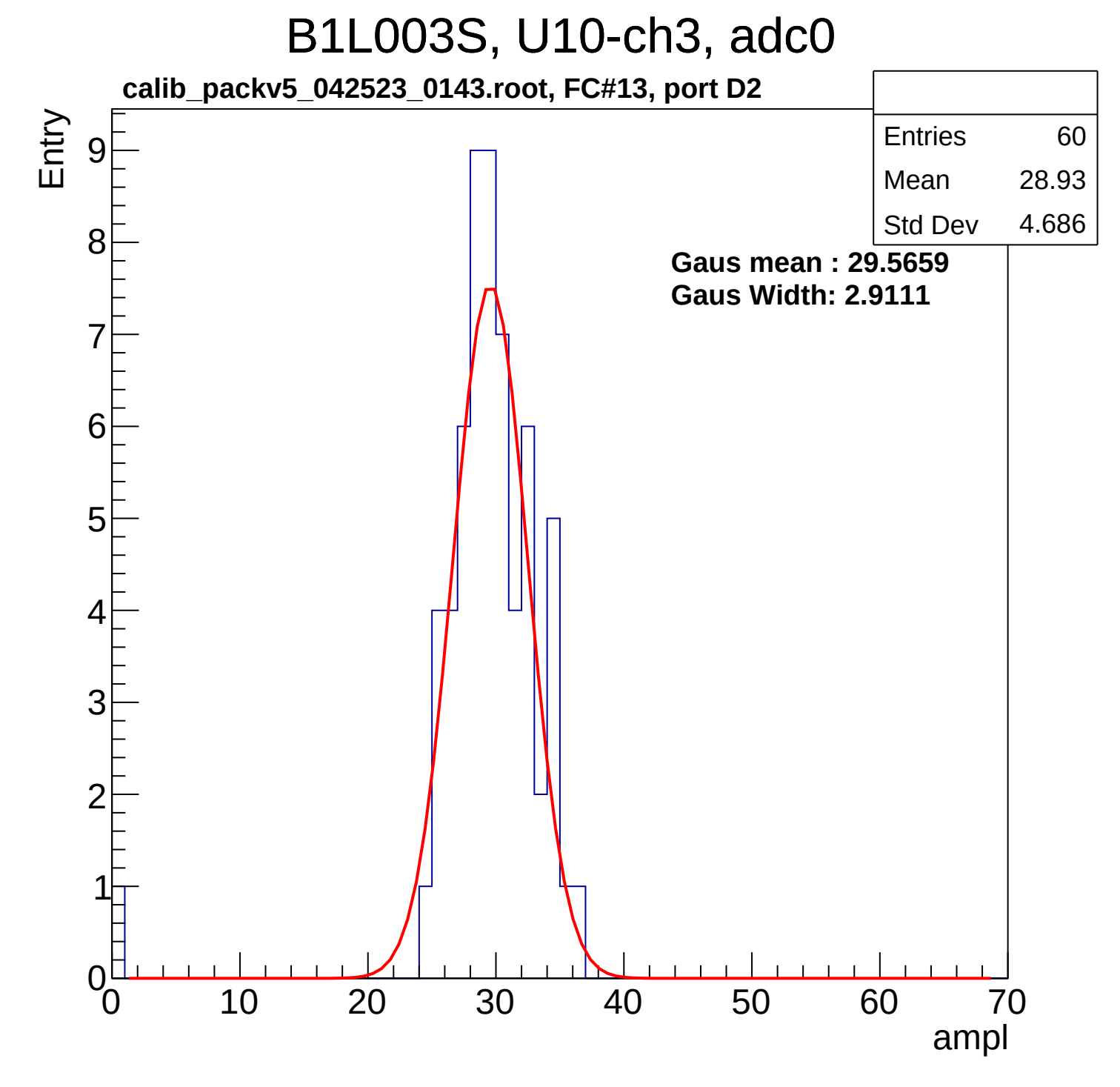
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch3, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.93
Std Dev 4.686
Gaus mean : 29.5659
Gaus Width: 2.9111
Entry
ampl
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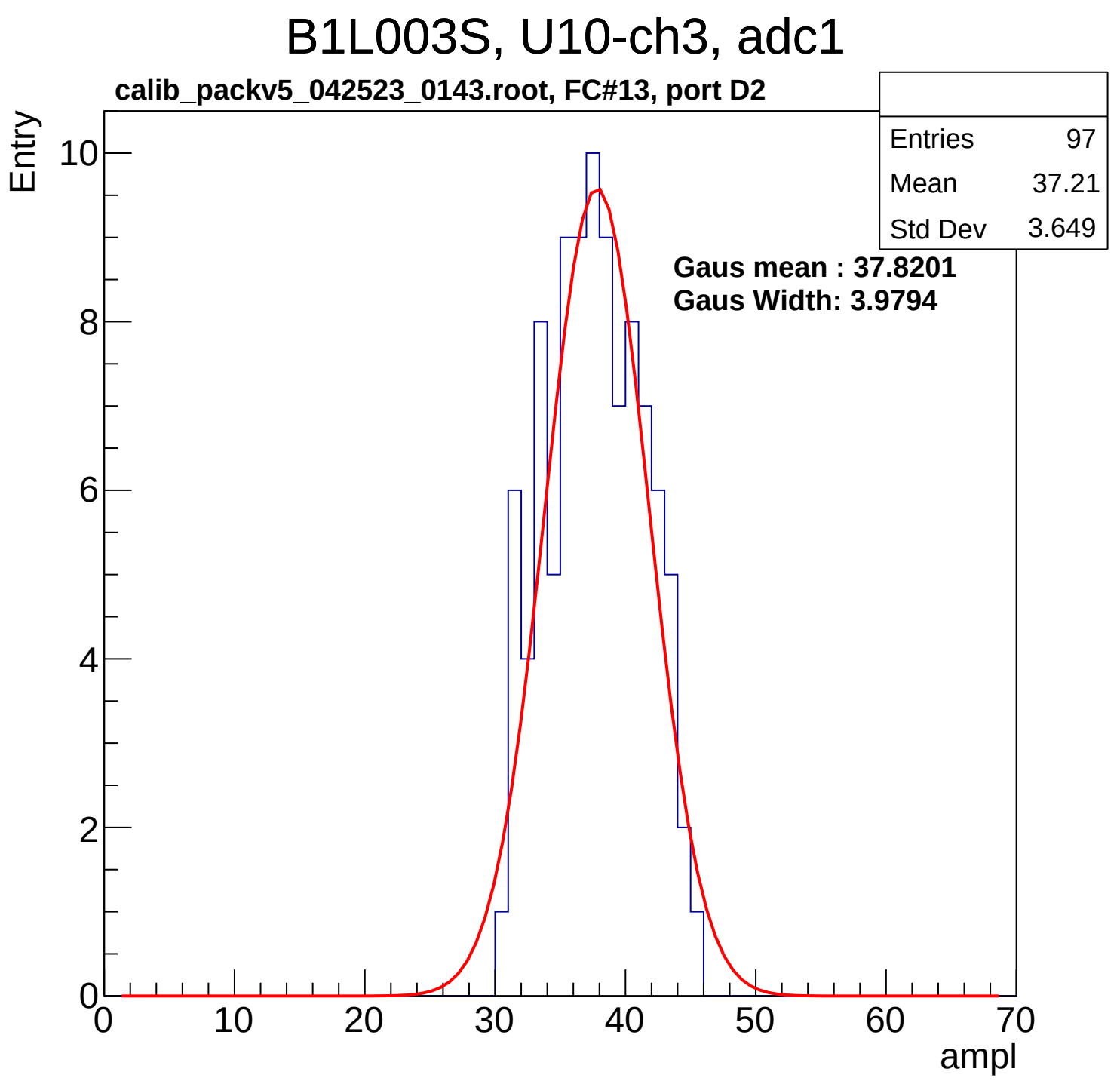
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch3, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 97
Mean 37.21
Std Dev 3.649
Gaus mean : 37.8201
Gaus Width: 3.9794
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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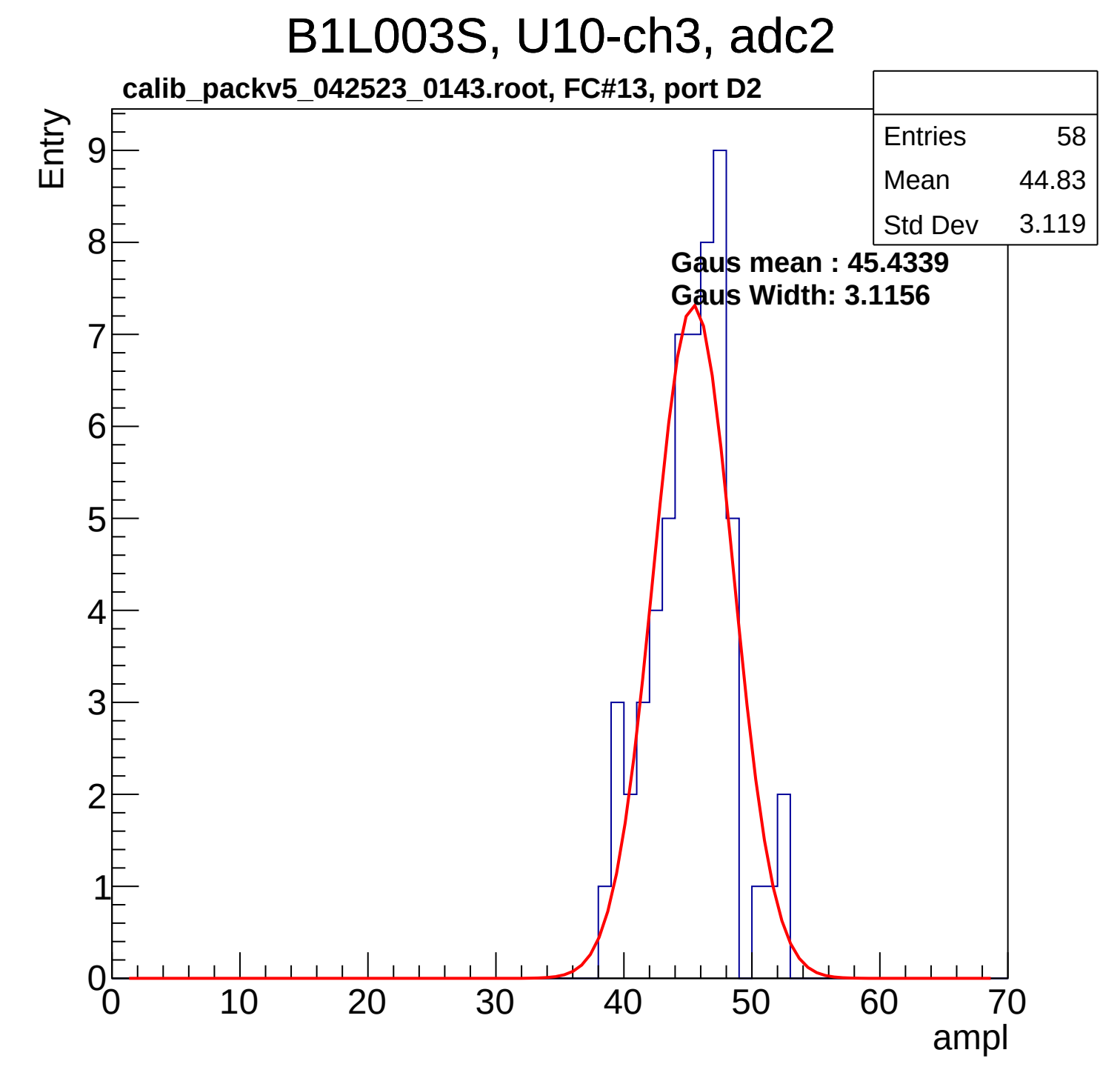
B1L003S, U10-ch3, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 44.83
Std Dev 3.119
Gaus mean : 45.4339
Gaus Width: 3.1156
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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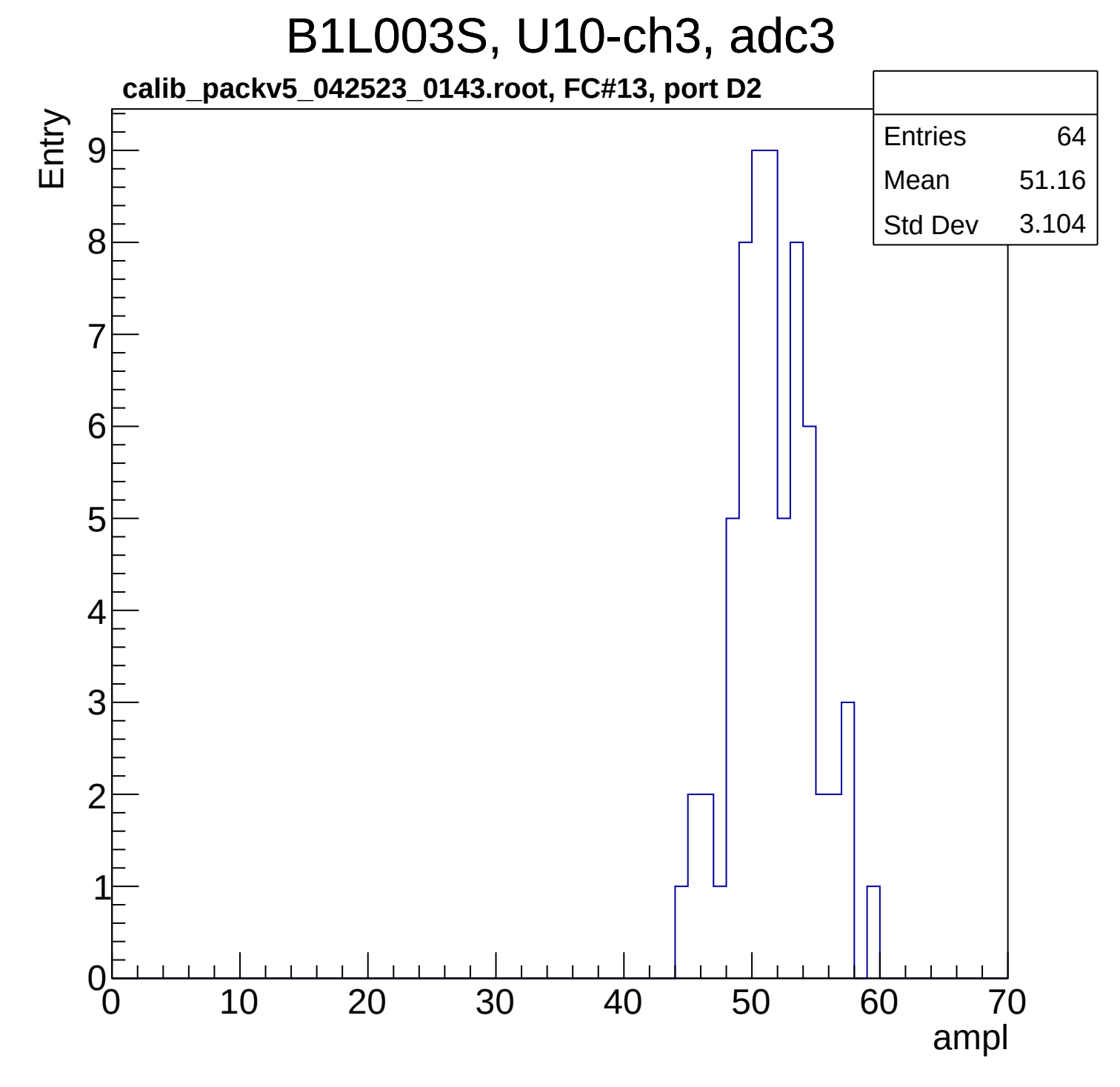
B1L003S, U10-ch3, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 51.16
Std Dev 3.104
Entry
ampl
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3
4
5
6
7
8
9
0
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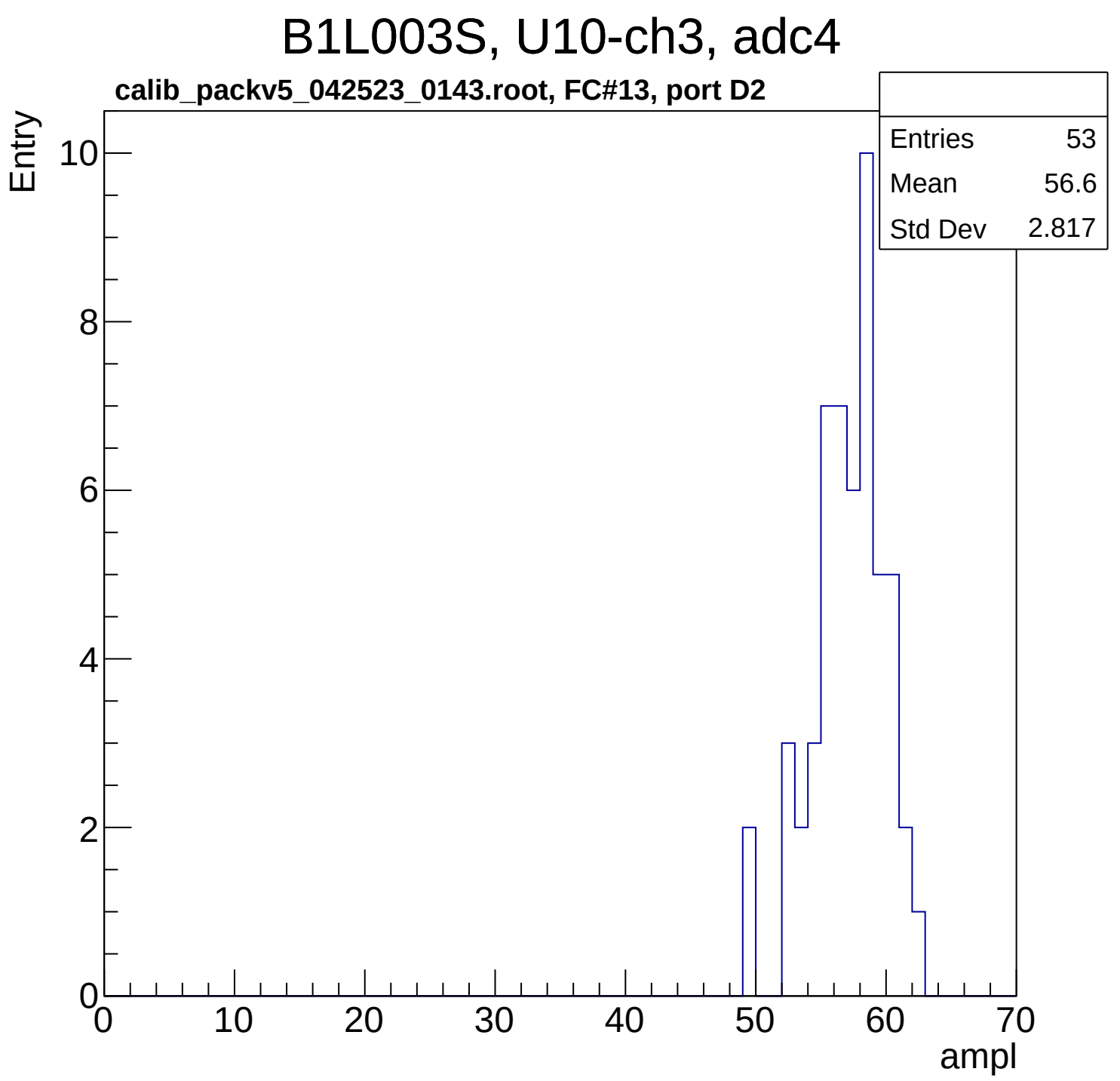
B1L003S, U10-ch3, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 56.6
Std Dev 2.817
Entry
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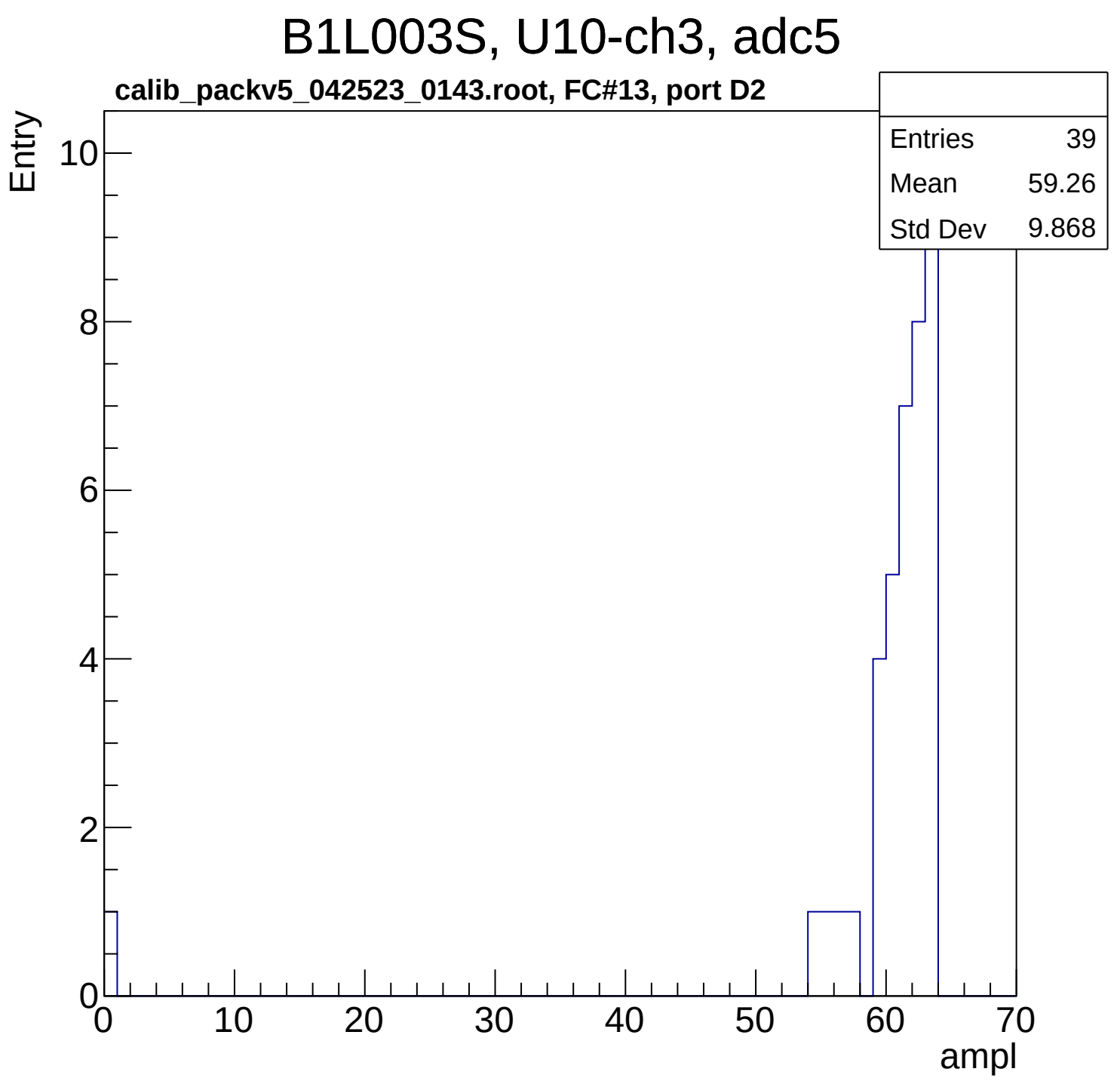
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch3, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 59.26
Std Dev 9.868
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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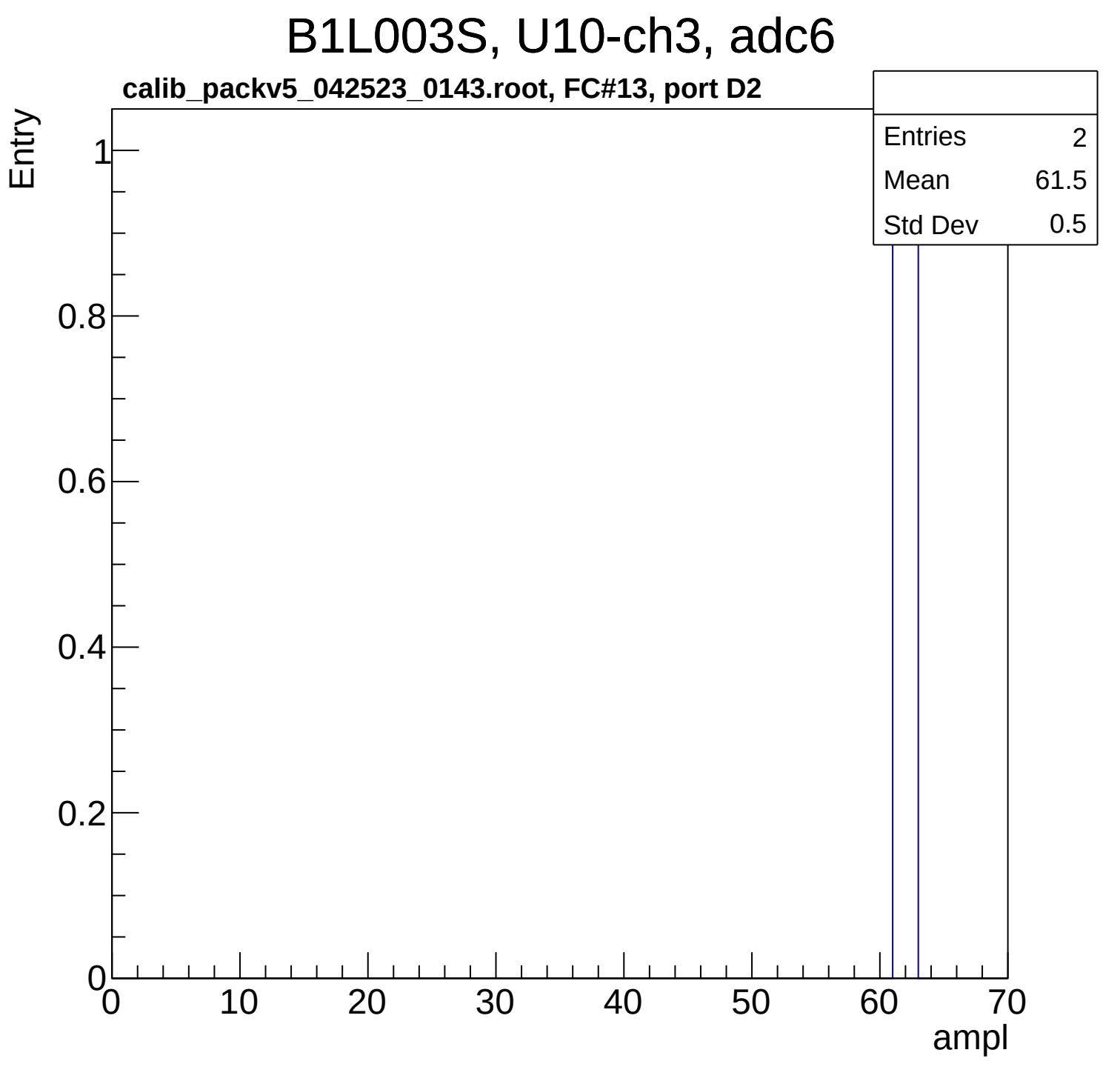
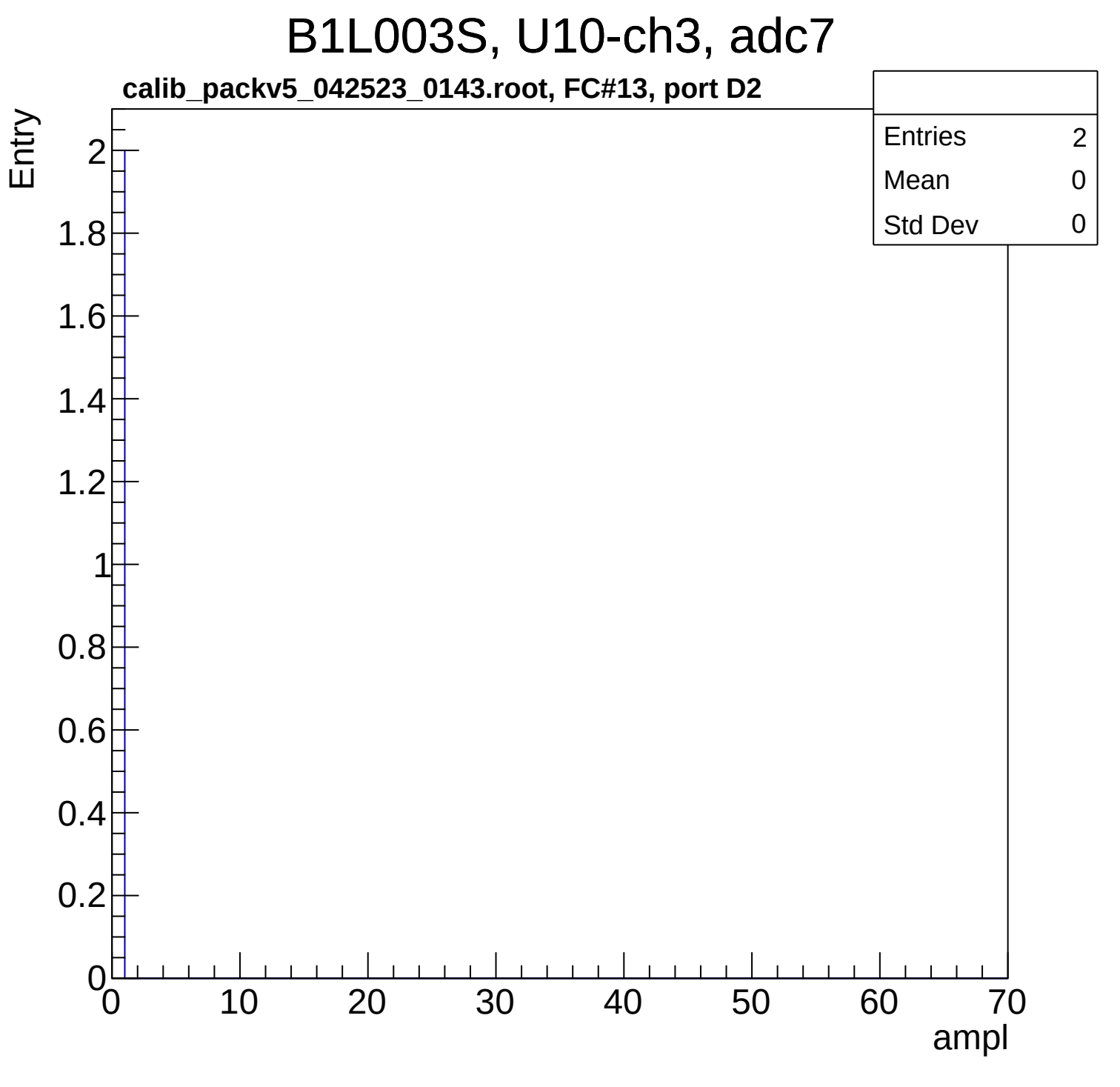

B1L003S, U10-ch3, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch3, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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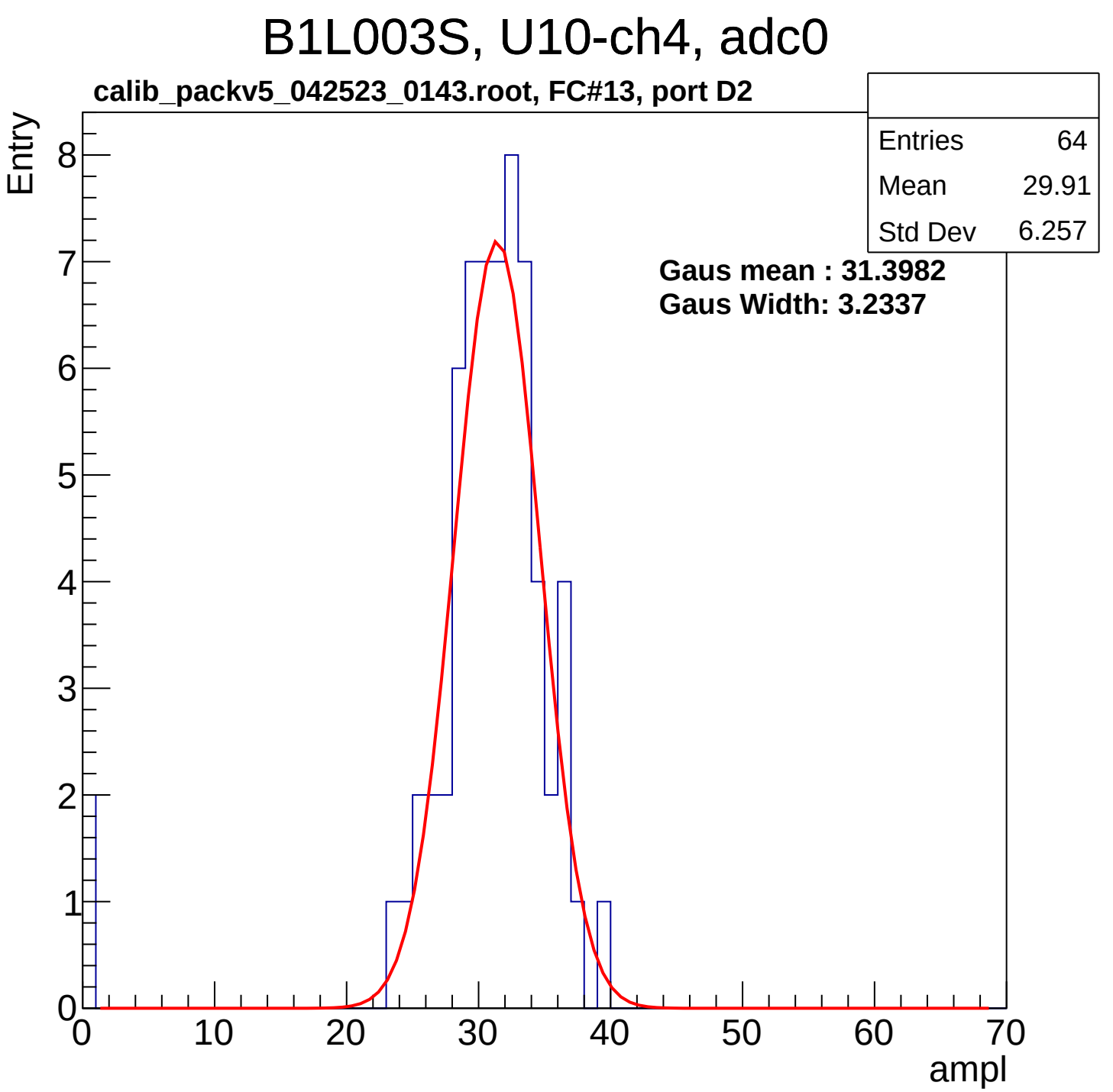
B1L003S, U10-ch4, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.91
Std Dev 6.257
Gaus mean : 31.3982
Gaus Width: 3.2337
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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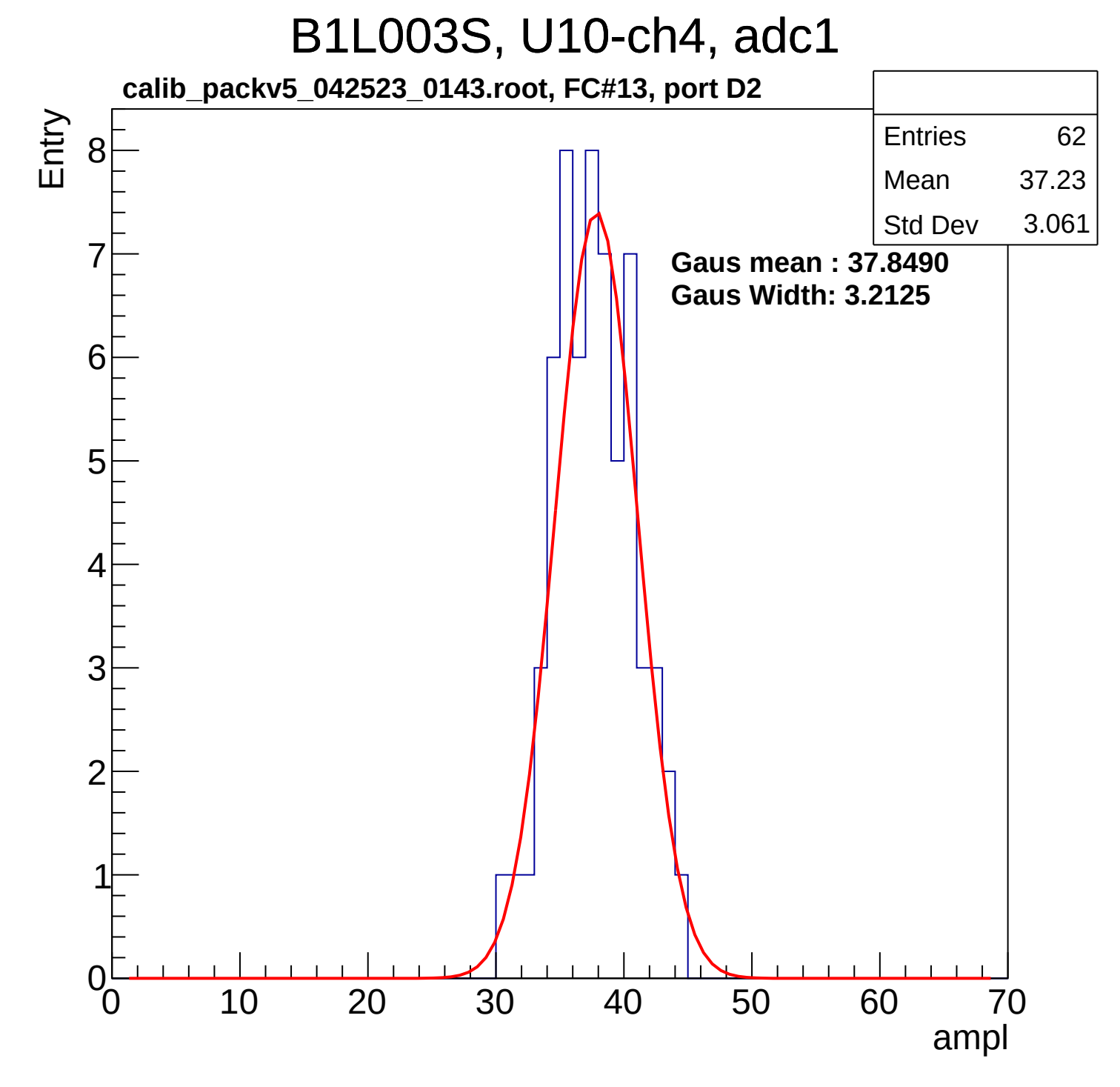
B1L003S, U10-ch4, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.23
Std Dev 3.061
Gaus mean : 37.8490
Gaus Width: 3.2125
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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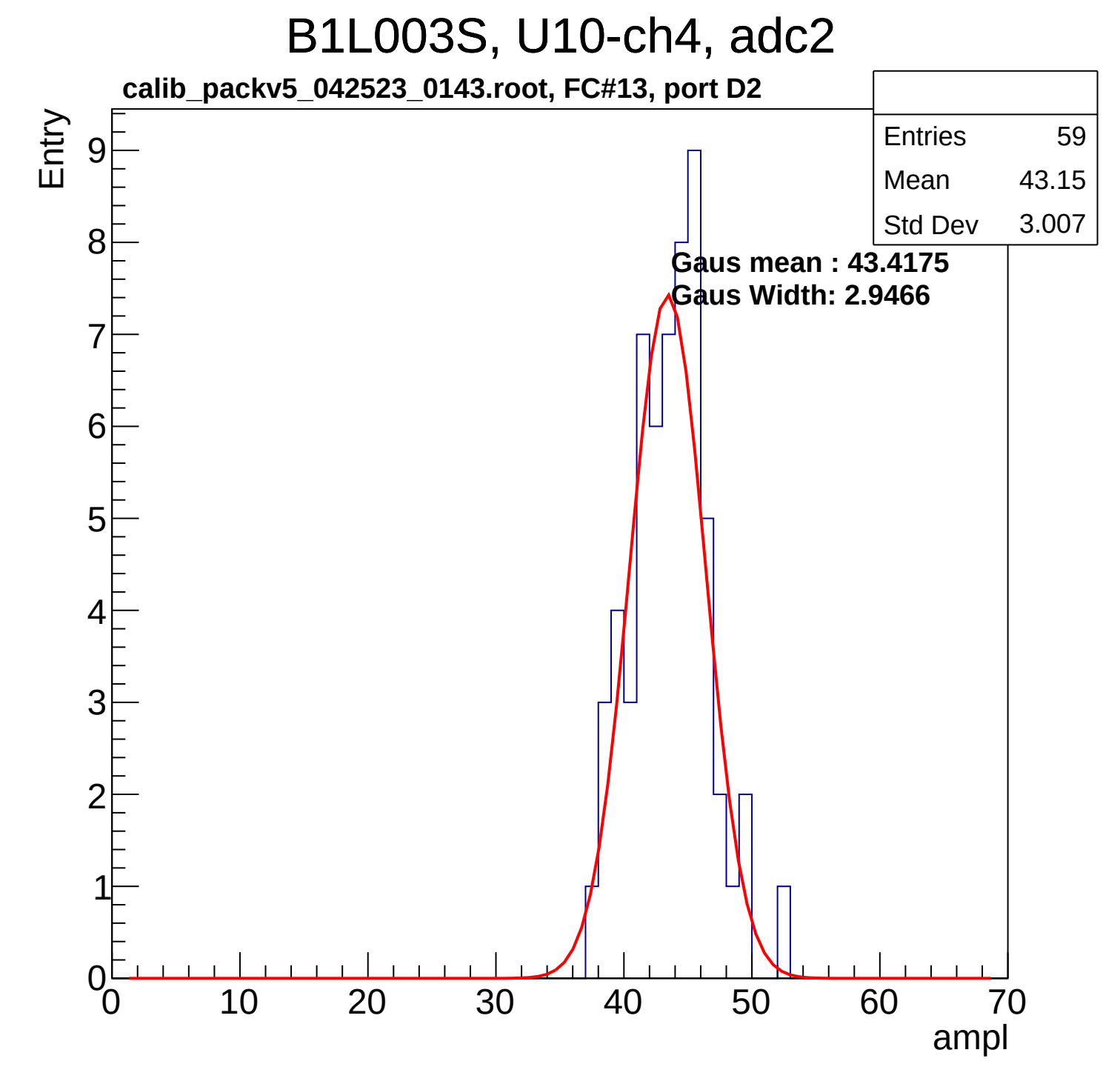
B1L003S, U10-ch4, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.15
Std Dev 3.007
Gaus mean : 43.4175
Gaus Width: 2.9466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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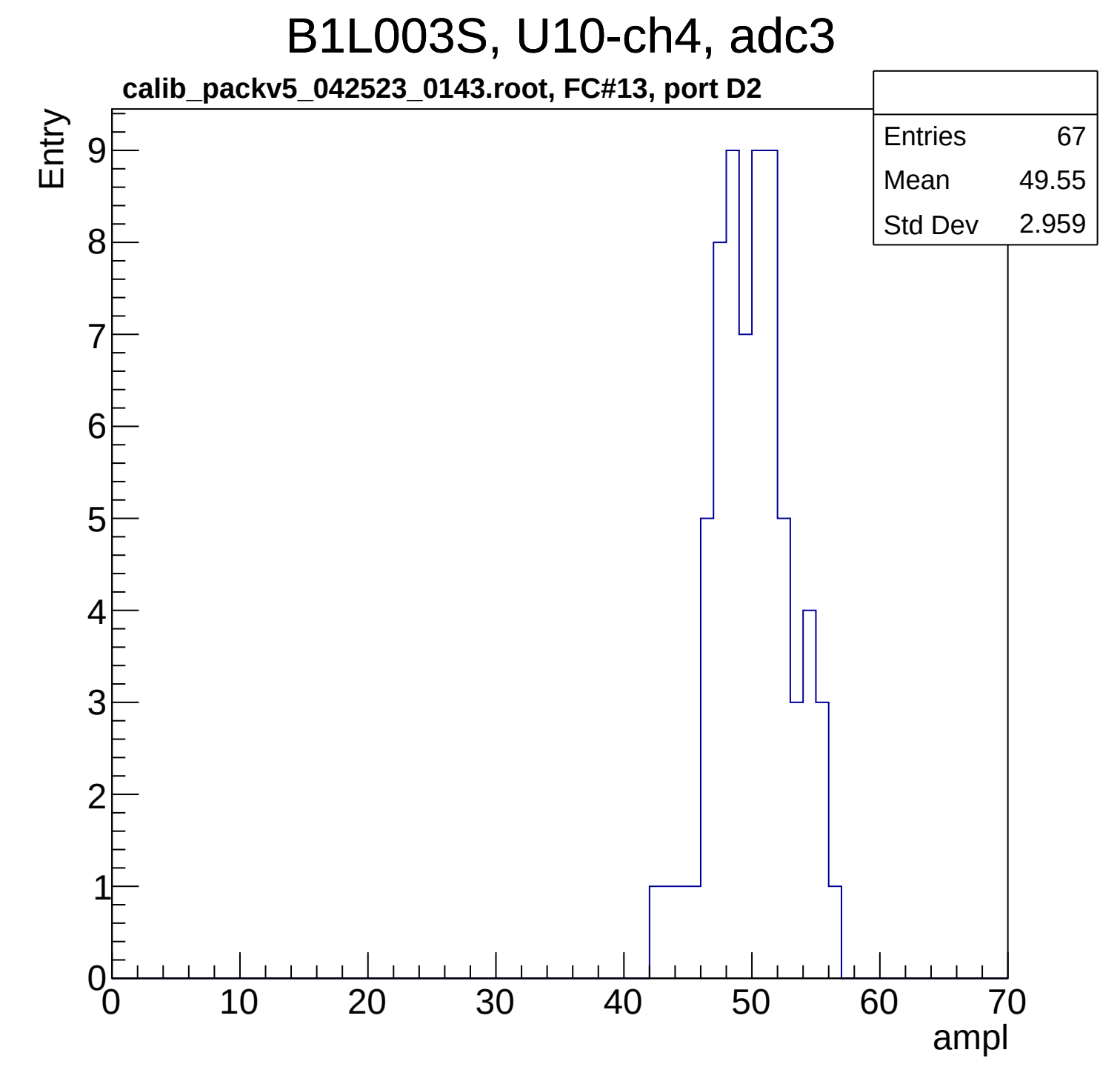
B1L003S, U10-ch4, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.55
Std Dev 2.959
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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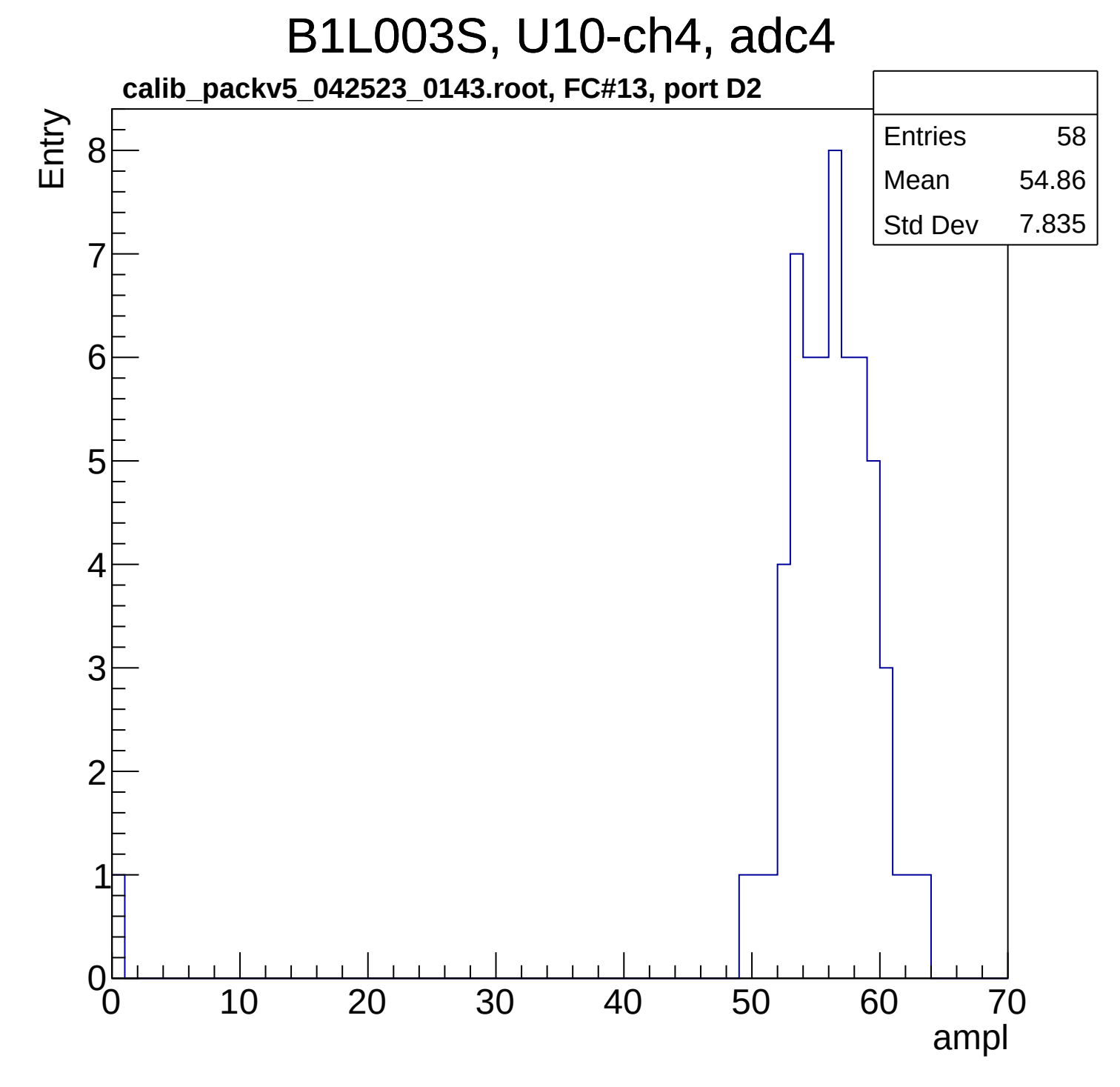
B1L003S, U10-ch4, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.86
Std Dev 7.835
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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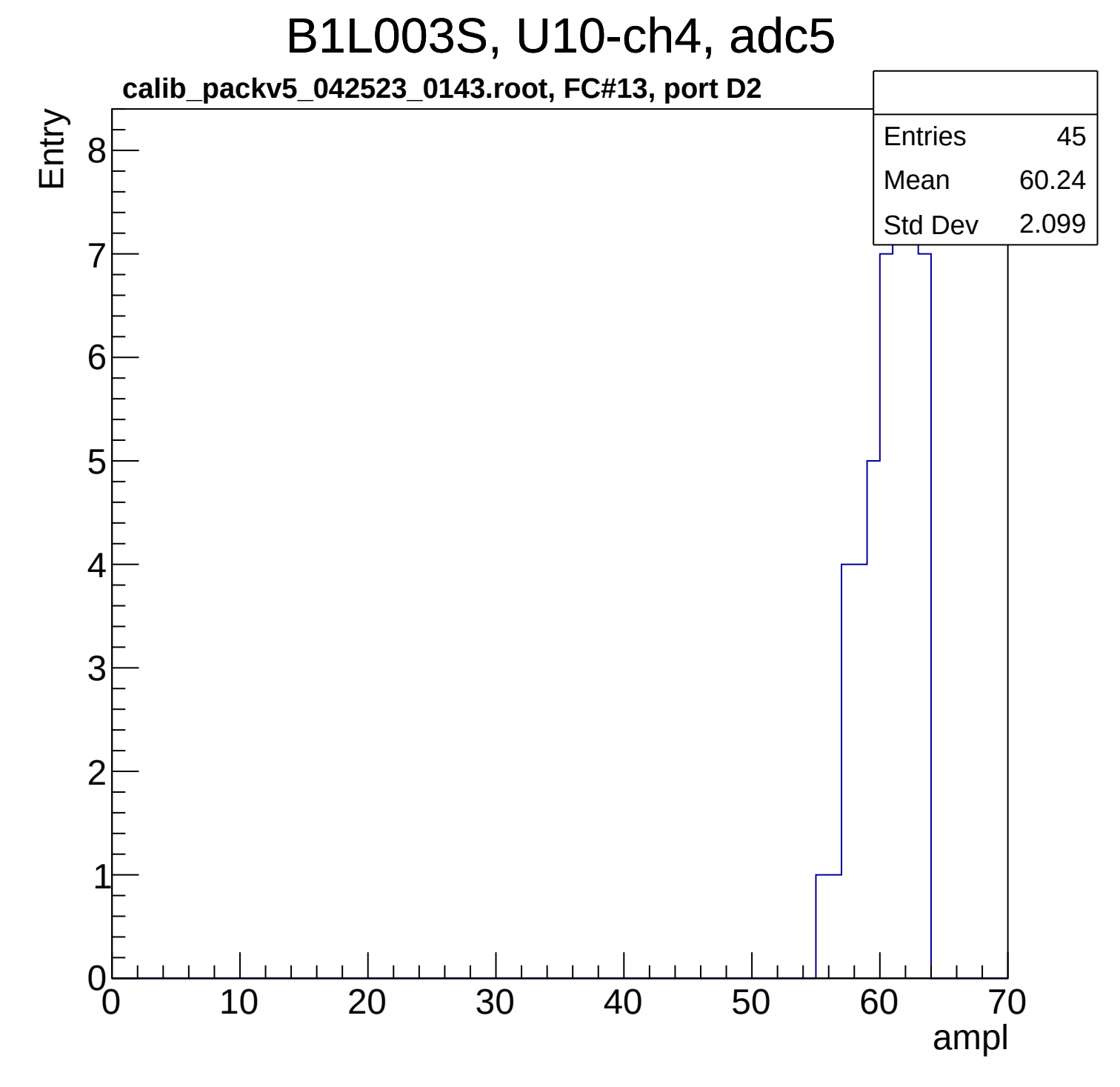
B1L003S, U10-ch4, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 60.24
Std Dev 2.099
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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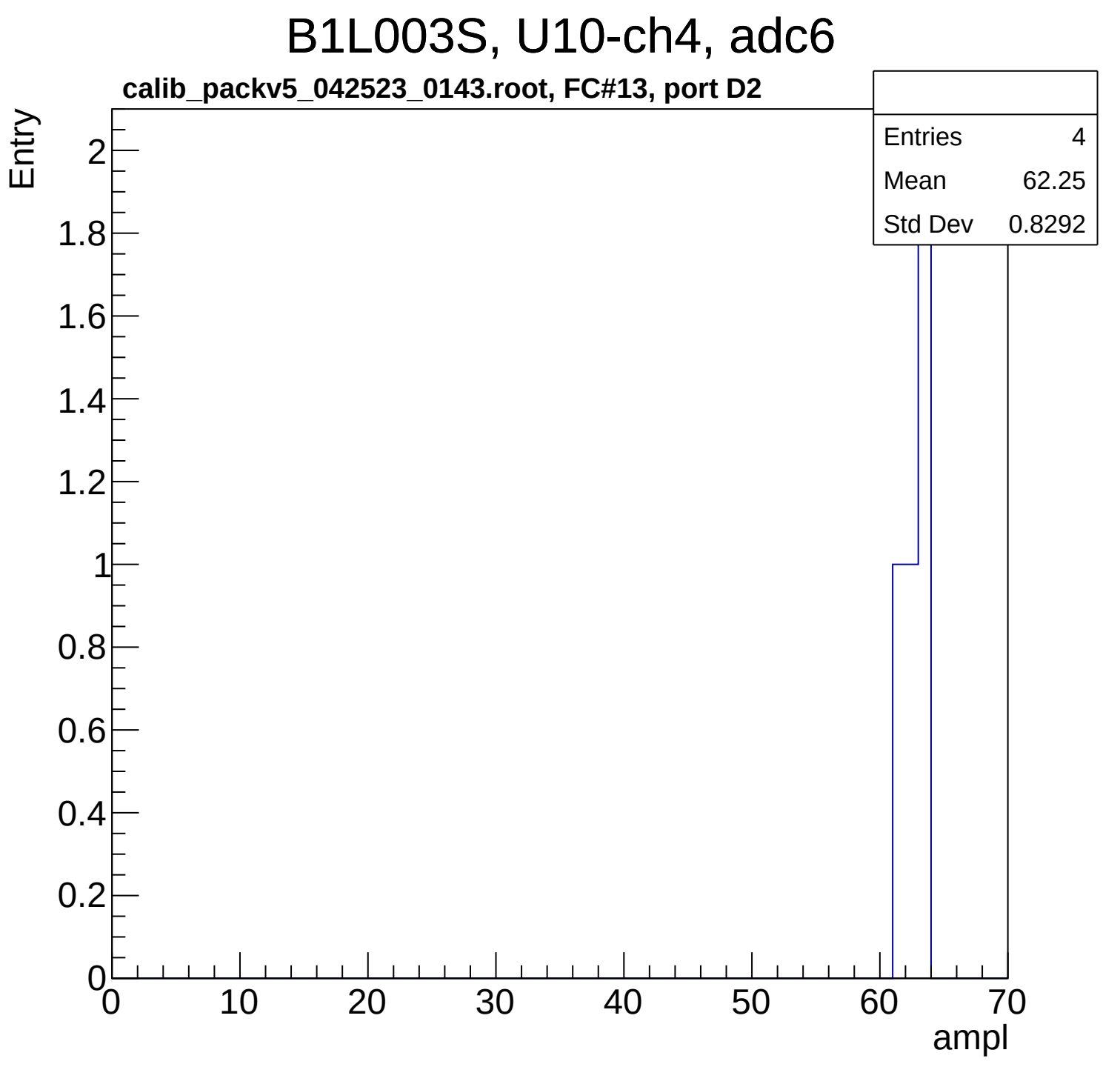
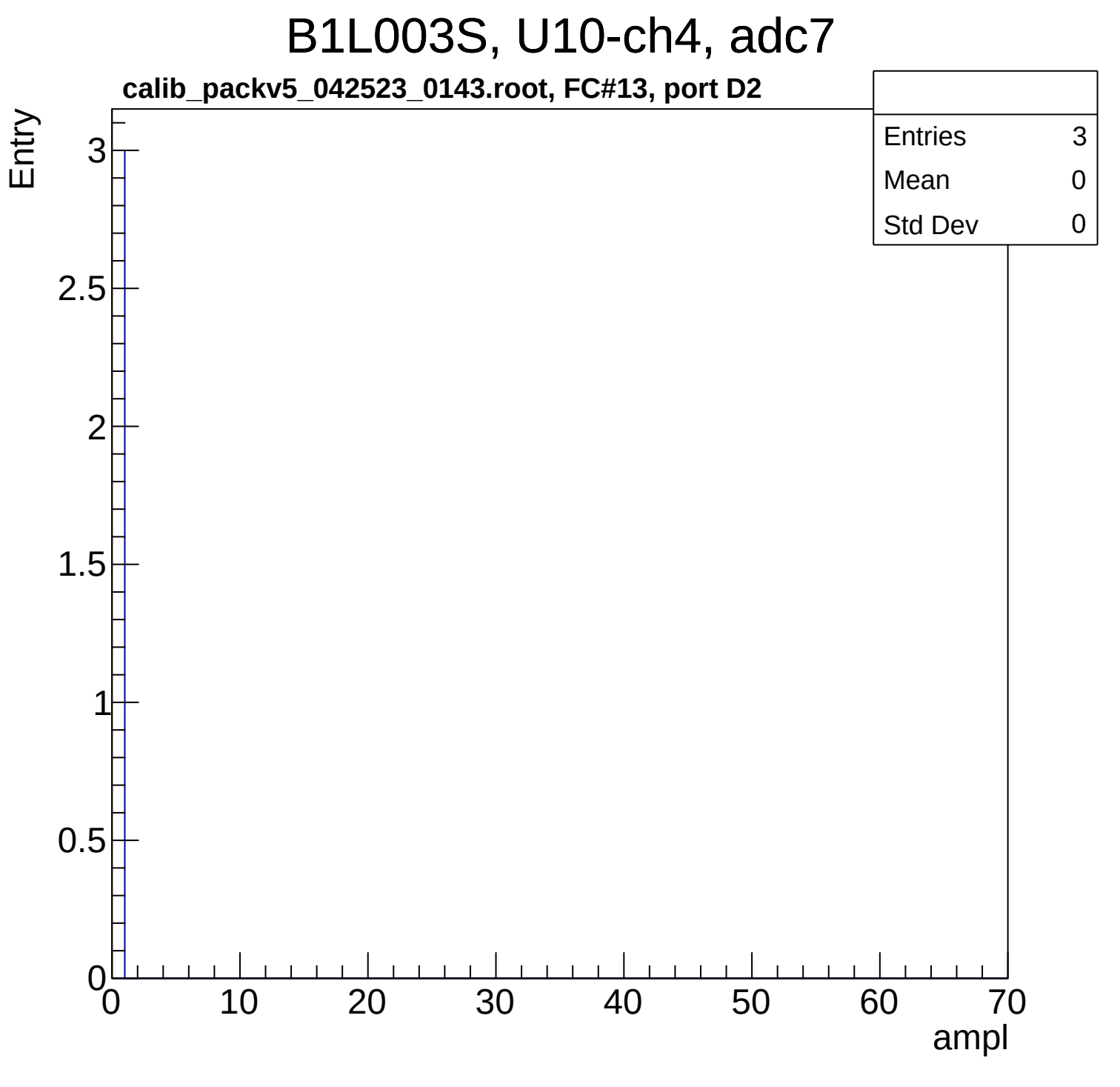
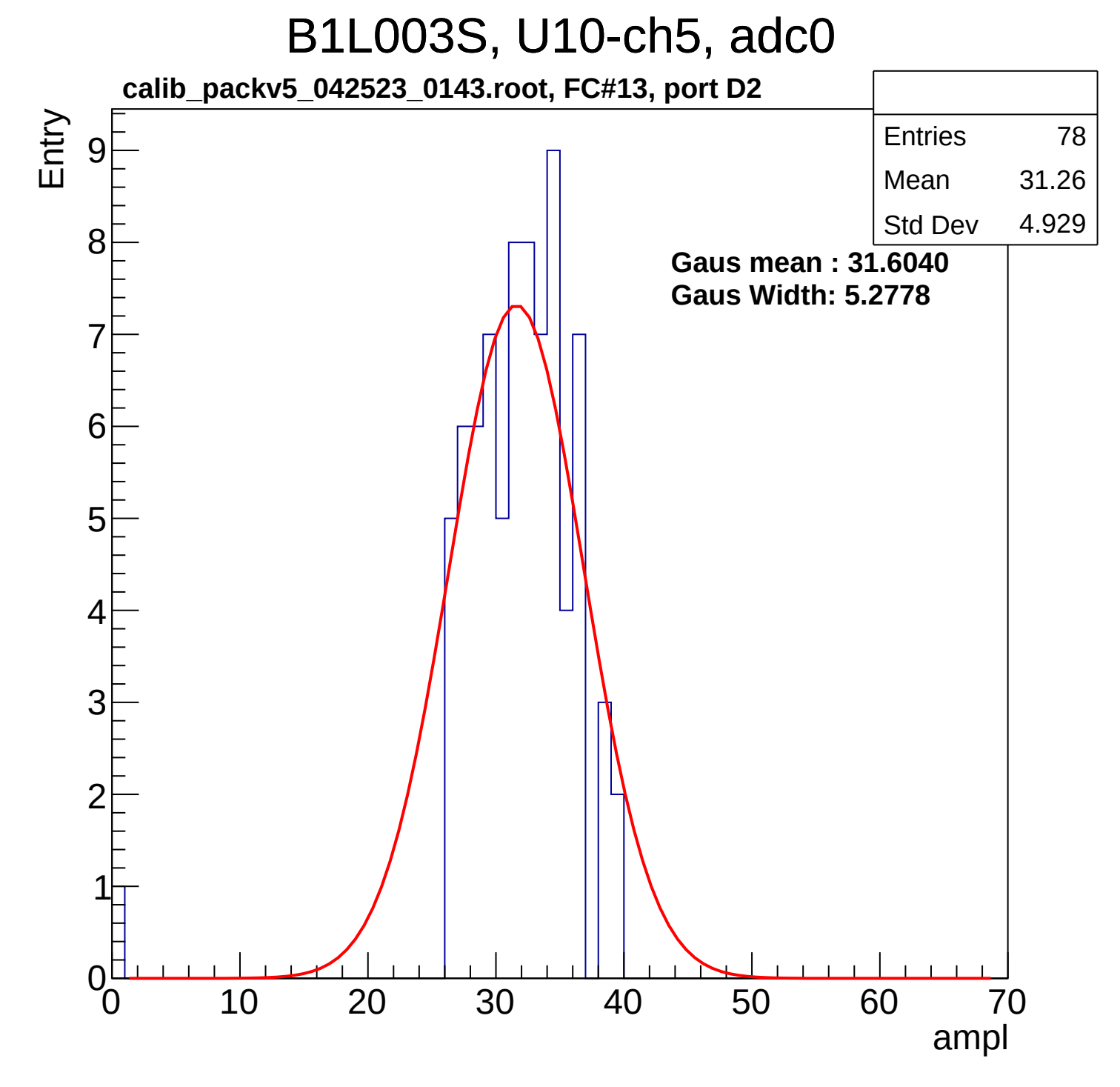
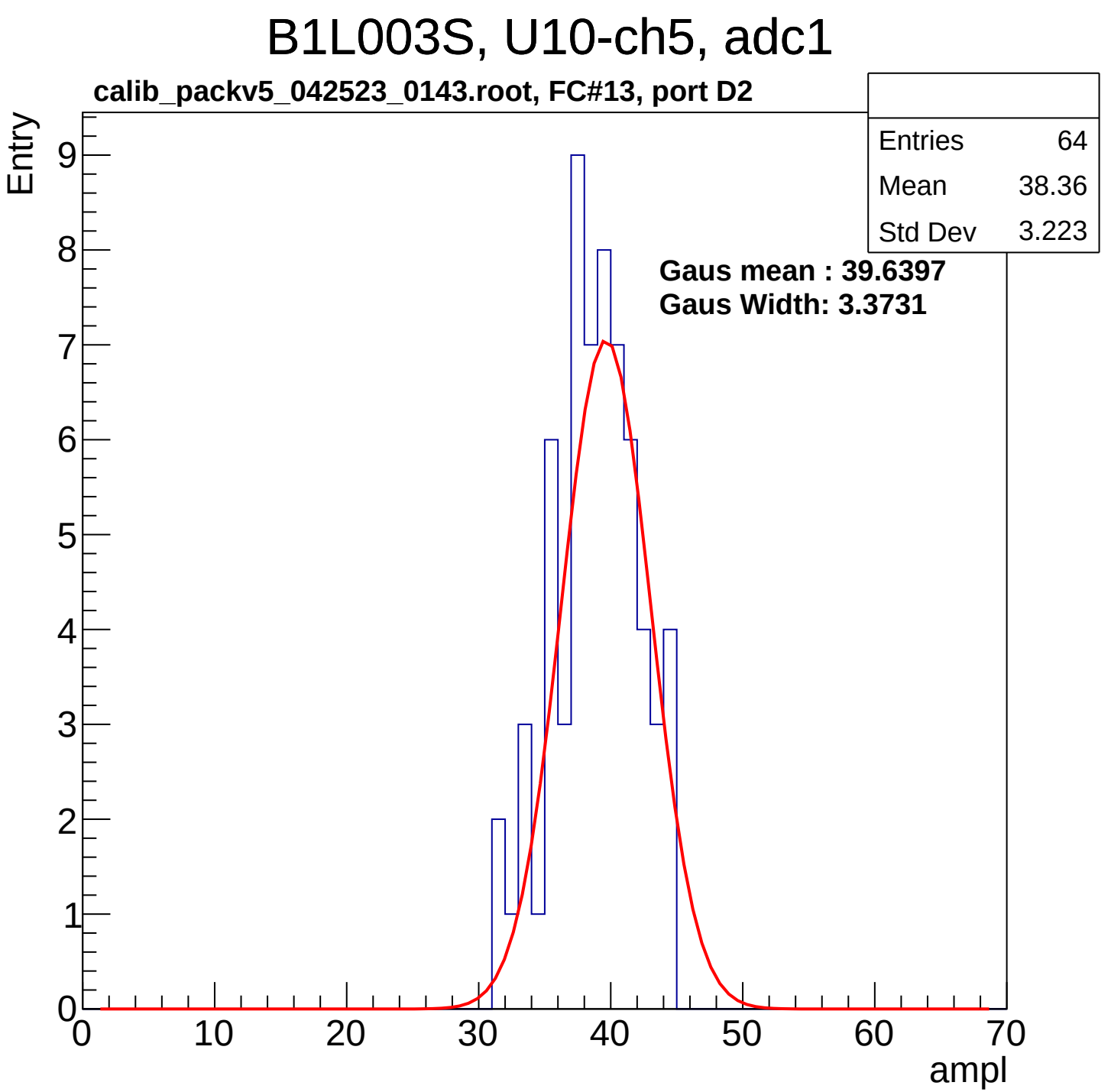

B1L003S, U10-ch4, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch4, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch5, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 31.26
Std Dev 4.929
Gaus mean : 31.6040
Gaus Width: 5.2778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch5, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 38.36
Std Dev 3.223
Gaus mean : 39.6397
Gaus Width: 3.3731
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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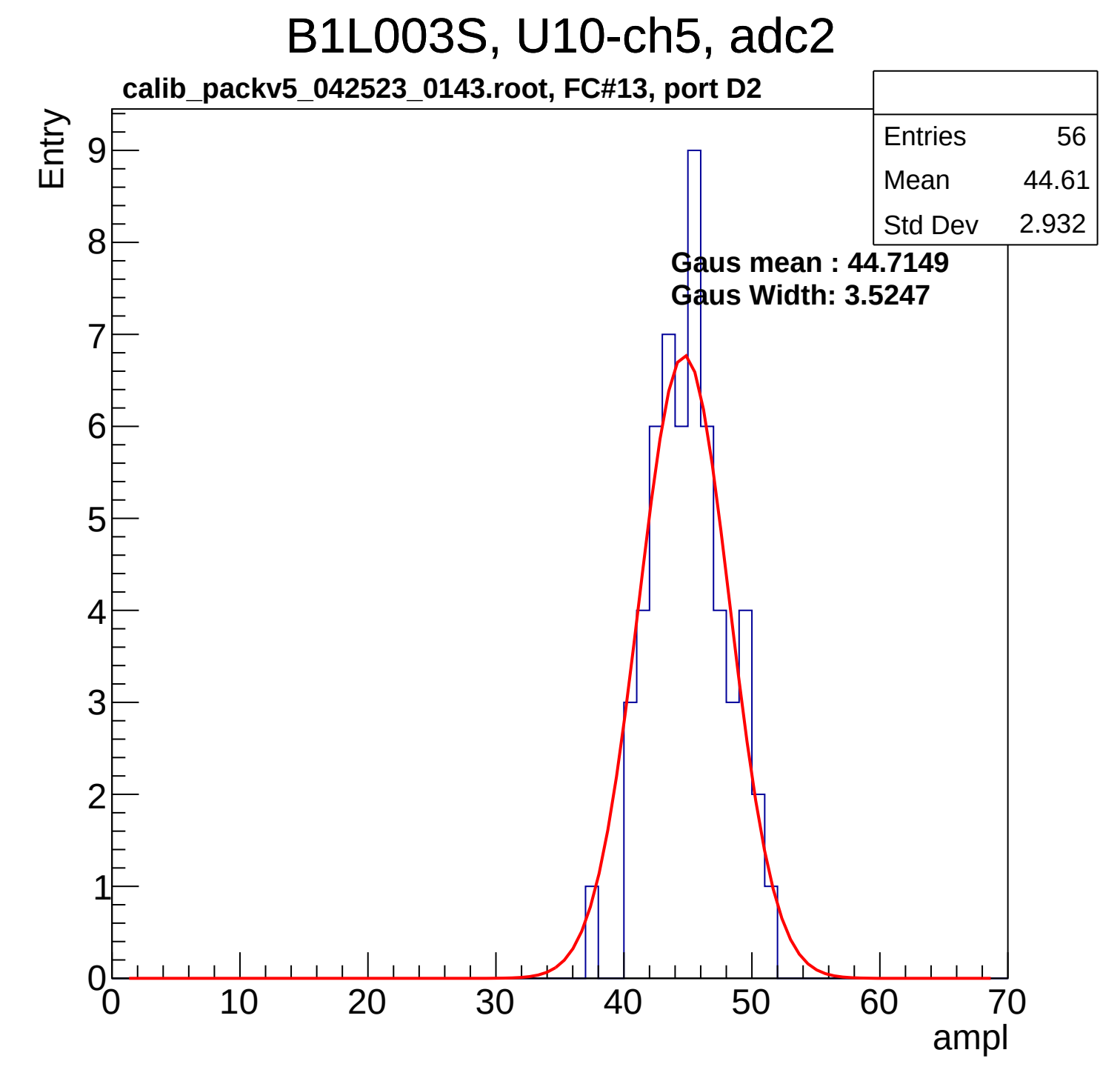
B1L003S, U10-ch5, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 44.61
Std Dev 2.932
Gaus mean : 44.7149
Gaus Width: 3.5247
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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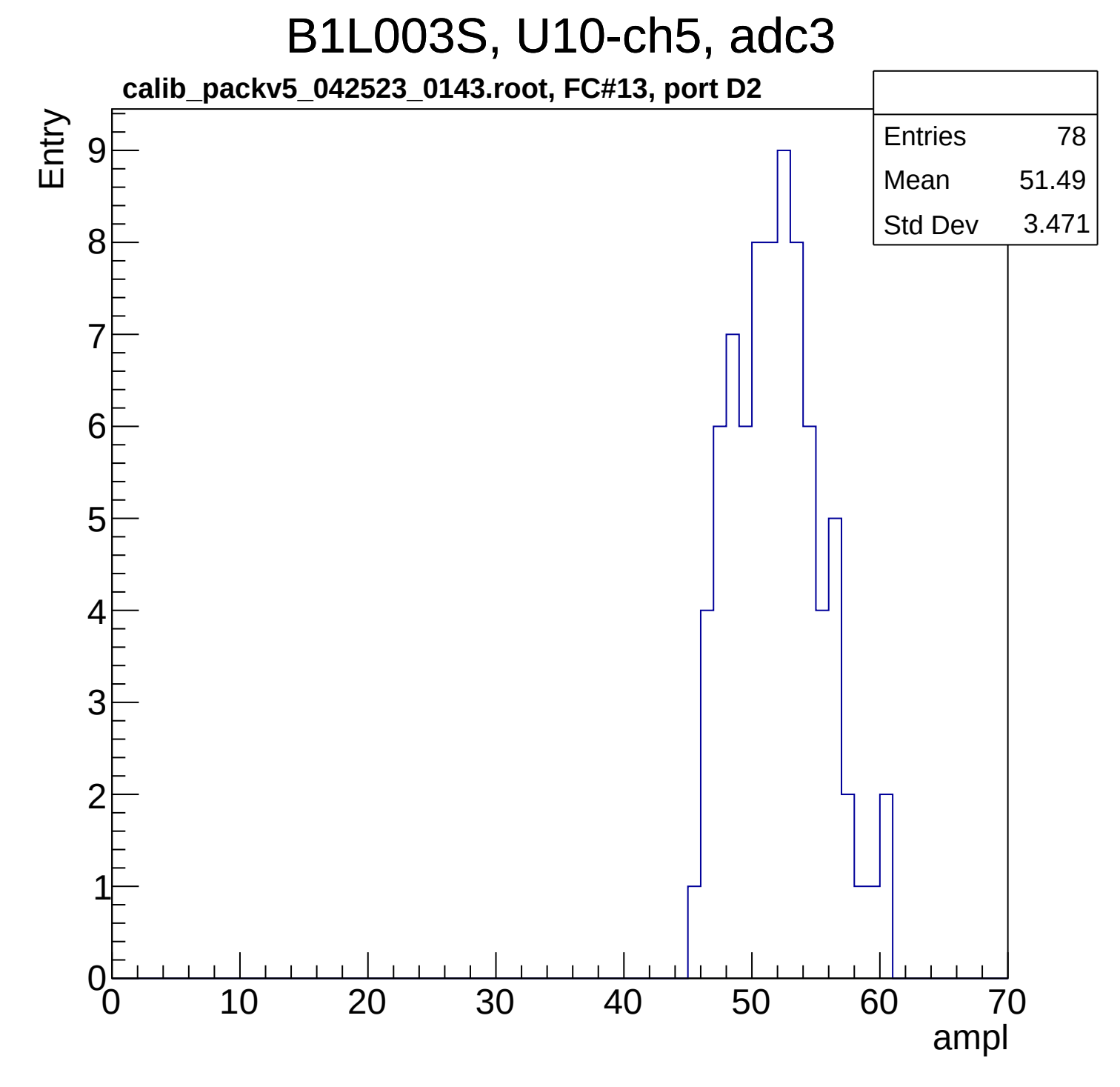
B1L003S, U10-ch5, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 51.49
Std Dev 3.471
Entry
ampl
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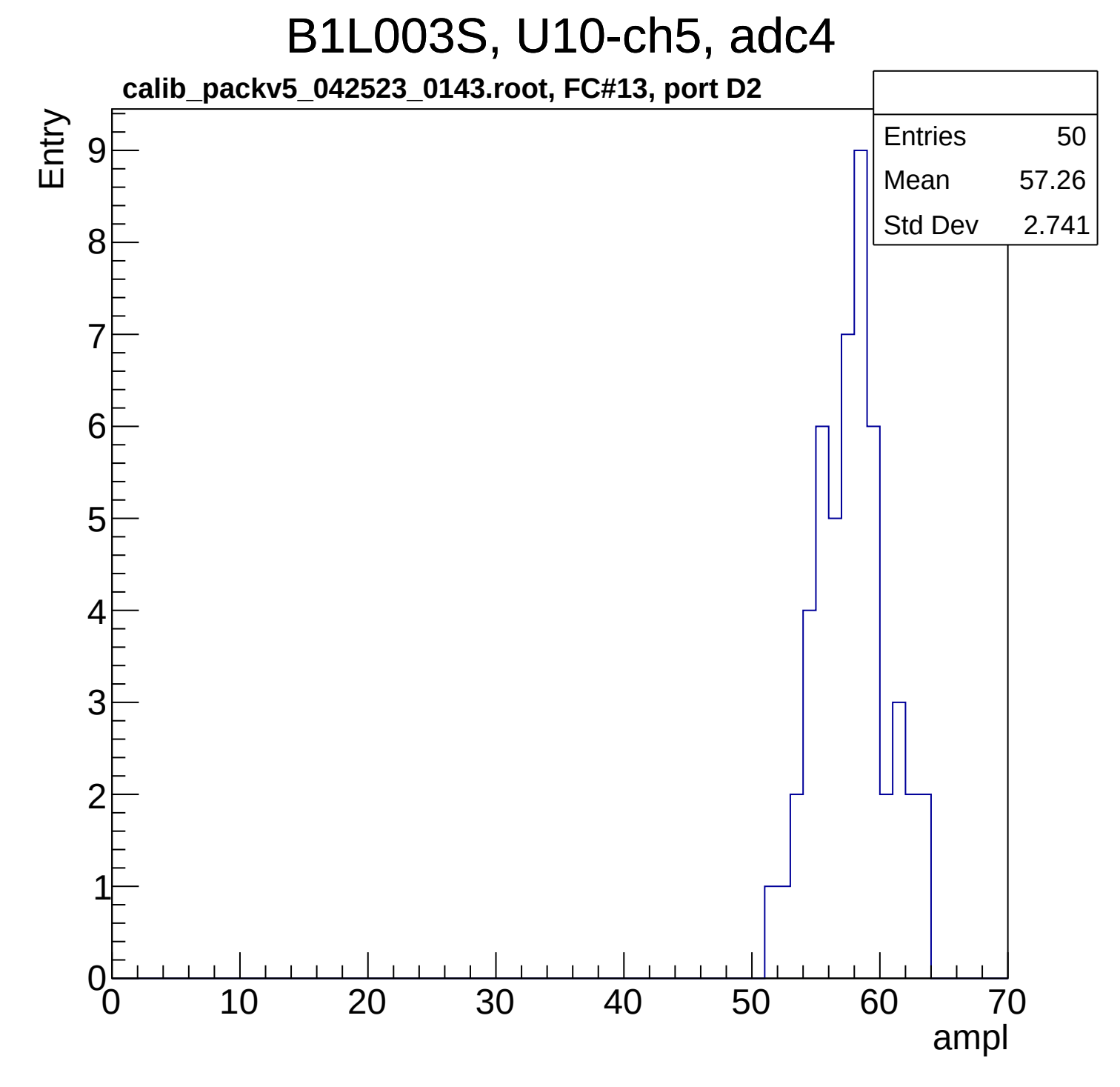
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch5, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 57.26
Std Dev 2.741
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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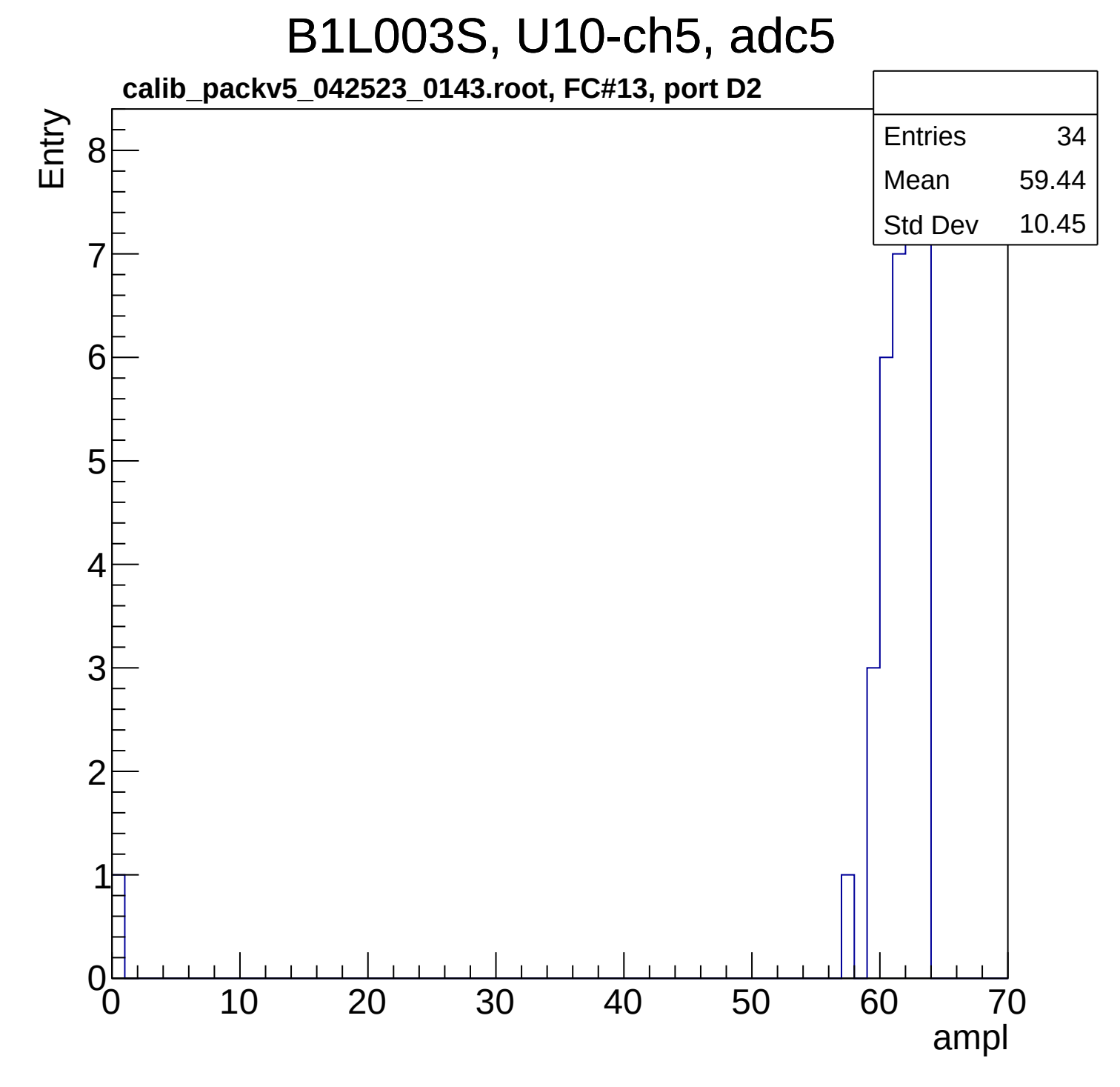
B1L003S, U10-ch5, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 59.44
Std Dev 10.45
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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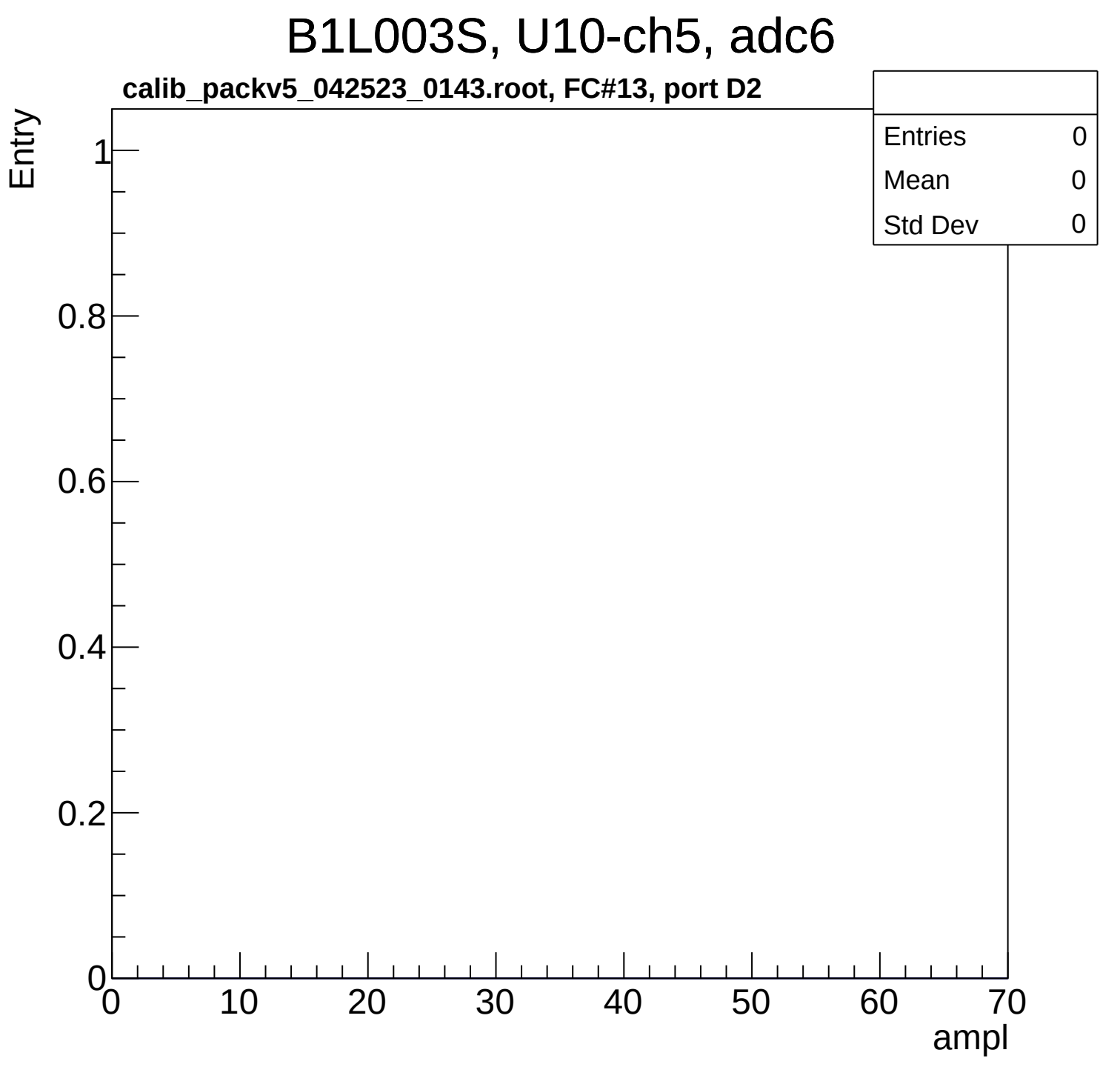
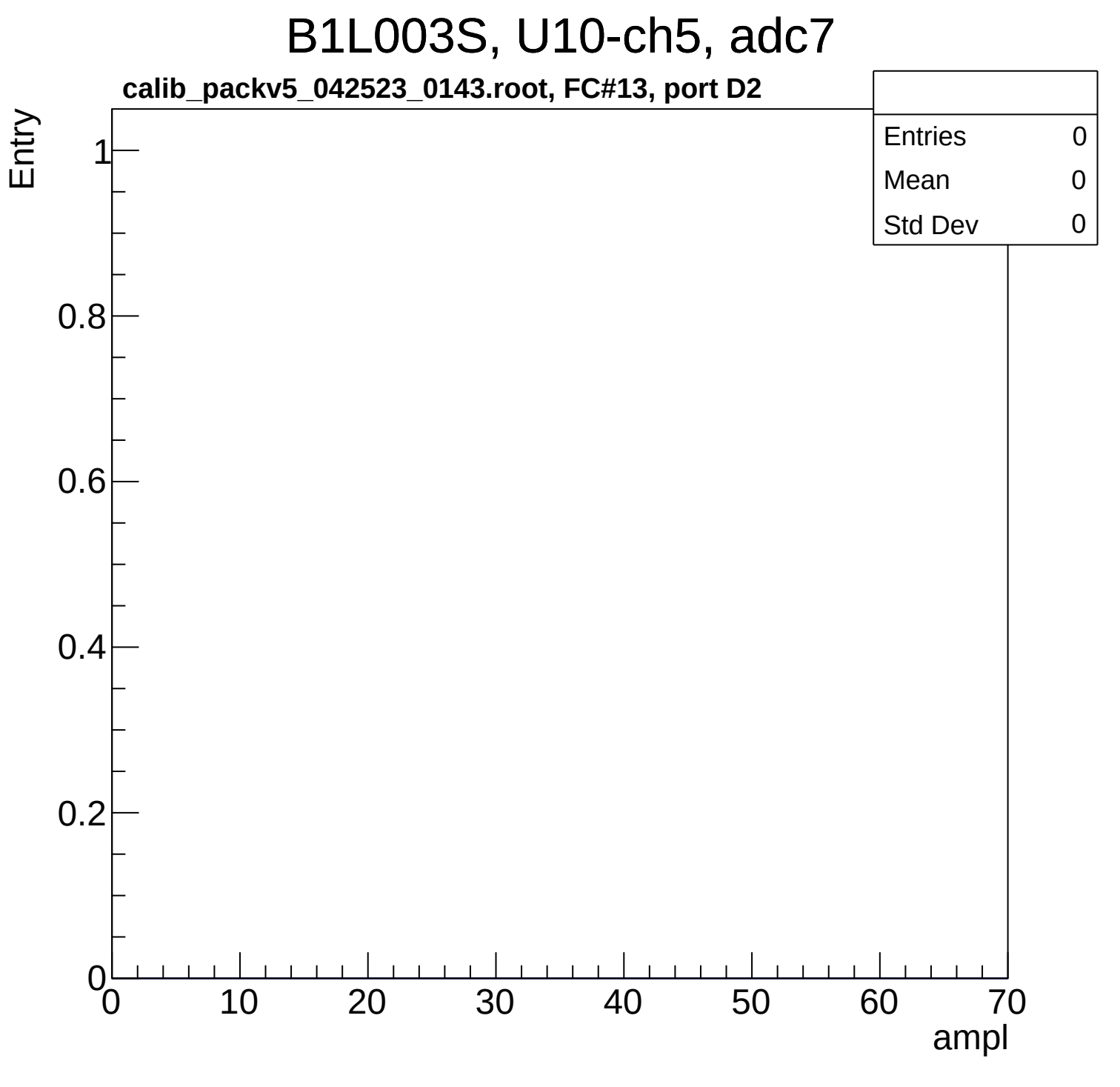
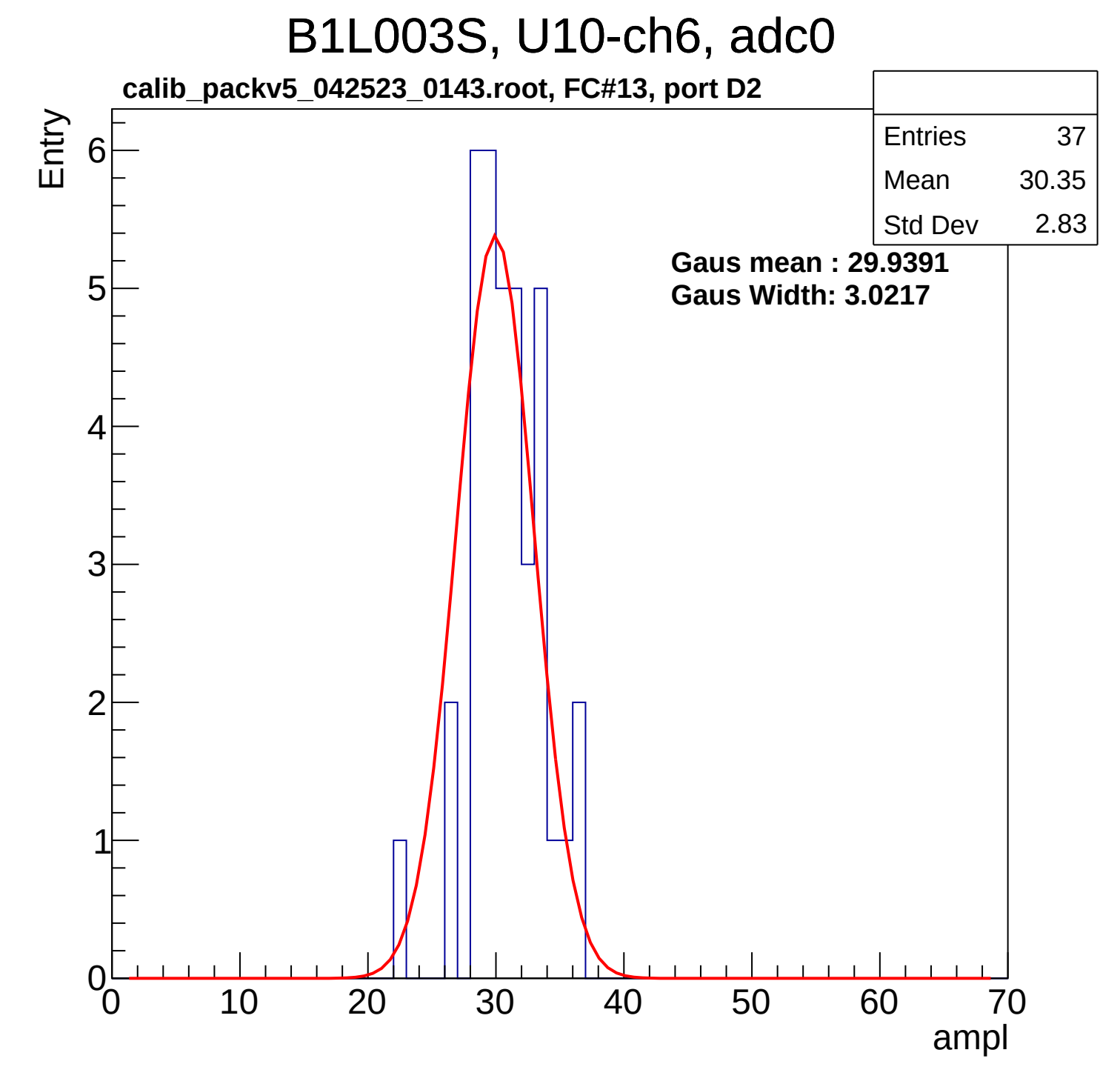
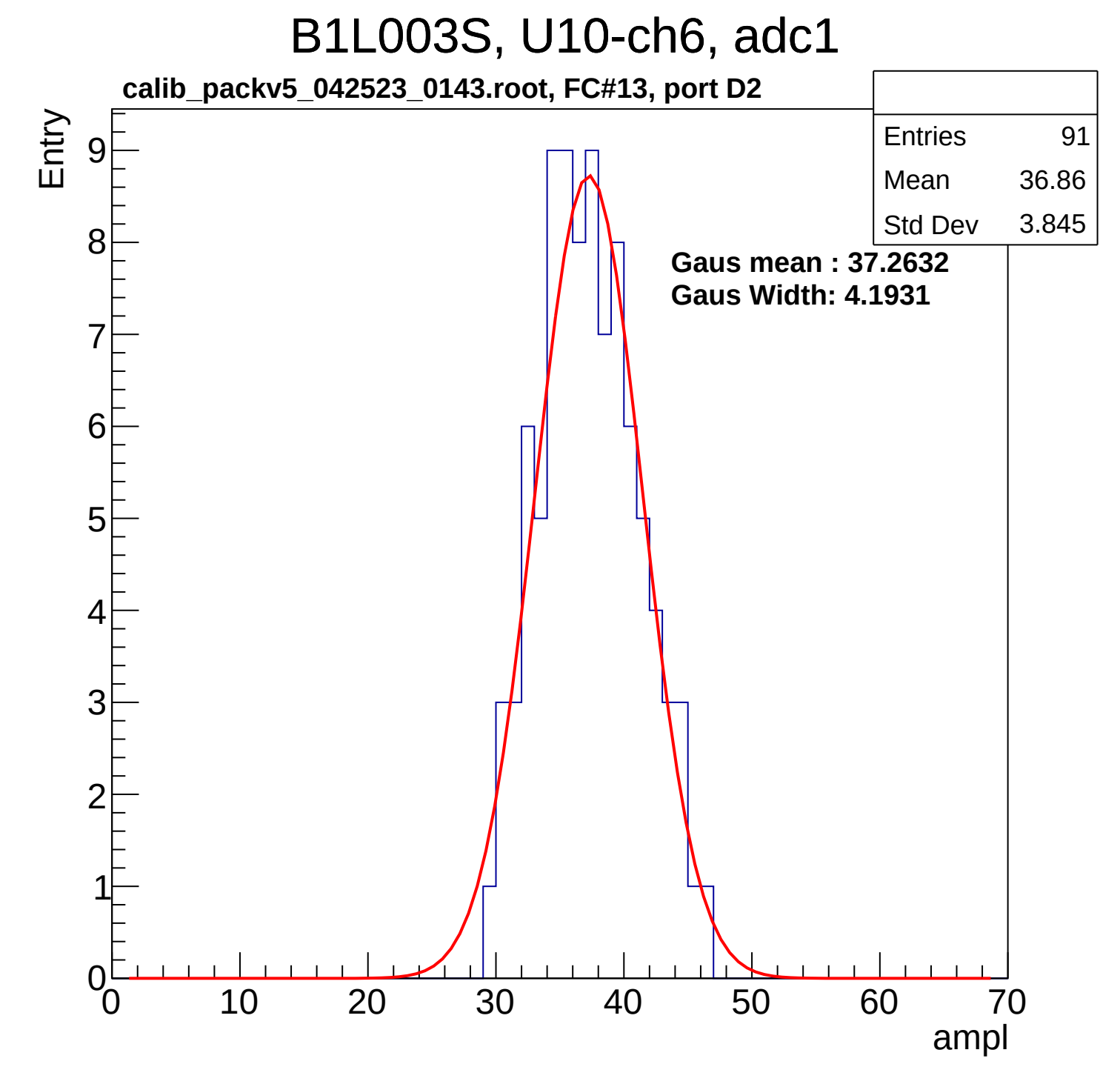

B1L003S, U10-ch5, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch5, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch6, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 30.35
Std Dev 2.83
Gaus mean : 29.9391
Gaus Width: 3.0217
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U10-ch6, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 91
Mean 36.86
Std Dev 3.845
Gaus mean : 37.2632
Gaus Width: 4.1931
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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60
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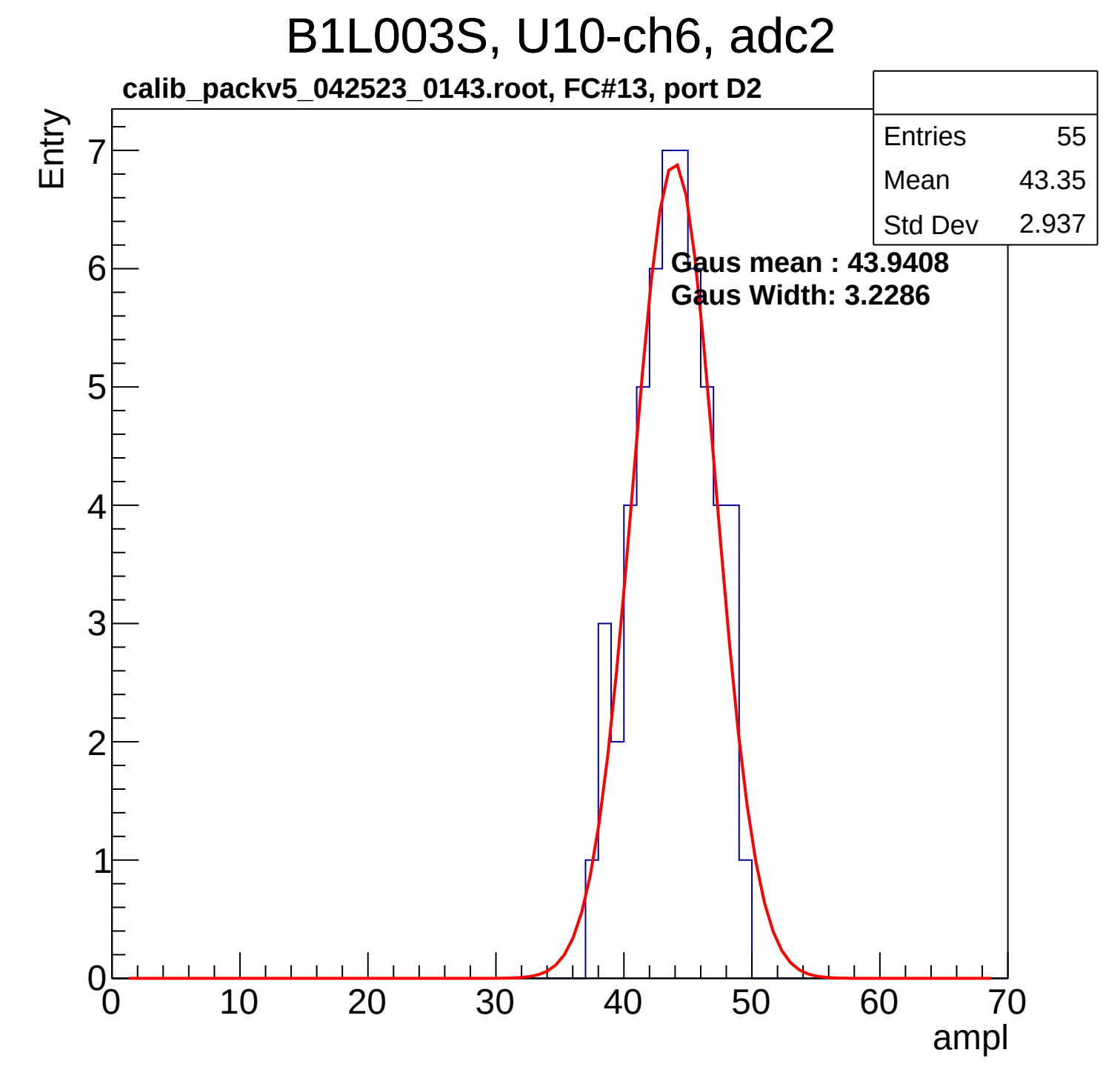
B1L003S, U10-ch6, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43.35
Std Dev 2.937
Gaus mean : 43.9408
Gaus Width: 3.2286
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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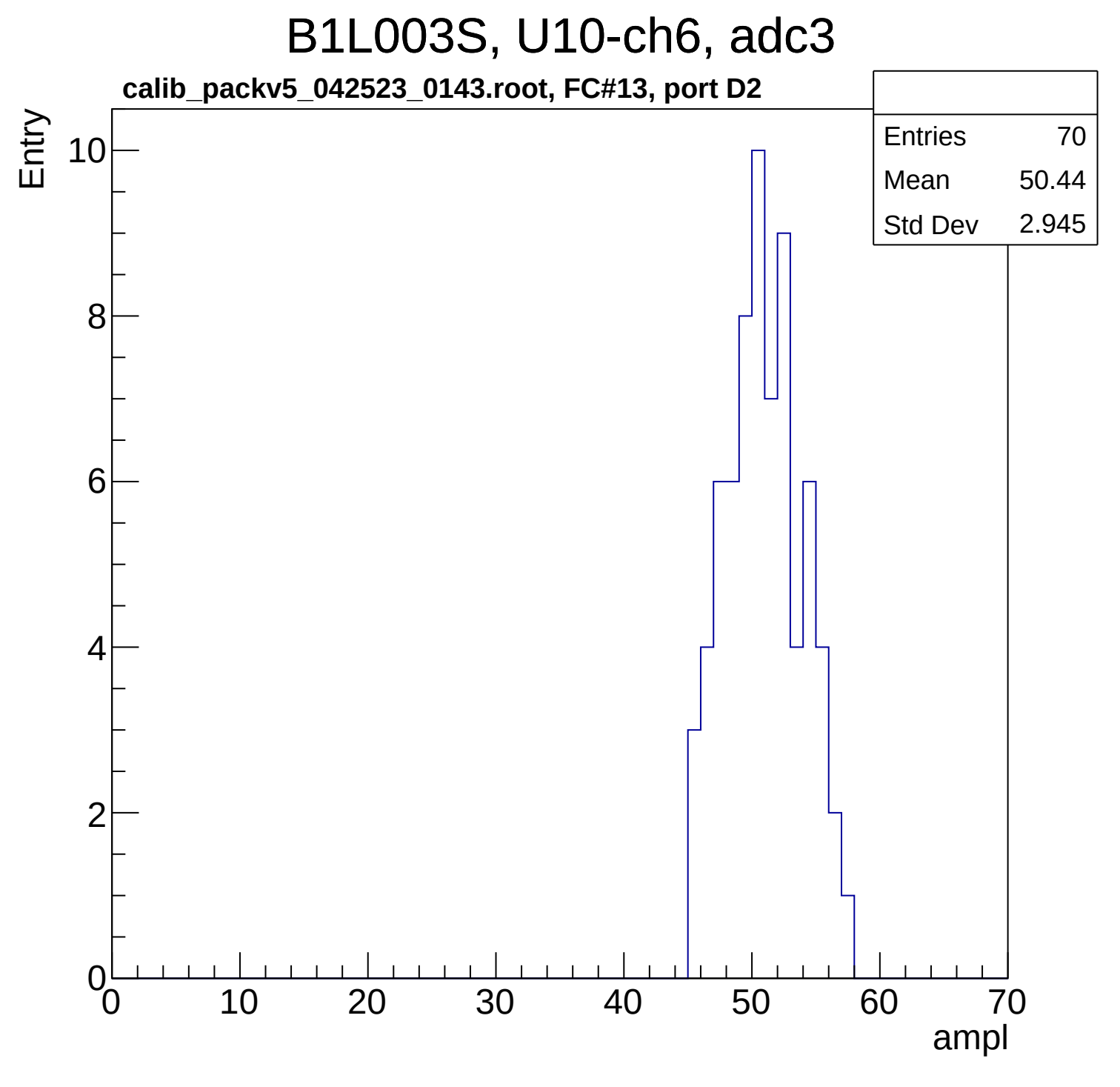
B1L003S, U10-ch6, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 50.44
Std Dev 2.945
Entry
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6
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10
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10
20
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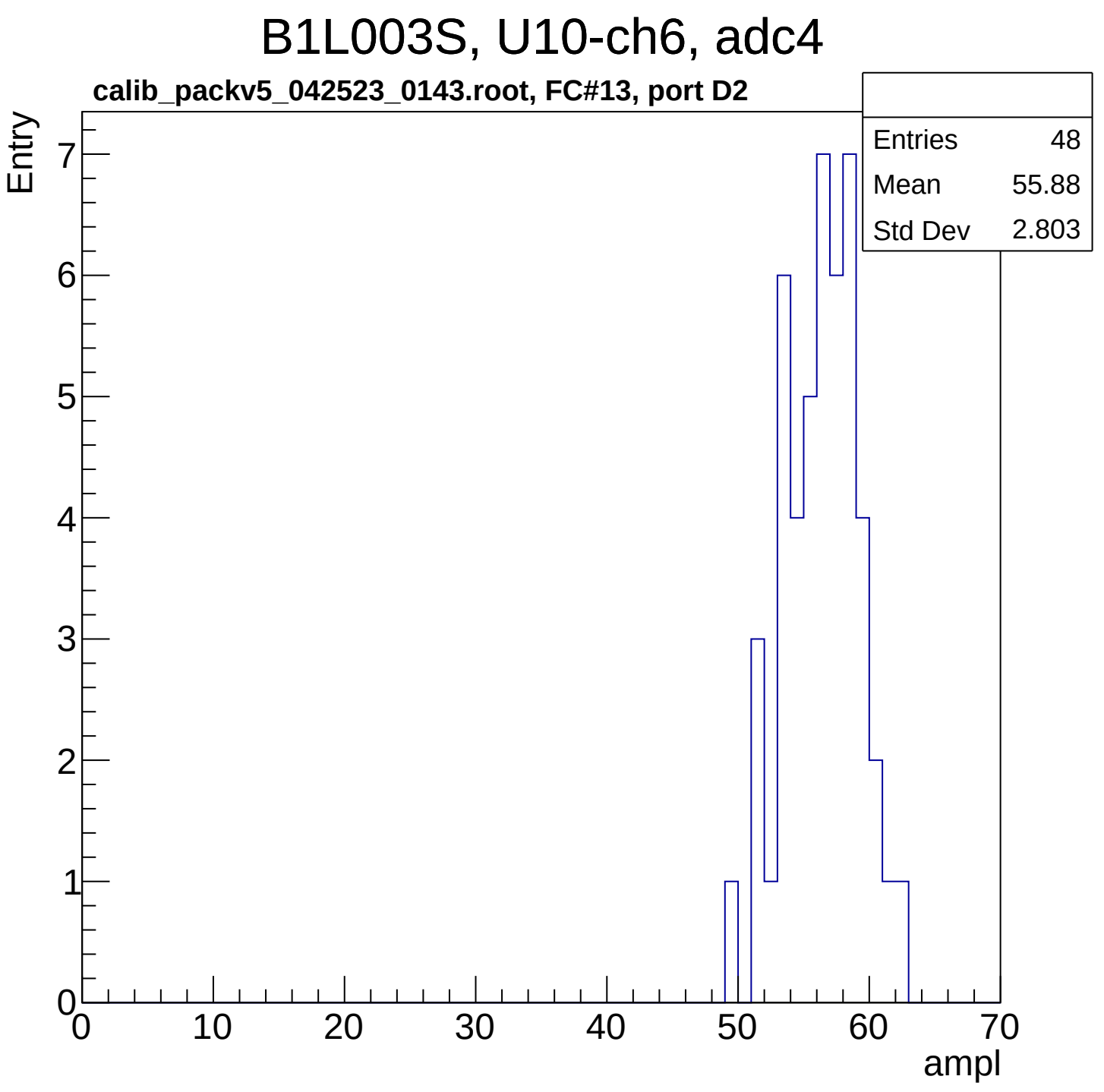
50
60
70
ampl


B1L003S, U10-ch6, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.88
Std Dev 2.803
Entry
ampl
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1
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3
4
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6
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60
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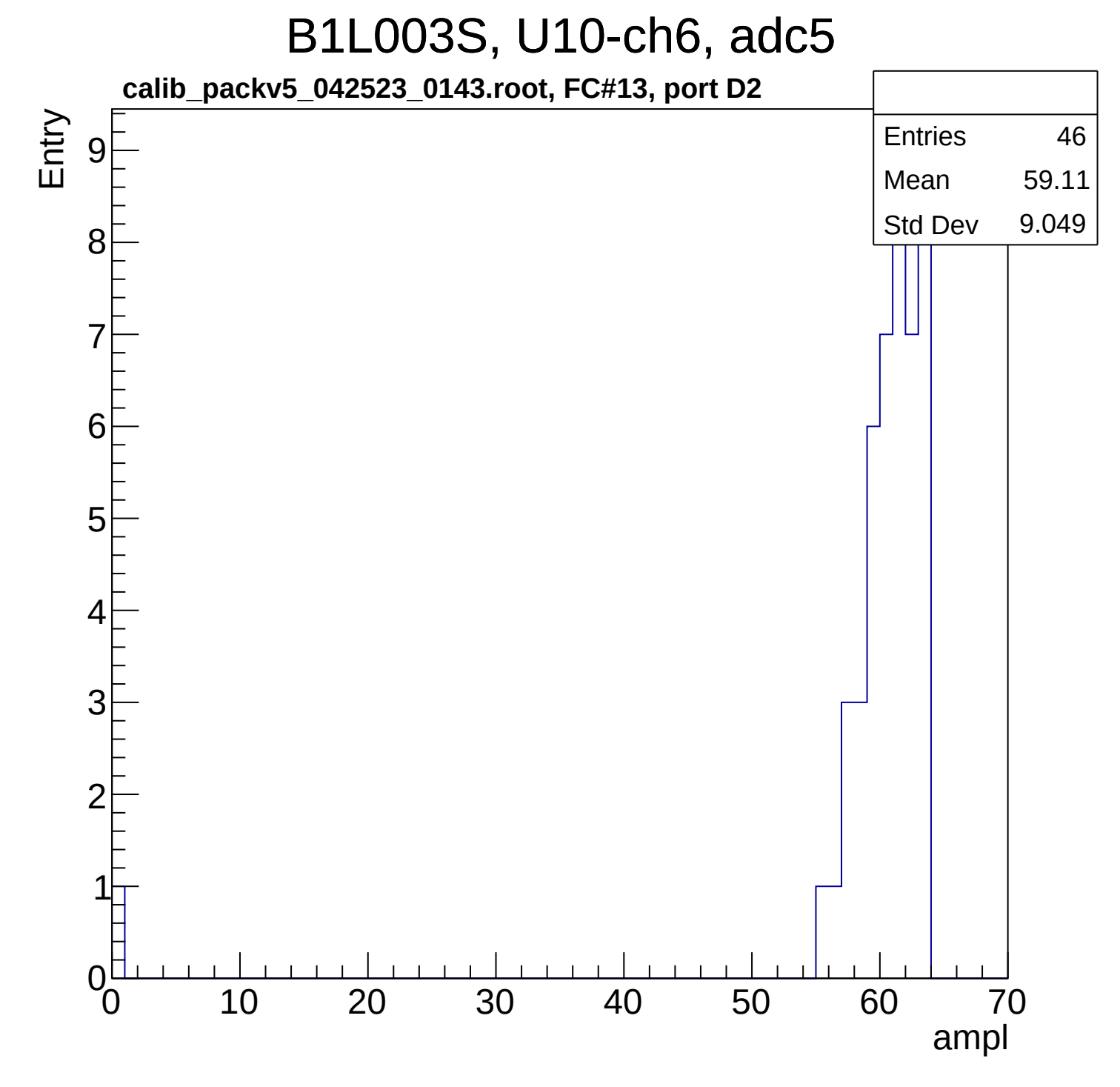
B1L003S, U10-ch6, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.11
Std Dev 9.049
Entry
ampl
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2
3
4
5
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7
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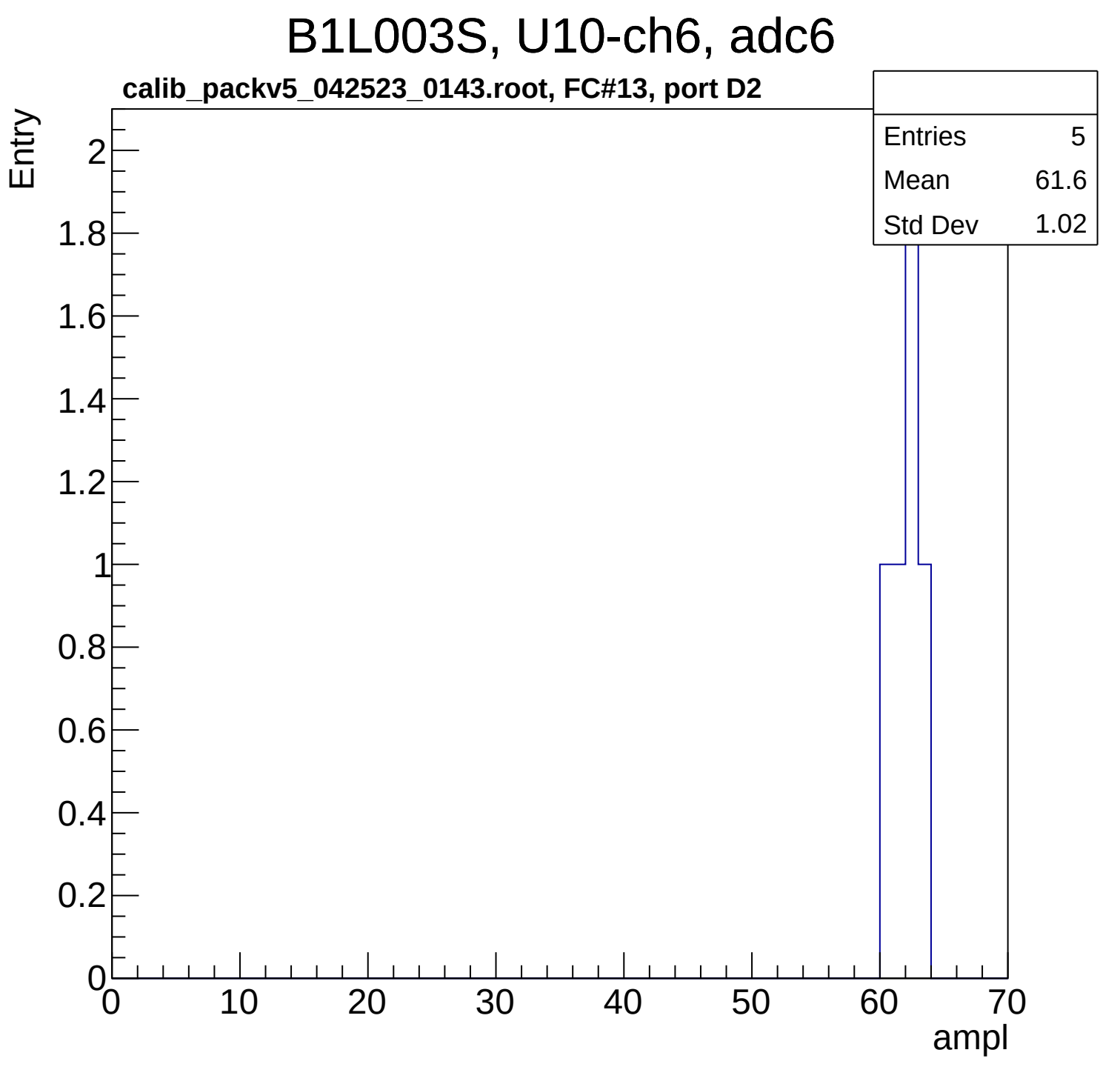
B1L003S, U10-ch6, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.6
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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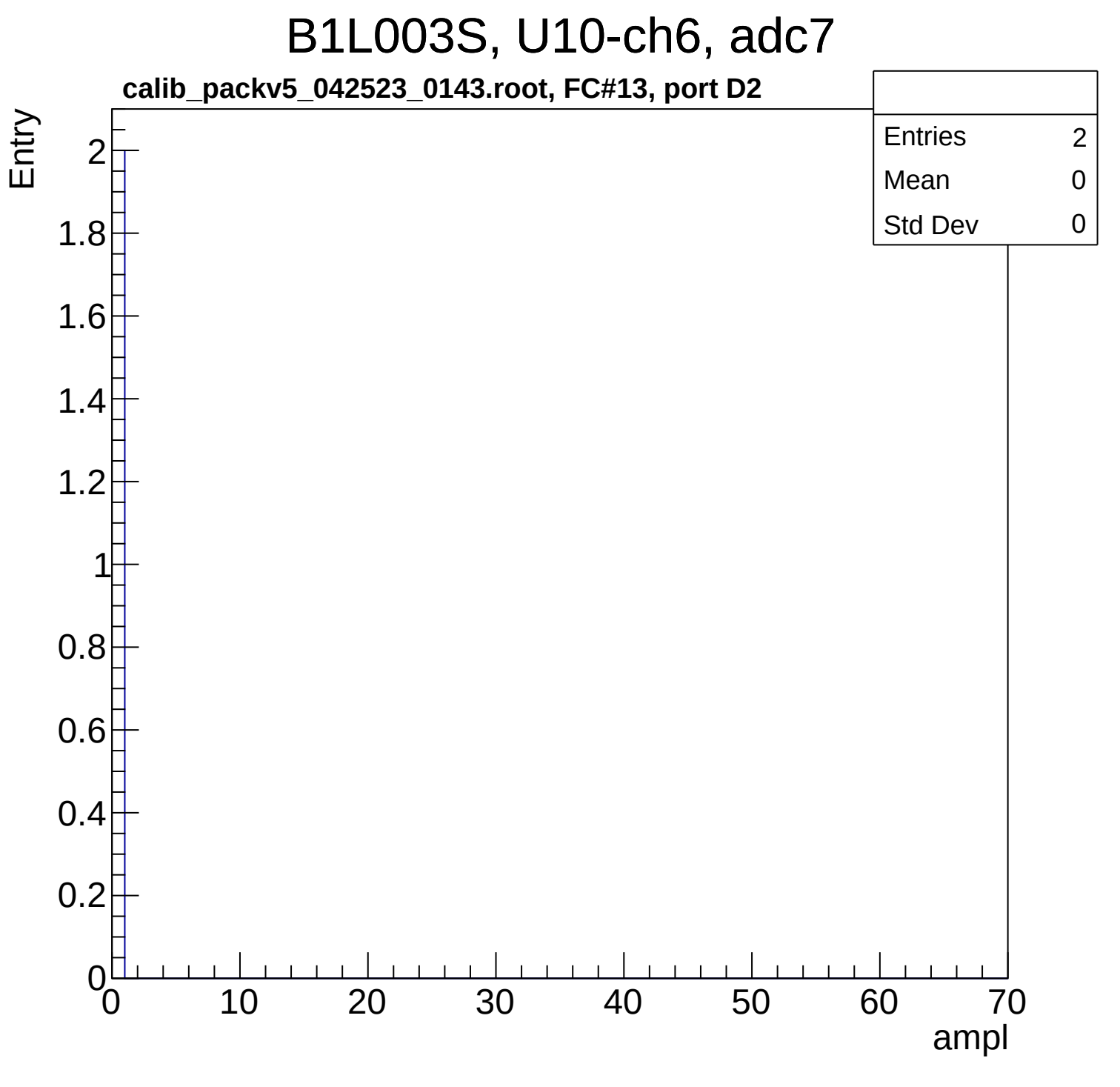
40
50
60
70


B1L003S, U10-ch6, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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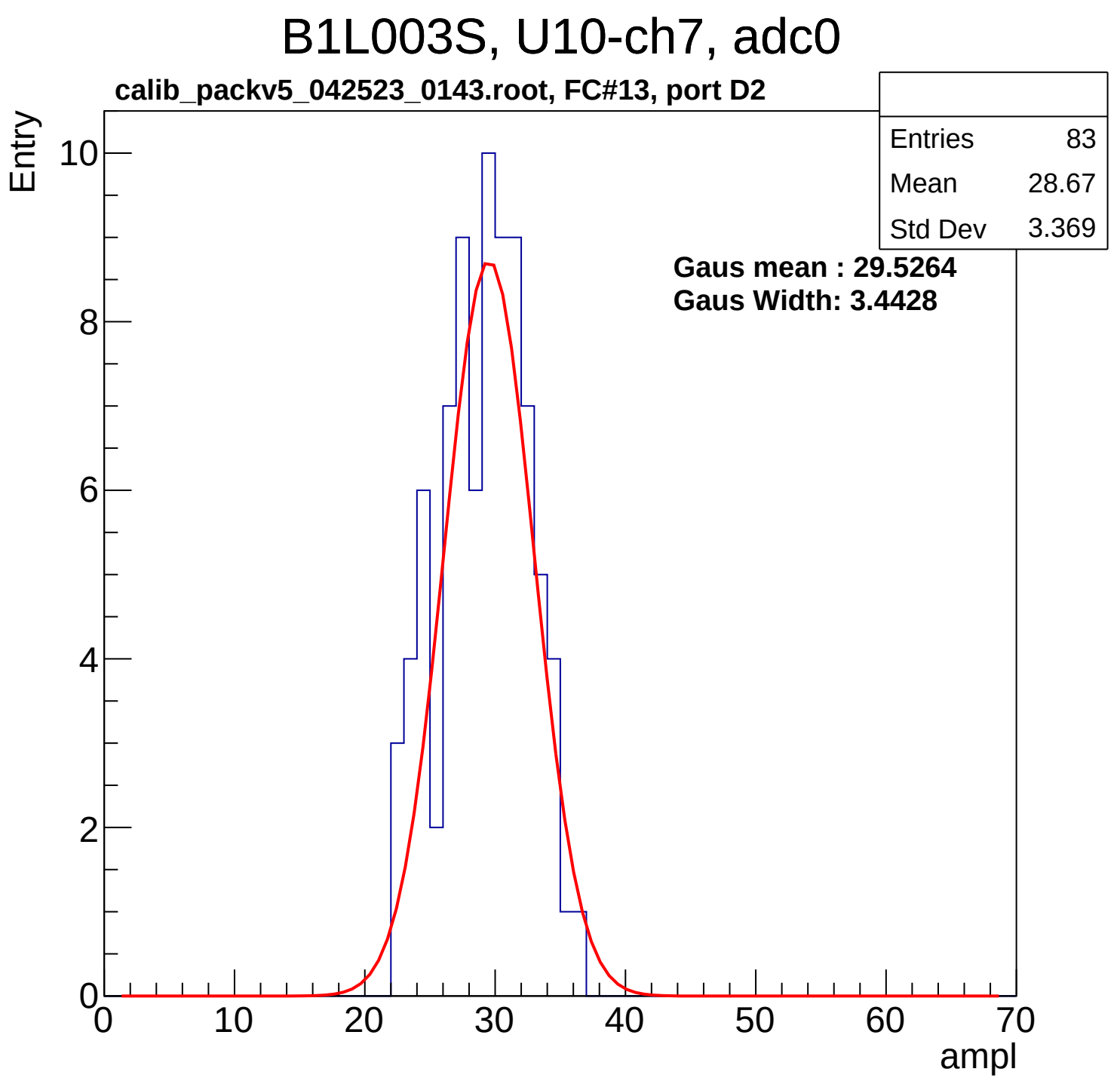
B1L003S, U10-ch7, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 28.67
Std Dev 3.369
Gaus mean : 29.5264
Gaus Width: 3.4428
Entry
ampl
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4
6
8
10
0
10
20
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40
50
60
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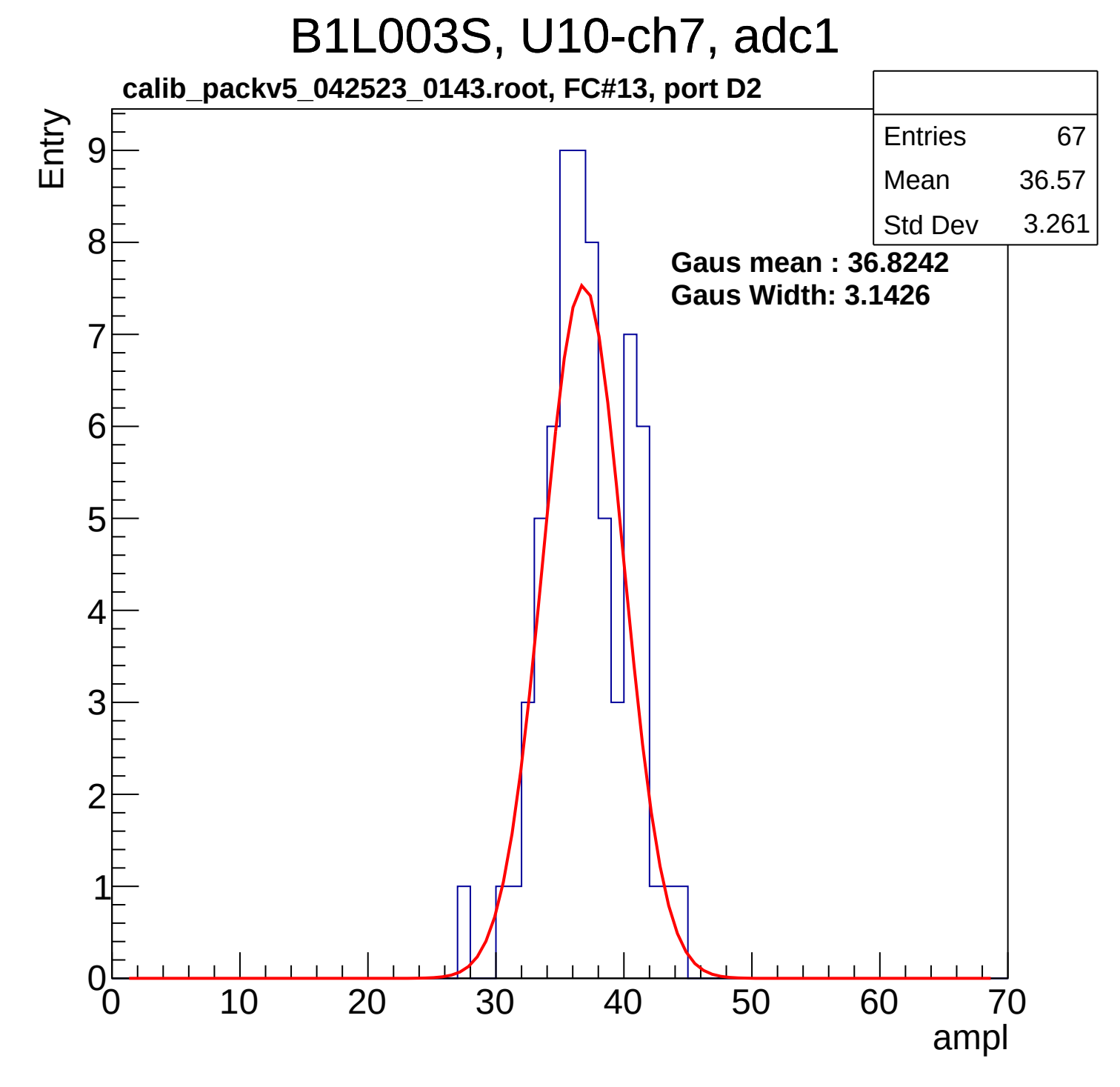
B1L003S, U10-ch7, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.57
Std Dev 3.261
Gaus mean : 36.8242
Gaus Width: 3.1426
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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30
40
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60
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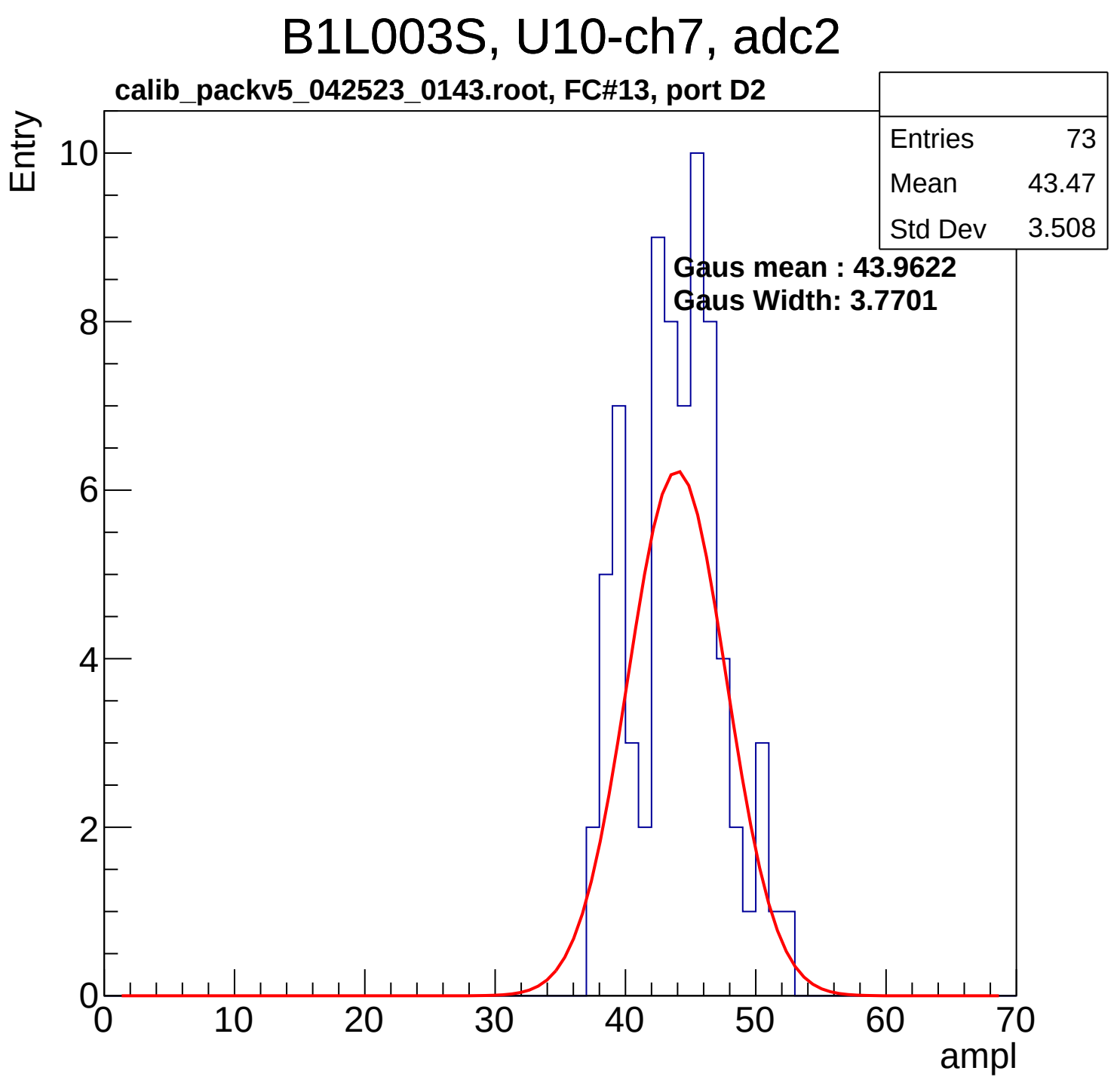
B1L003S, U10-ch7, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 43.47
Std Dev 3.508
Gaus mean : 43.9622
Gaus Width: 3.7701
Entry
ampl
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2
4
6
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40
50
60
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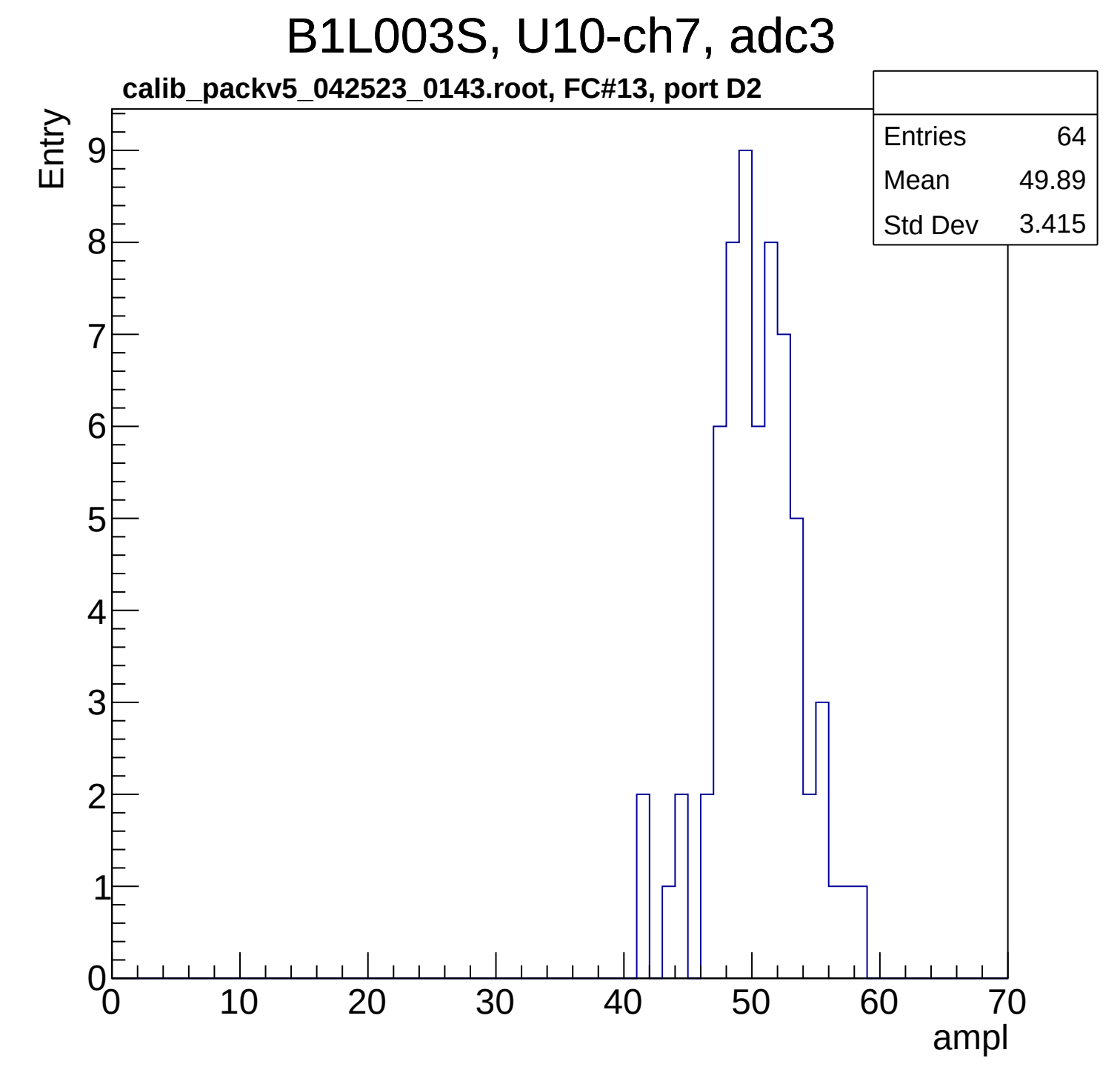
B1L003S, U10-ch7, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49.89
Std Dev 3.415
Entry
ampl
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3
4
5
6
7
8
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0
10
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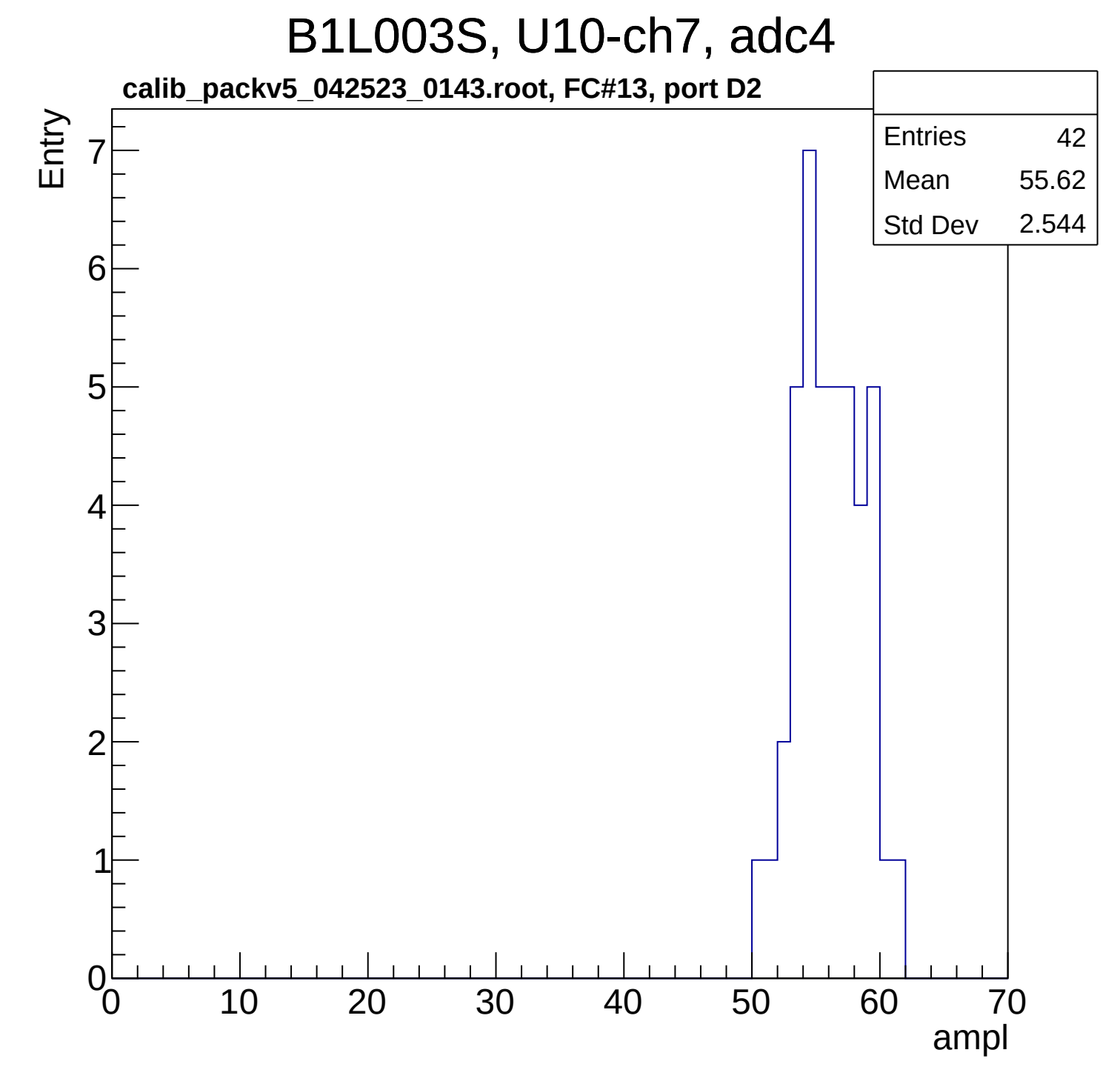
B1L003S, U10-ch7, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 55.62
Std Dev 2.544
Entry
ampl
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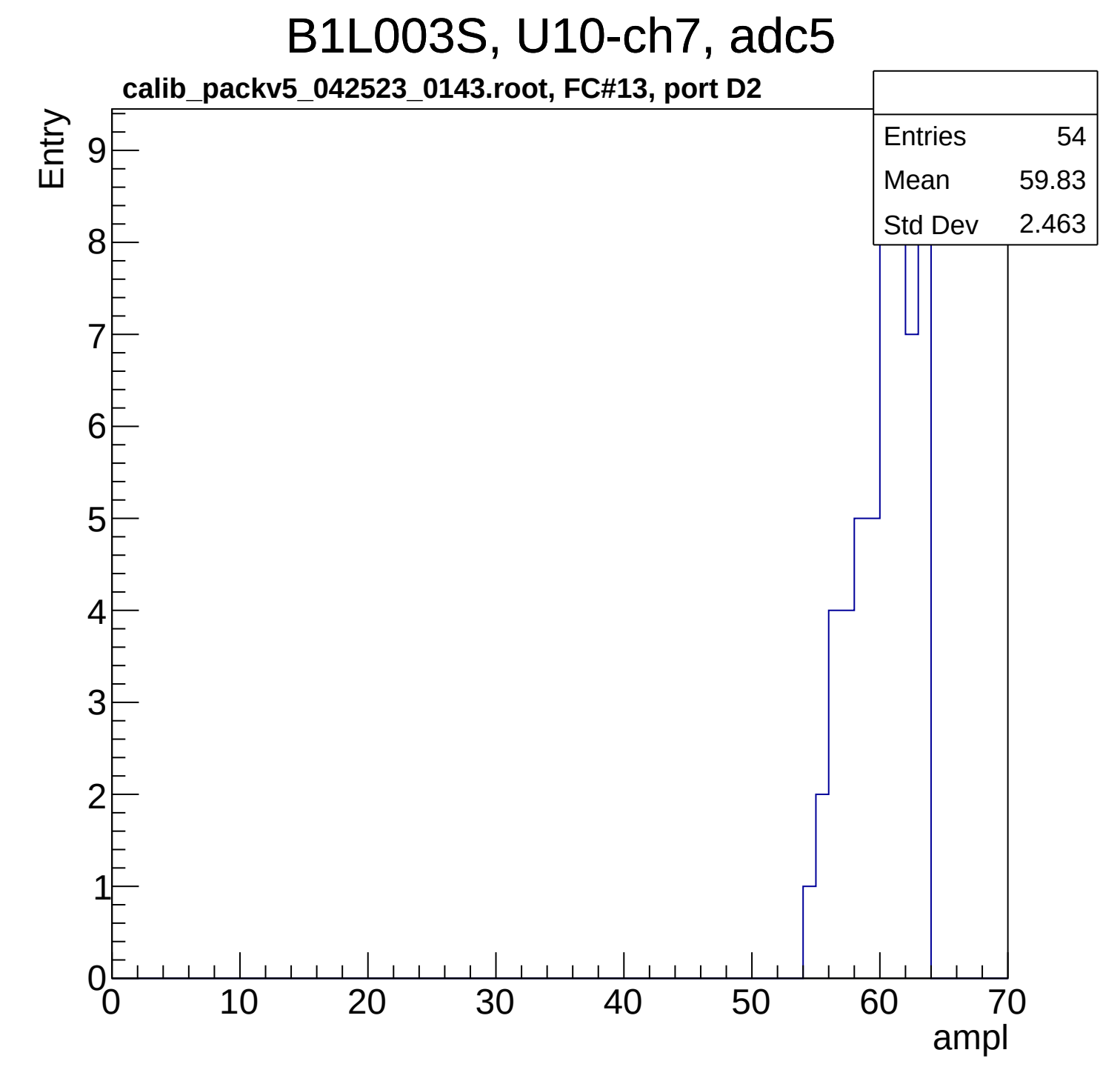
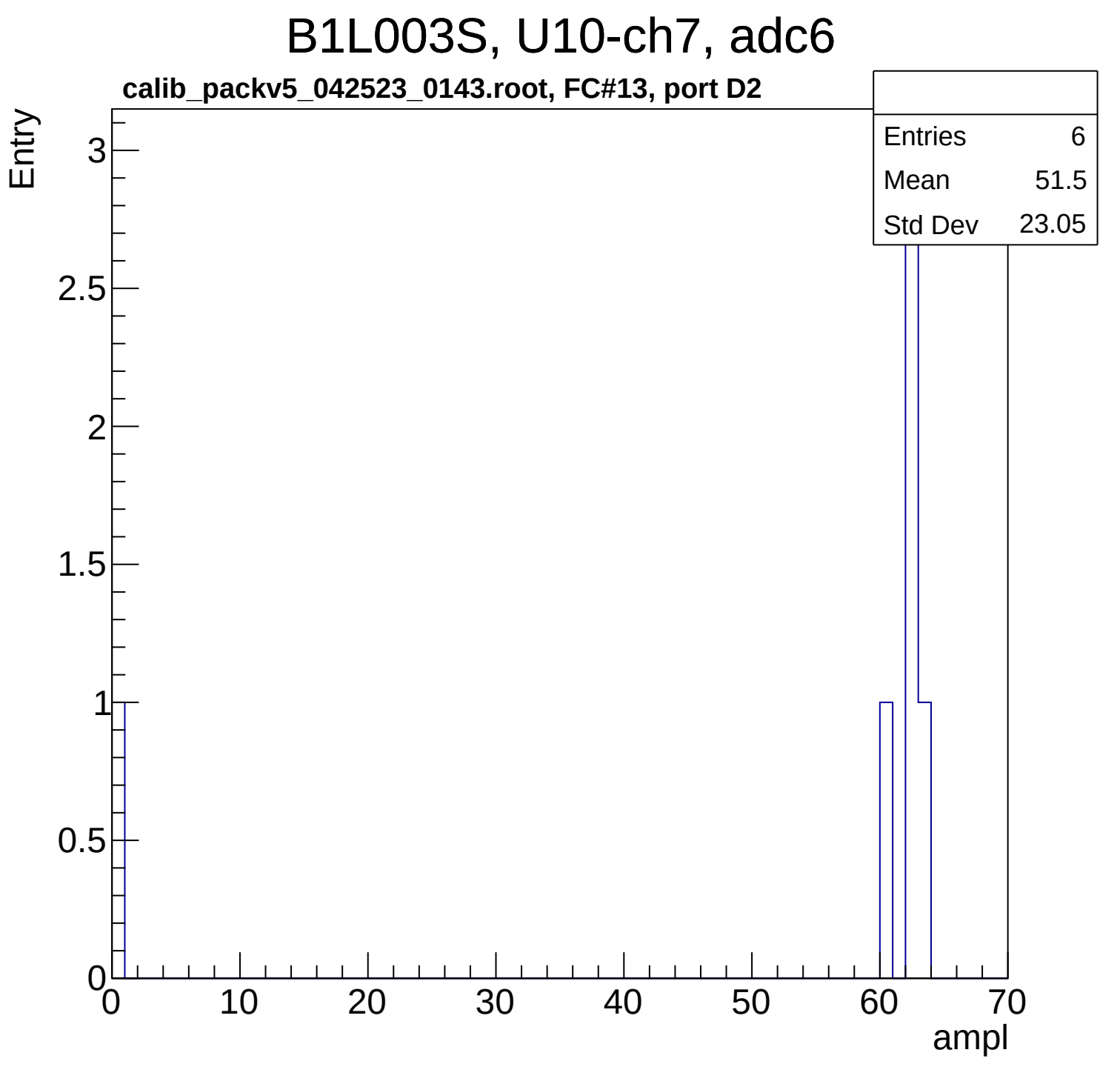
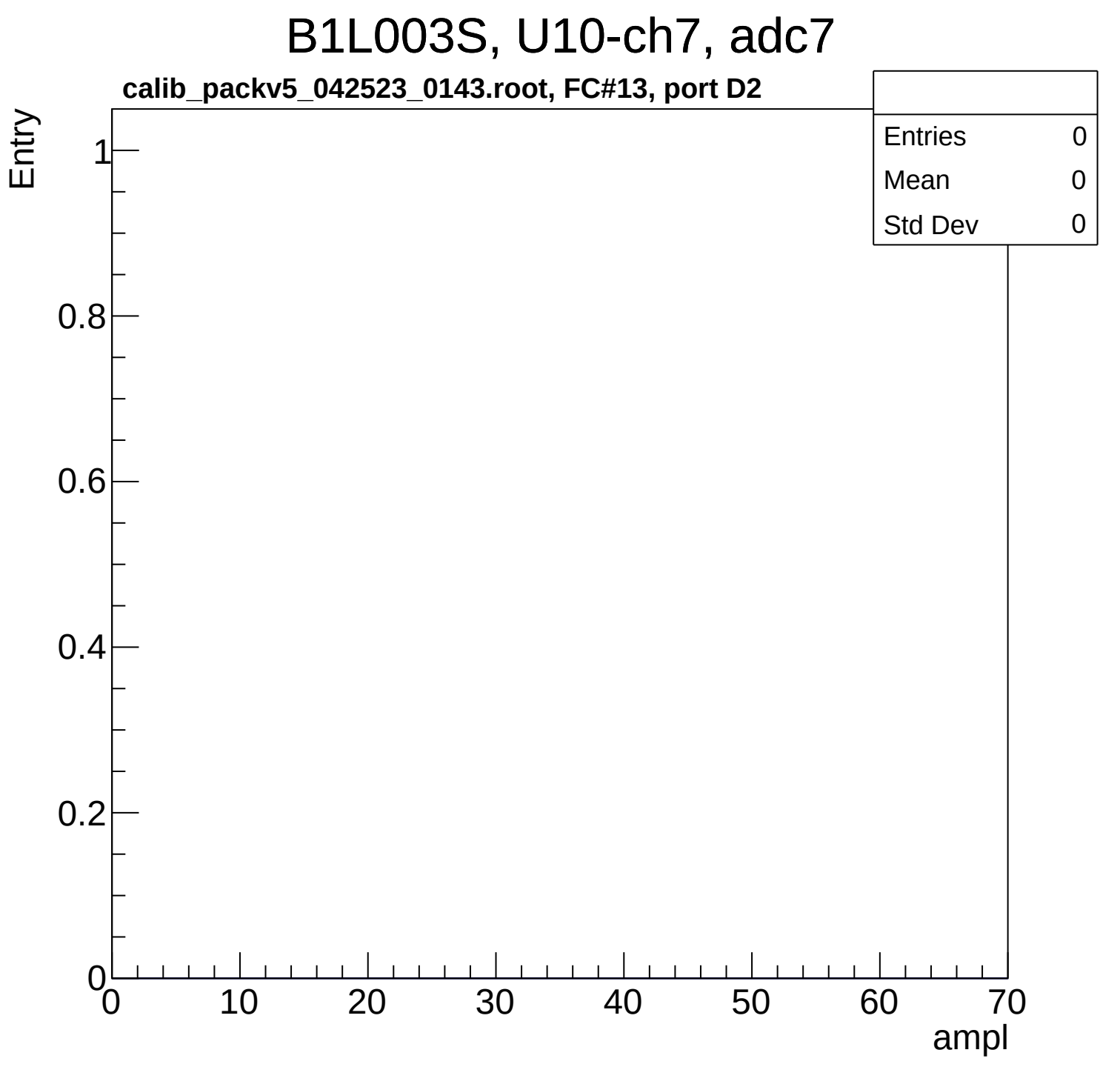
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch7, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.83
Std Dev 2.463
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch7, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 51.5
Std Dev 23.05
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch7, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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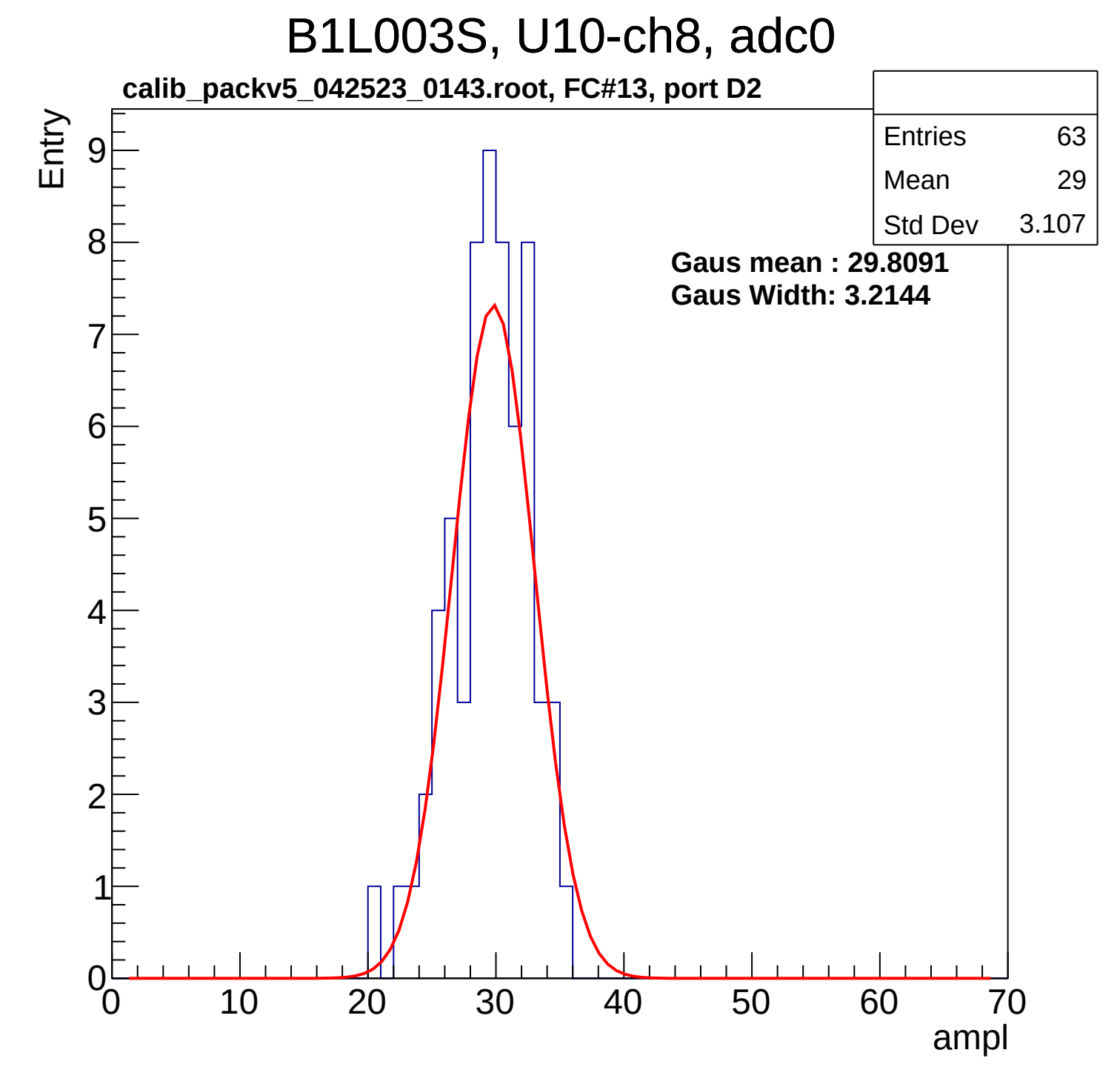
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch8, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29
Std Dev 3.107
Gaus mean : 29.8091
Gaus Width: 3.2144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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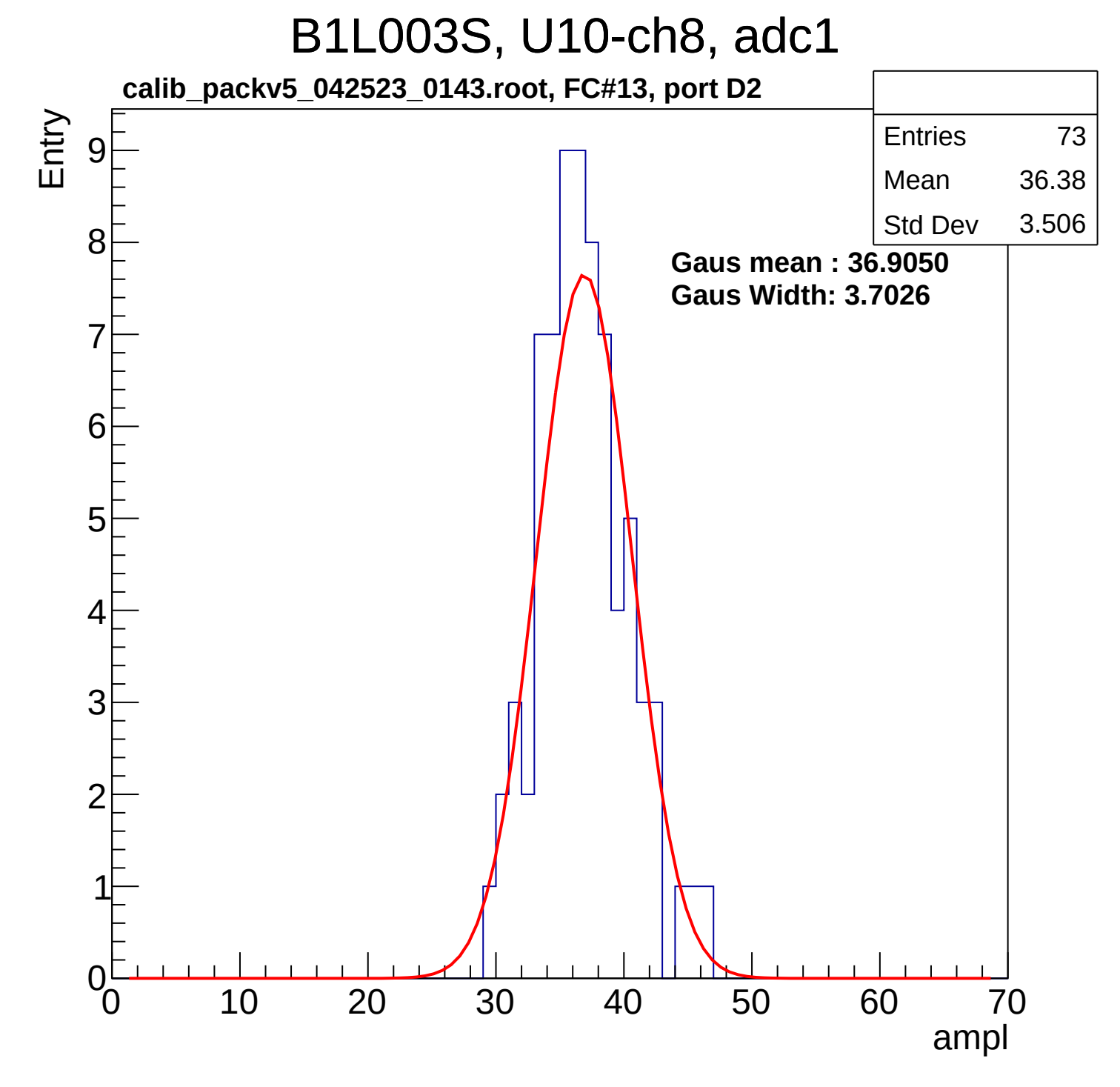
B1L003S, U10-ch8, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.38
Std Dev 3.506
Gaus mean : 36.9050
Gaus Width: 3.7026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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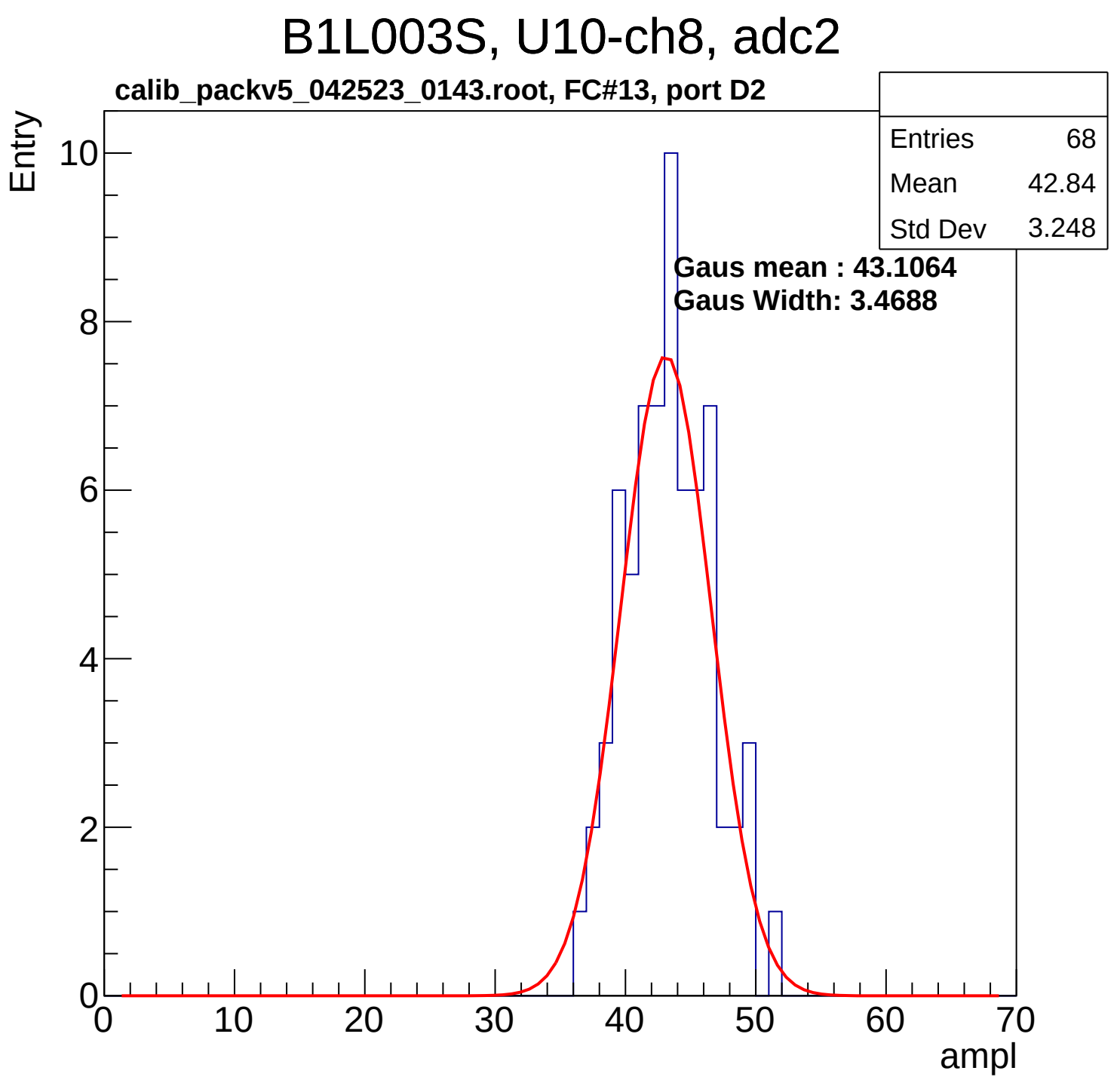
B1L003S, U10-ch8, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.84
Std Dev 3.248
Gaus mean : 43.1064
Gaus Width: 3.4688
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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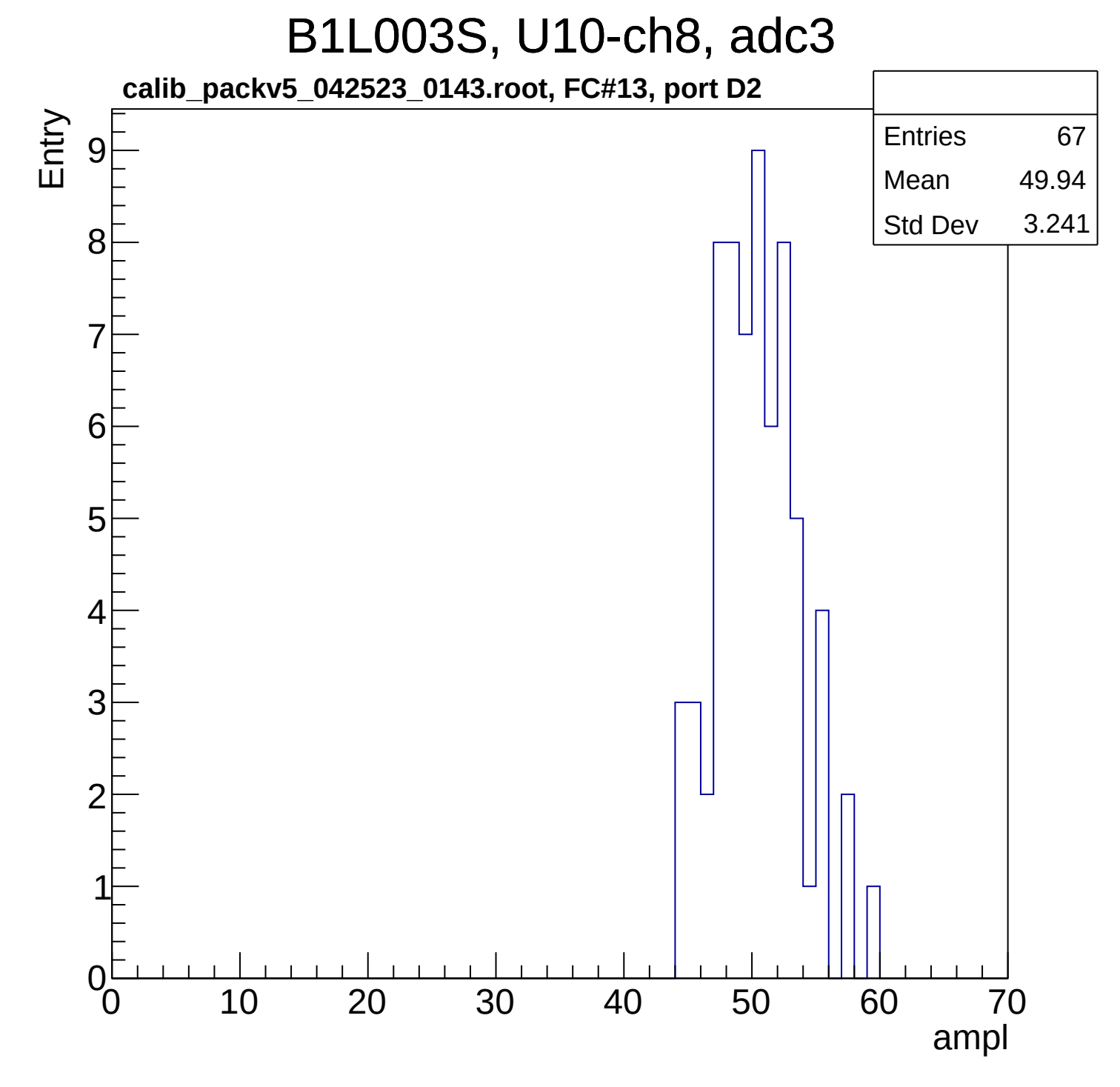
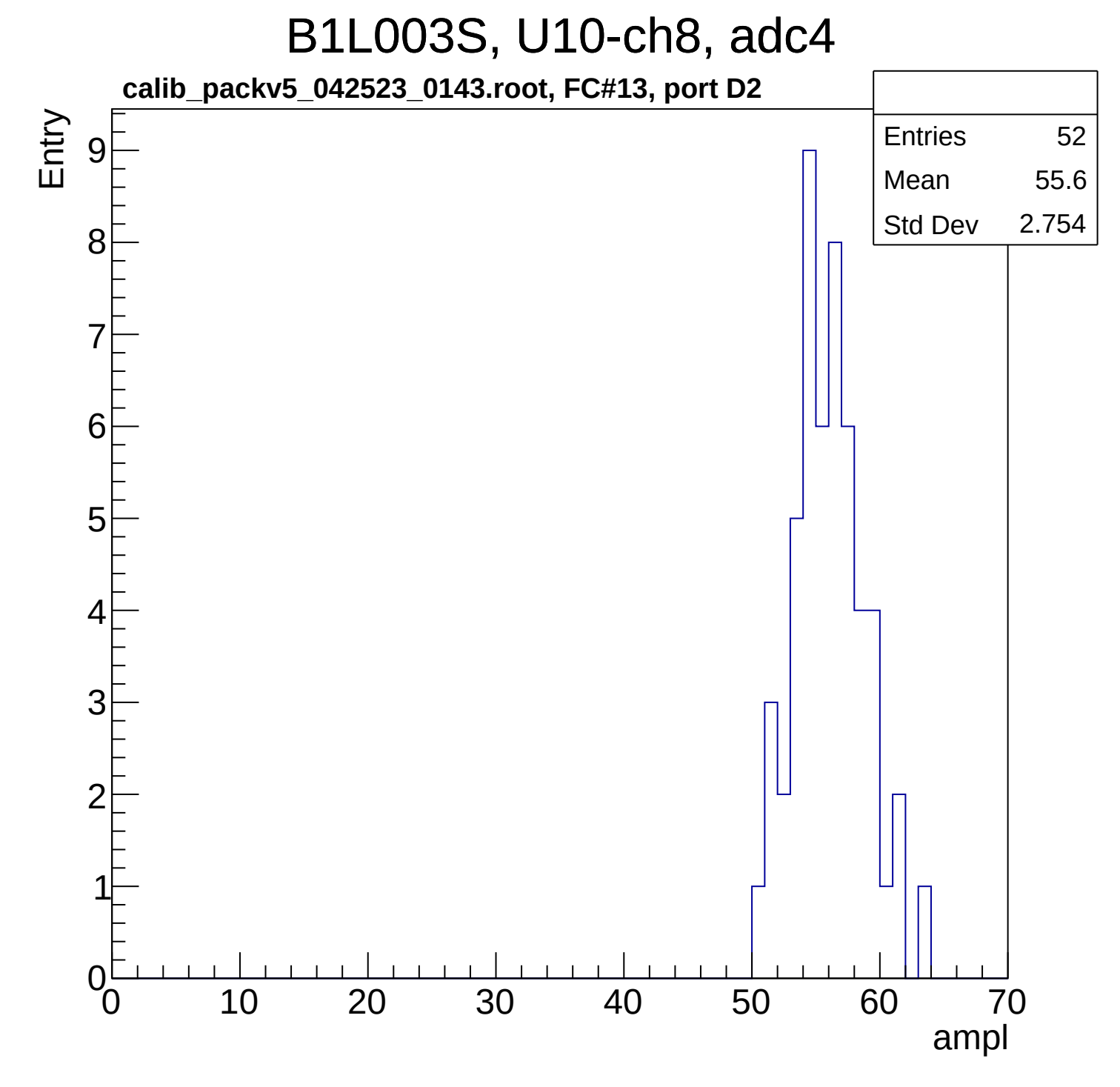

B1L003S, U10-ch8, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.94
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch8, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.6
Std Dev 2.754
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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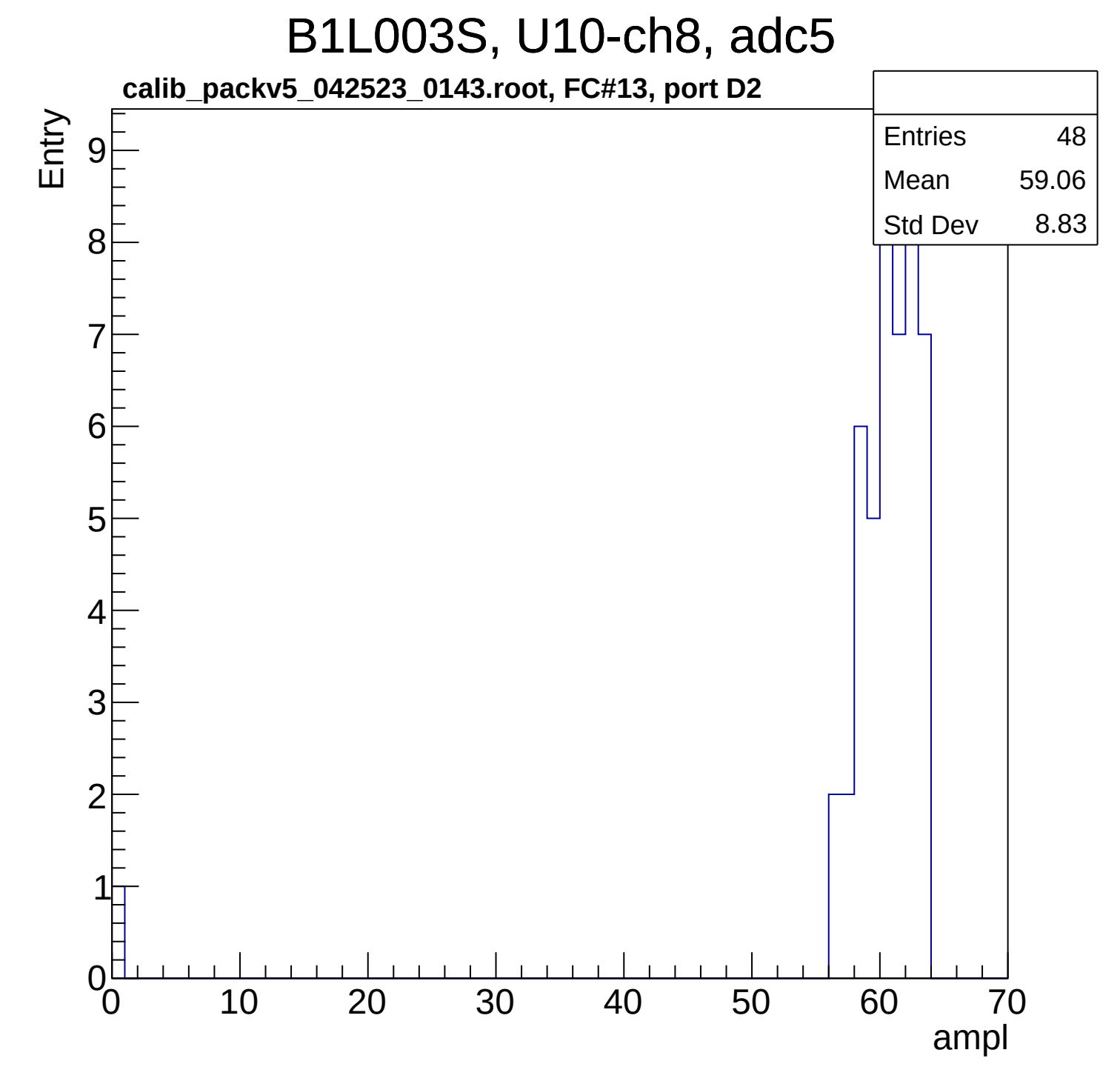
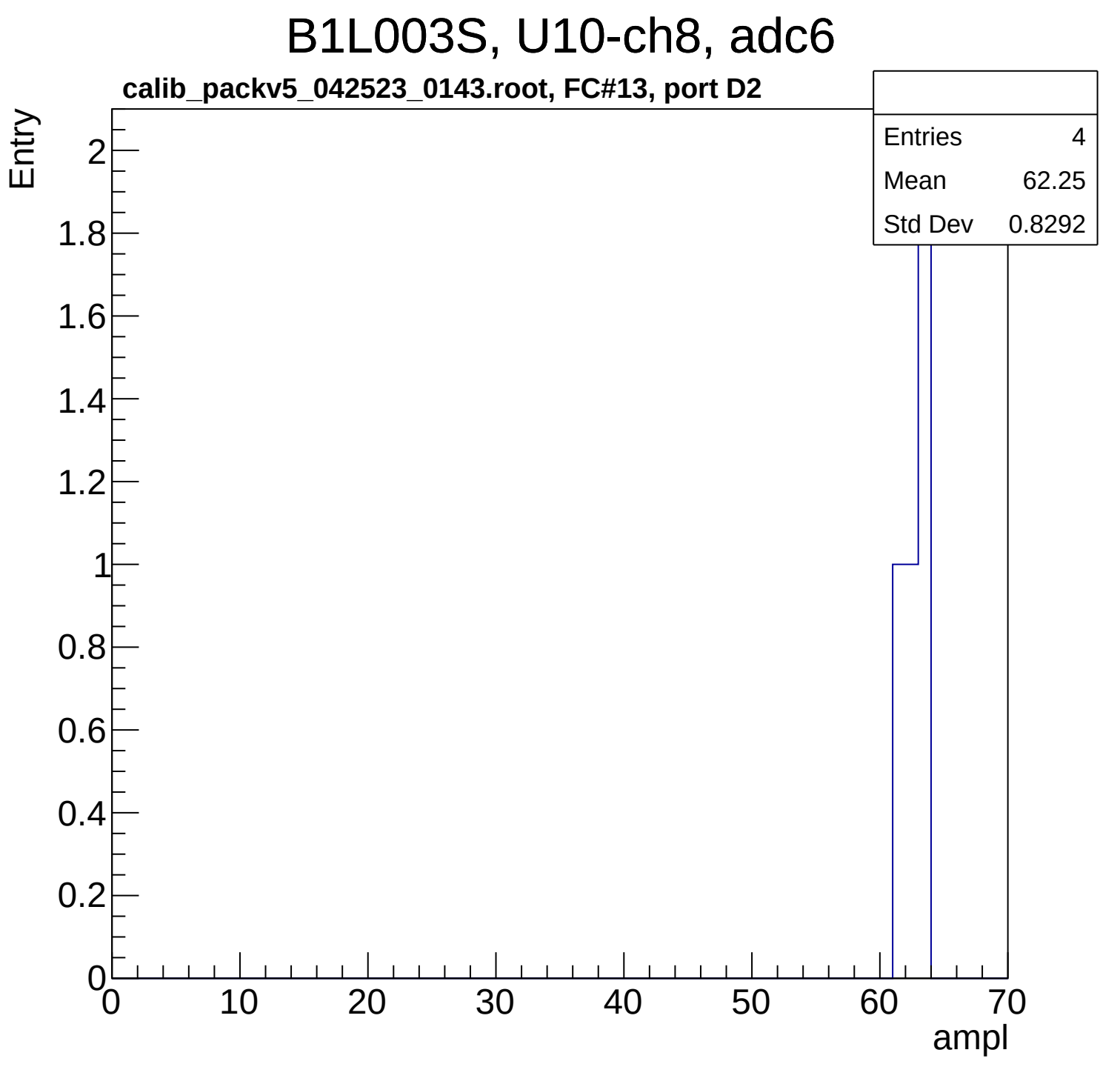
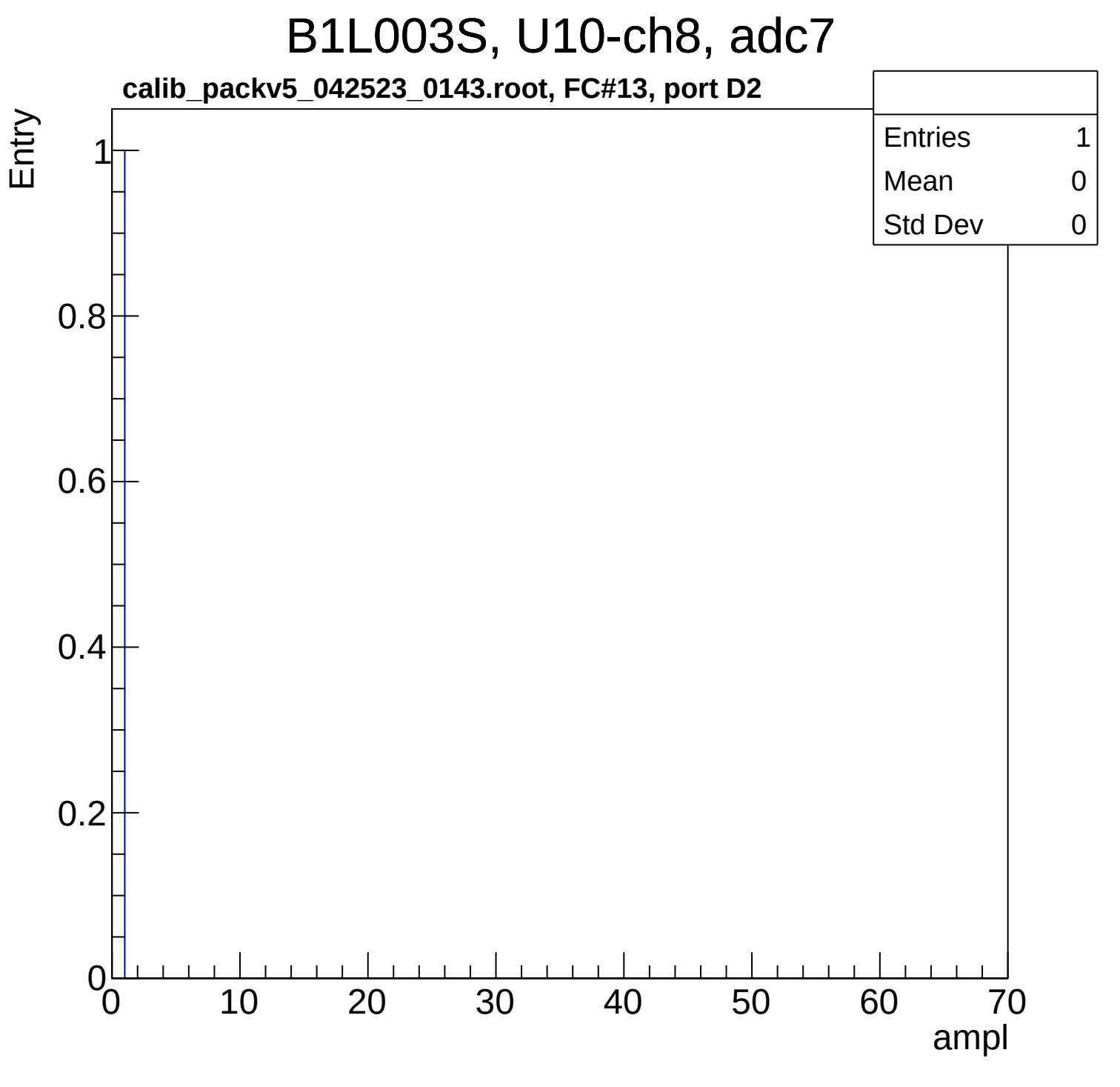
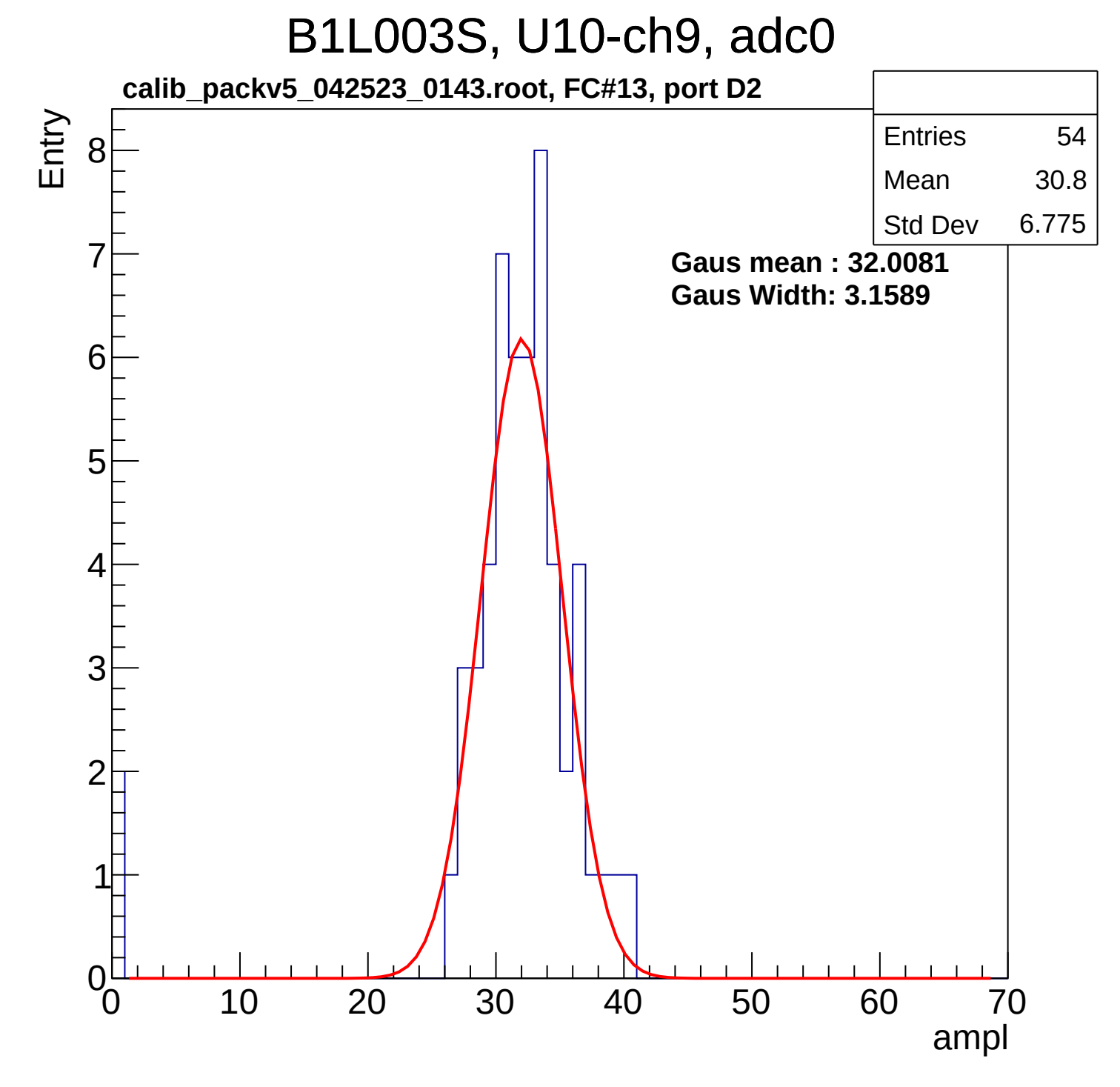

B1L003S, U10-ch8, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.06
Std Dev 8.83
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch8, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch8, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch9, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 30.8
Std Dev 6.775
Gaus mean : 32.0081
Gaus Width: 3.1589
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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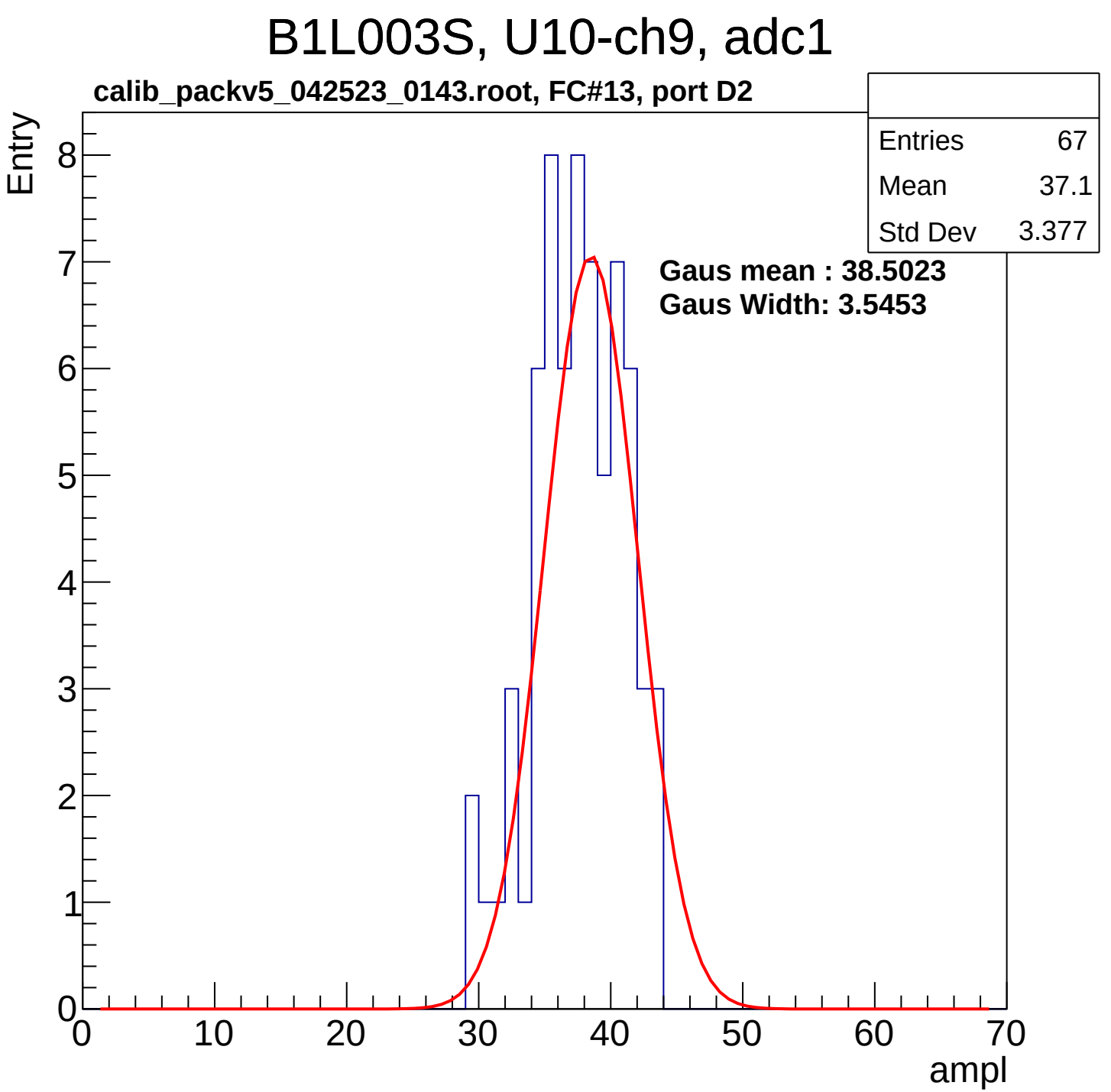
B1L003S, U10-ch9, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.1
Std Dev 3.377
Gaus mean : 38.5023
Gaus Width: 3.5453
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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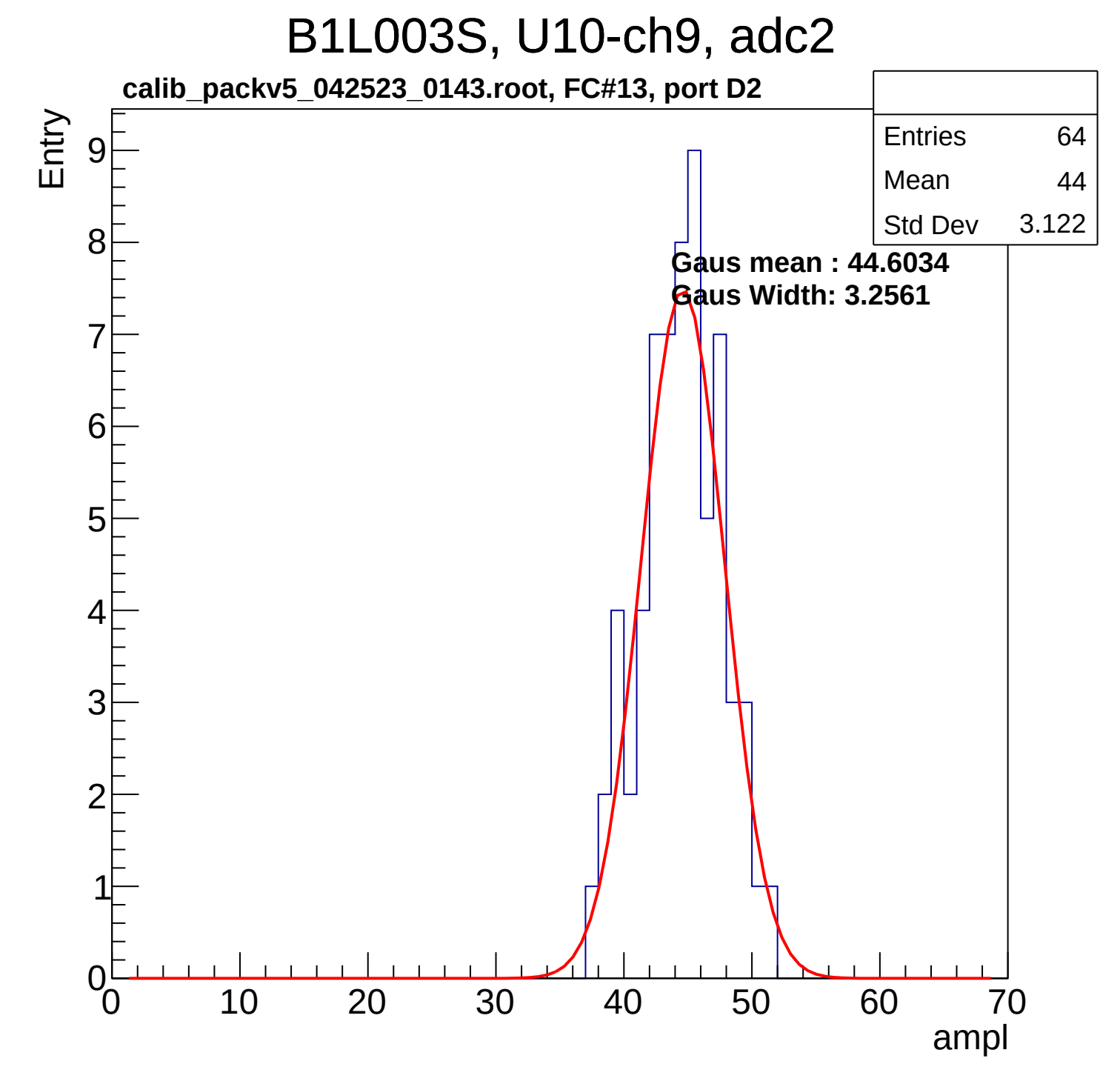
B1L003S, U10-ch9, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 44
Std Dev 3.122
Gaus mean : 44.6034
Gaus Width: 3.2561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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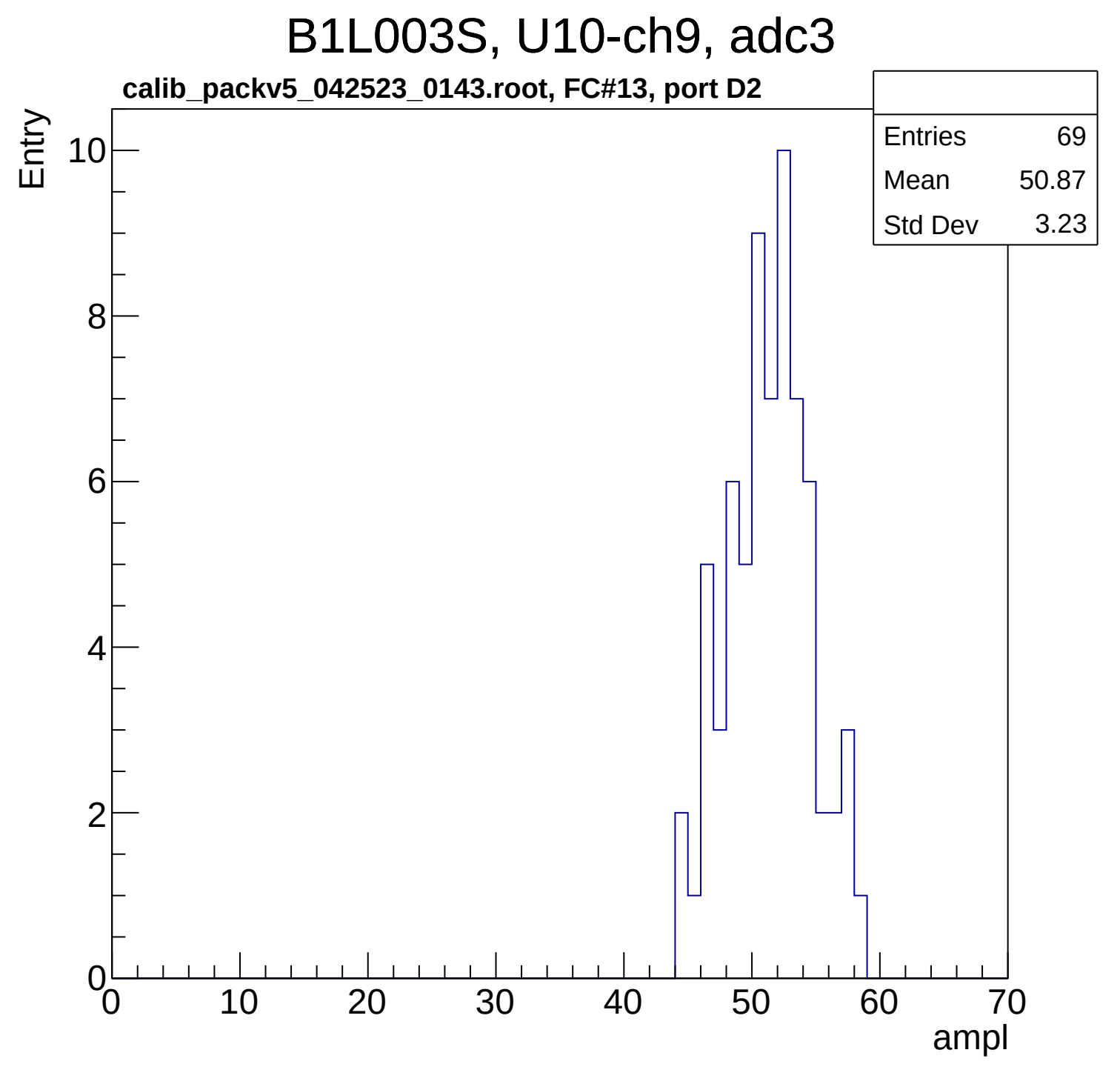
B1L003S, U10-ch9, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.87
Std Dev 3.23
Entry
ampl
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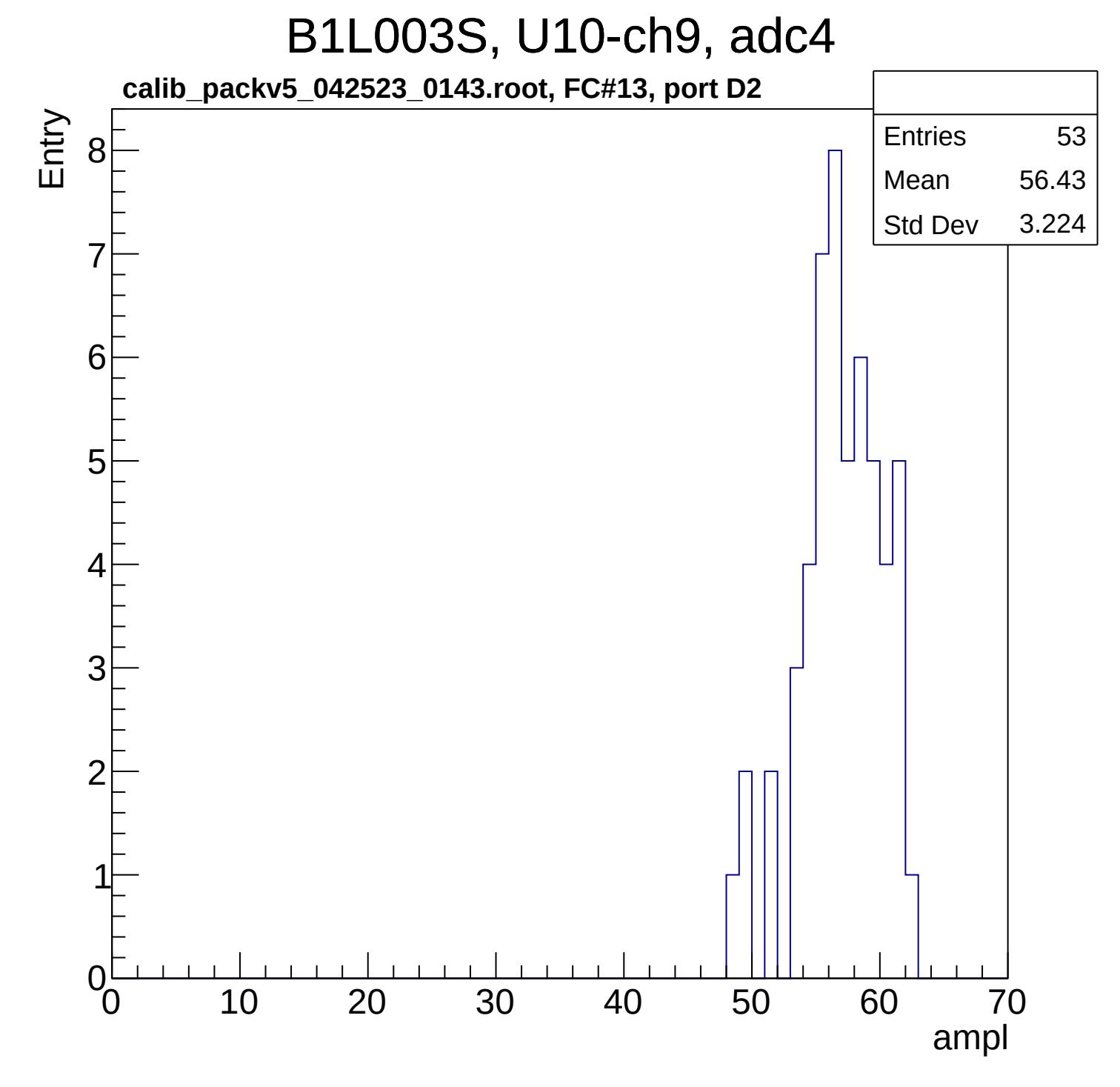
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch9, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 56.43
Std Dev 3.224
Entry
ampl
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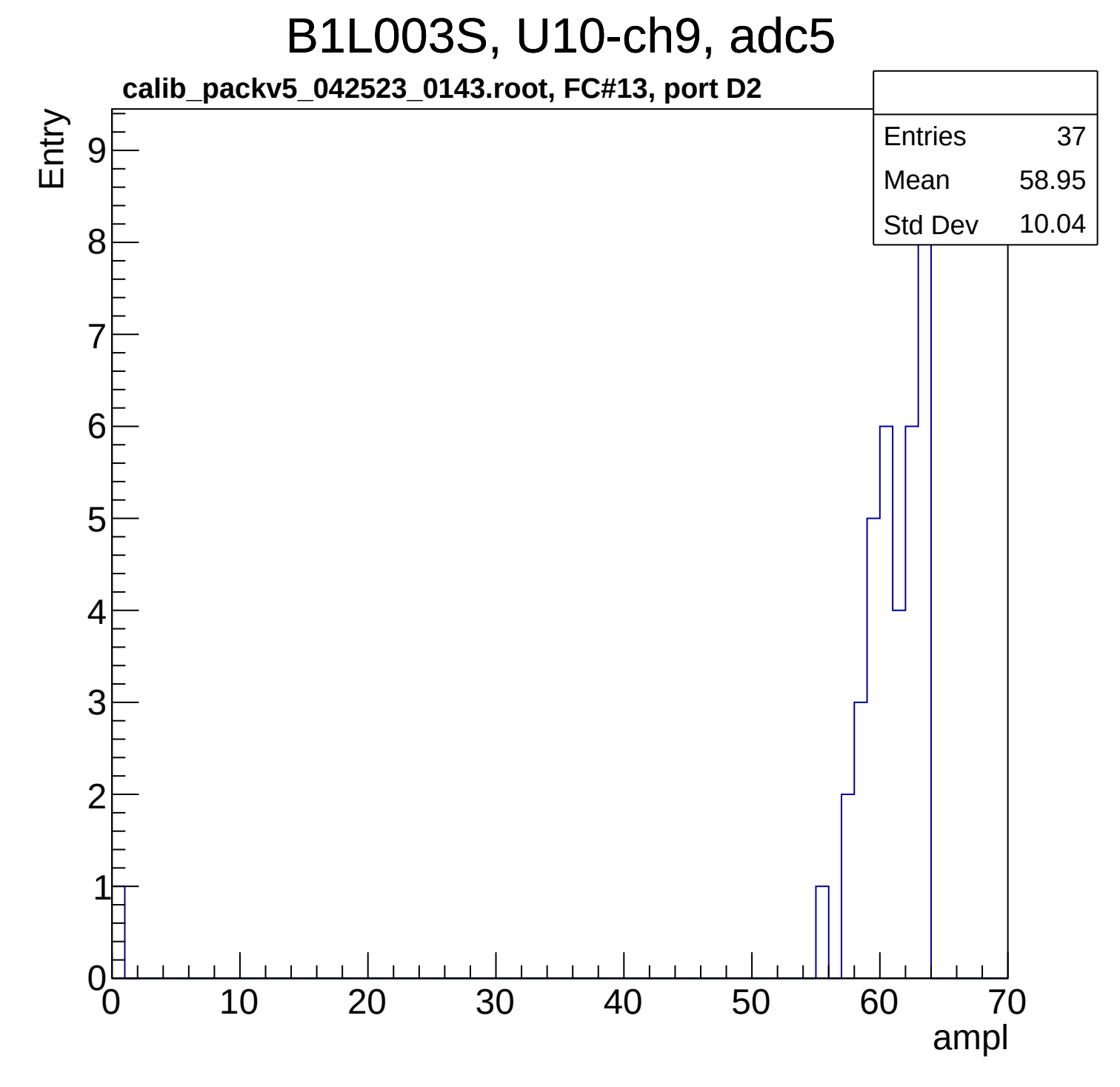
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch9, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 58.95
Std Dev 10.04
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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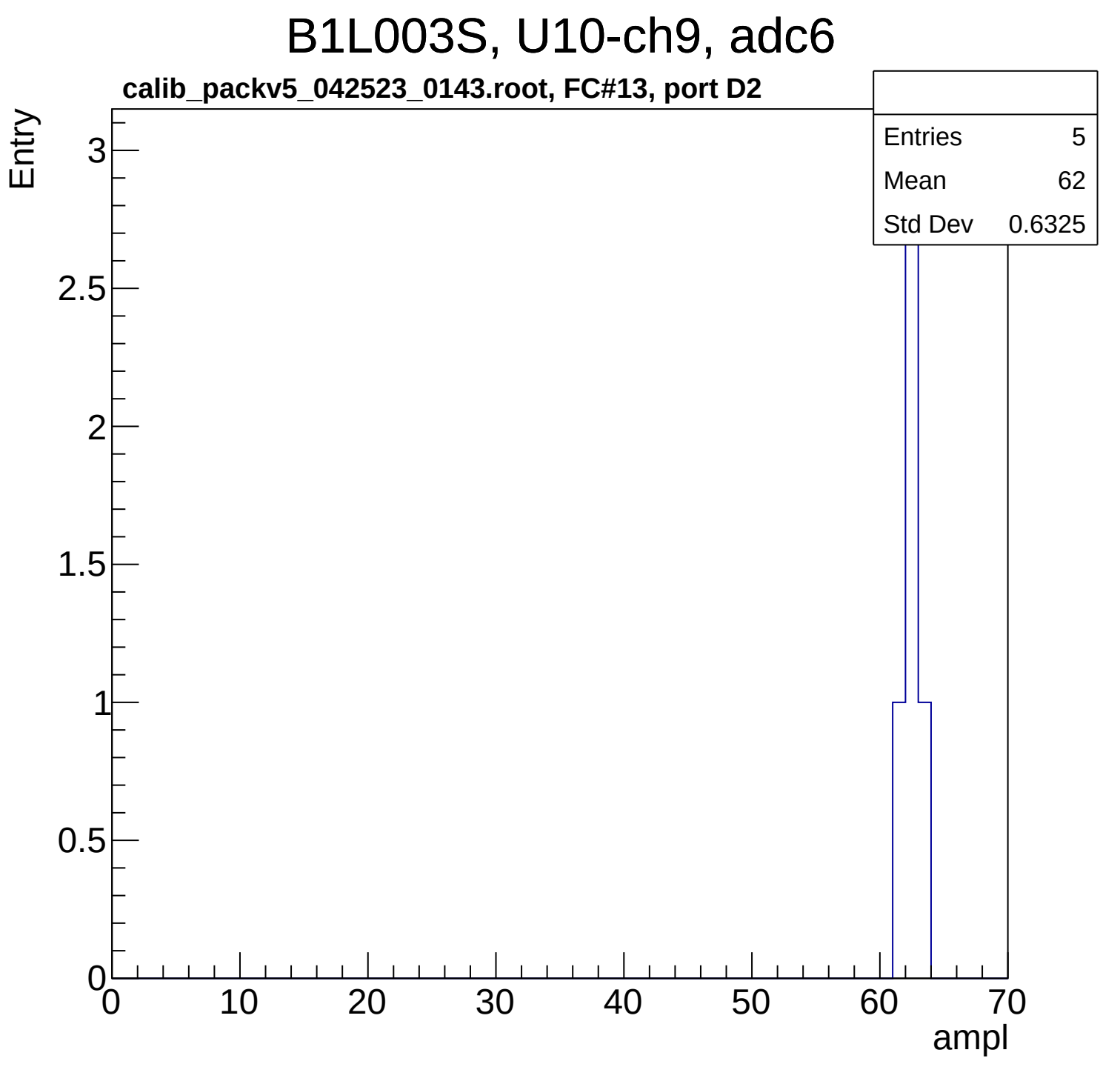
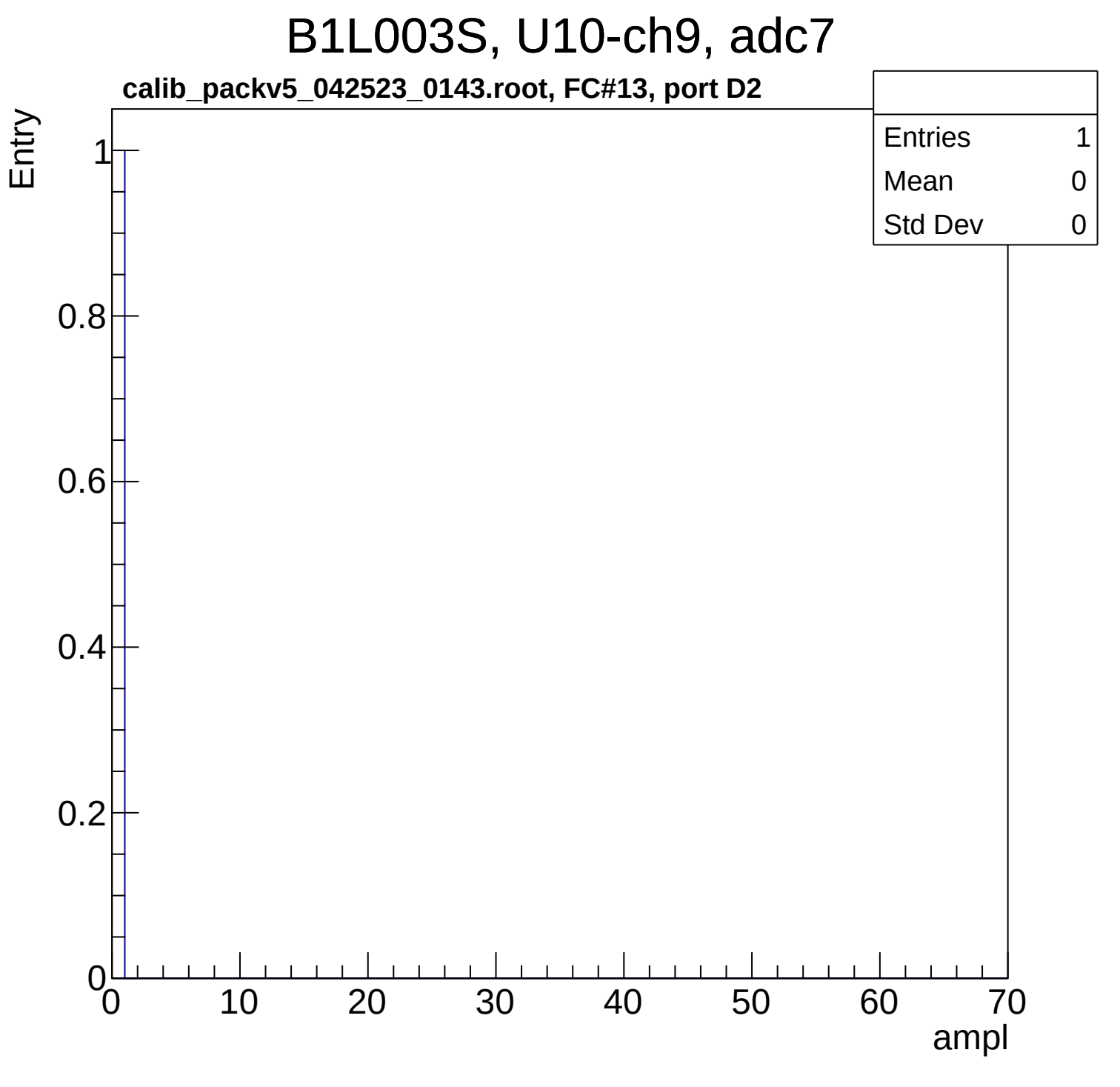

B1L003S, U10-ch9, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 0.6325
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch9, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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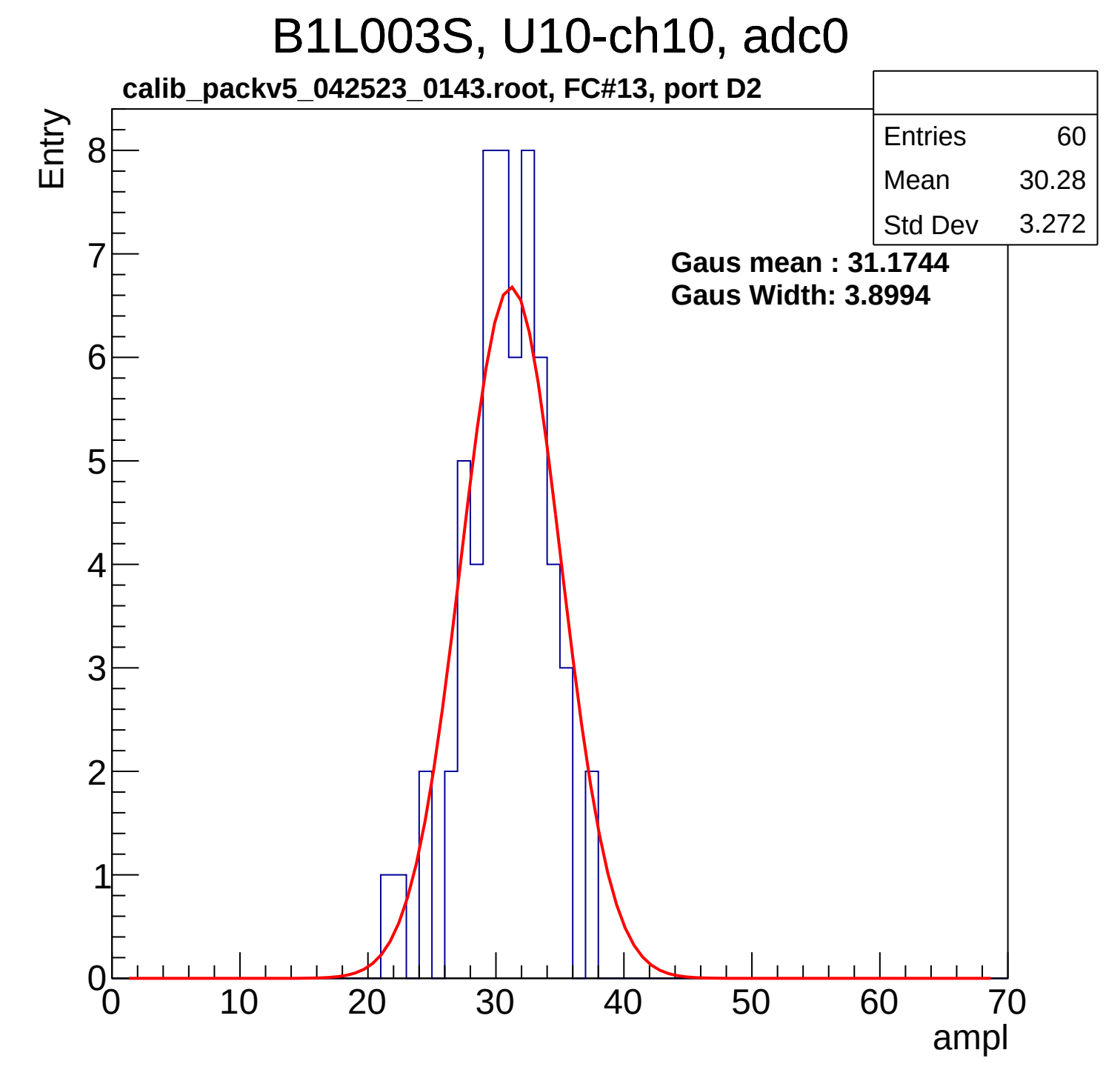
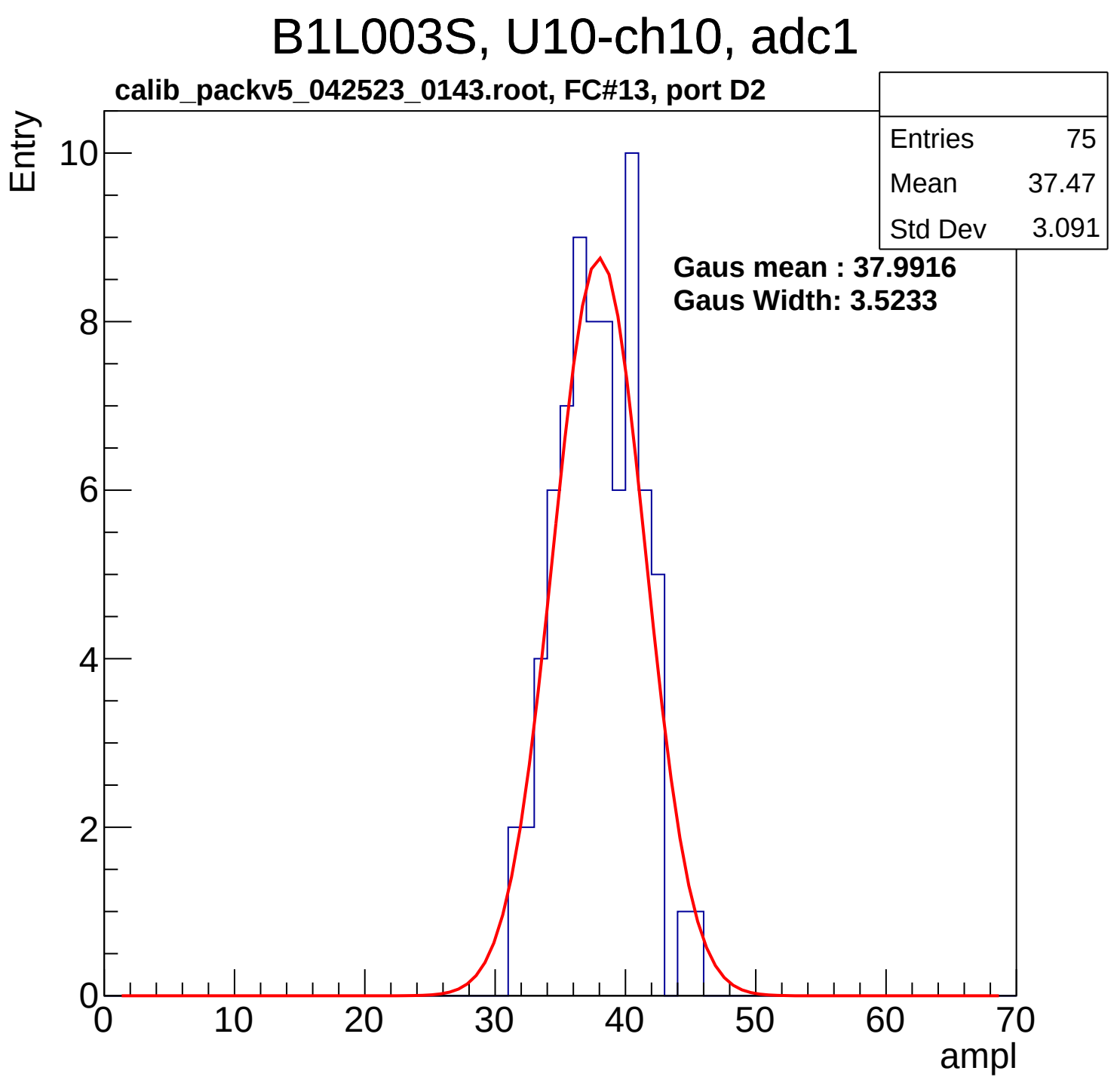
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch10, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 30.28
Std Dev 3.272
Gaus mean : 31.1744
Gaus Width: 3.8994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch10, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 37.47
Std Dev 3.091
Gaus mean : 37.9916
Gaus Width: 3.5233
Entry
ampl
0
2
4
6
8
10
0
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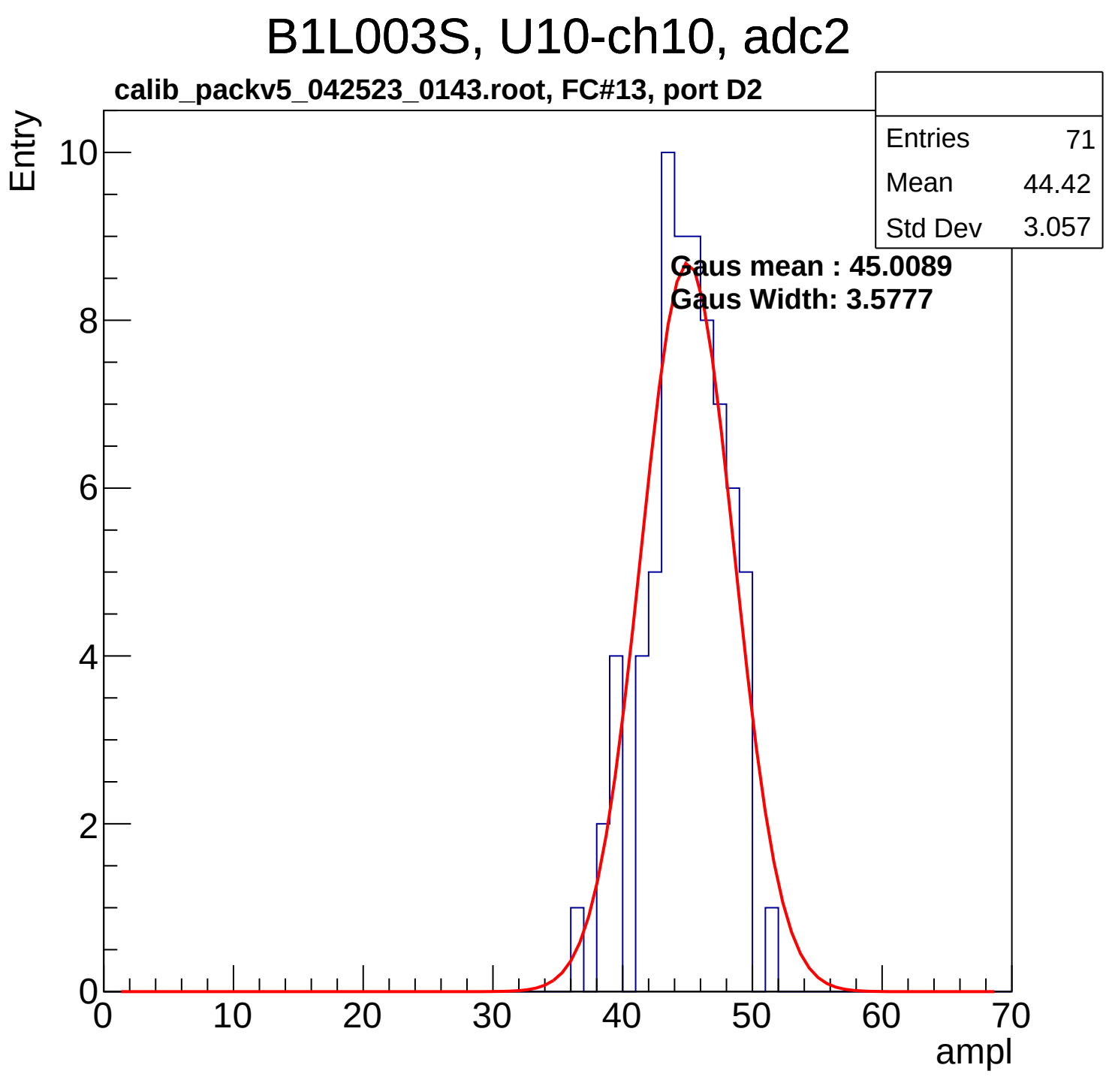
20
30
40
50
60
70


B1L003S, U10-ch10, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 44.42
Std Dev 3.057
Gaus mean : 45.0089
Gaus Width: 3.5777
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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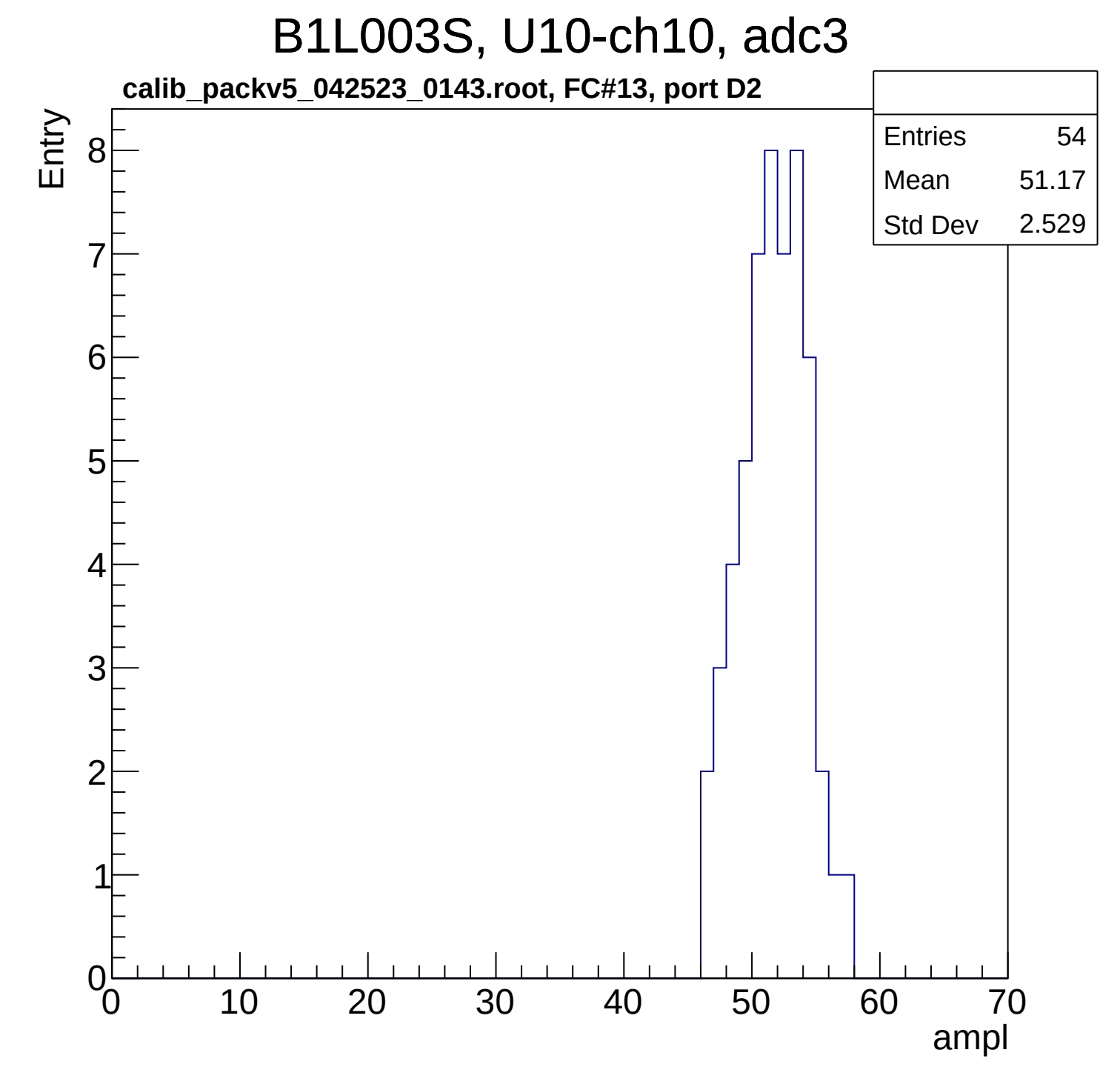
B1L003S, U10-ch10, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 51.17
Std Dev 2.529
Entry
ampl
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2
3
4
5
6
7
8
0
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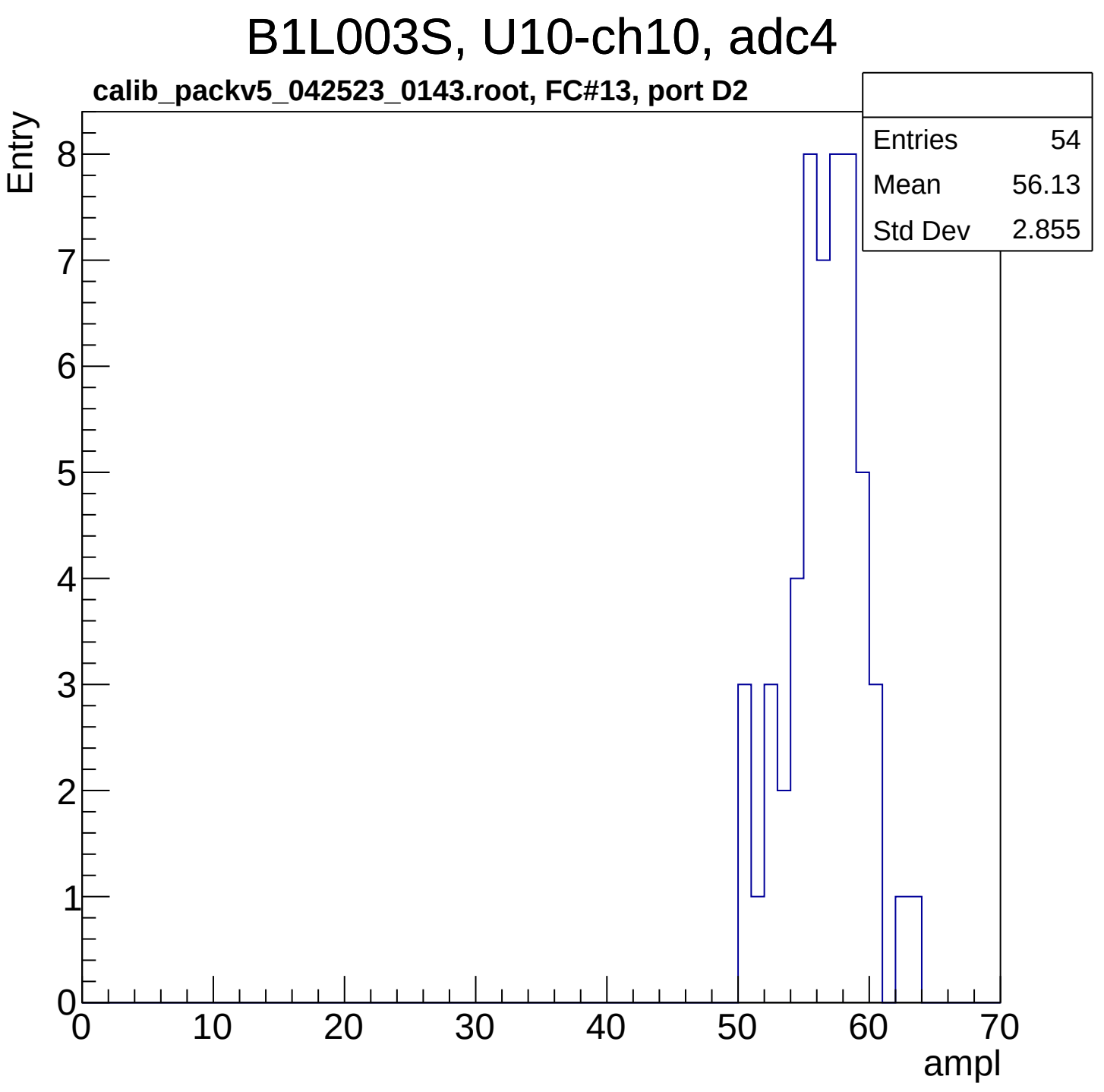
B1L003S, U10-ch10, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 56.13
Std Dev 2.855
Entry
ampl
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2
3
4
5
6
7
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0
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60
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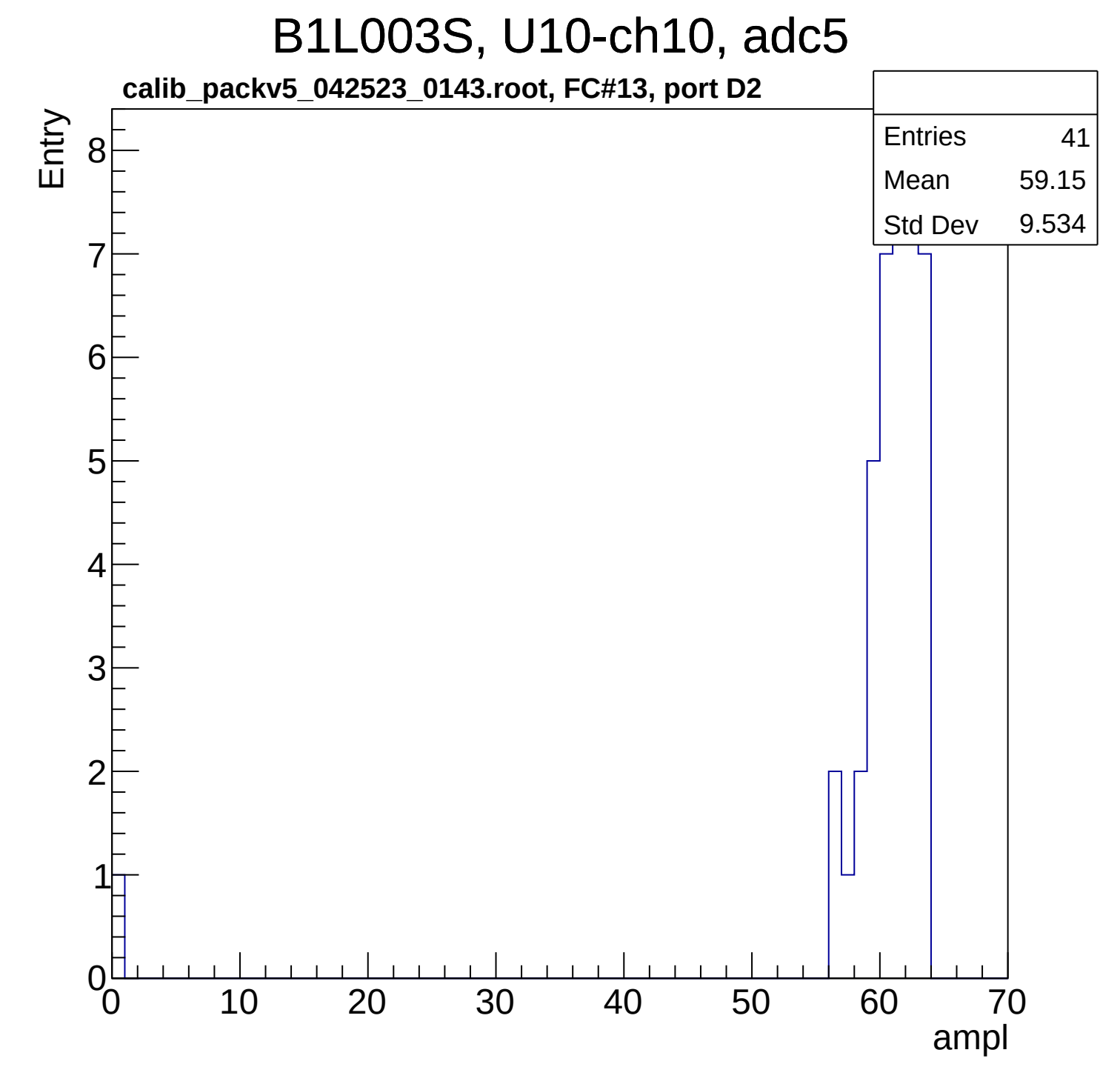
B1L003S, U10-ch10, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59.15
Std Dev 9.534
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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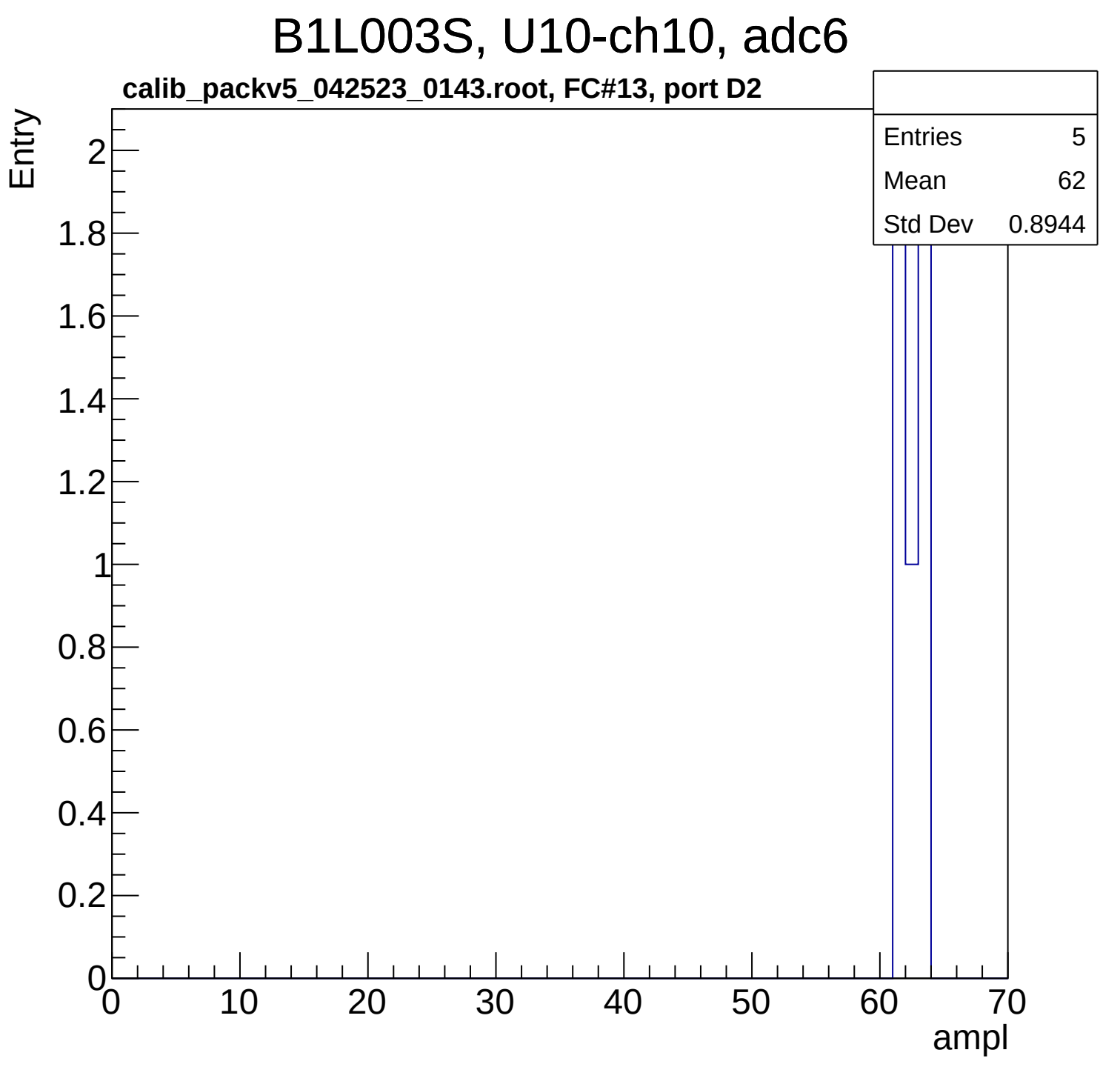
B1L003S, U10-ch10, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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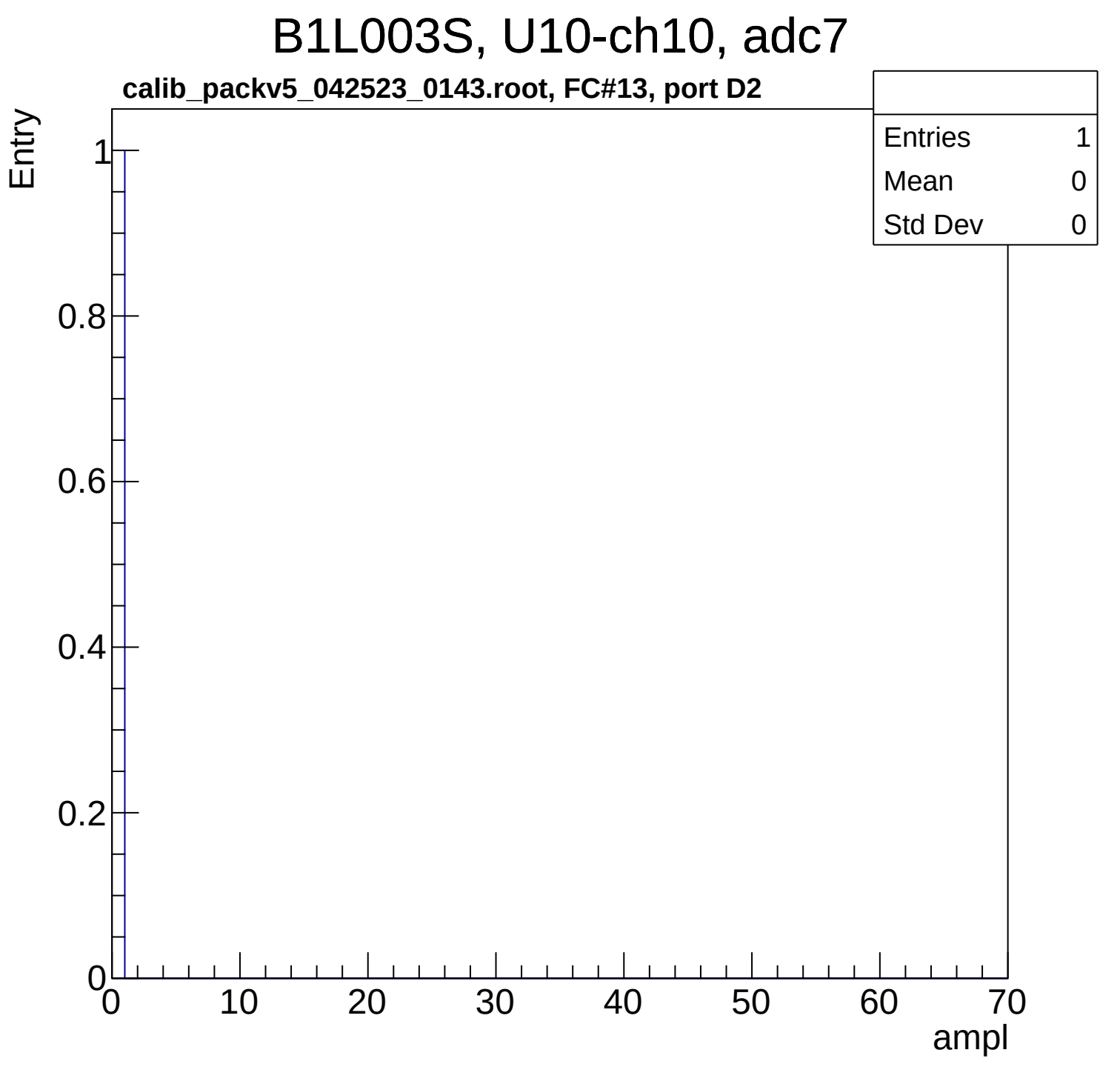
B1L003S, U10-ch10, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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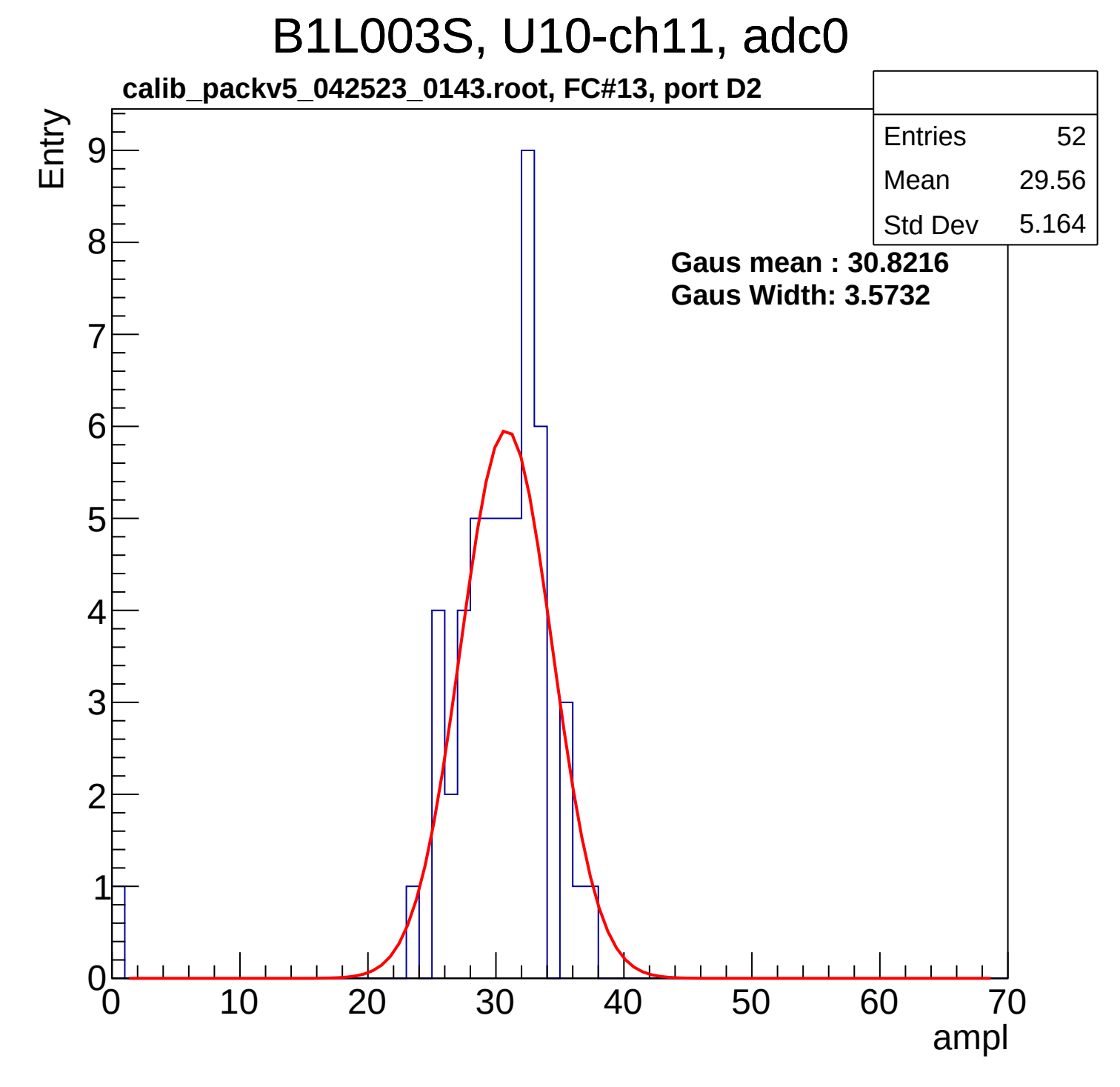
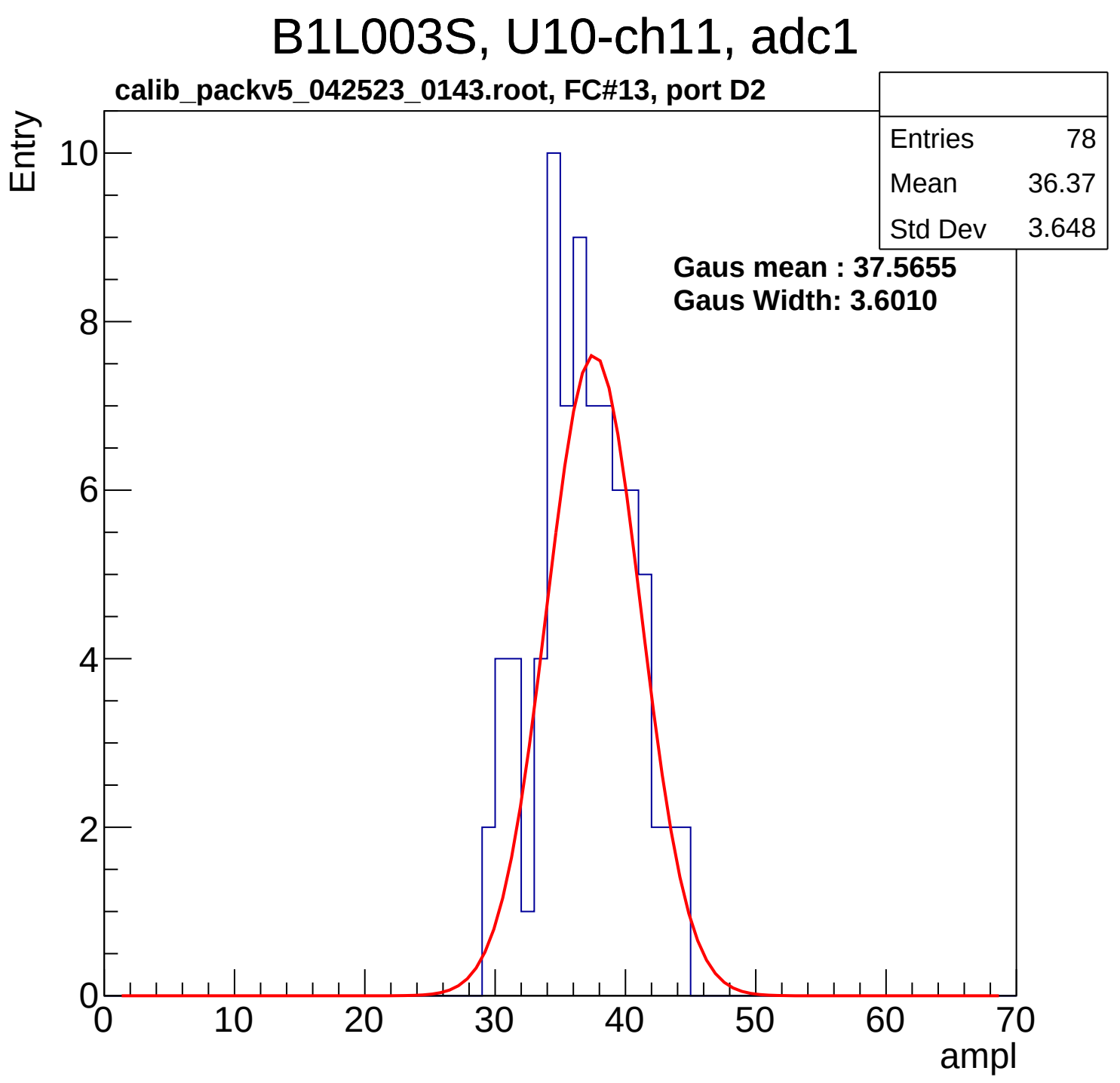
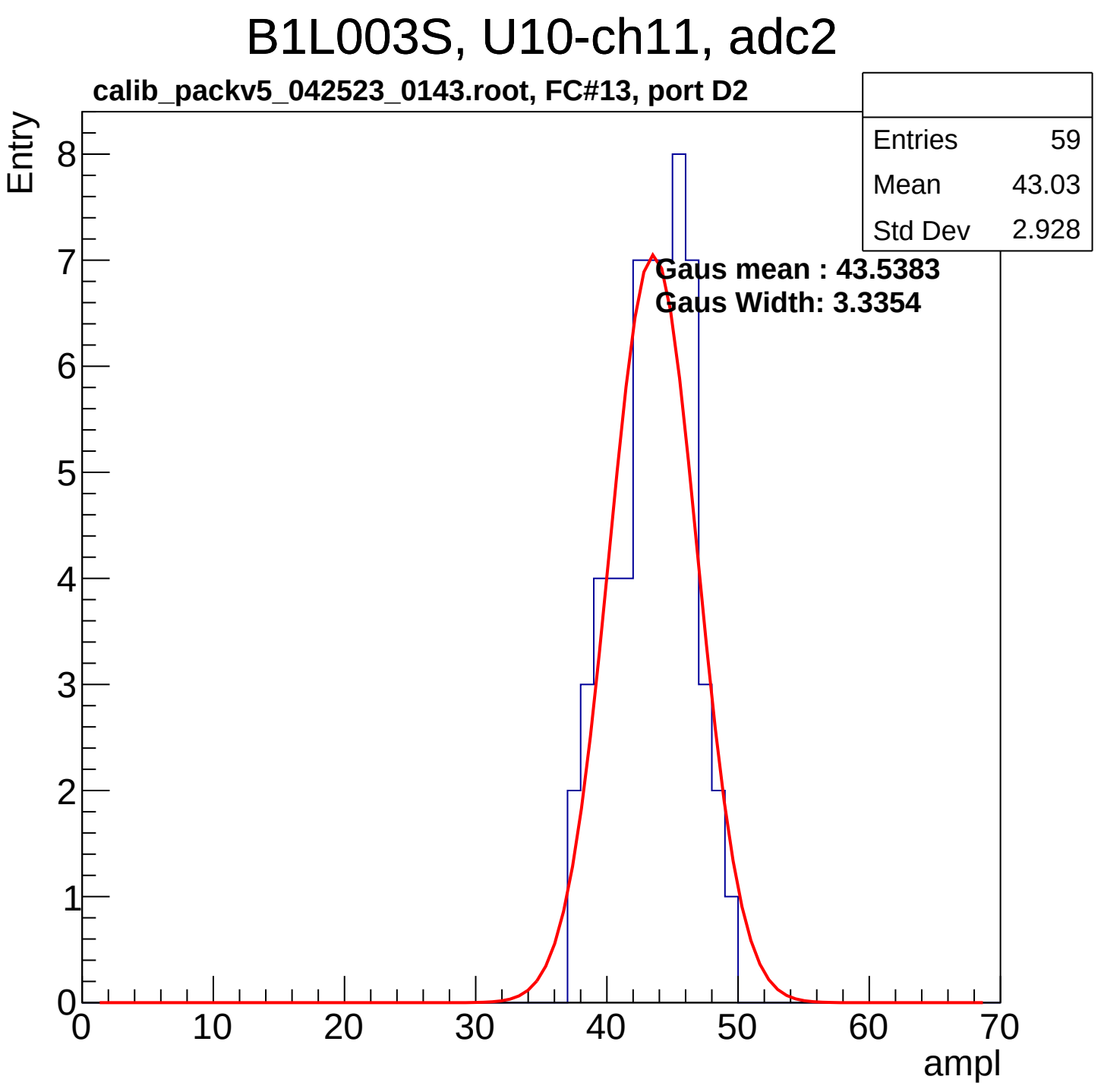
40
50
60
70
ampl


B1L003S, U10-ch11, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 29.56
Std Dev 5.164
Gaus mean : 30.8216
Gaus Width: 3.5732
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch11, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 36.37
Std Dev 3.648
Gaus mean : 37.5655
Gaus Width: 3.6010
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch11, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.03
Std Dev 2.928
Gaus mean : 43.5383
Gaus Width: 3.3354
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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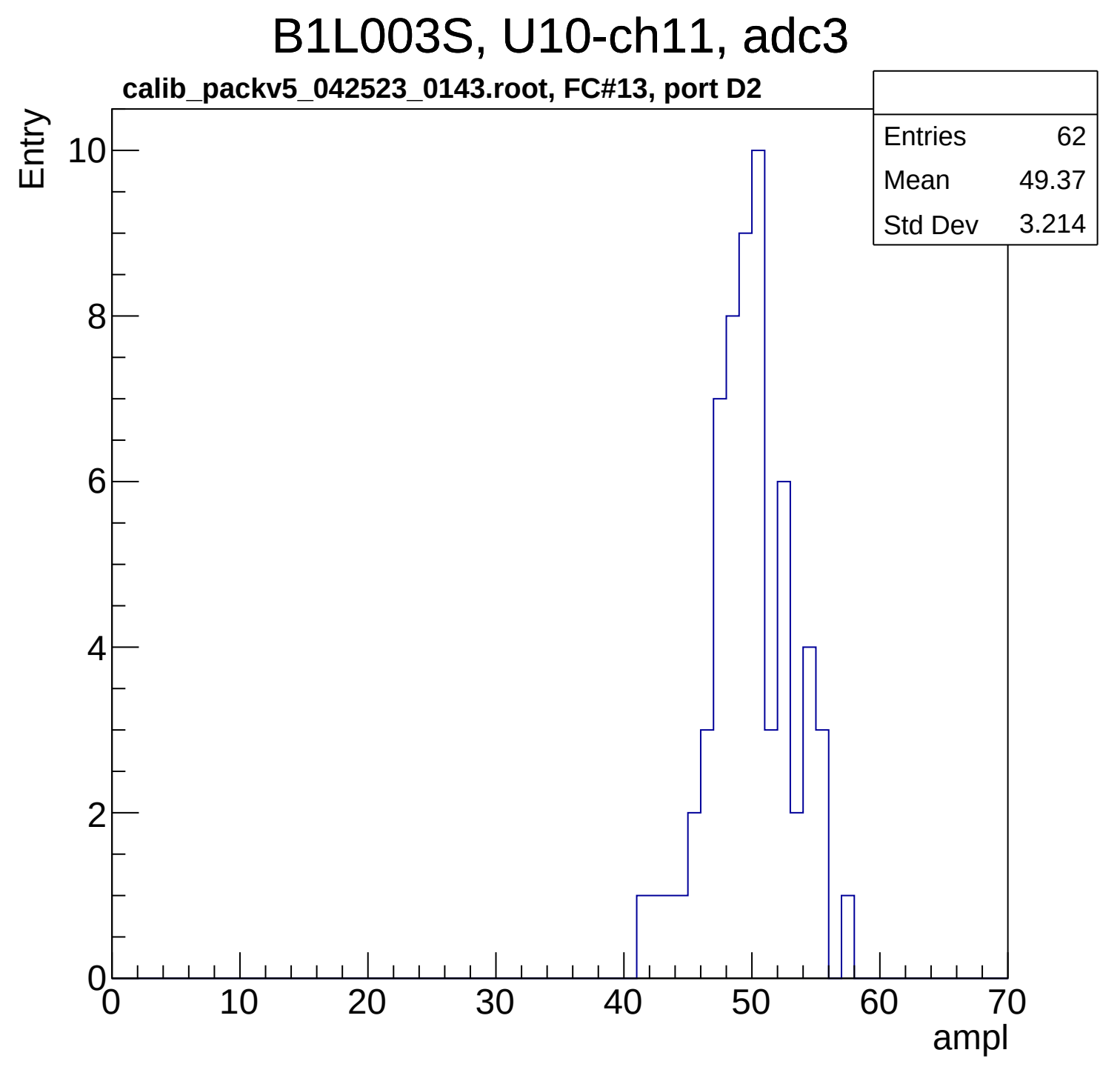
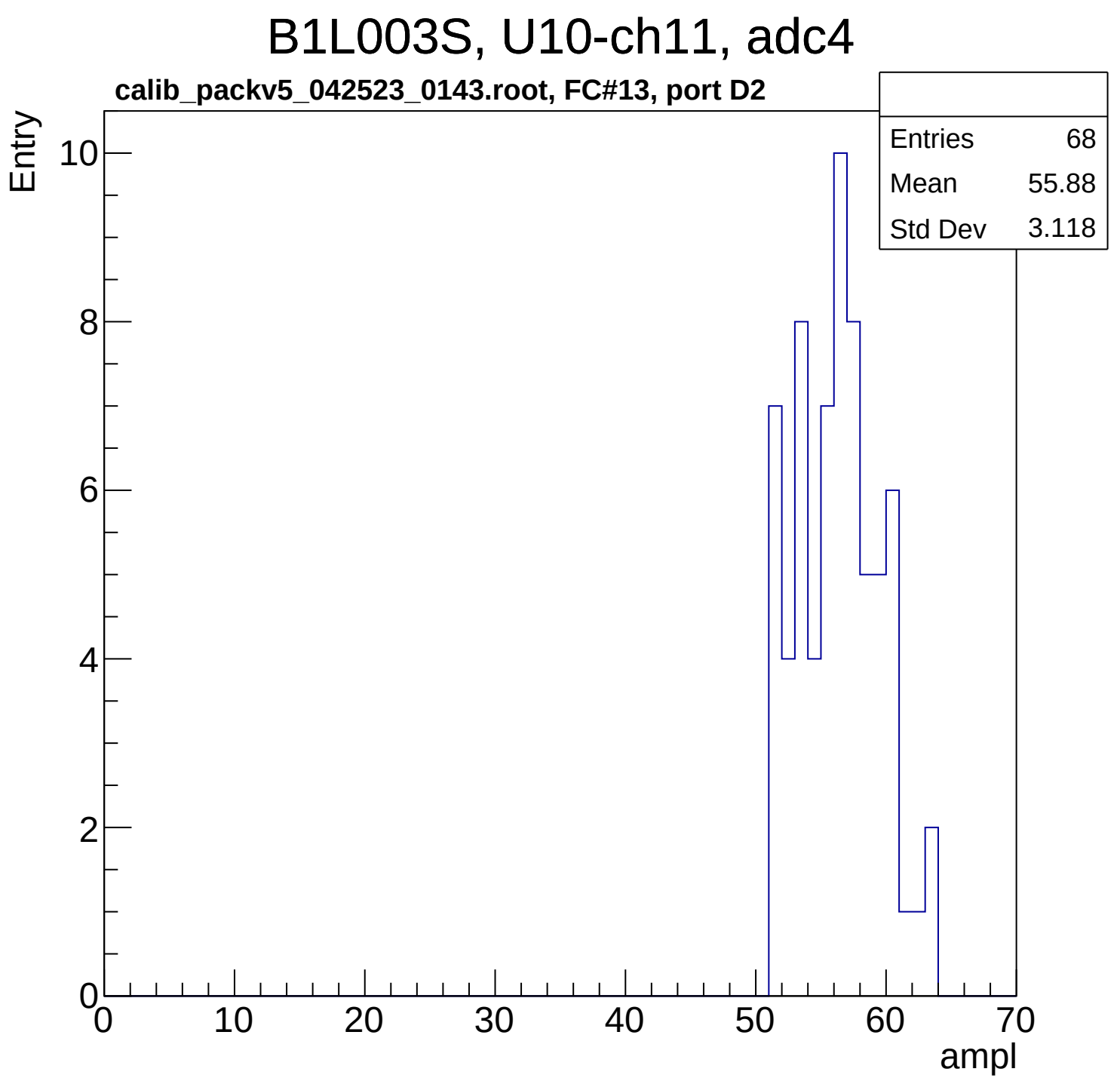

B1L003S, U10-ch11, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 49.37
Std Dev 3.214
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch11, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 55.88
Std Dev 3.118
Entry
0
2
4
6
8
10
0
10
20
30
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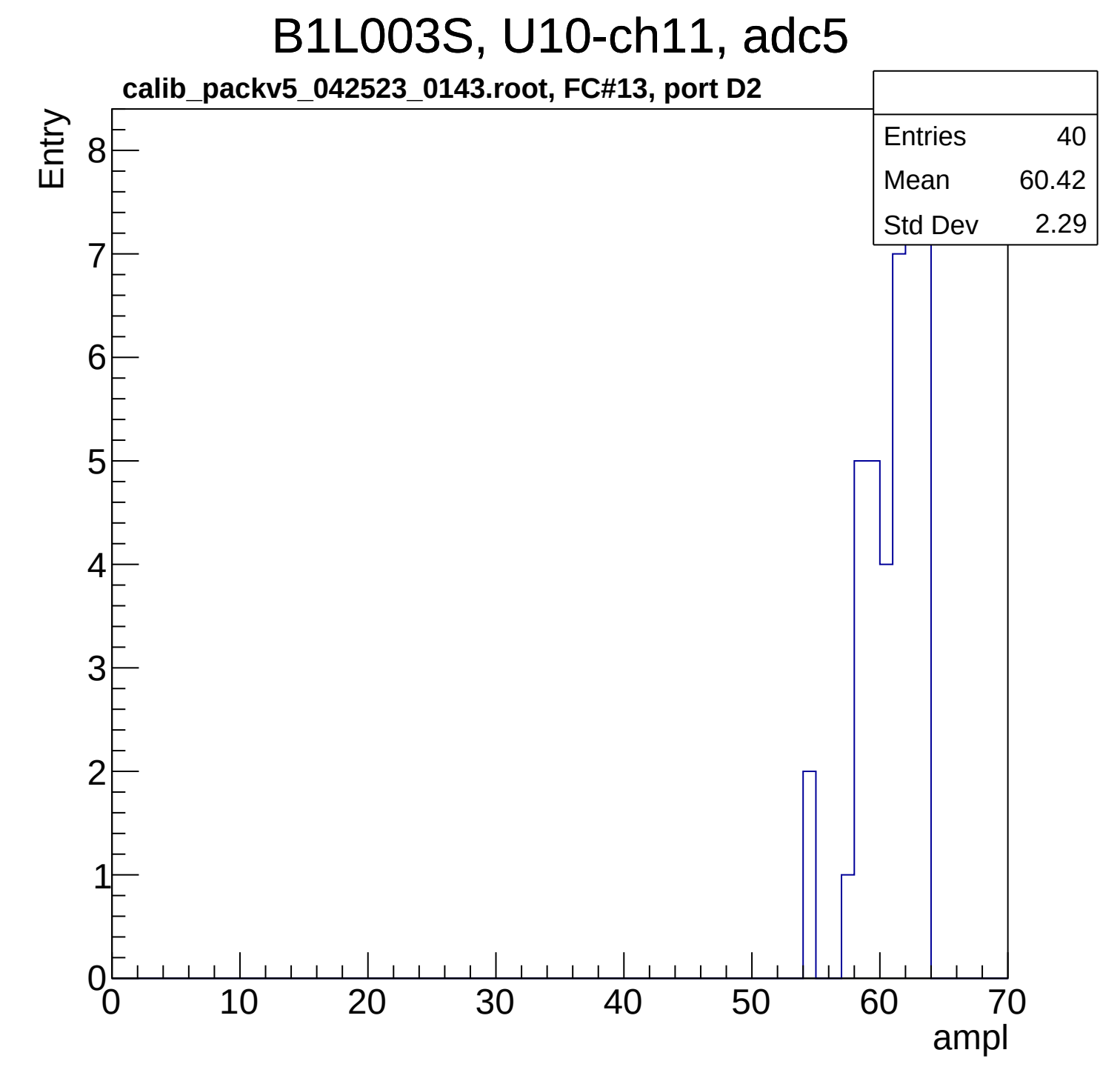
50
60
70
ampl


B1L003S, U10-ch11, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 60.42
Std Dev 2.29
Entry
0
1
2
3
4
5
6
7
8
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10
20
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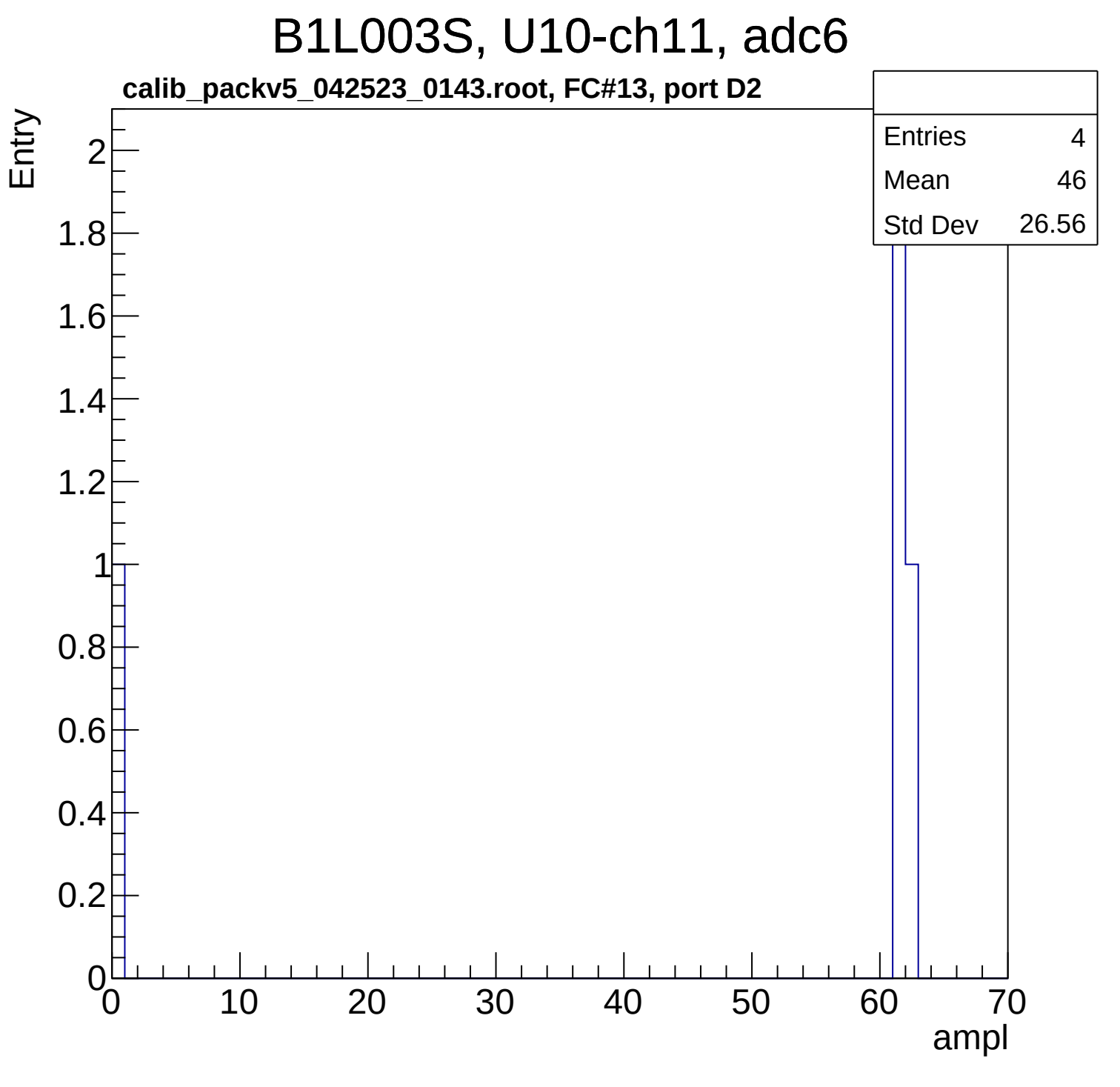
40
50
60
70
ampl


B1L003S, U10-ch11, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 46
Std Dev 26.56
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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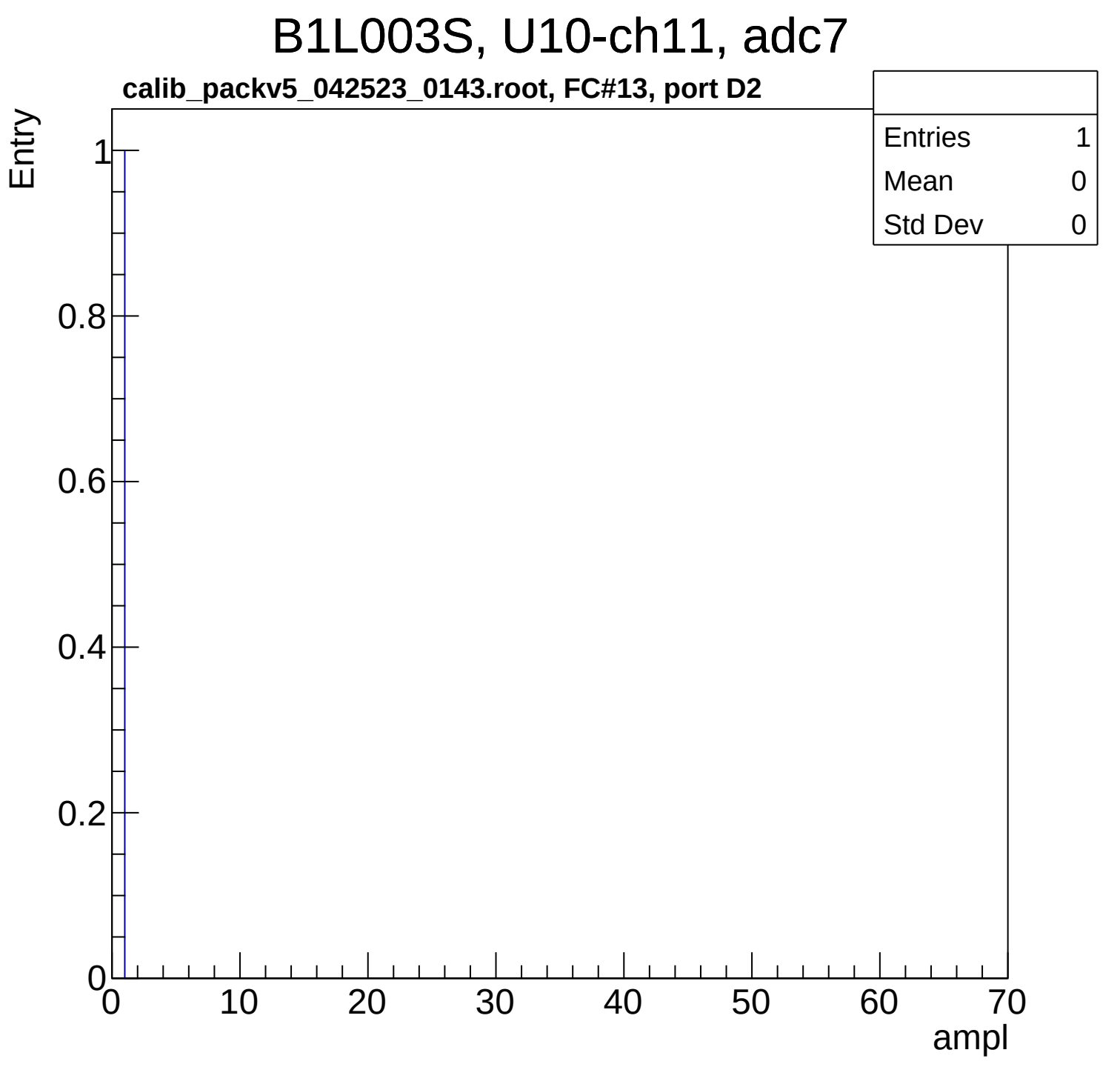
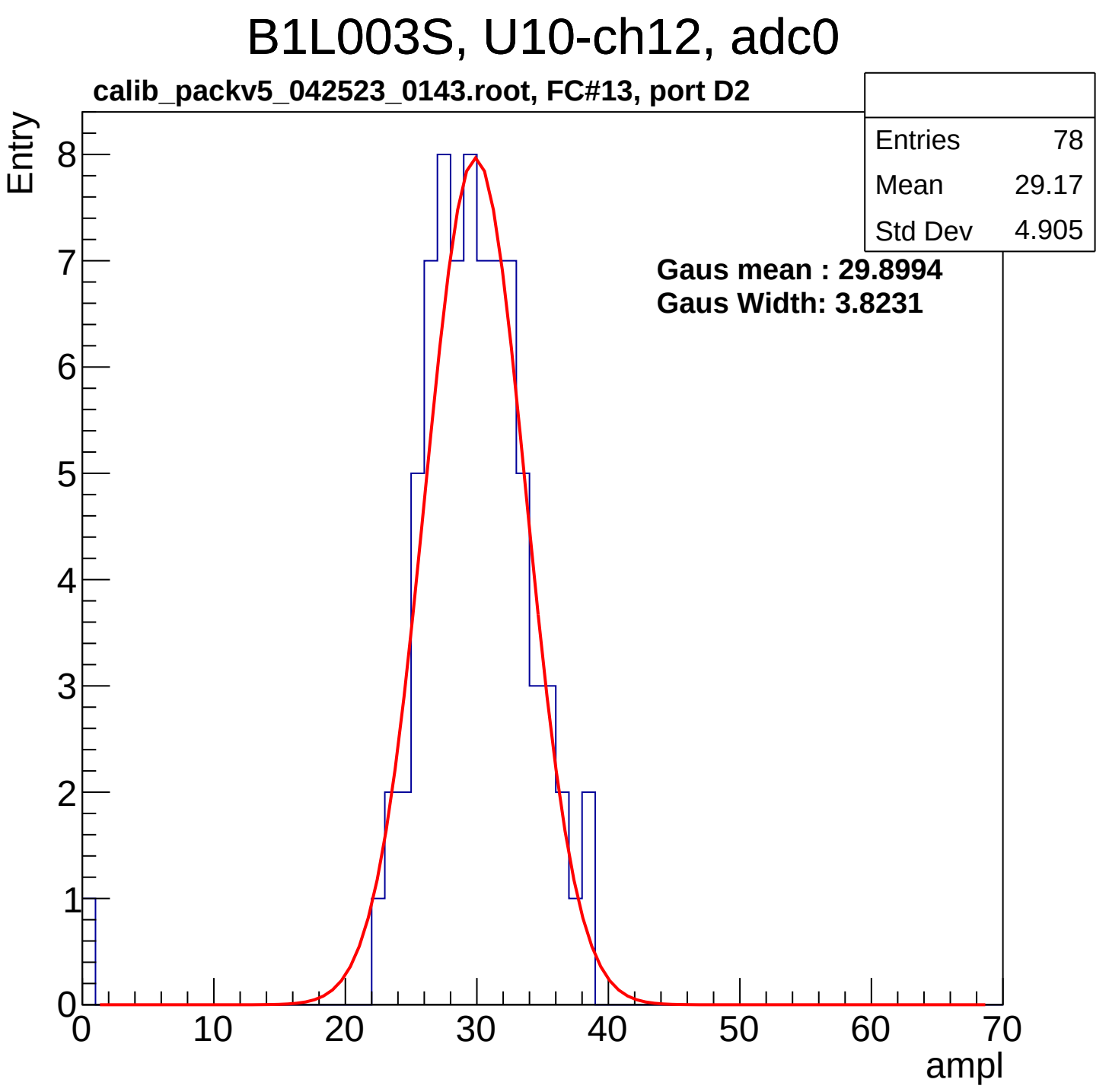

B1L003S, U10-ch11, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch12, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 29.17
Std Dev 4.905
Gaus mean : 29.8994
Gaus Width: 3.8231
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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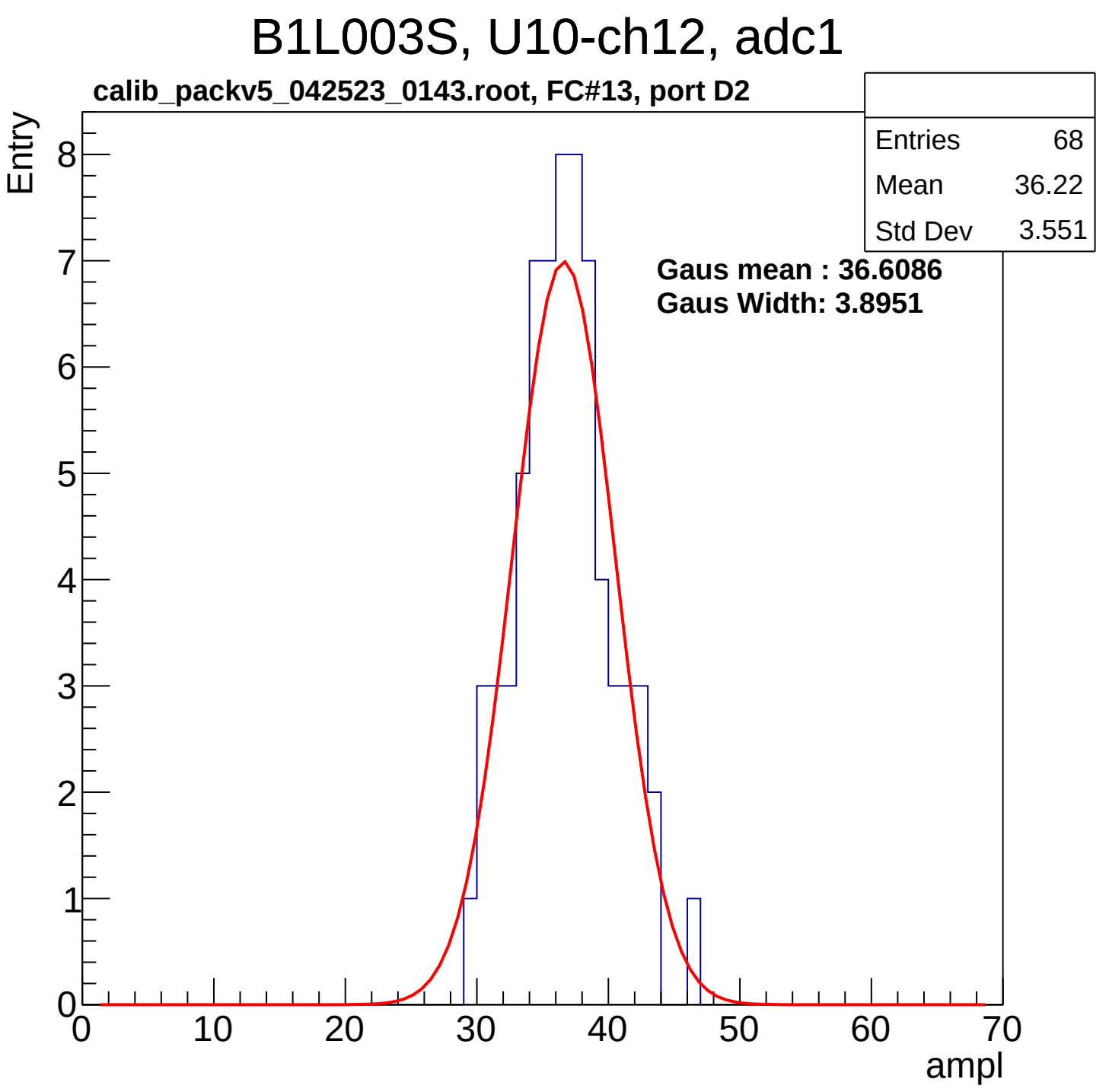
B1L003S, U10-ch12, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.22
Std Dev 3.551
Gaus mean : 36.6086
Gaus Width: 3.8951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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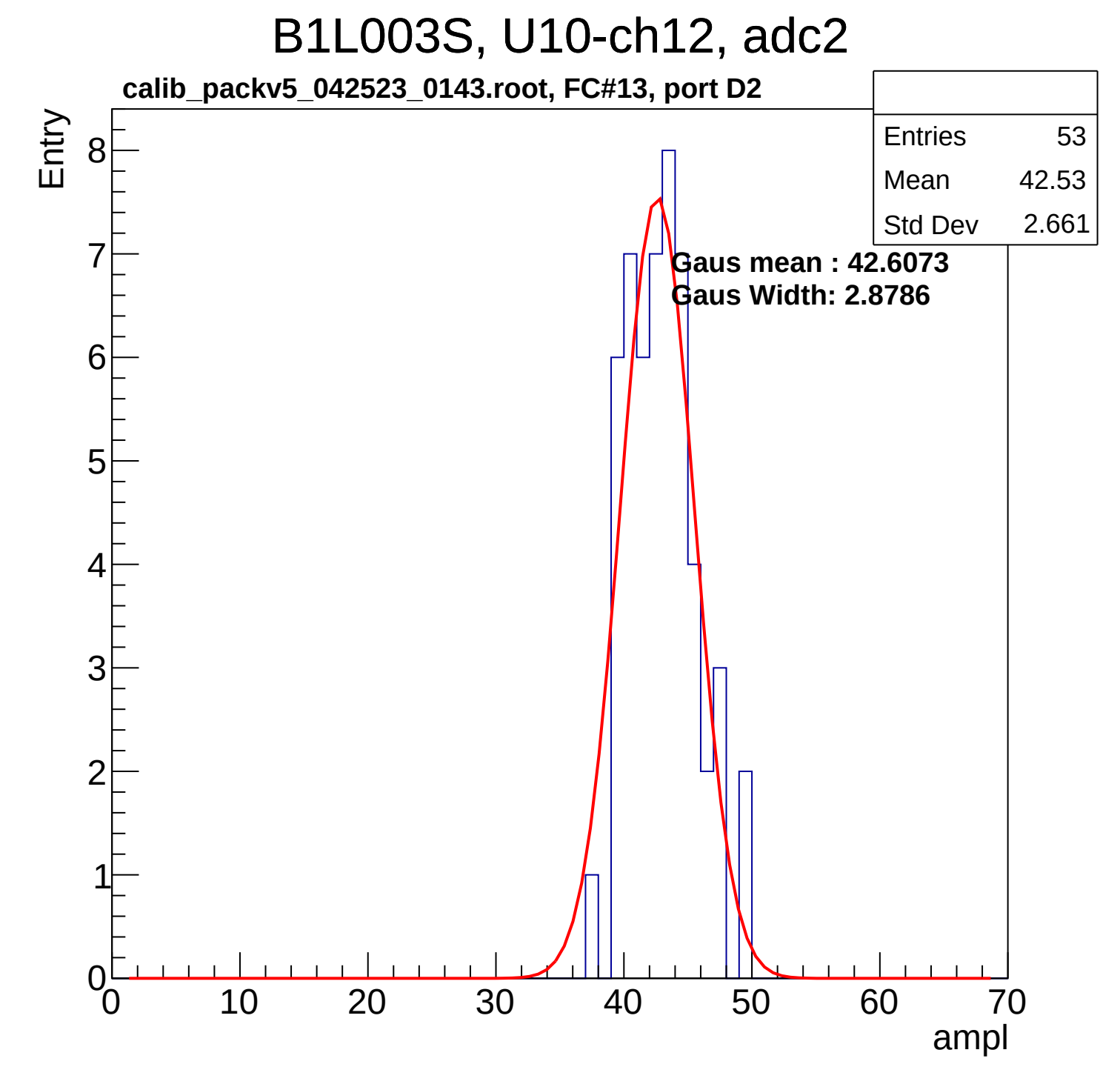
B1L003S, U10-ch12, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.53
Std Dev 2.661
Gaus mean : 42.6073
Gaus Width: 2.8786
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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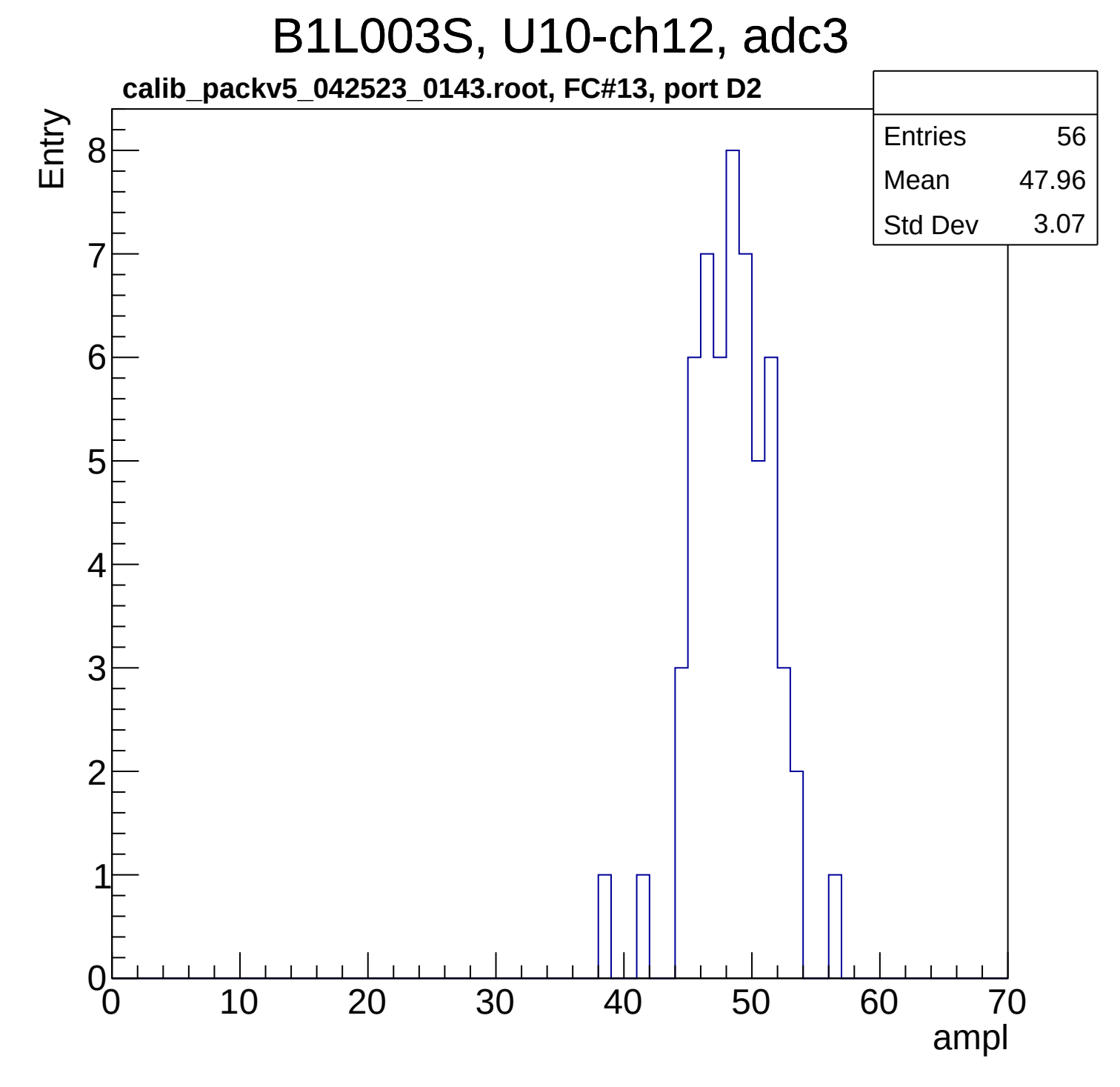
B1L003S, U10-ch12, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 47.96
Std Dev 3.07
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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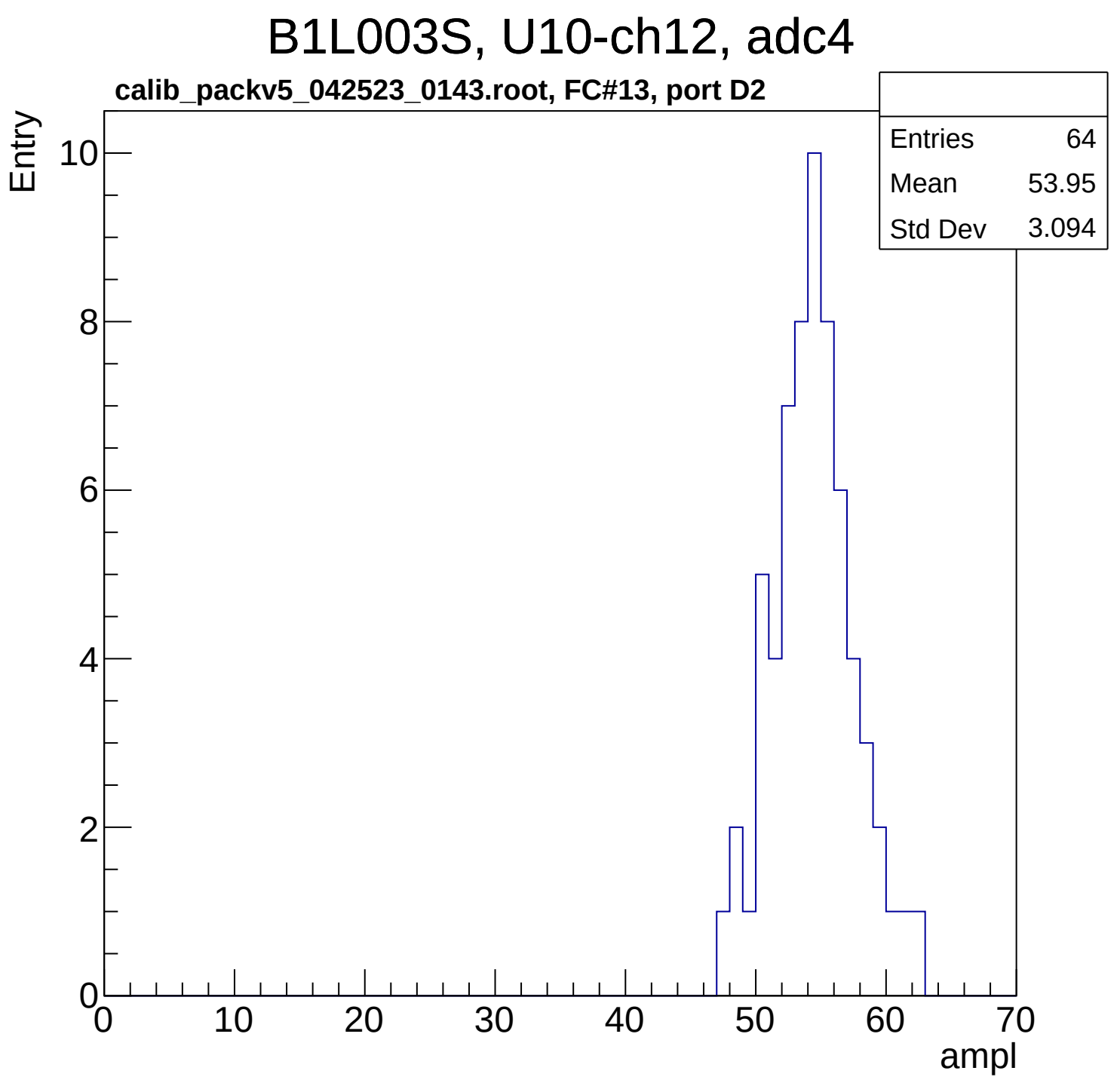
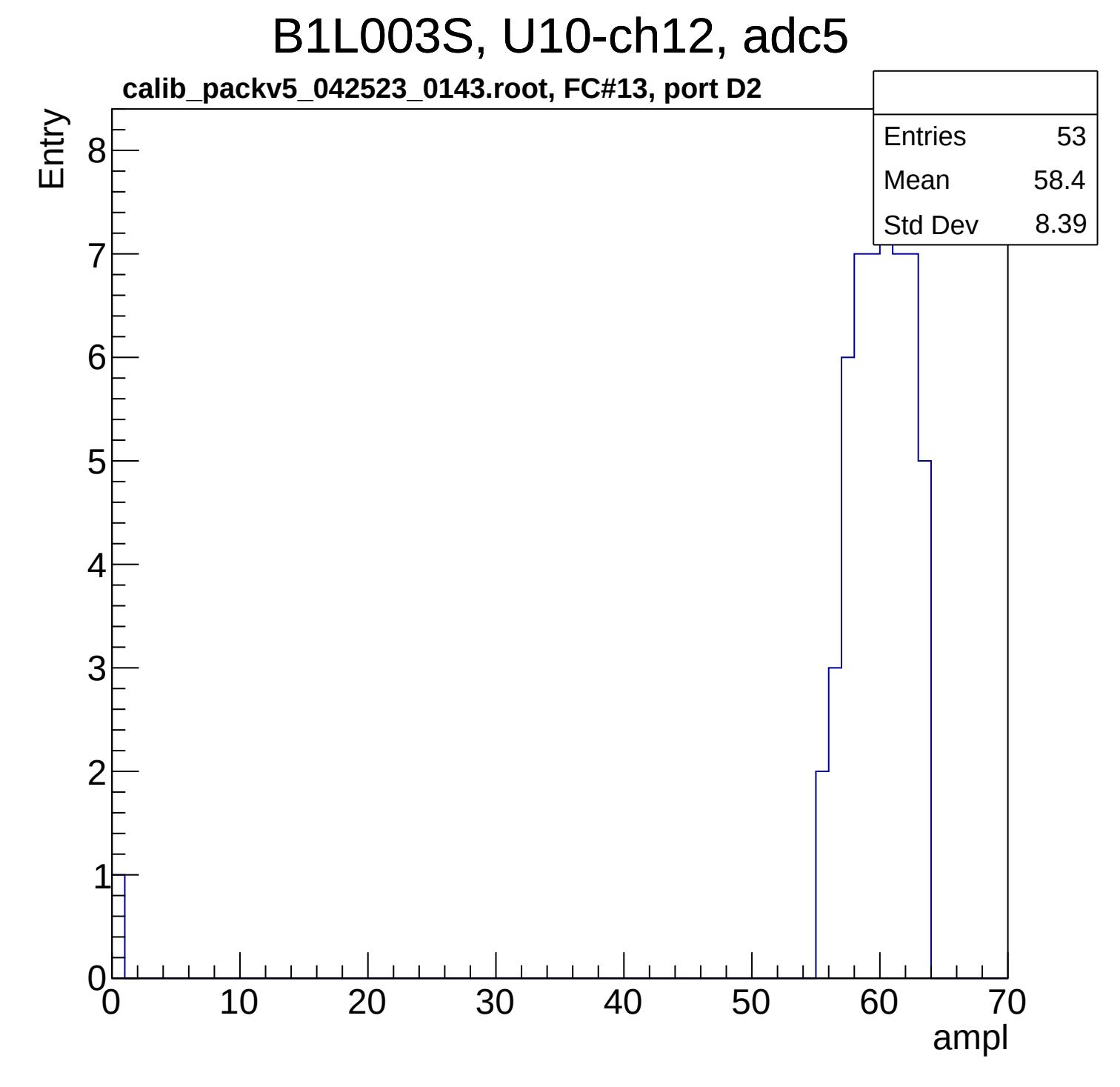
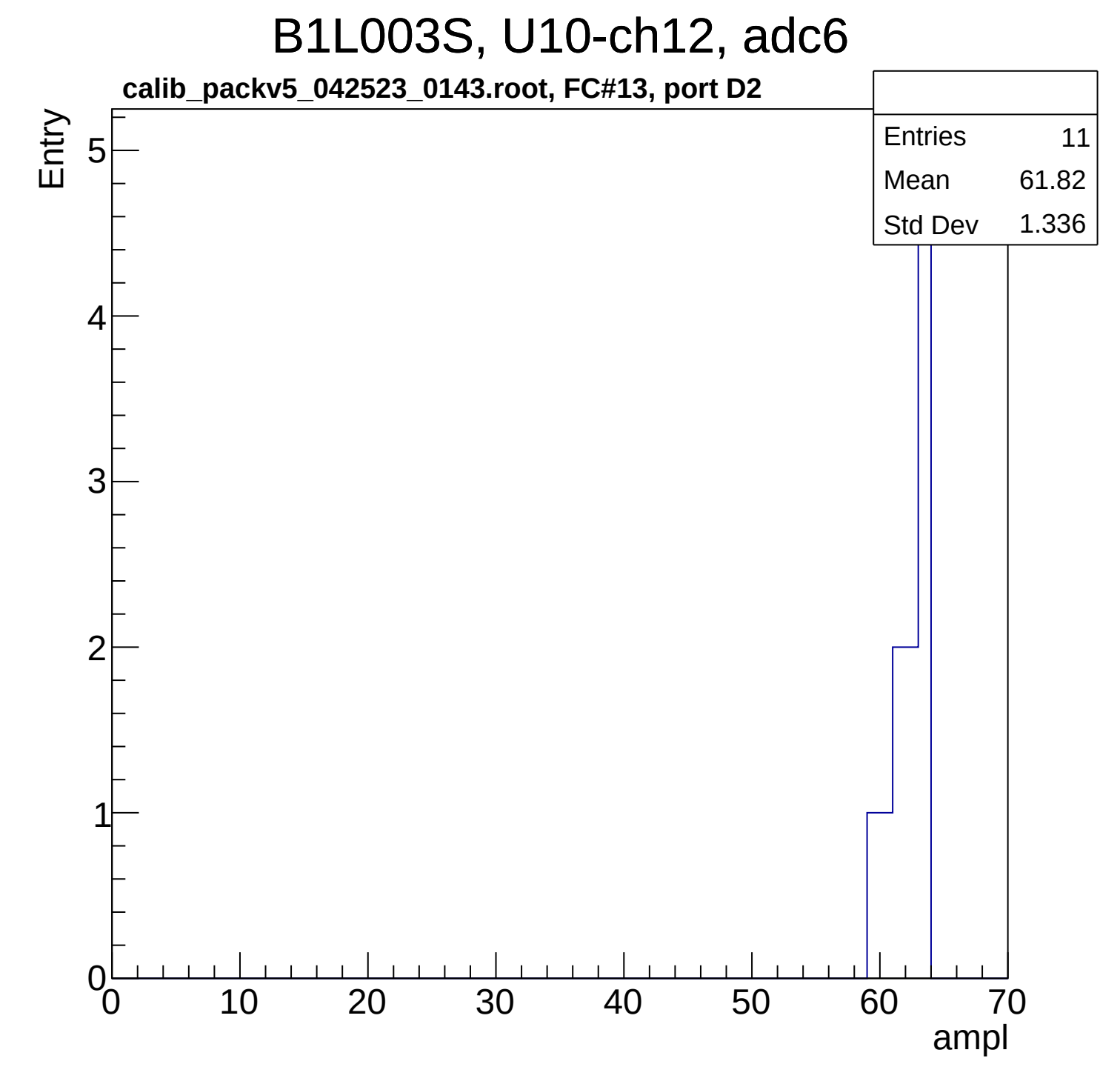
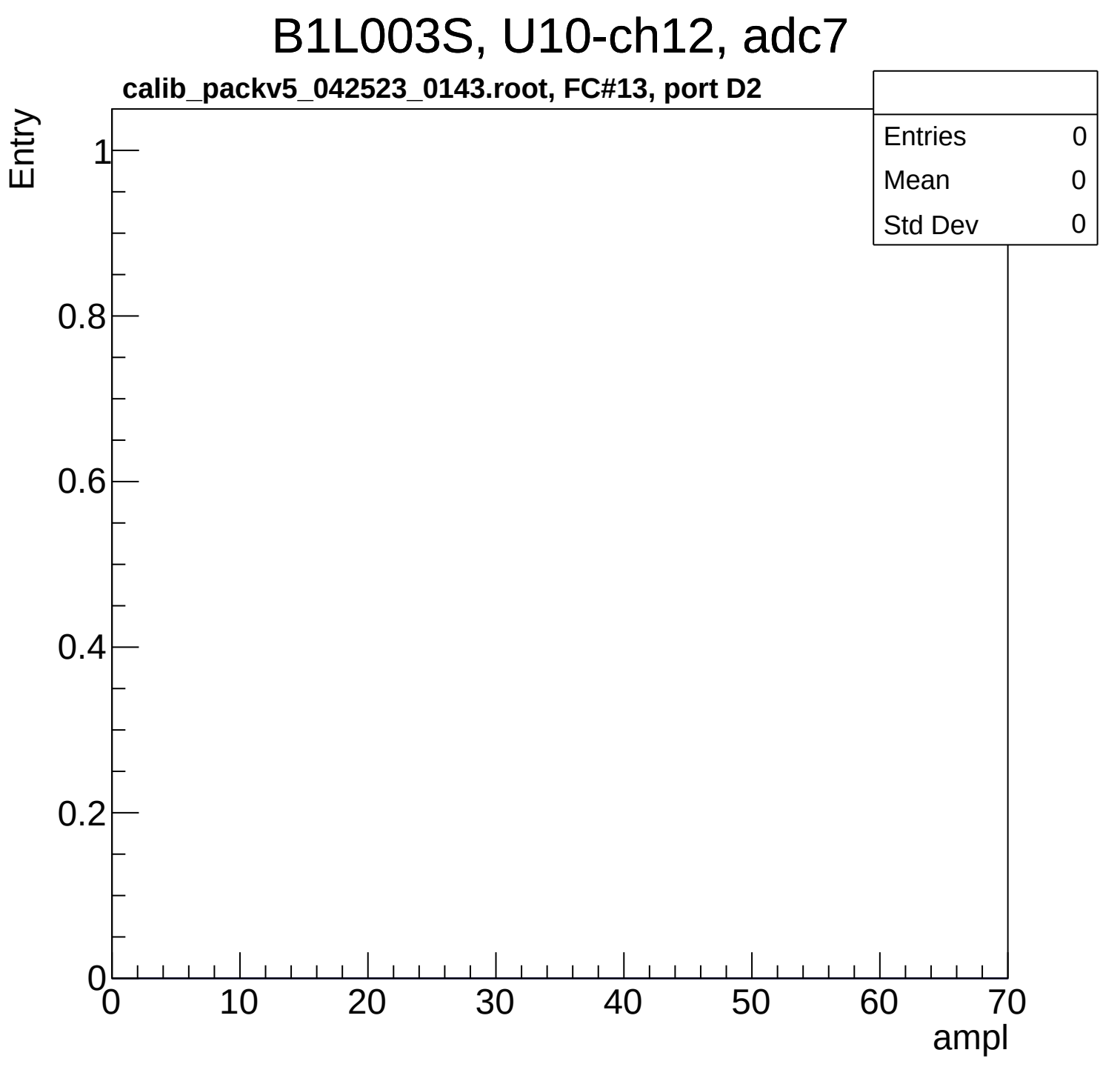
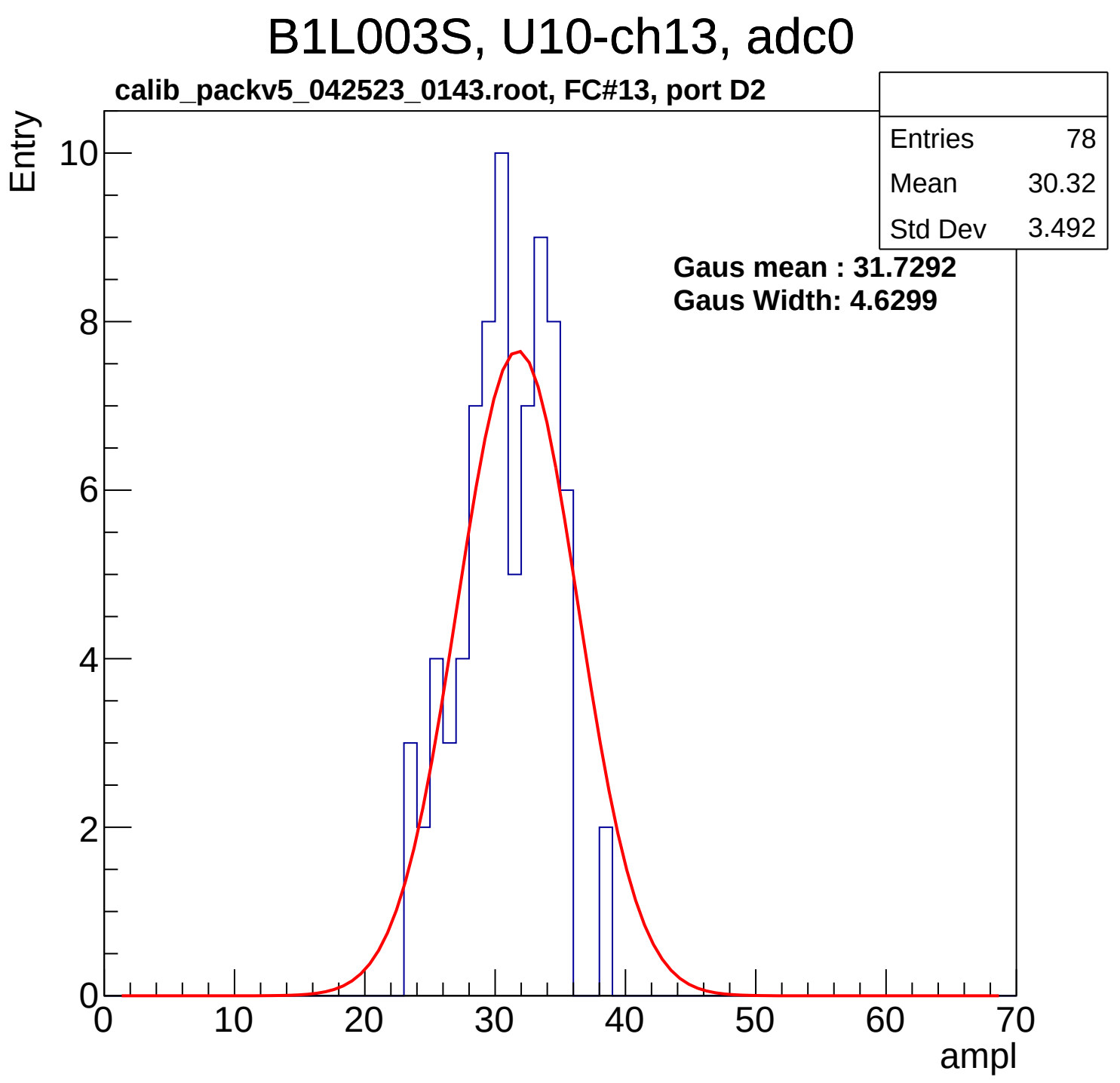

B1L003S, U10-ch12, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 53.95
Std Dev 3.094
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch12, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.4
Std Dev 8.39
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch12, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.82
Std Dev 1.336
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch12, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch13, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 30.32
Std Dev 3.492
Gaus mean : 31.7292
Gaus Width: 4.6299
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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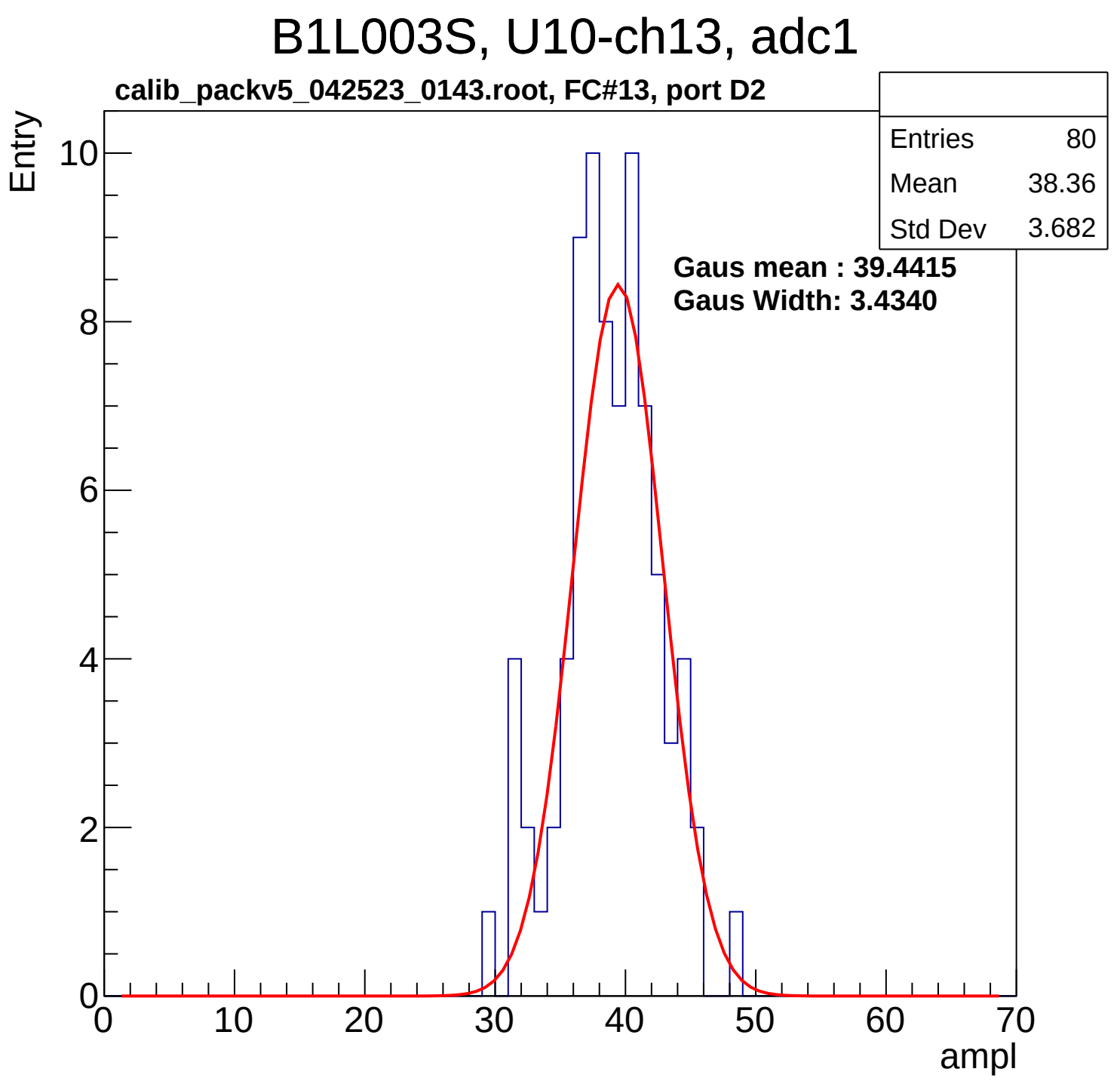
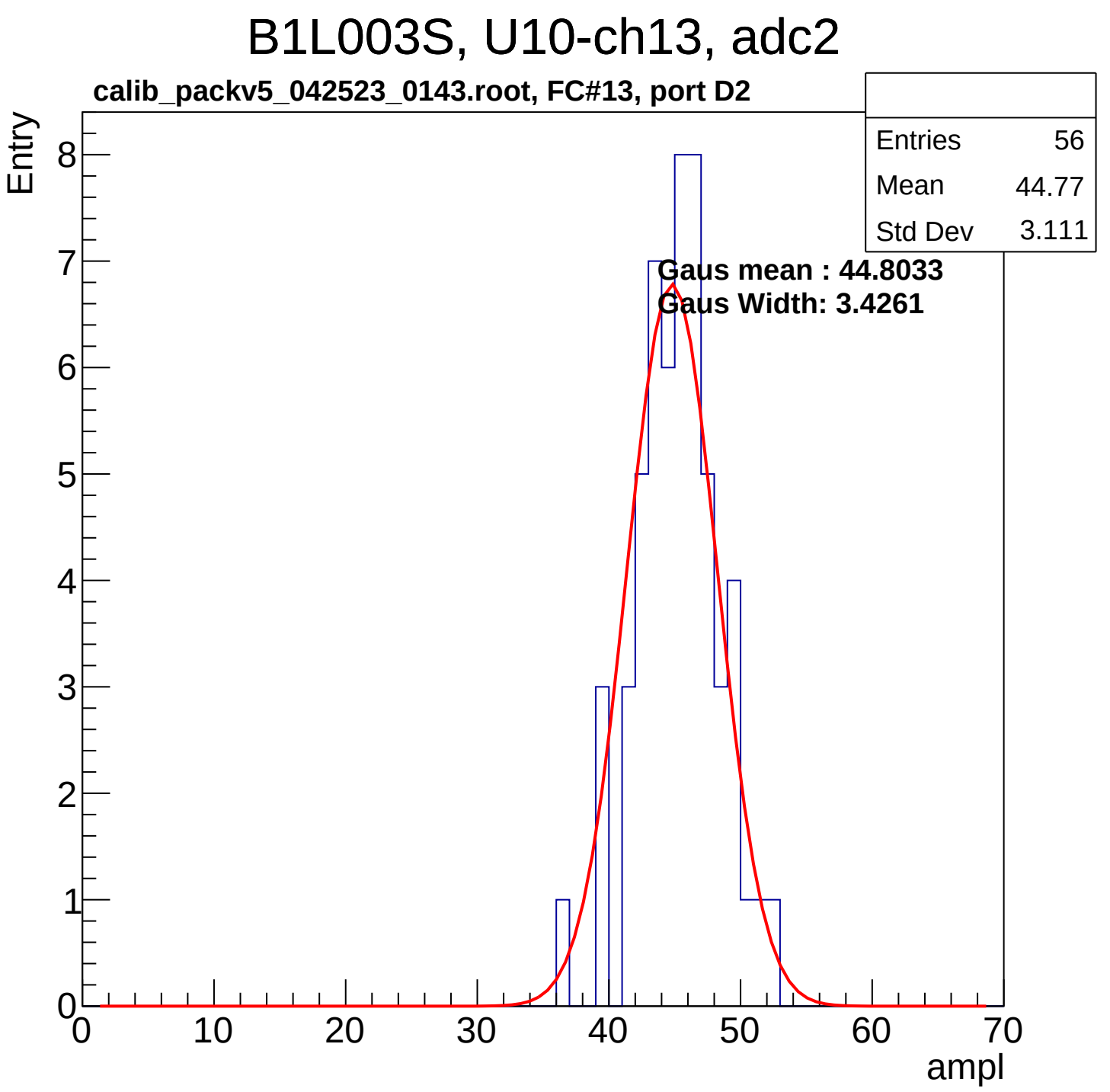
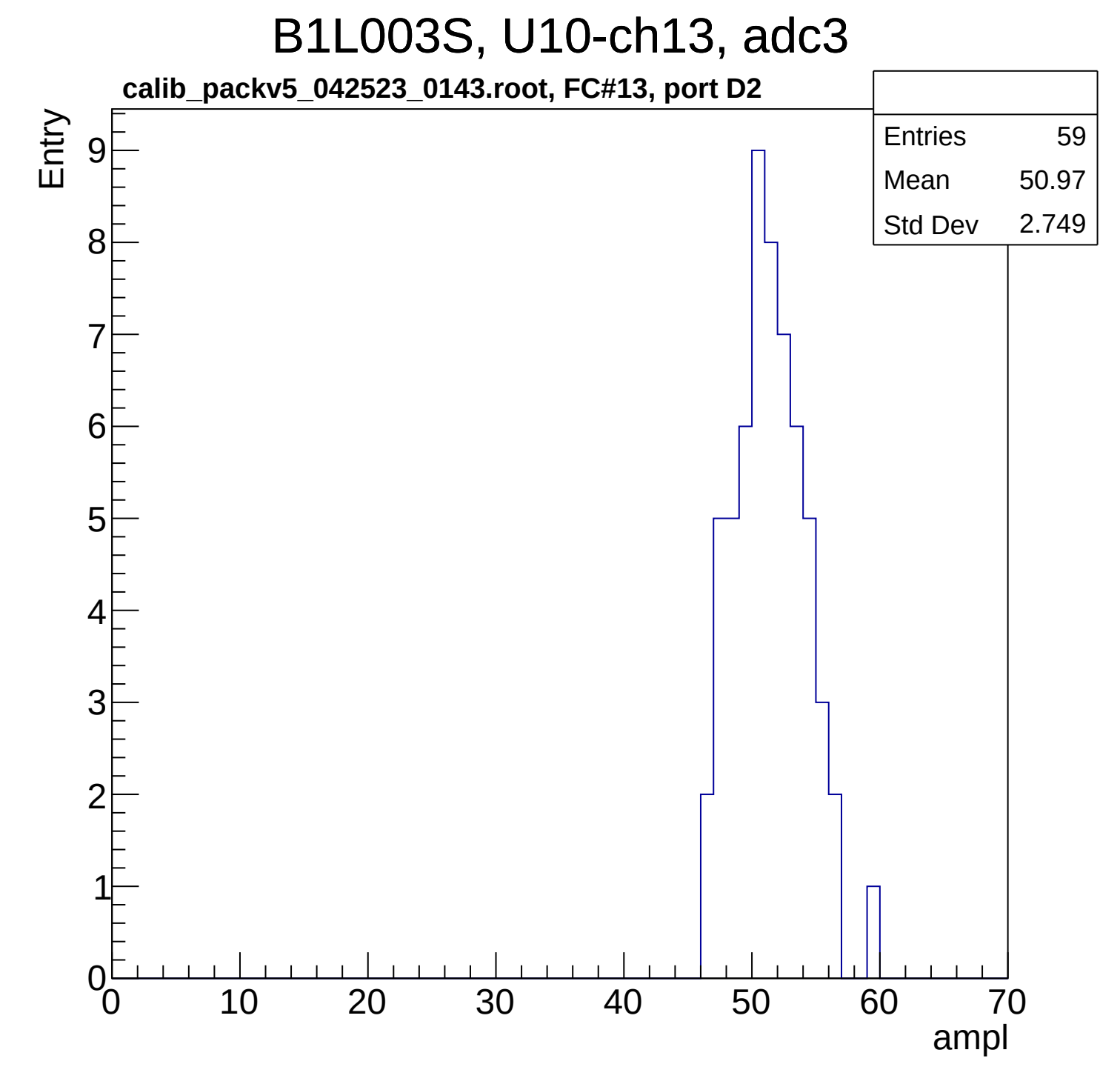
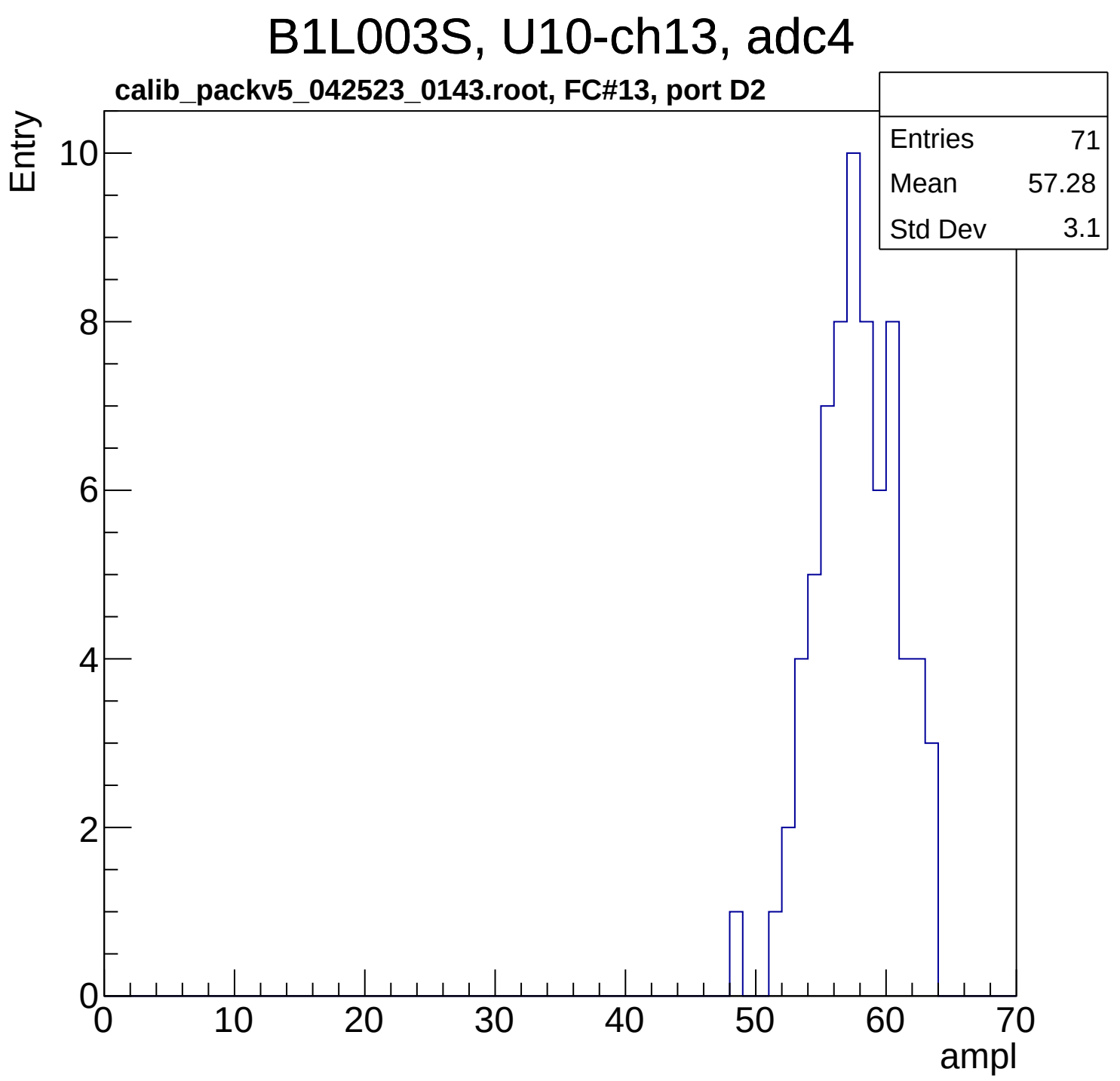
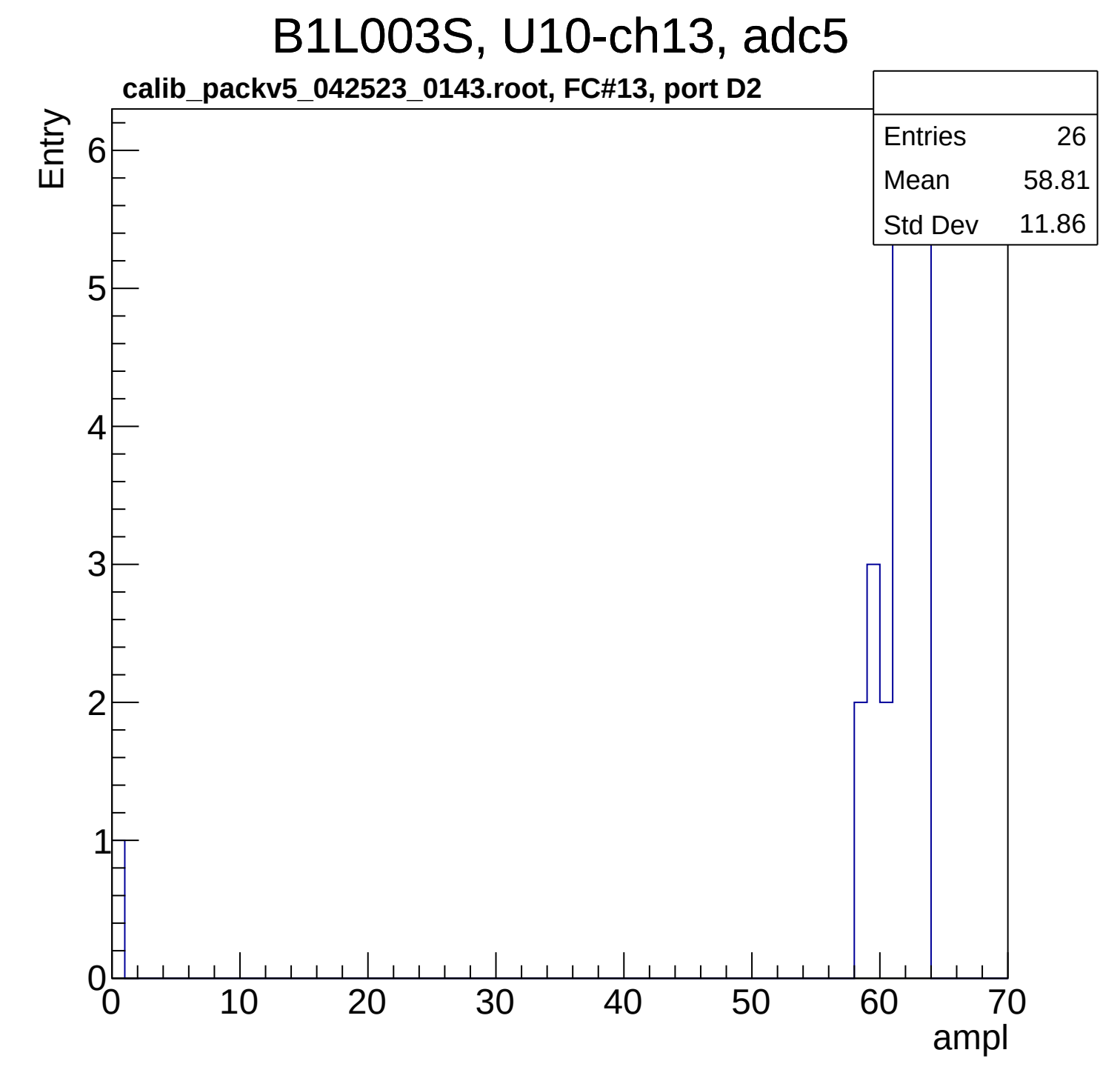
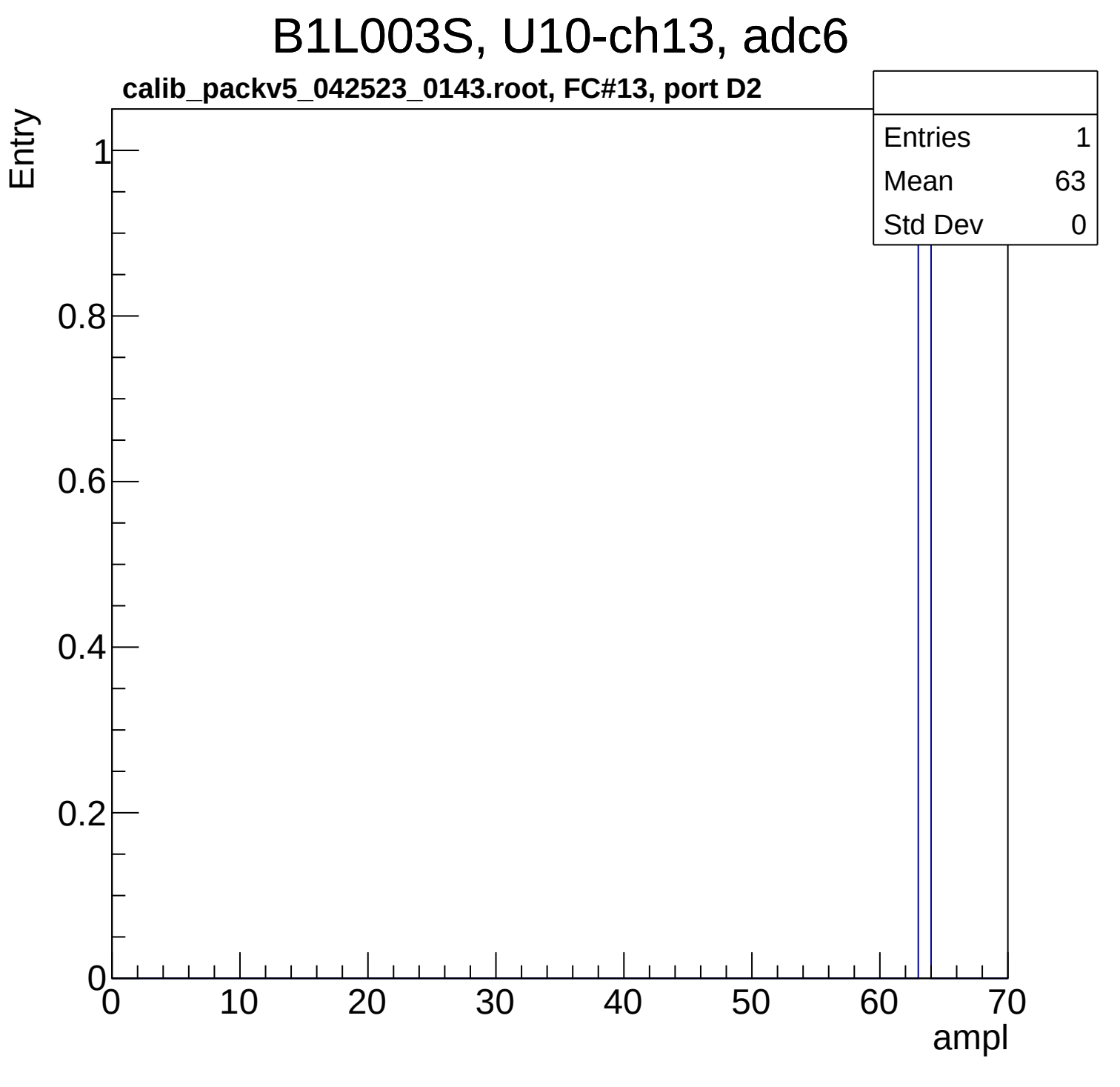

B1L003S, U10-ch13, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 38.36
Std Dev 3.682
Gaus mean : 39.4415
Gaus Width: 3.4340
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

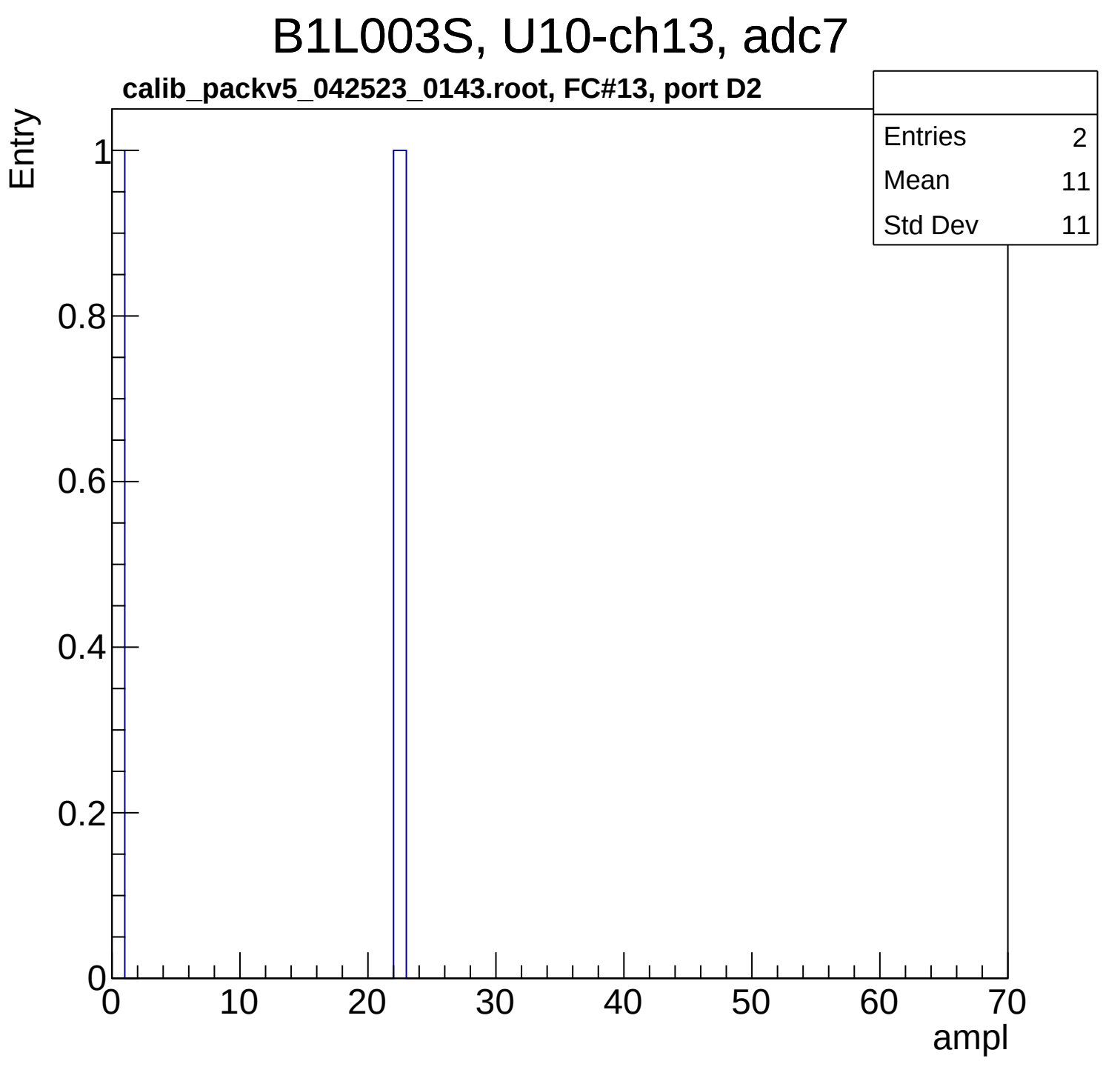

B1L003S, U10-ch13, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 44.77
Std Dev 3.111
Gaus mean : 44.8033
Gaus Width: 3.4261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch13, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 50.97
Std Dev 2.749
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch13, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 57.28
Std Dev 3.1
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch13, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 26
Mean 58.81
Std Dev 11.86
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch13, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch13, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
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10
20
30
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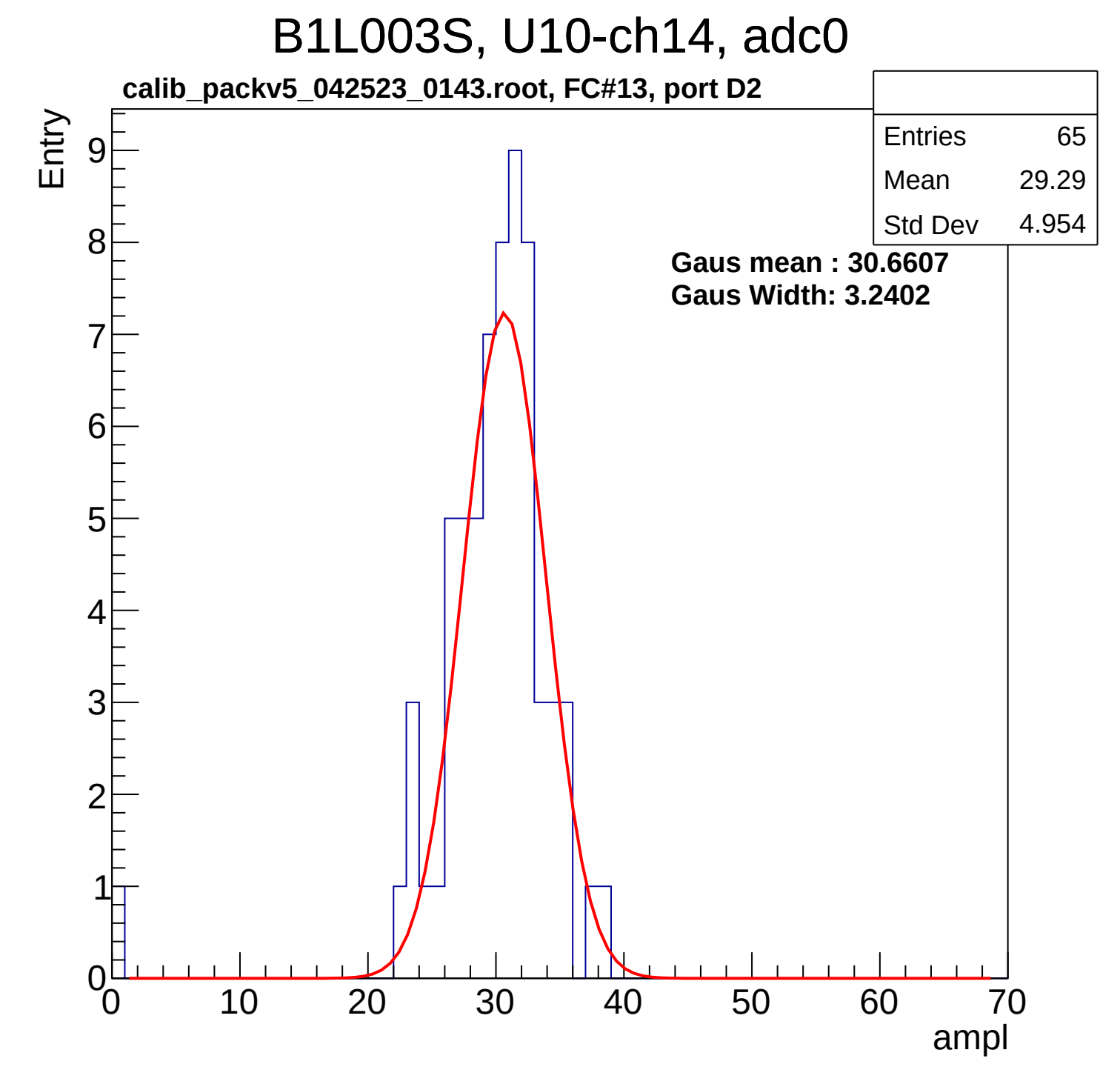
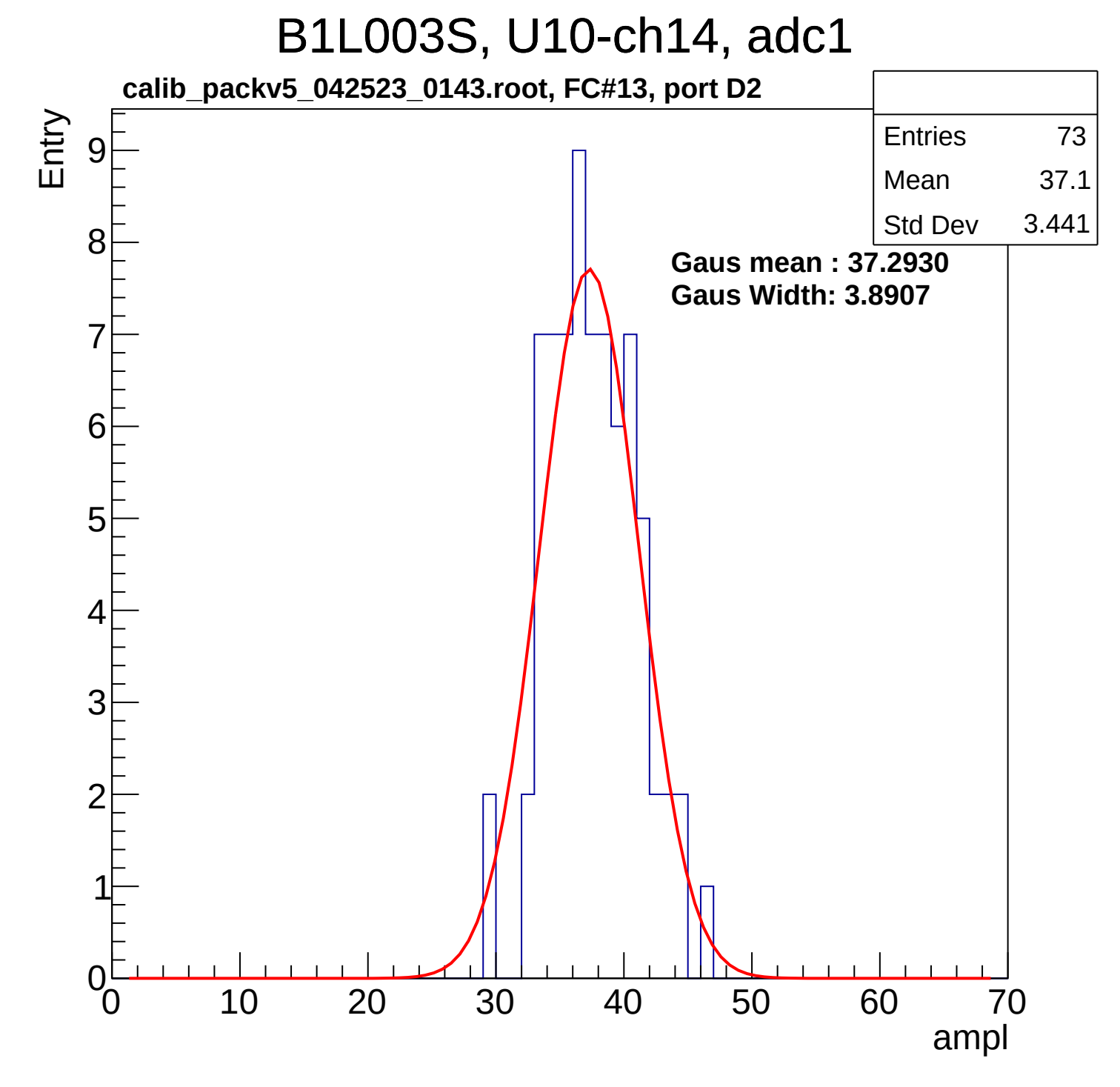
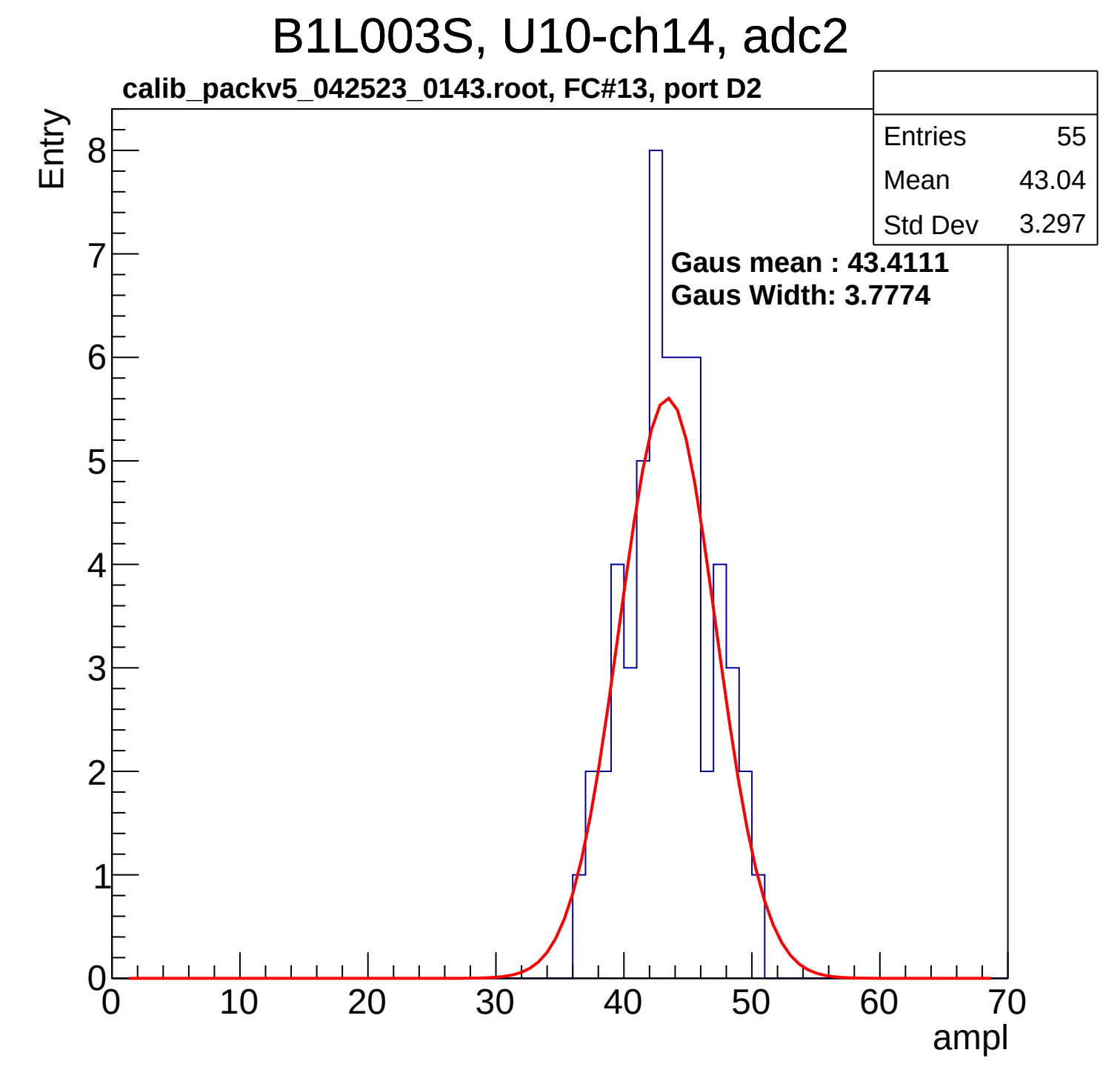
50
60
70
ampl


B1L003S, U10-ch14, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.29
Std Dev 4.954
Gaus mean : 30.6607
Gaus Width: 3.2402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch14, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 37.1
Std Dev 3.441
Gaus mean : 37.2930
Gaus Width: 3.8907
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch14, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43.04
Std Dev 3.297
Gaus mean : 43.4111
Gaus Width: 3.7774
Entry
ampl
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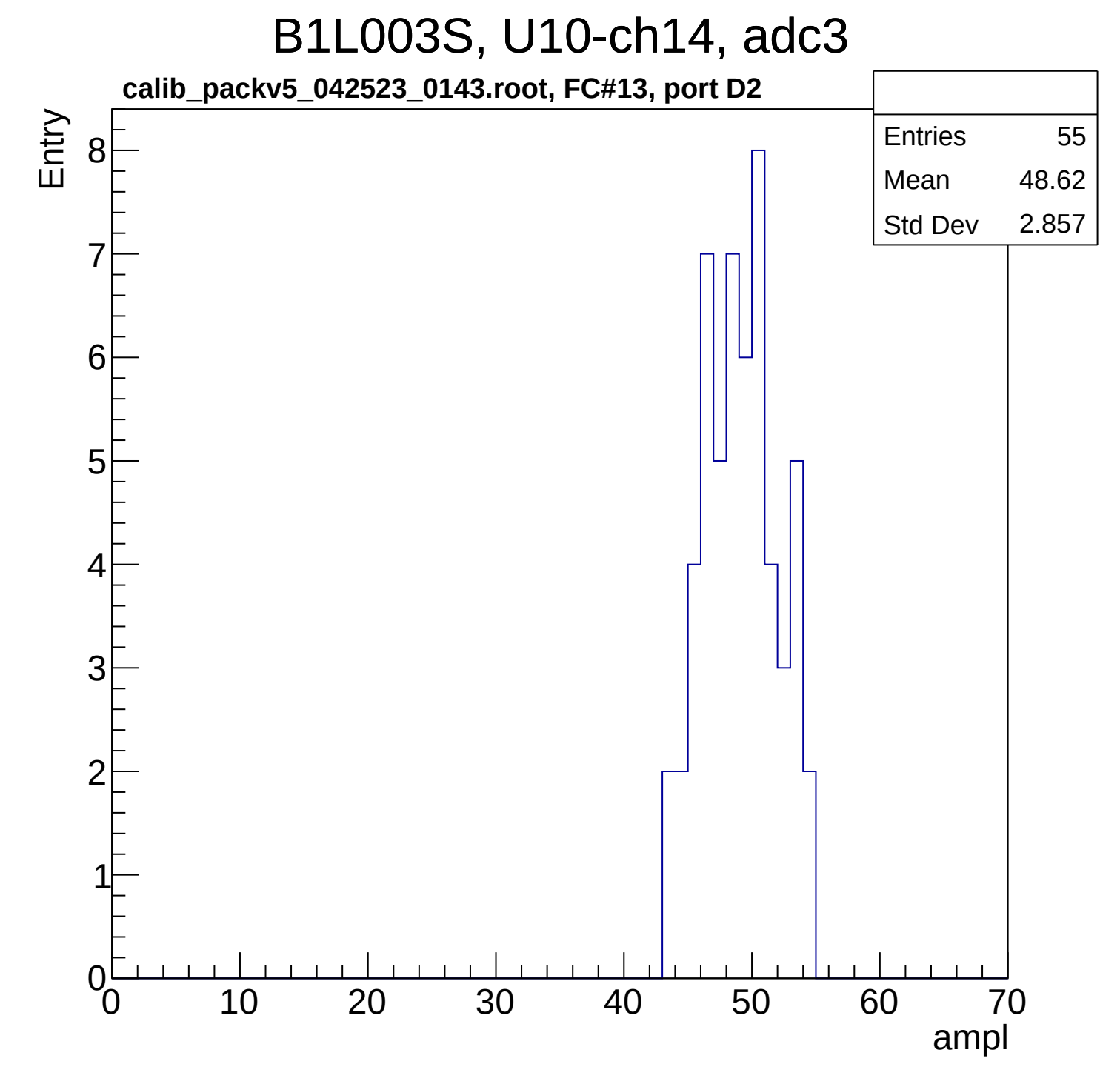
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch14, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 48.62
Std Dev 2.857
Entry
ampl
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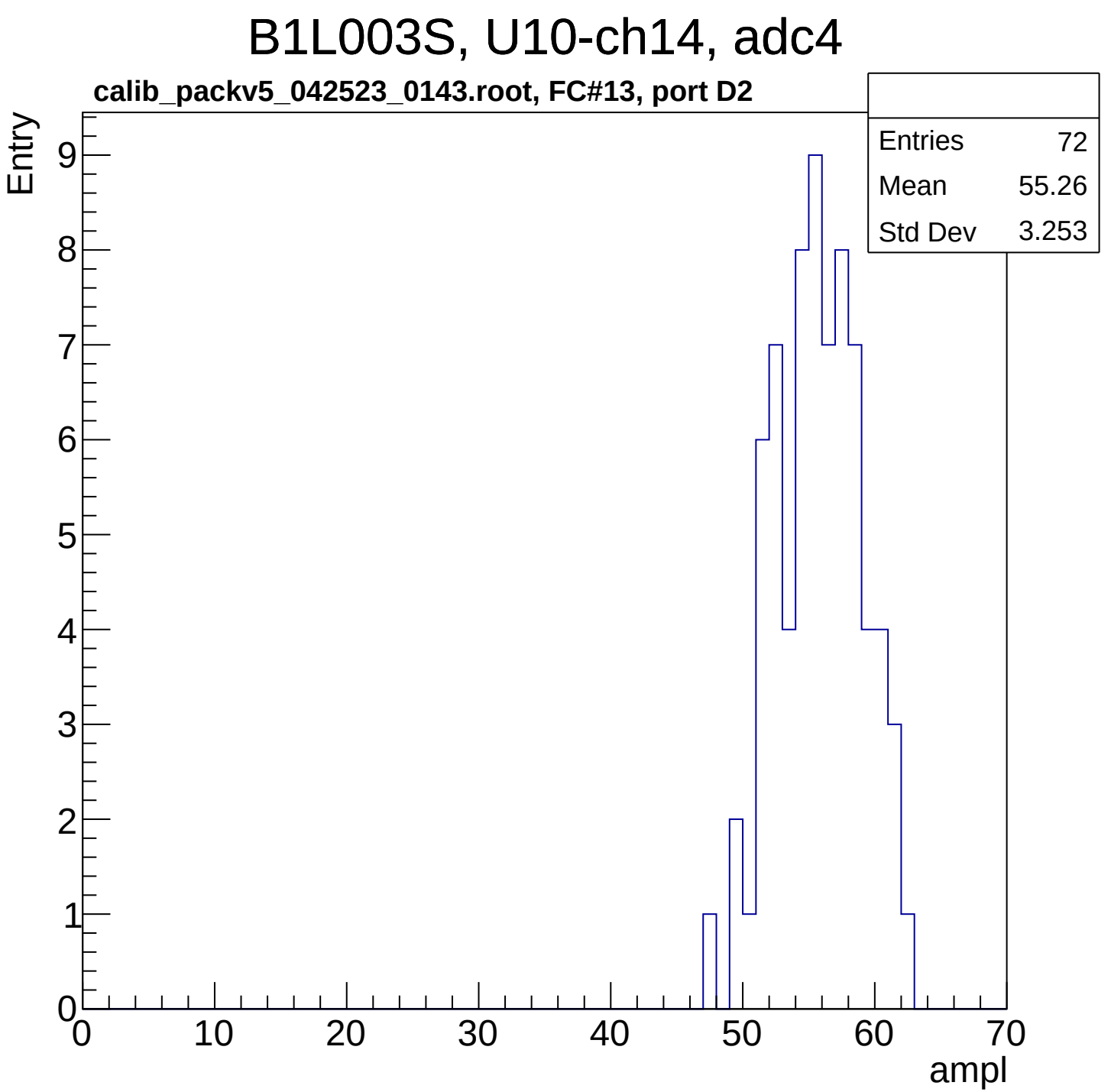
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch14, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 55.26
Std Dev 3.253
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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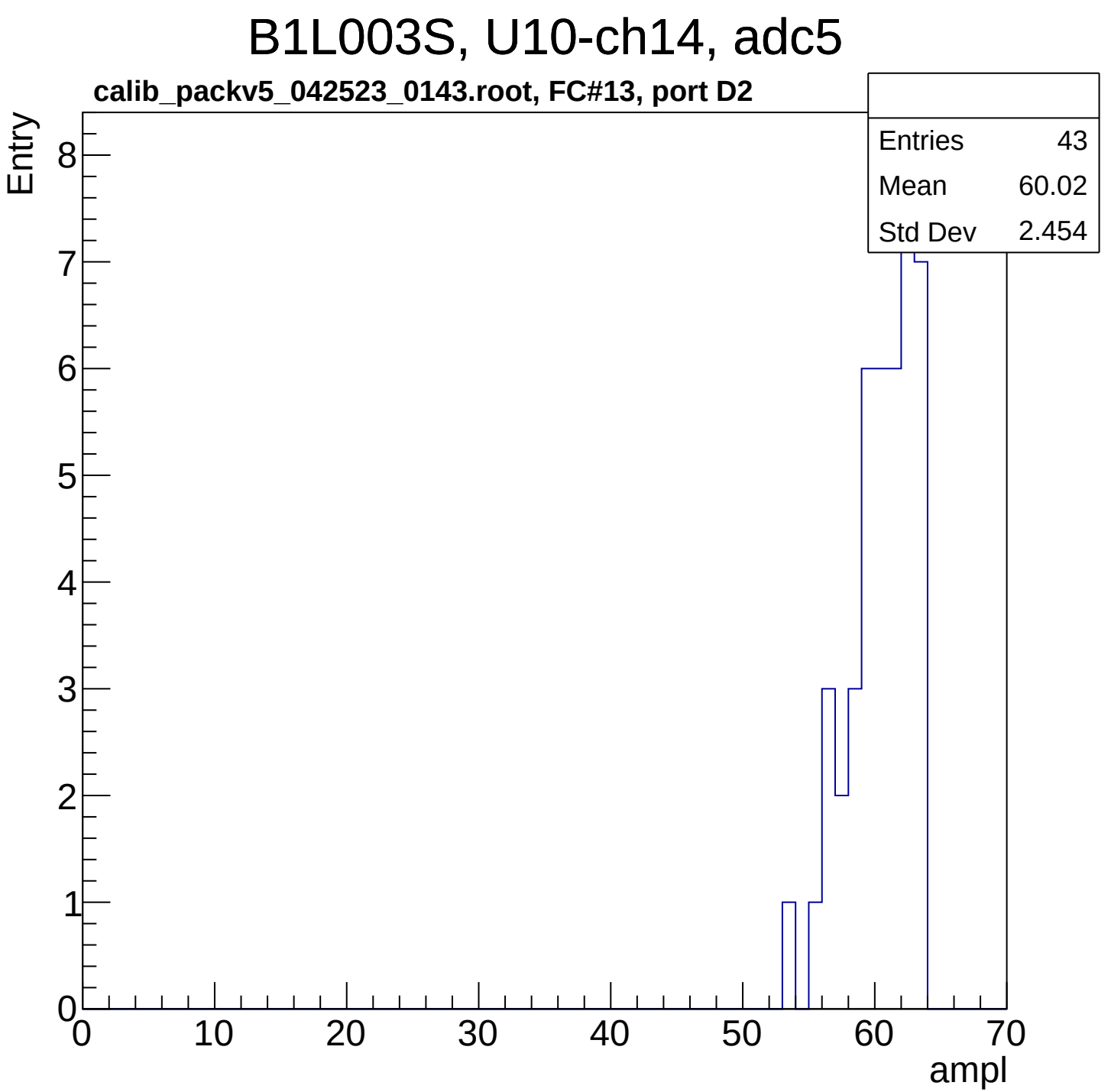
B1L003S, U10-ch14, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 60.02
Std Dev 2.454
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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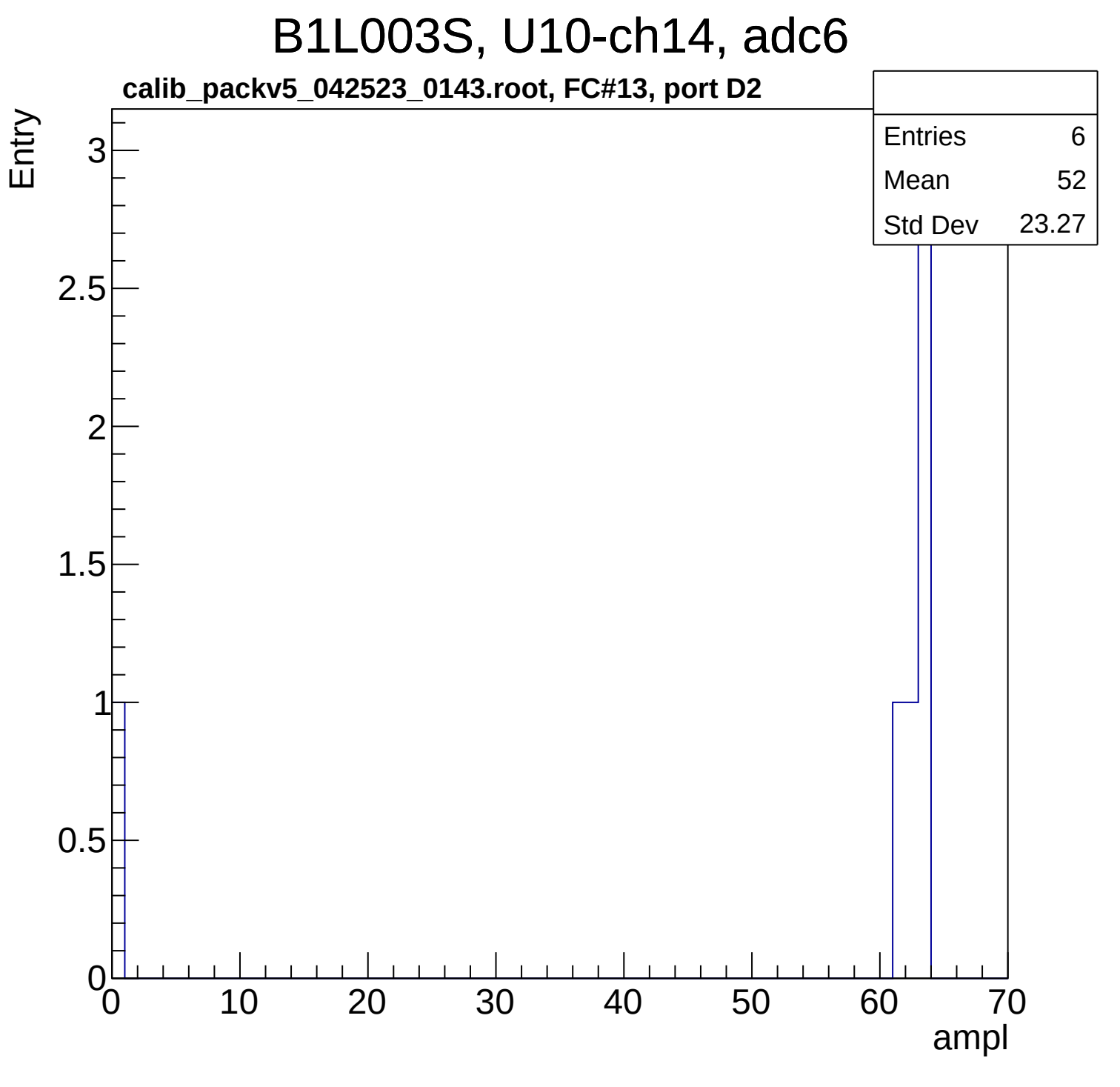
B1L003S, U10-ch14, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
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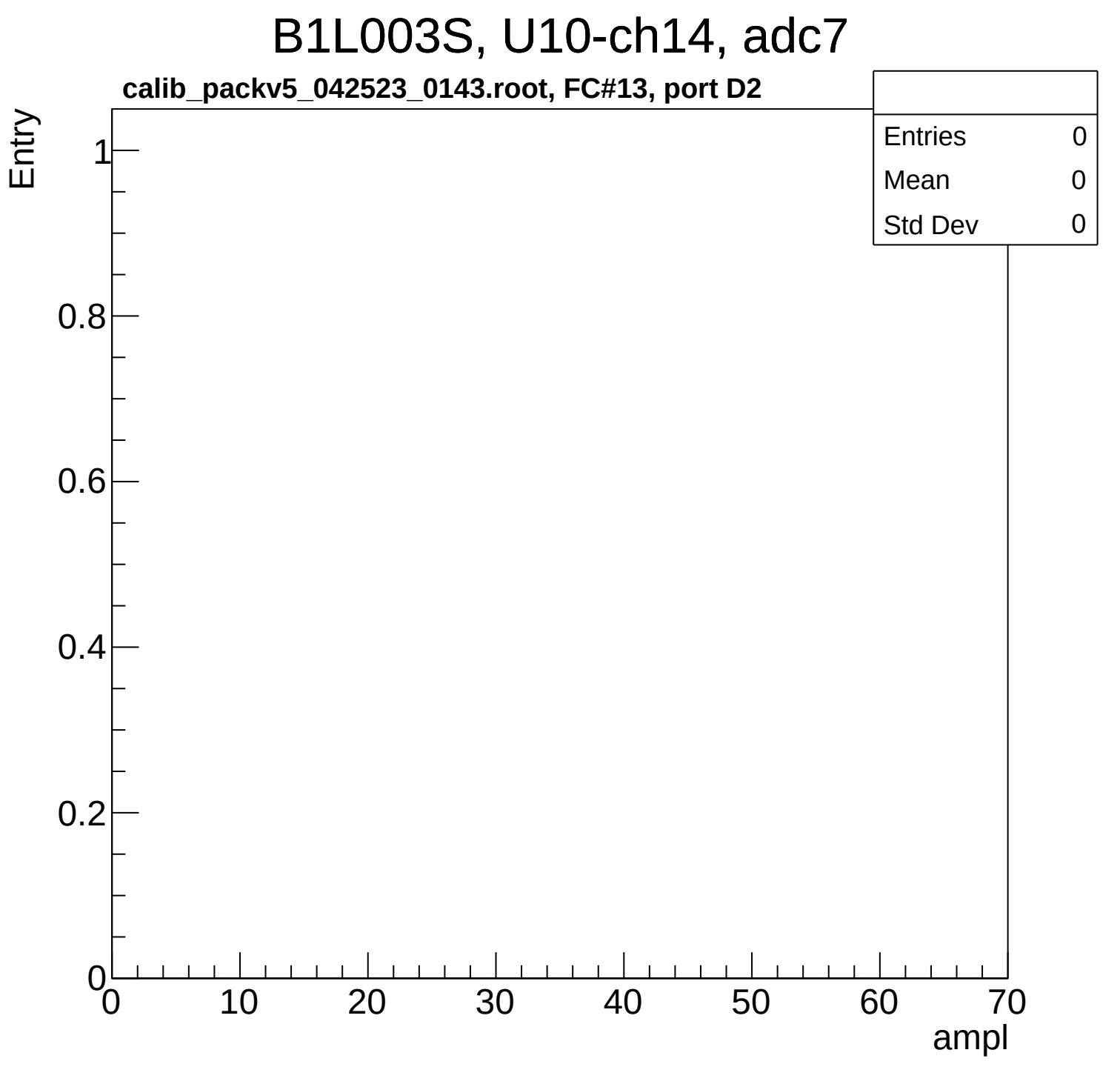
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch14, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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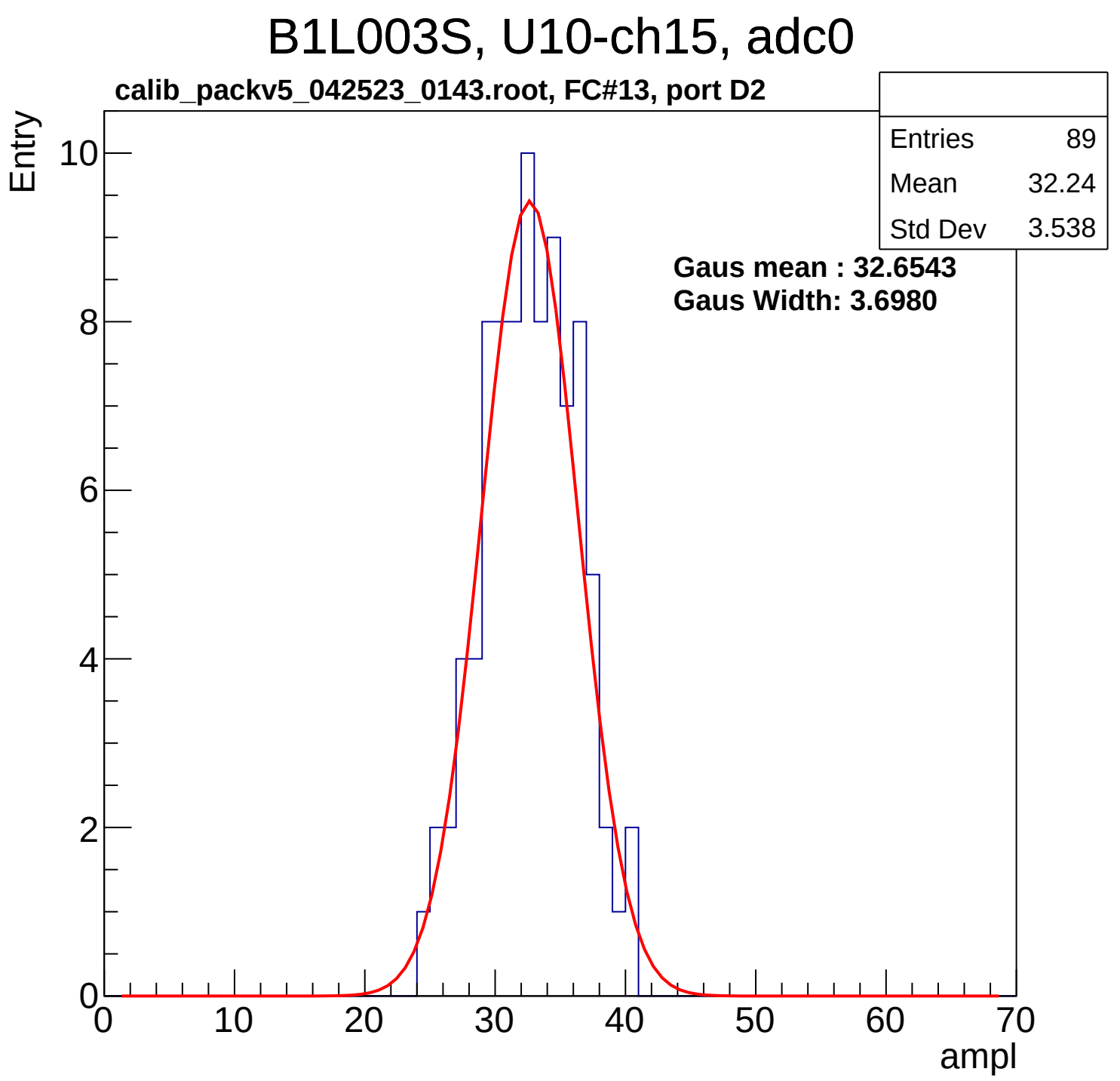
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch15, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 89
Mean 32.24
Std Dev 3.538
Gaus mean : 32.6543
Gaus Width: 3.6980
Entry
ampl
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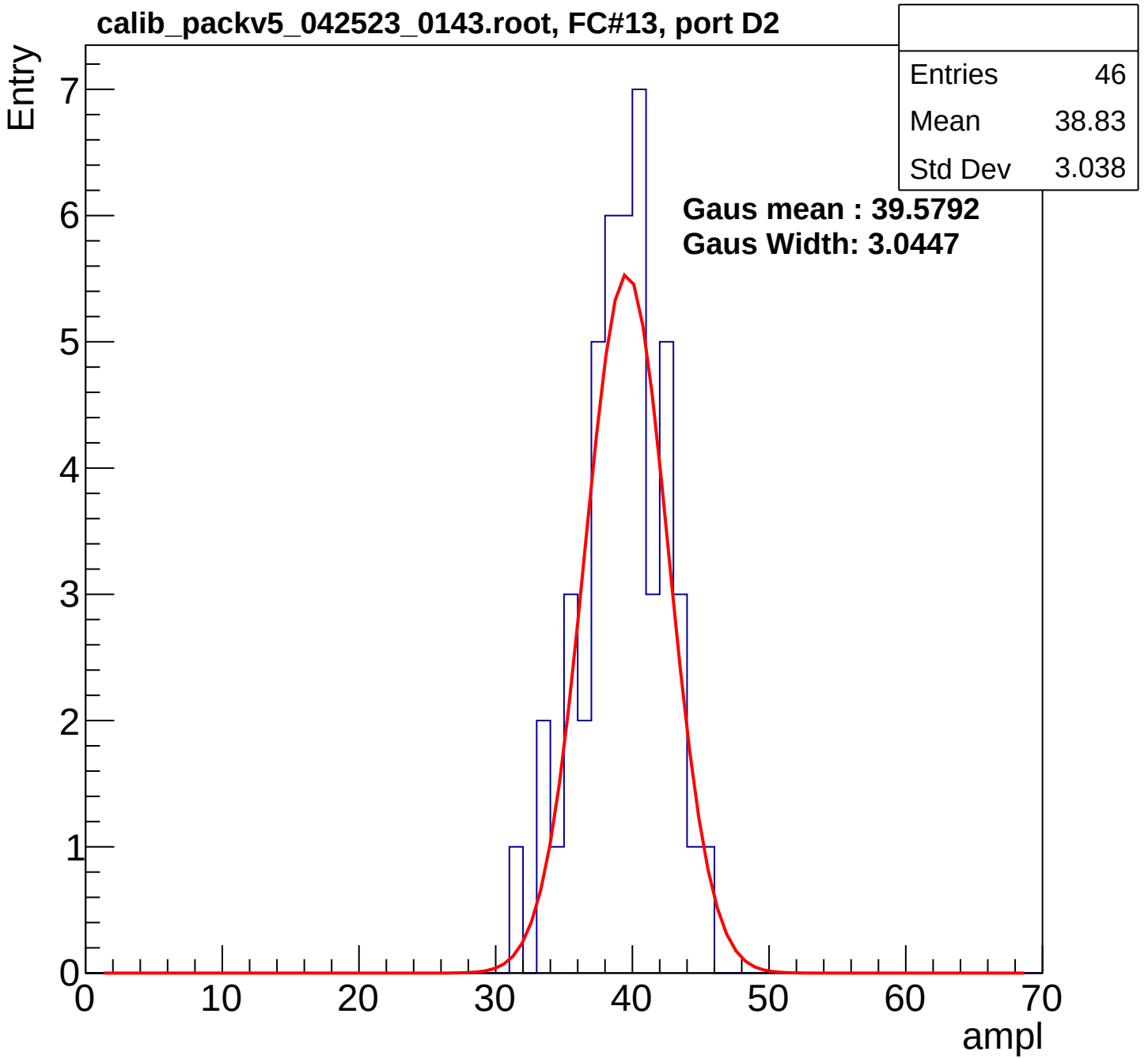
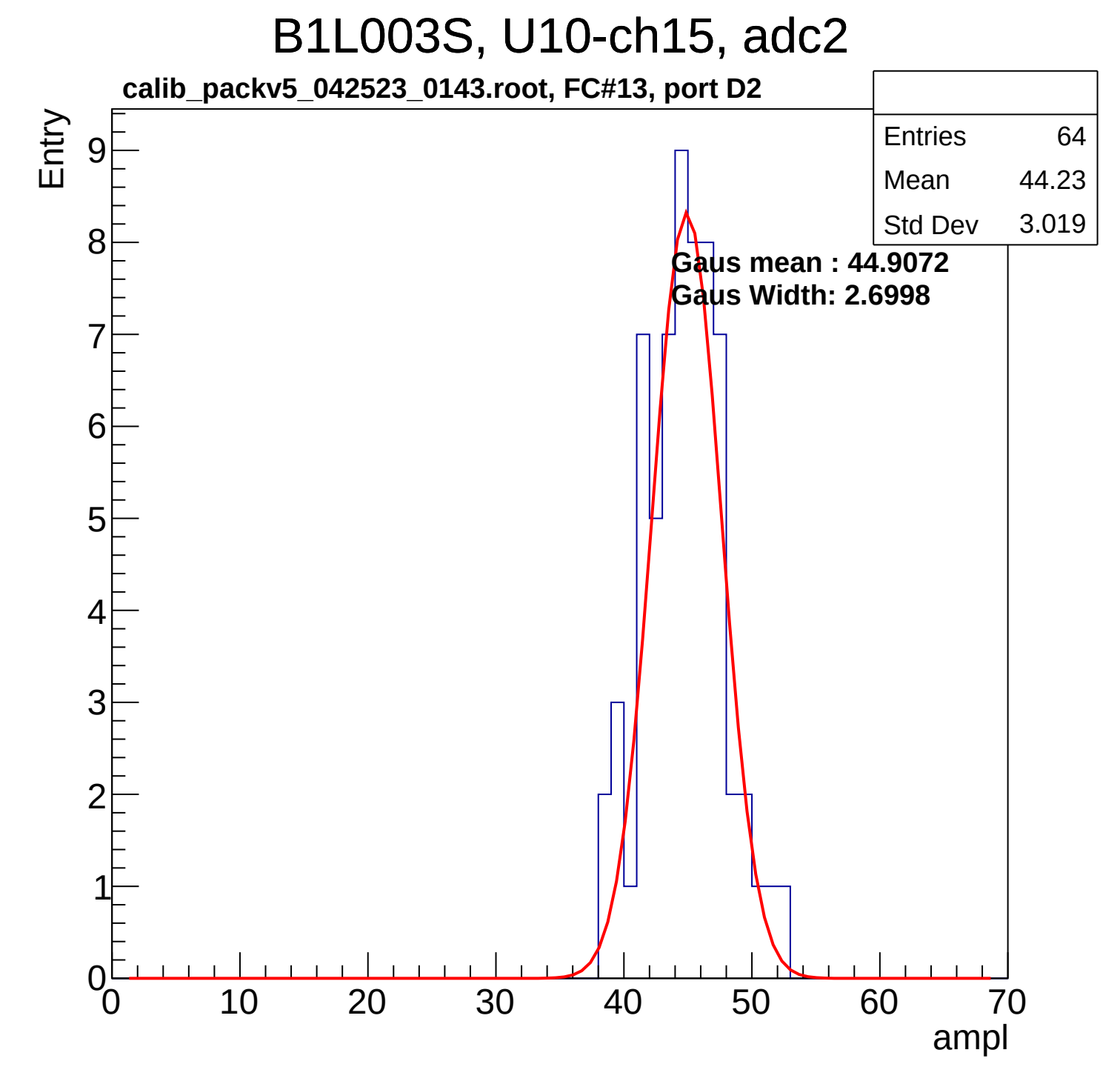
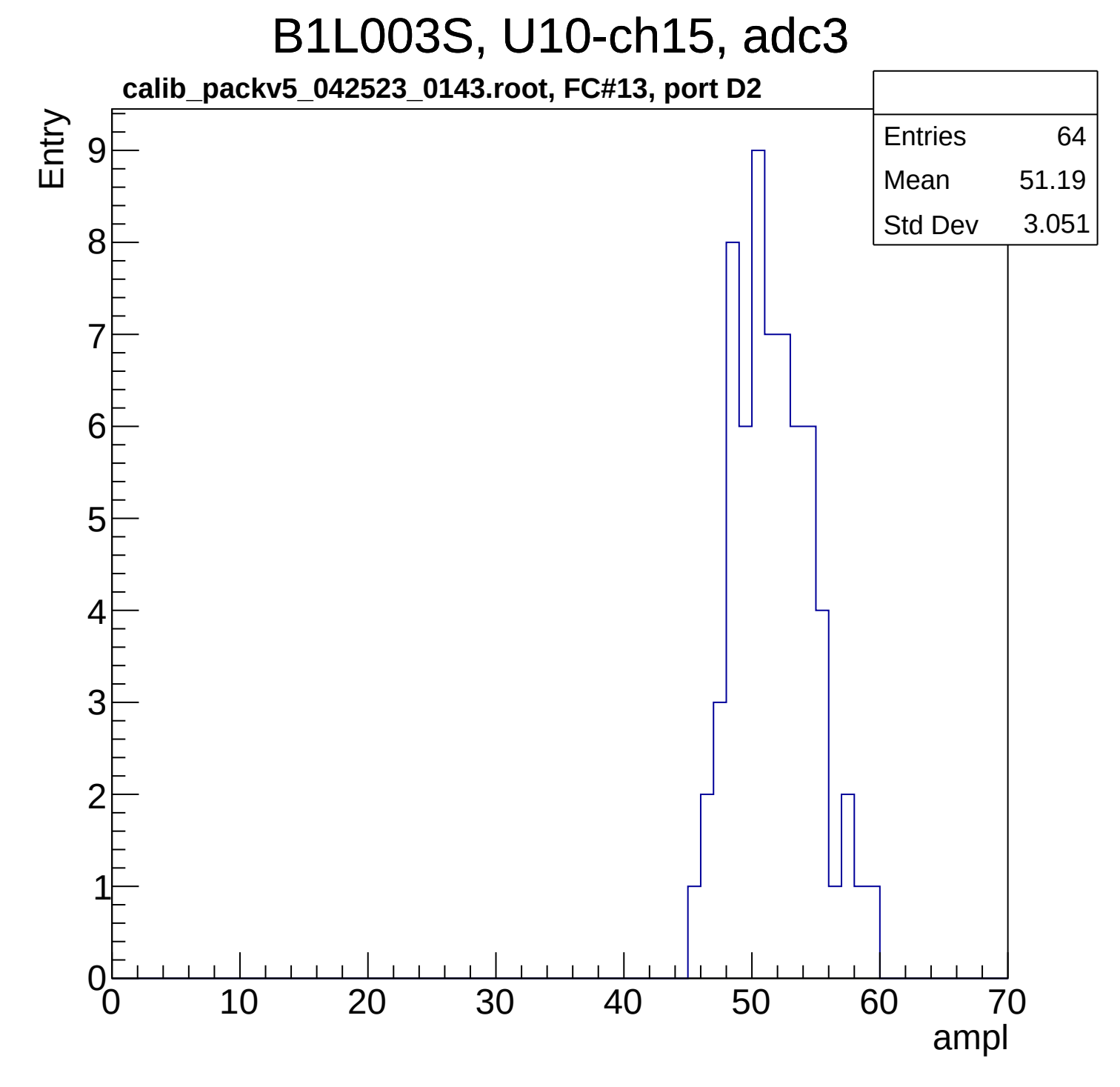
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch15, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 38.83
Std Dev 3.038
Gaus mean : 39.5792
Gaus Width: 3.0447
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch15, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 44.23
Std Dev 3.019
Gaus mean : 44.9072
Gaus Width: 2.6998
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch15, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 51.19
Std Dev 3.051
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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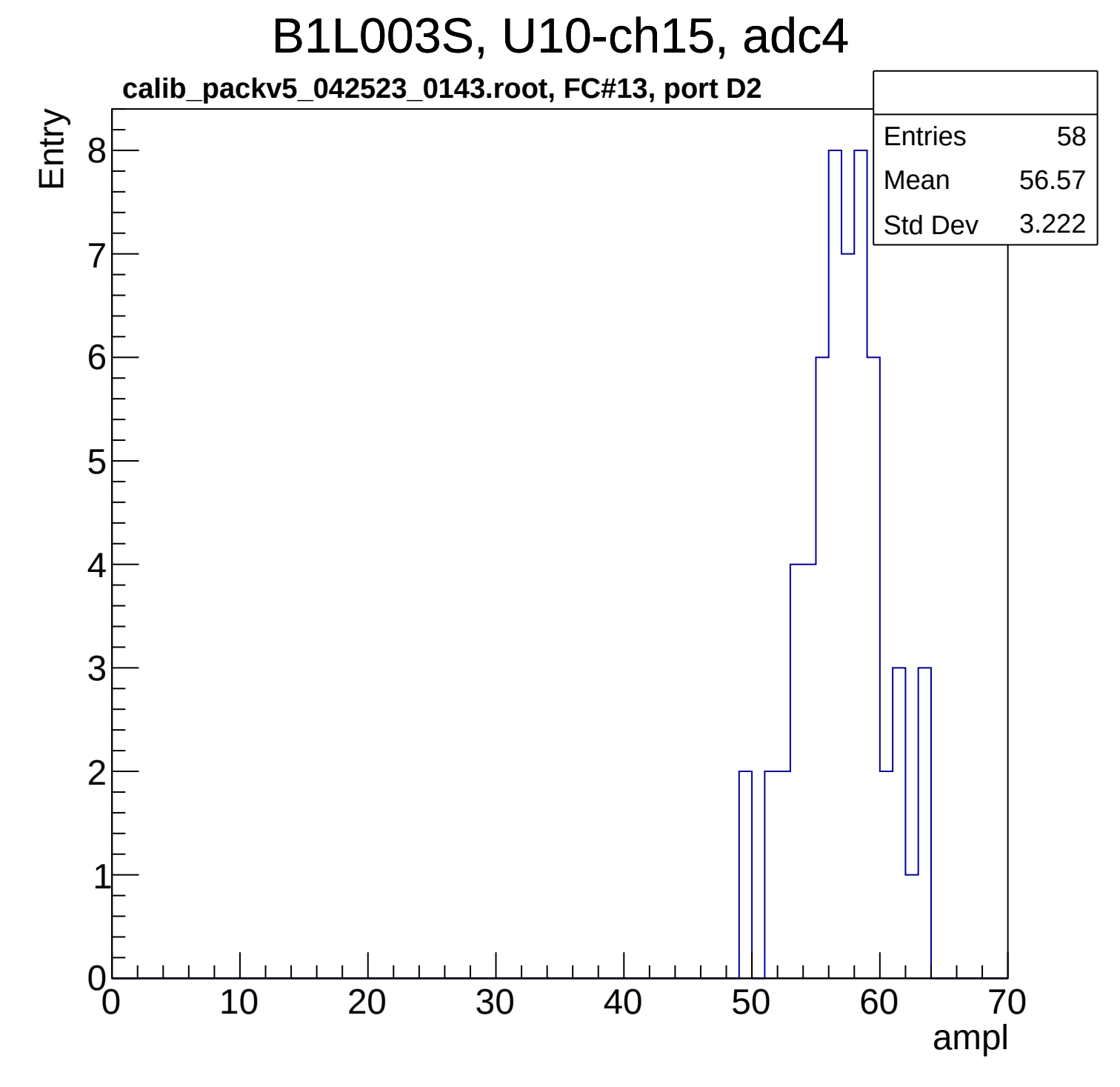
B1L003S, U10-ch15, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 56.57
Std Dev 3.222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
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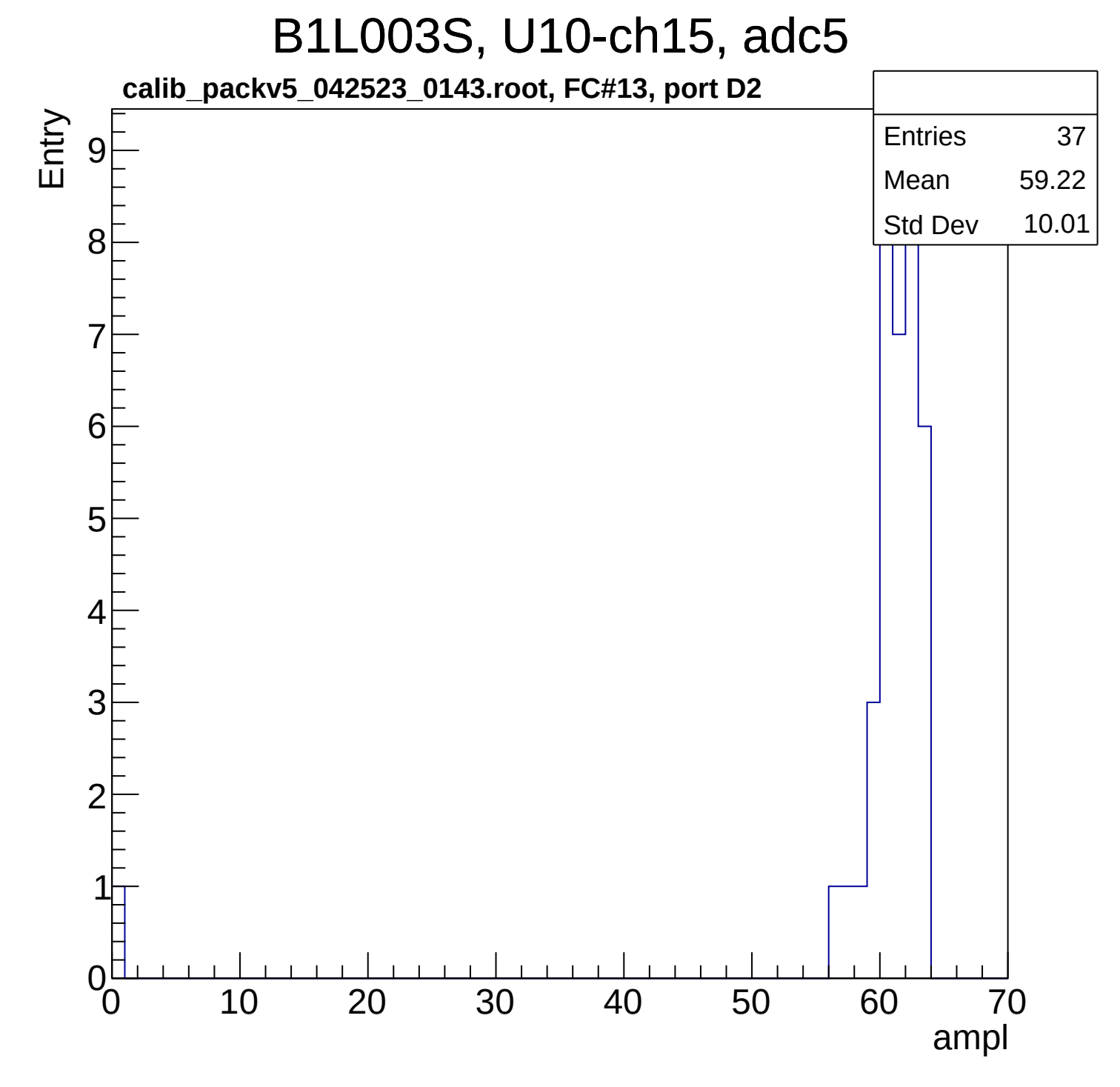
B1L003S, U10-ch15, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 59.22
Std Dev 10.01
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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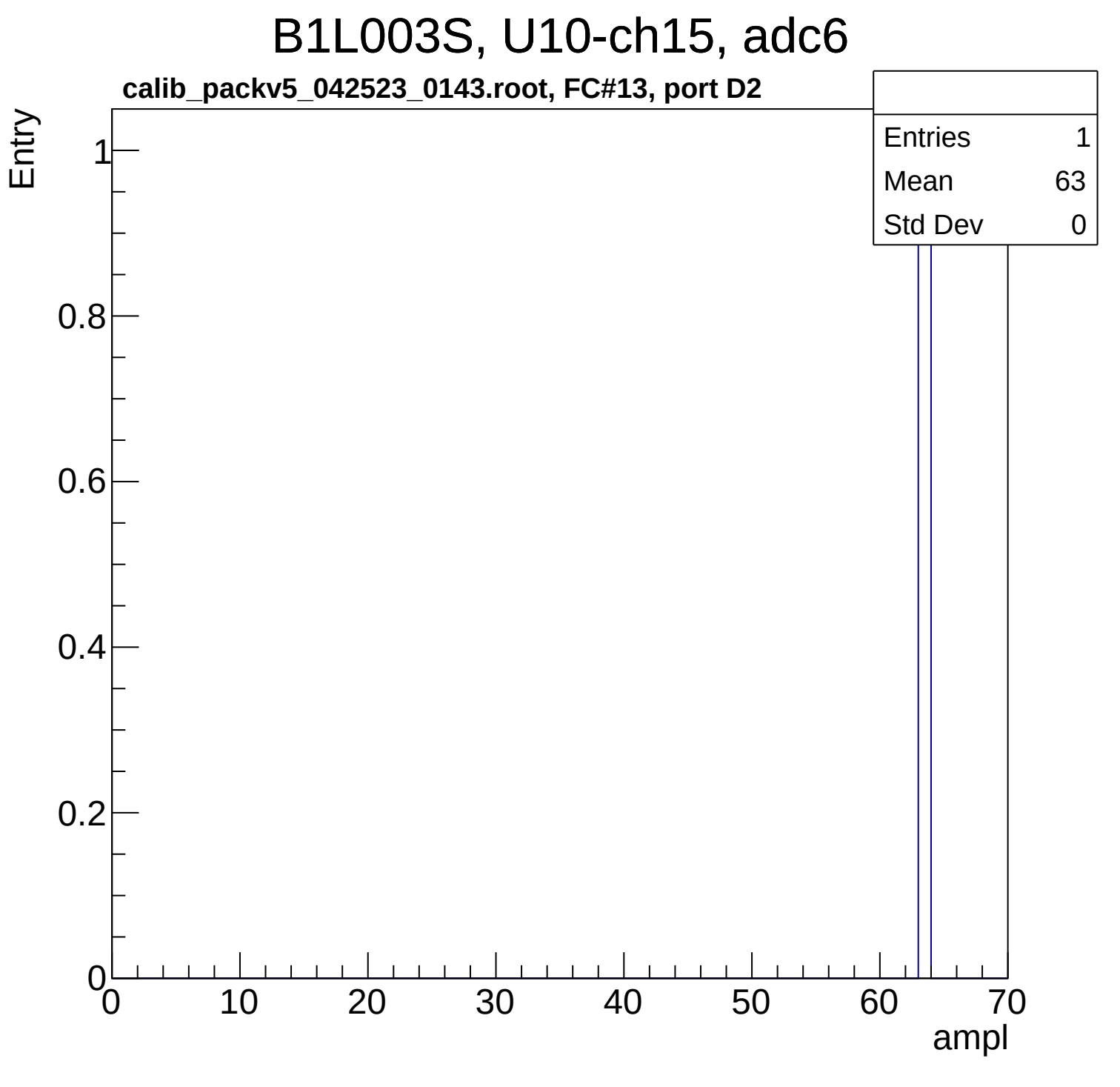
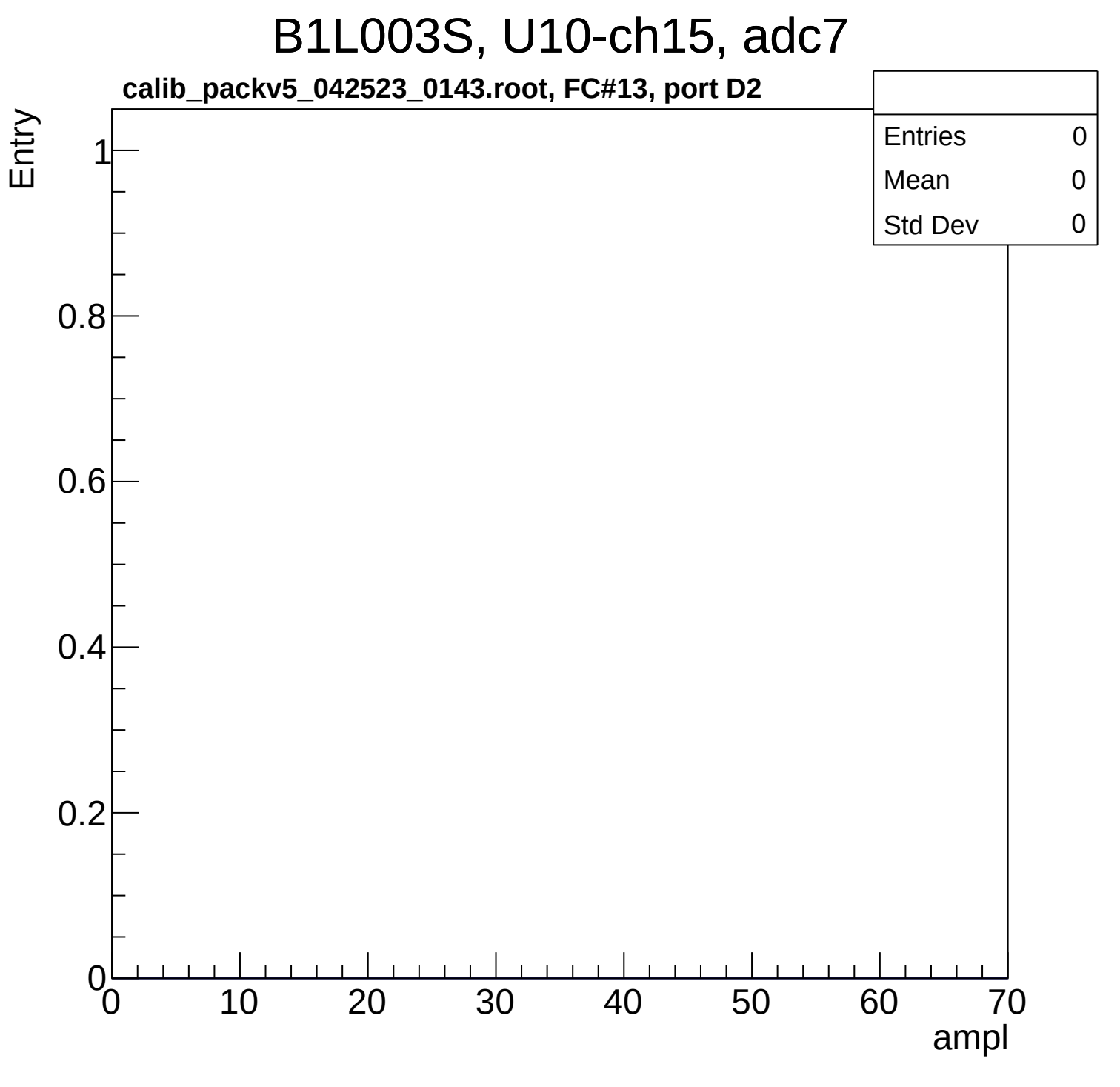

B1L003S, U10-ch15, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch15, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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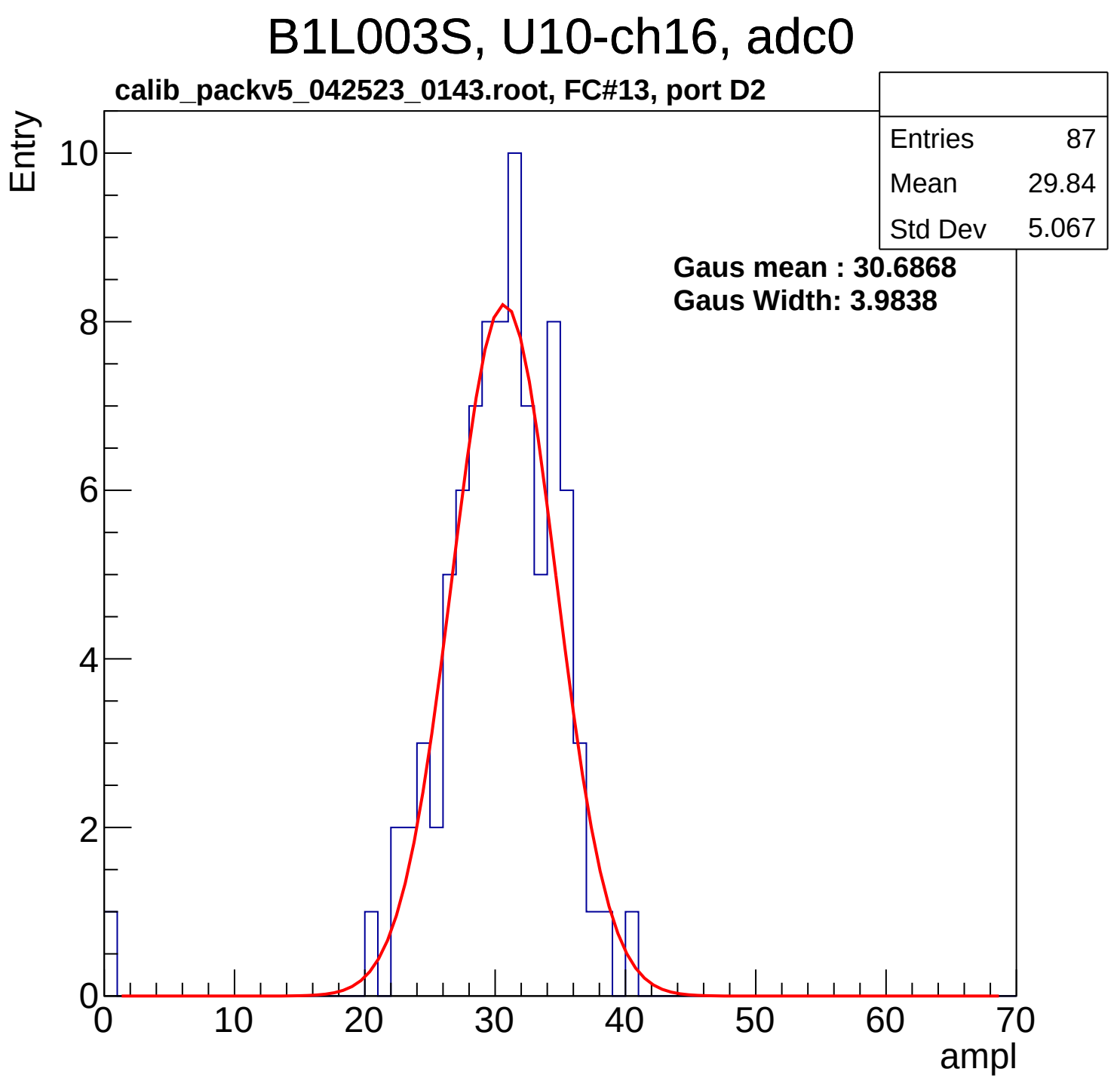
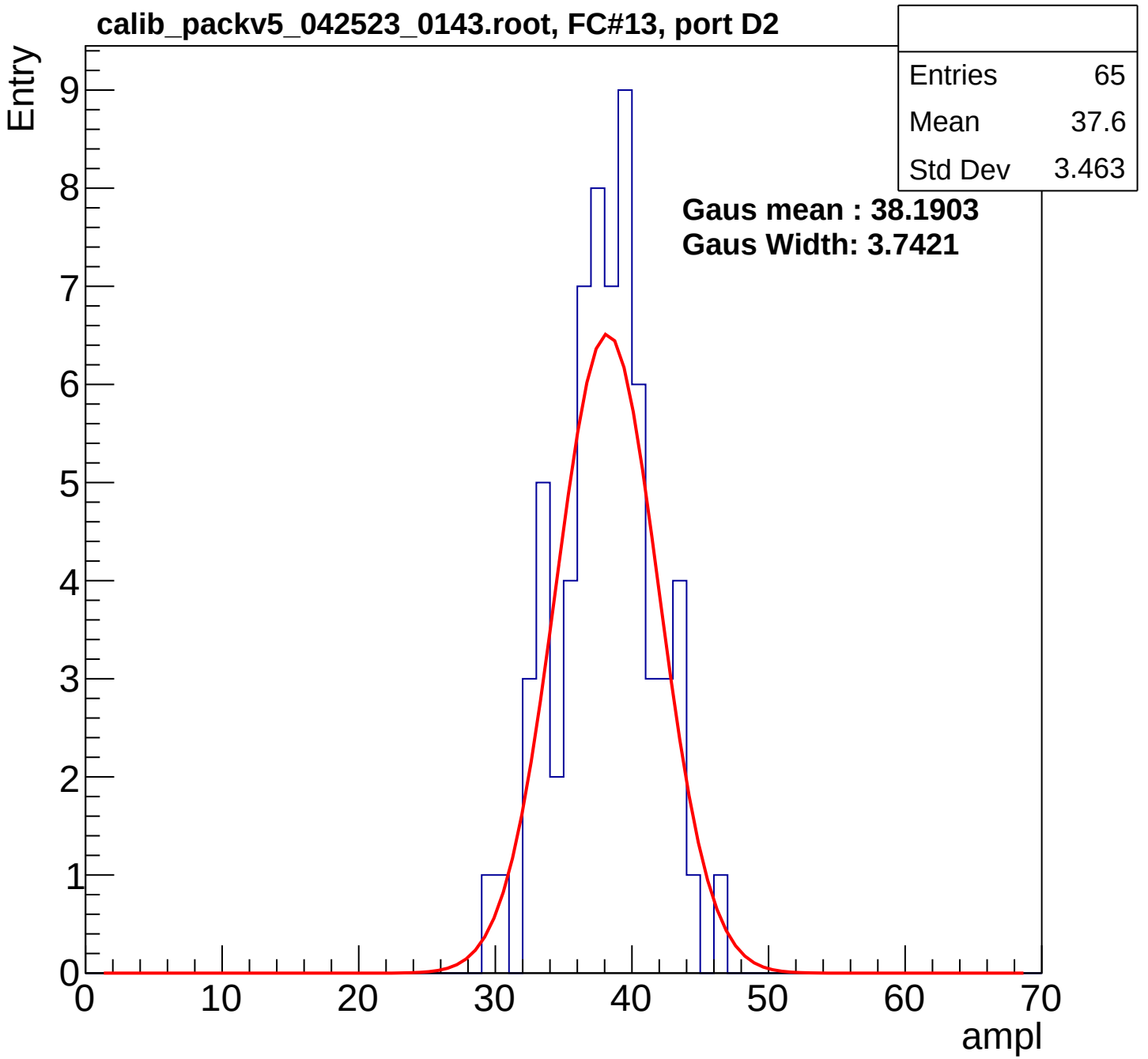
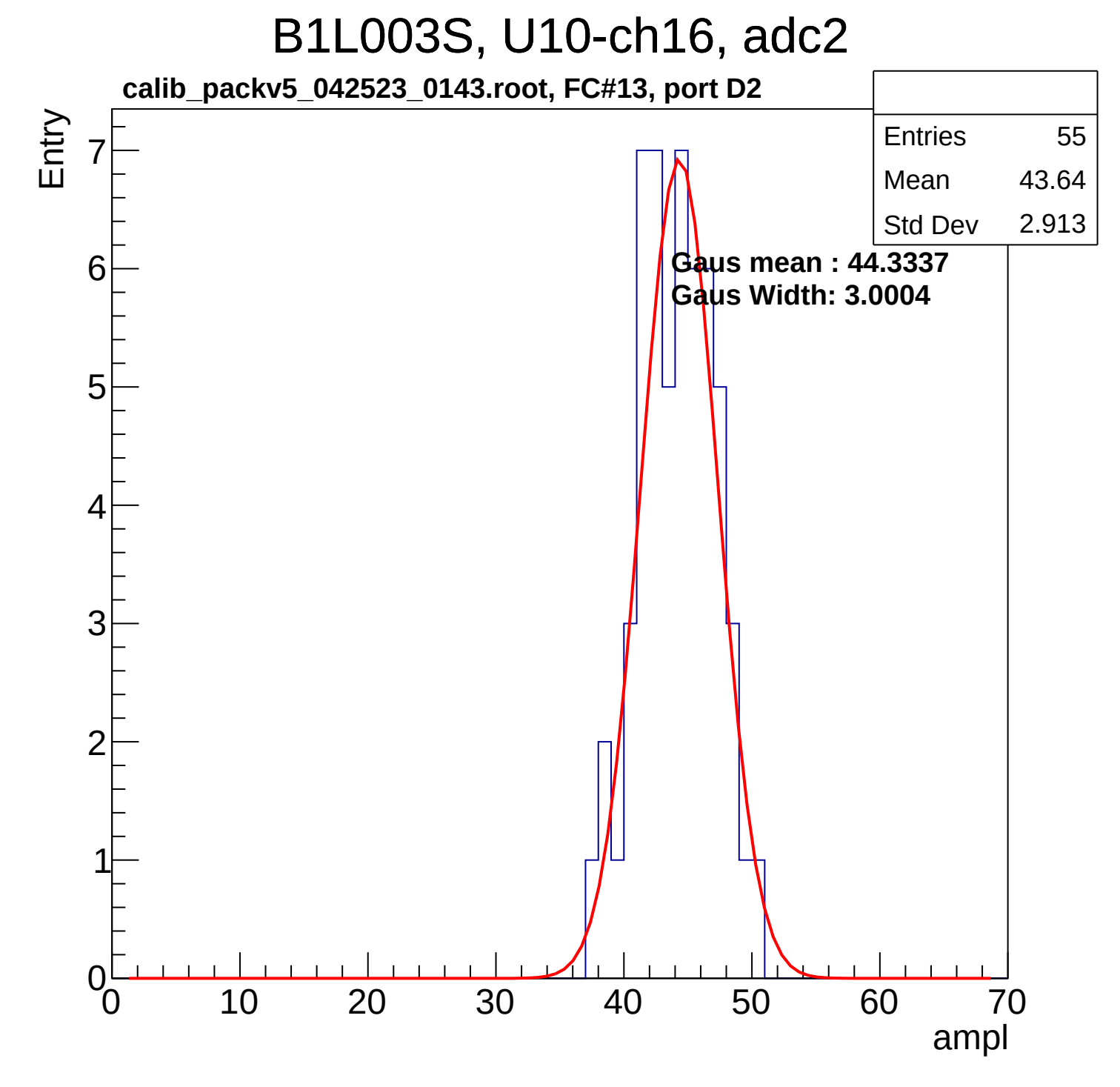
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch16, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 87
Mean 29.84
Std Dev 5.067
Gaus mean : 30.6868
Gaus Width: 3.9838
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch16, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 37.6
Std Dev 3.463
Gaus mean : 38.1903
Gaus Width: 3.7421
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch16, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43.64
Std Dev 2.913
Gaus mean : 44.3337
Gaus Width: 3.0004
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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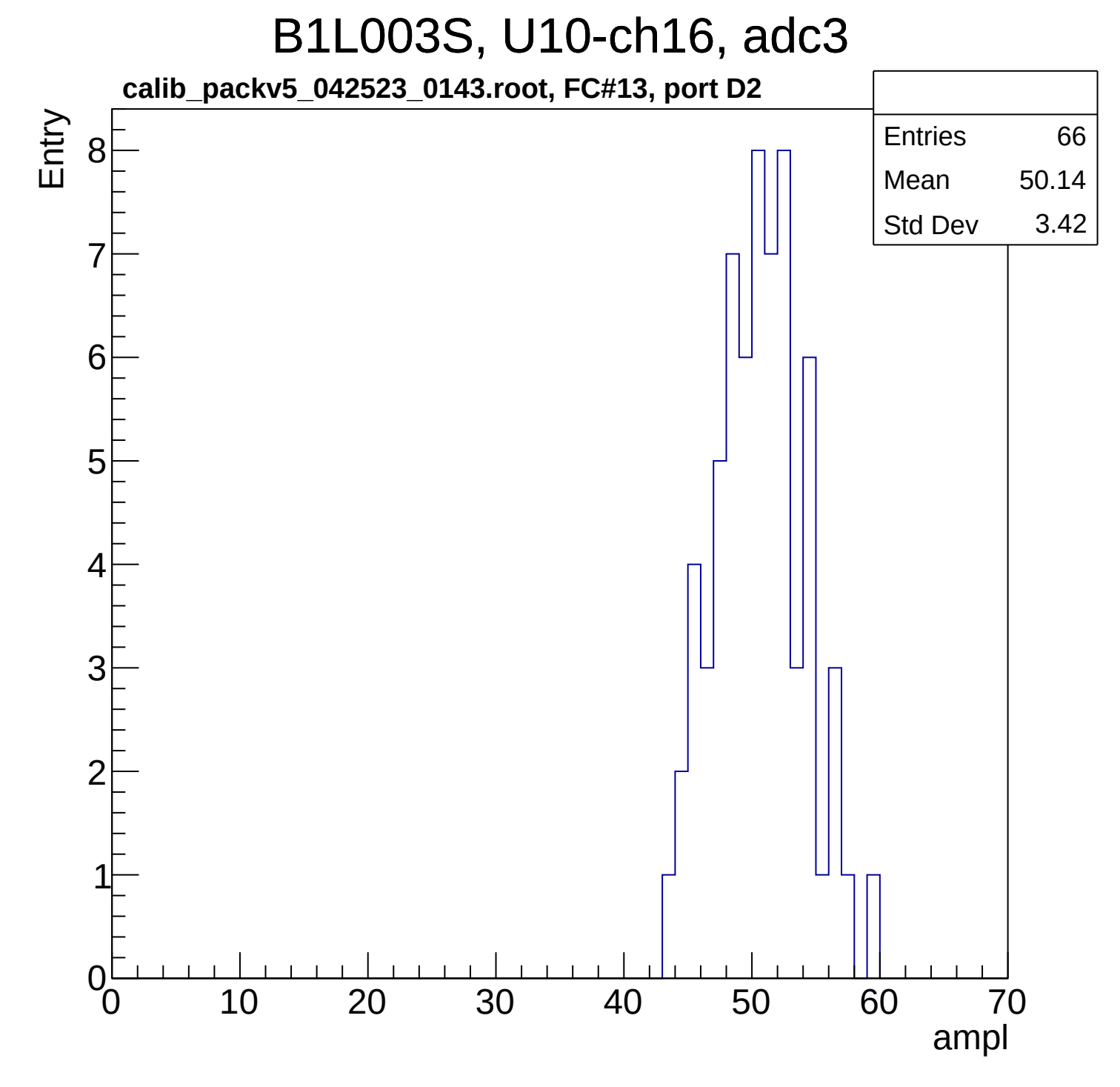
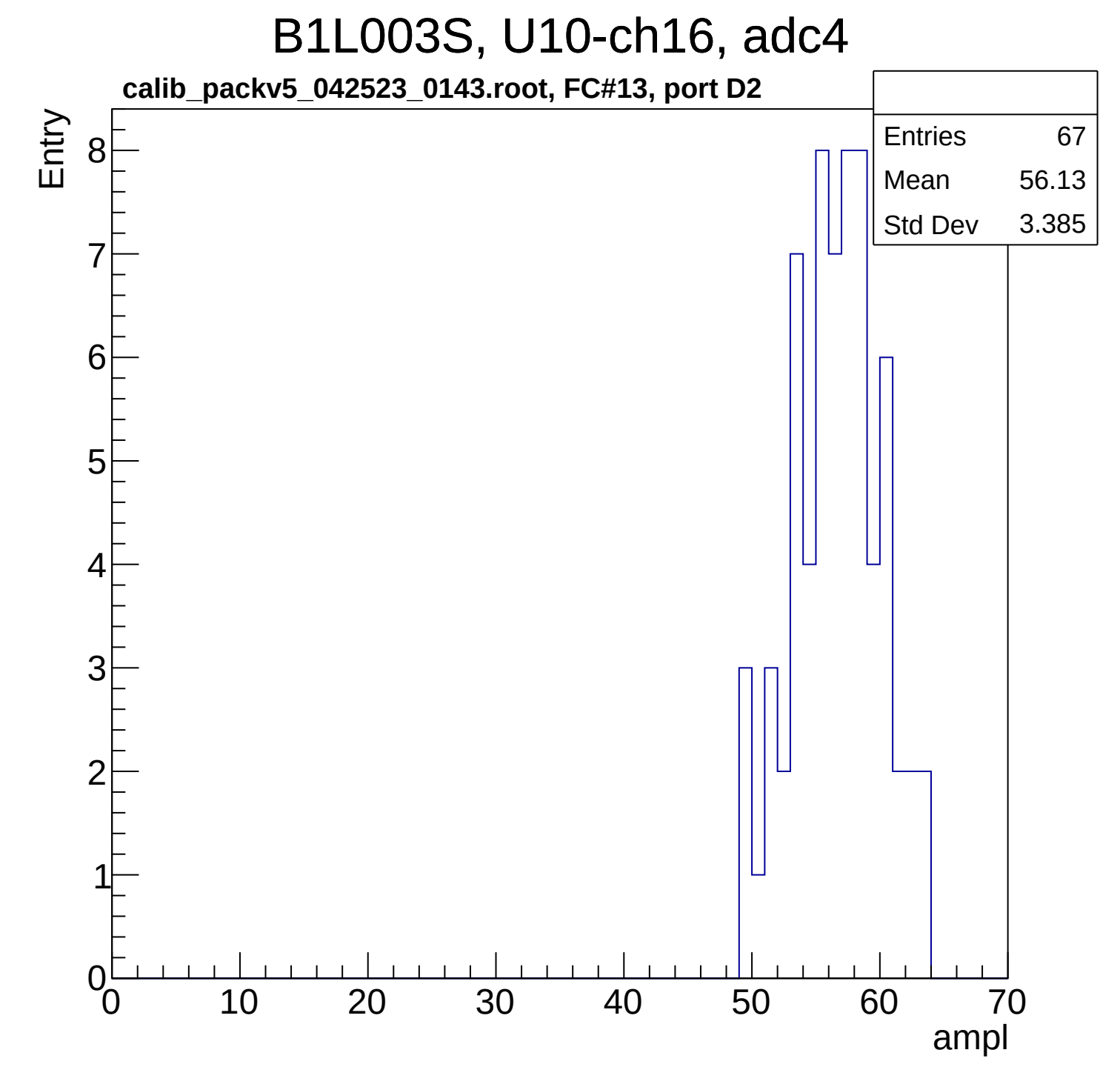
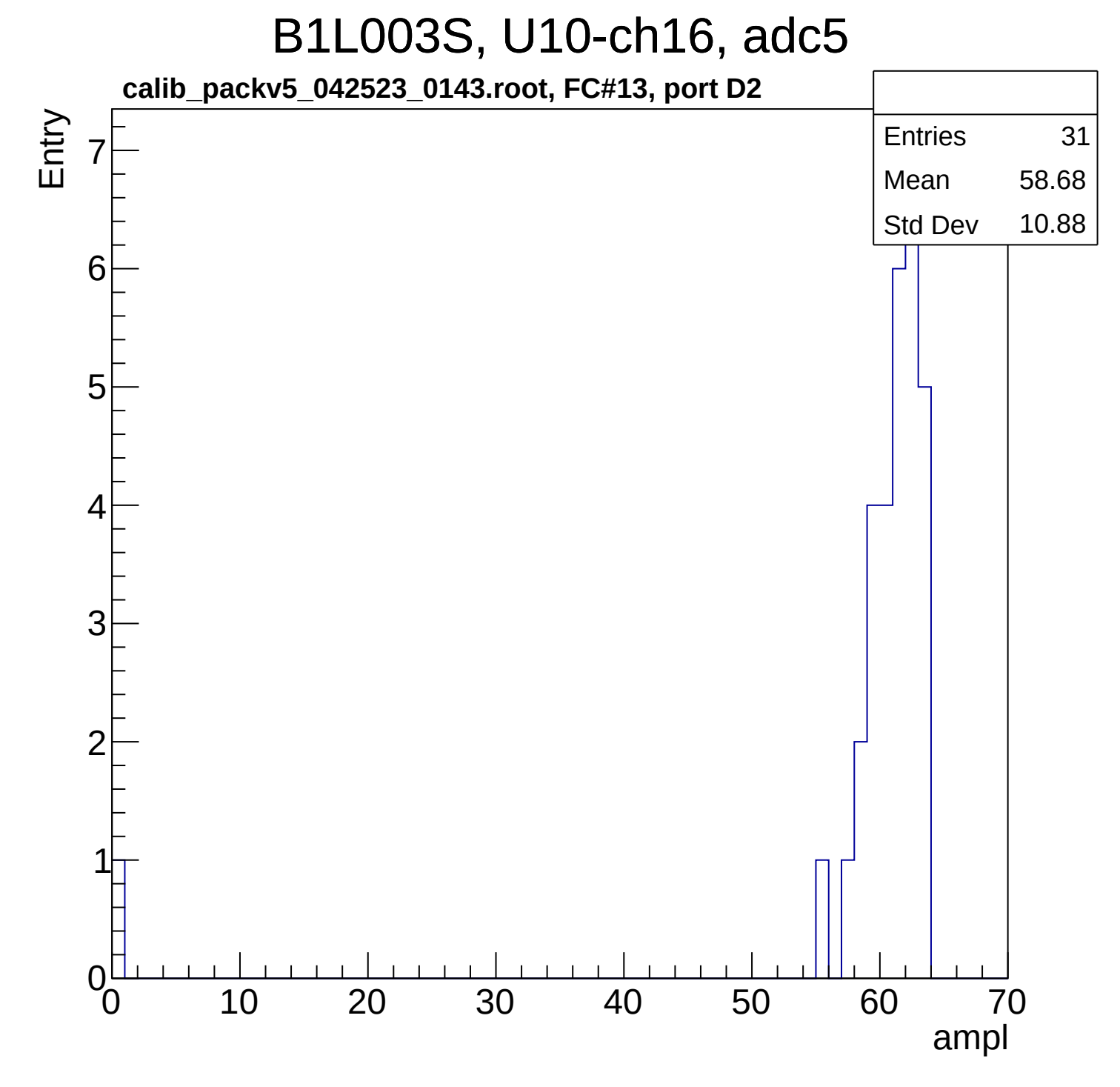
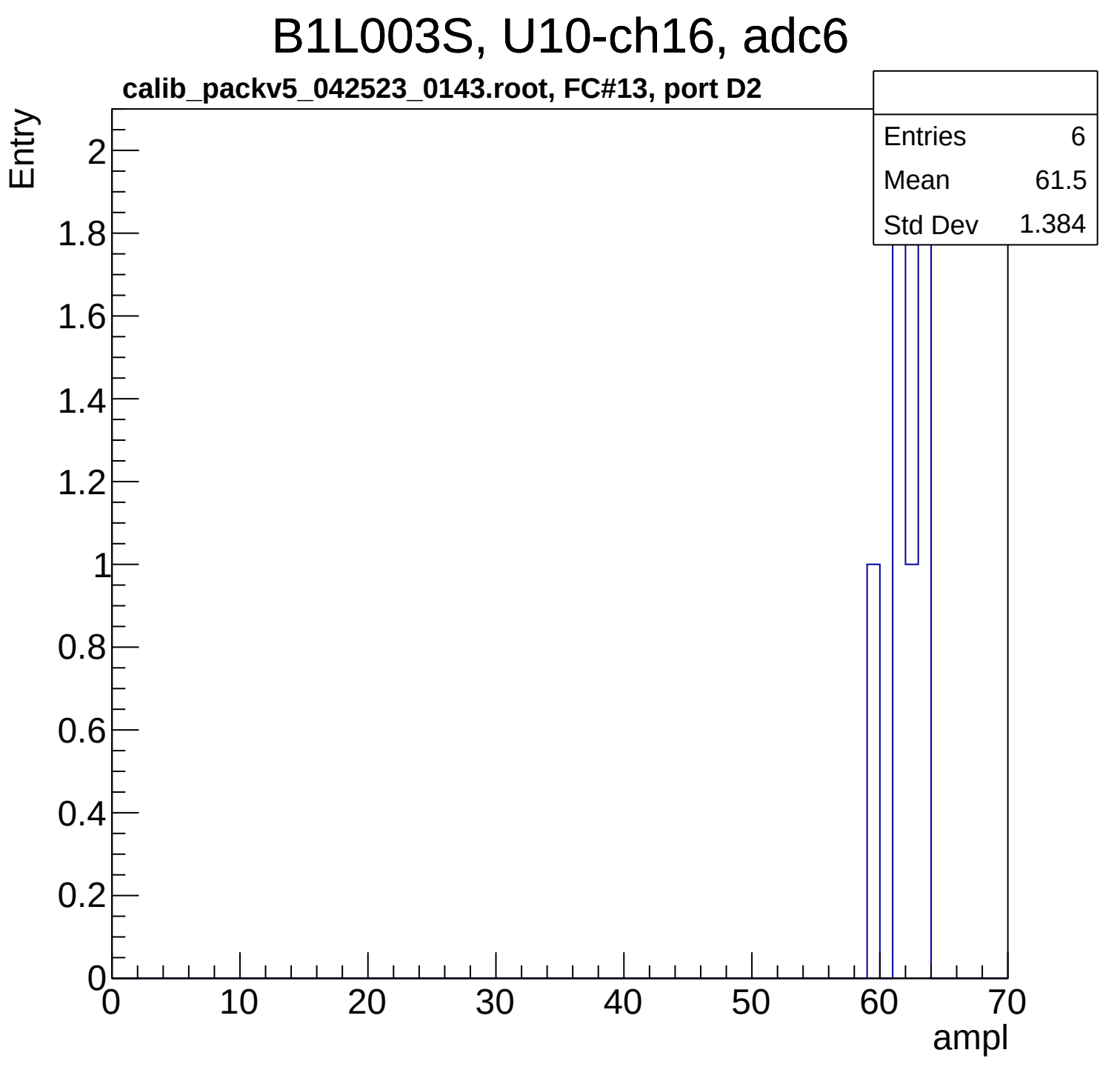

B1L003S, U10-ch16, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 50.14
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch16, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 56.13
Std Dev 3.385
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch16, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 31
Mean 58.68
Std Dev 10.88
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch16, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.5
Std Dev 1.384
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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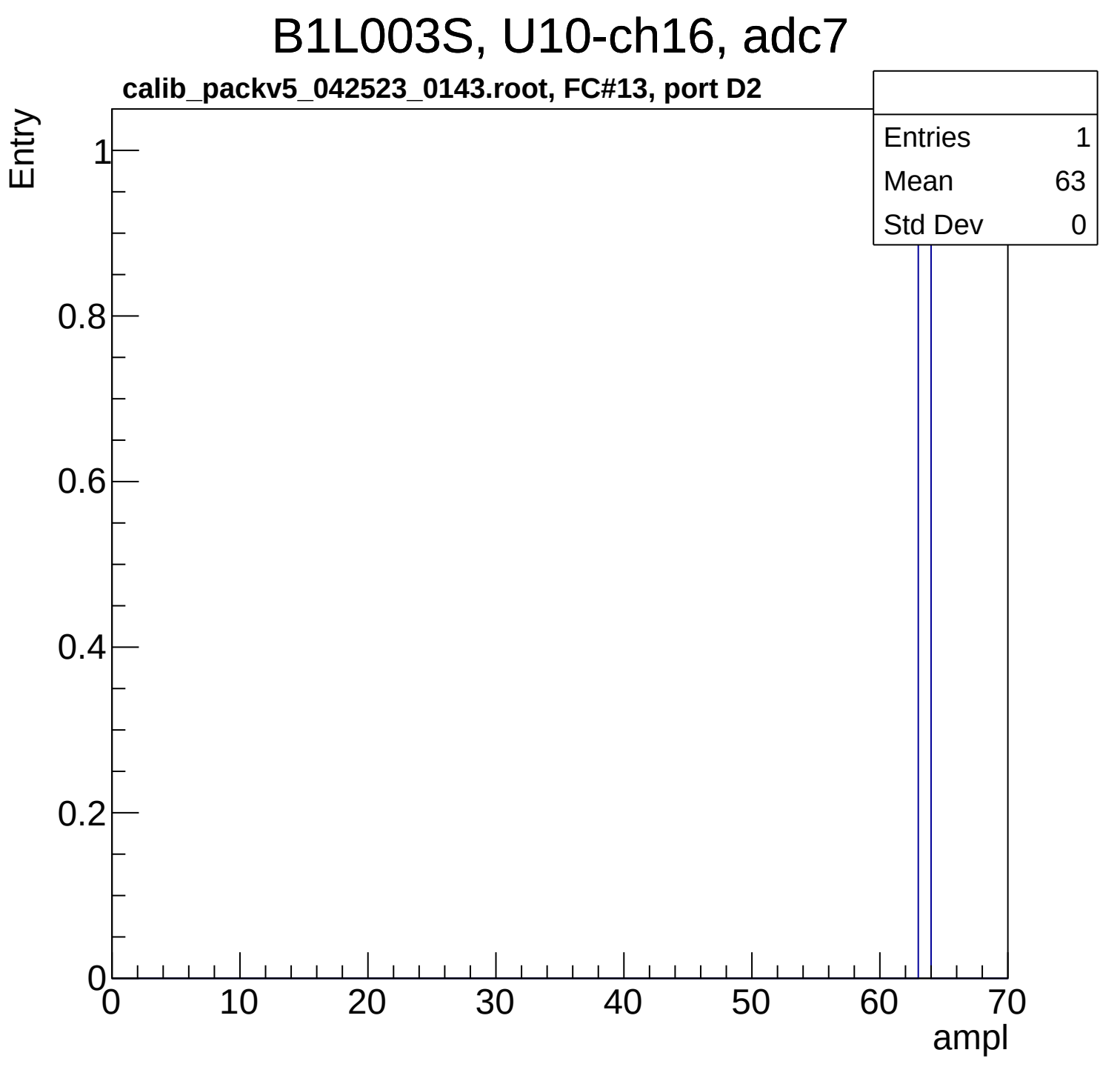
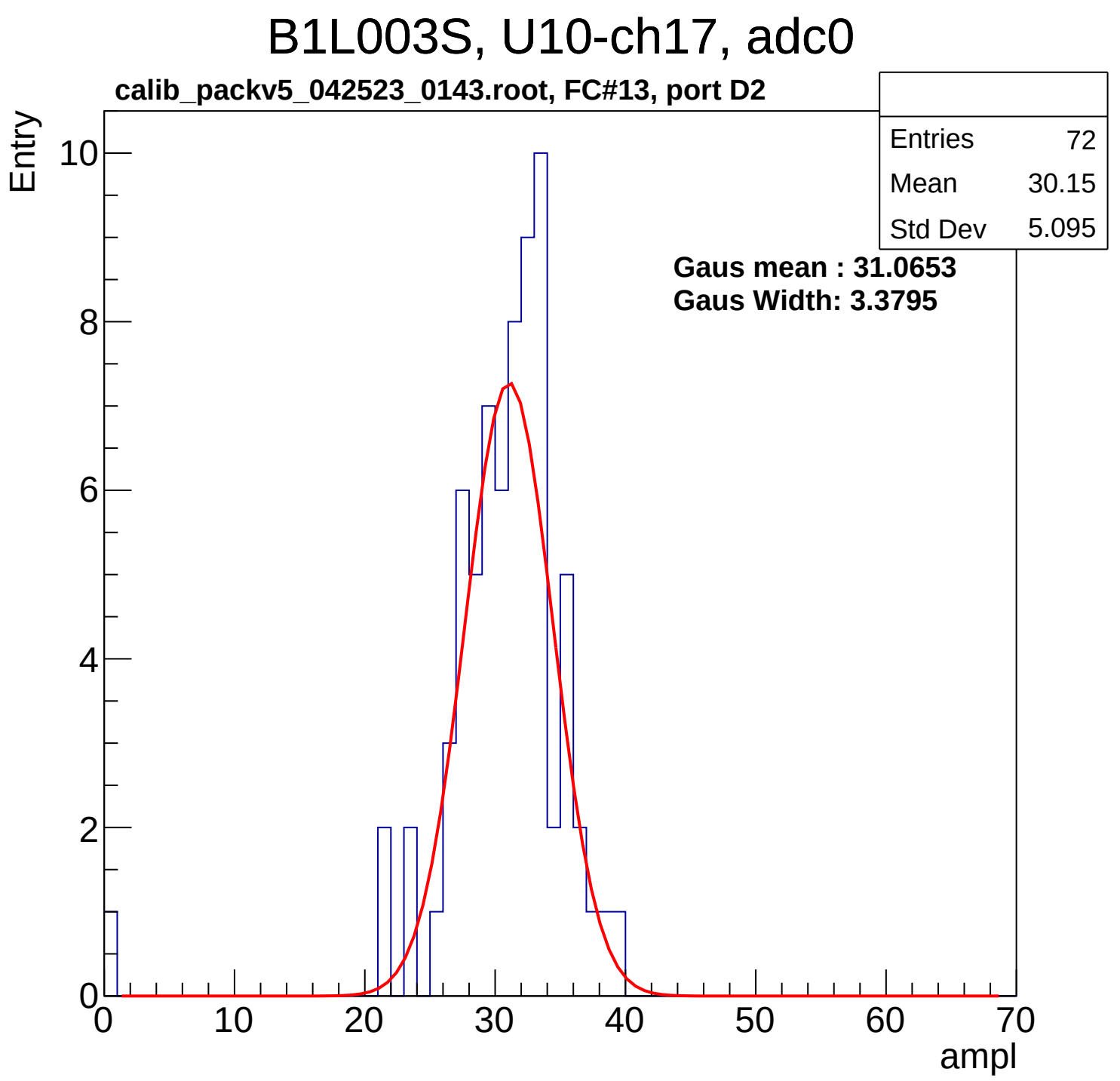
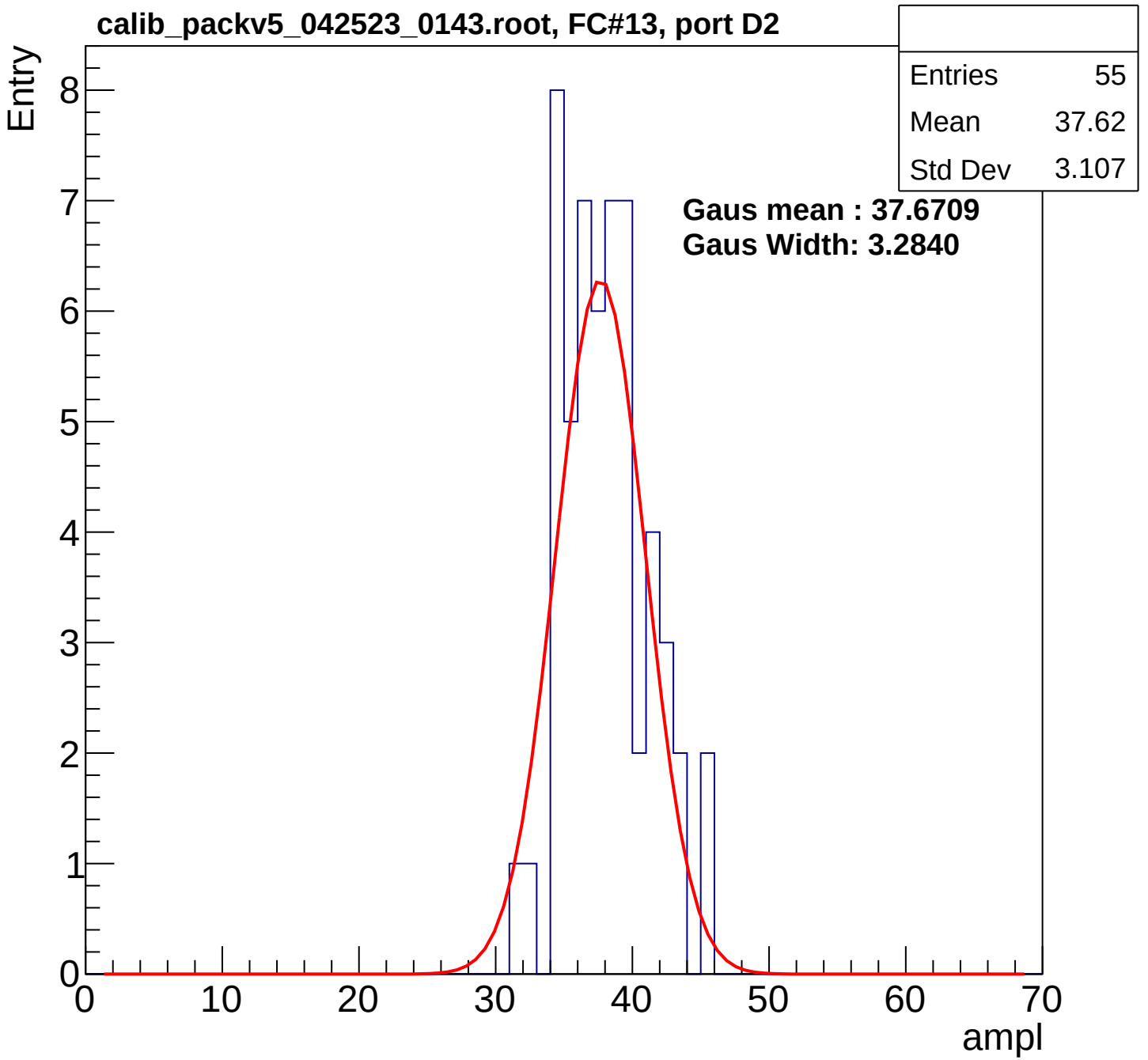

B1L003S, U10-ch16, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch17, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 30.15
Std Dev 5.095
Gaus mean : 31.0653
Gaus Width: 3.3795
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch17, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 37.62
Std Dev 3.107
Gaus mean : 37.6709
Gaus Width: 3.2840
Entry
ampl
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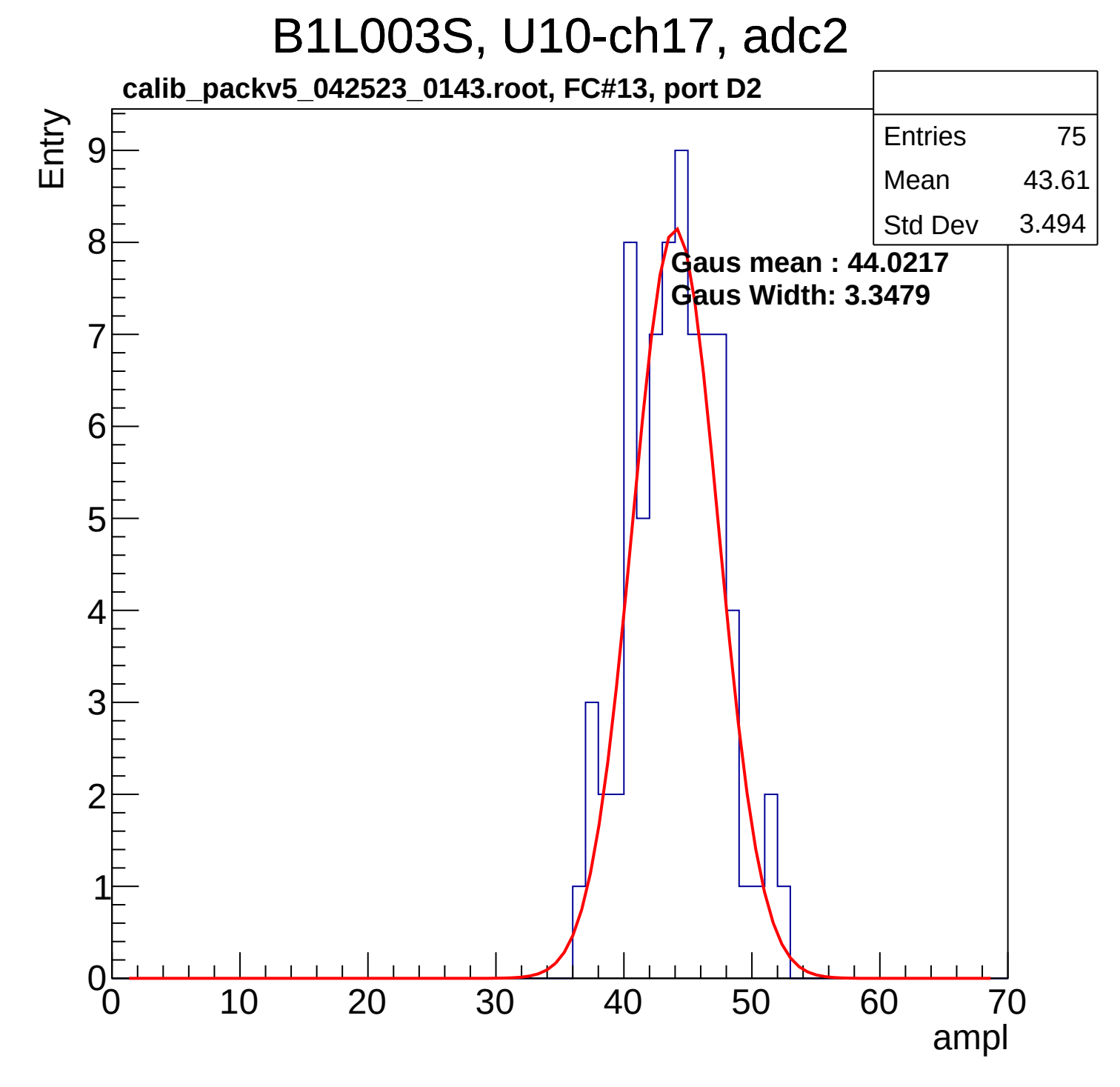
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch17, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 43.61
Std Dev 3.494
Gaus mean : 44.0217
Gaus Width: 3.3479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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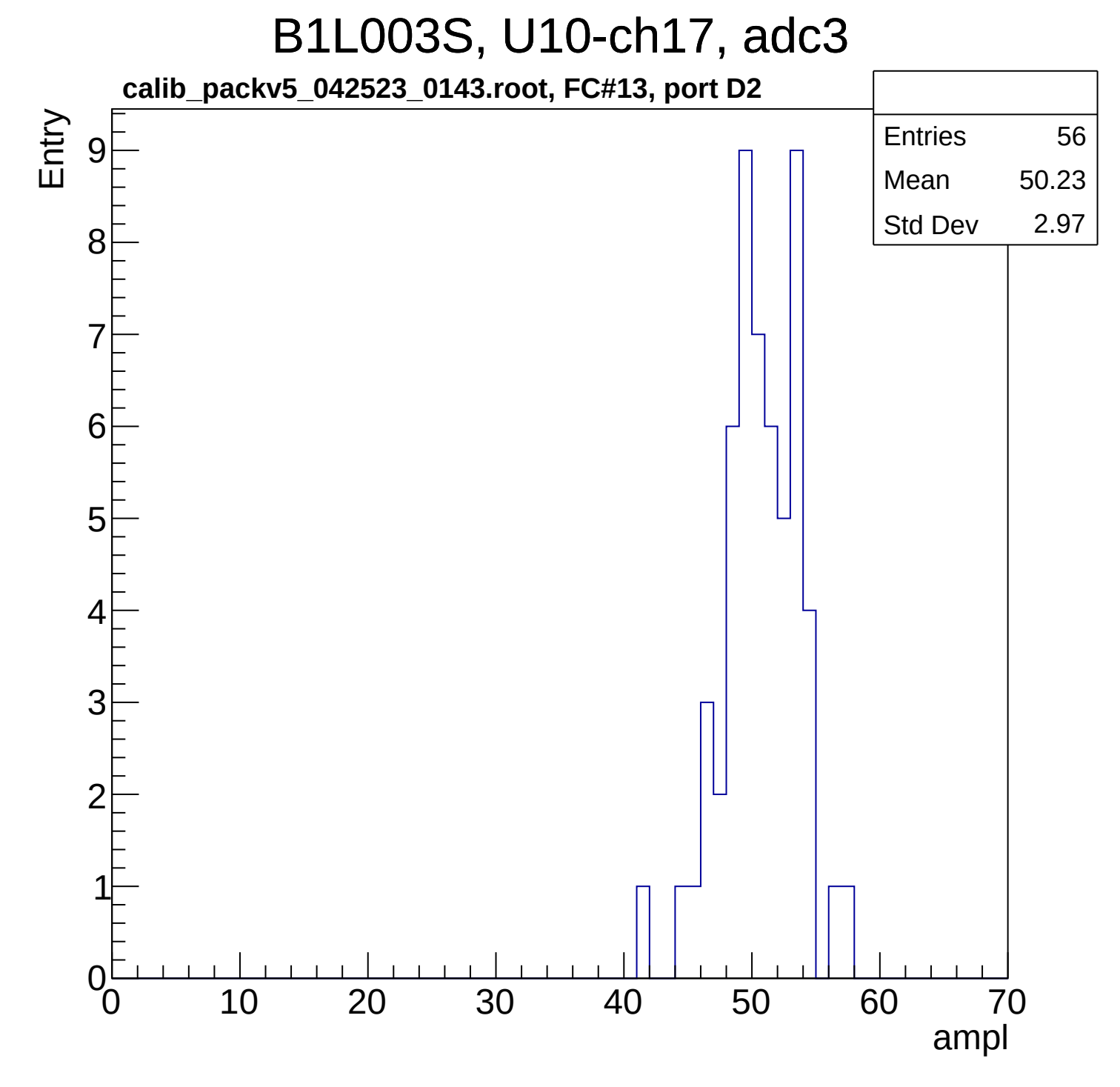
B1L003S, U10-ch17, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 50.23
Std Dev 2.97
Entry
ampl
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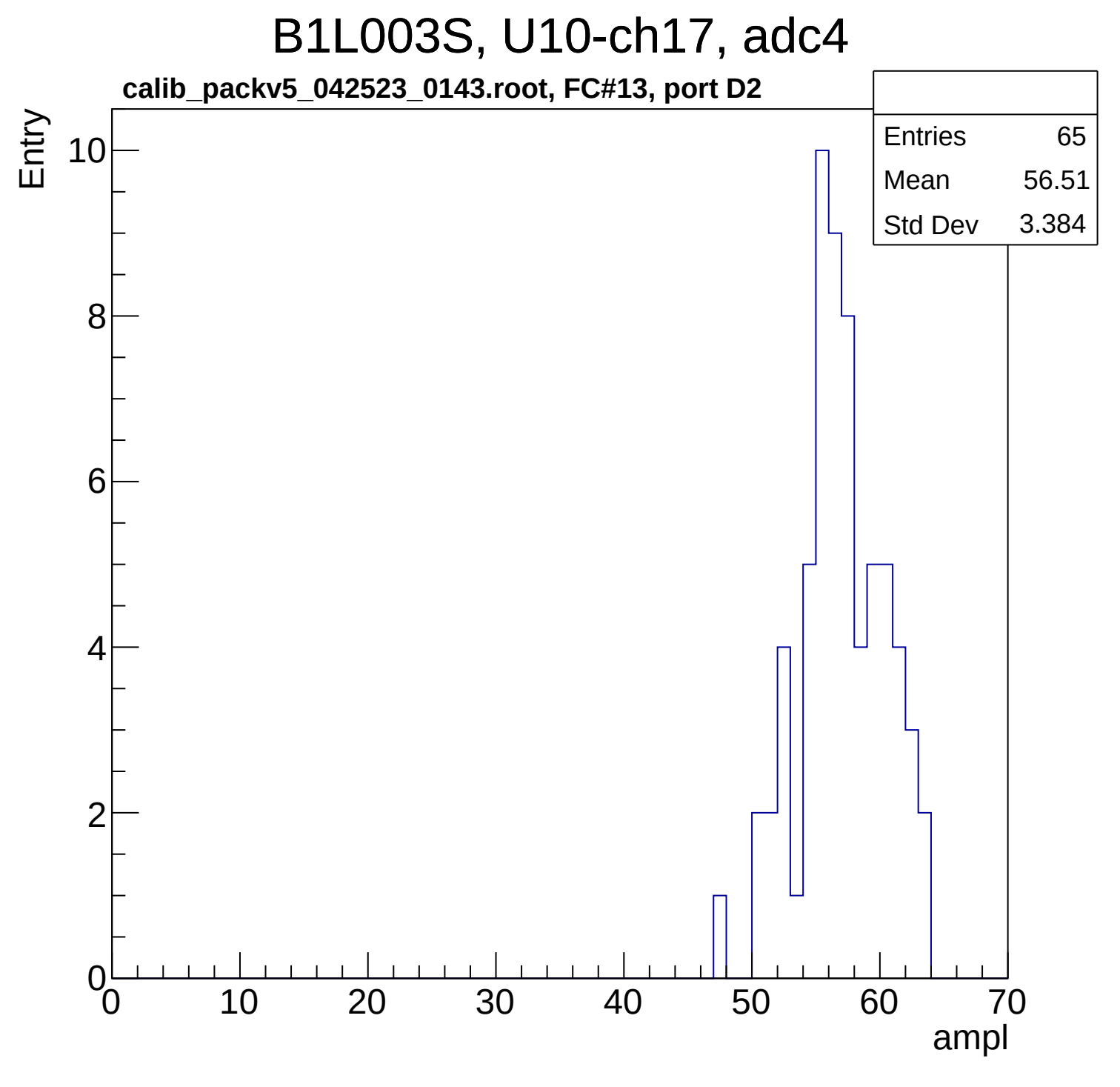
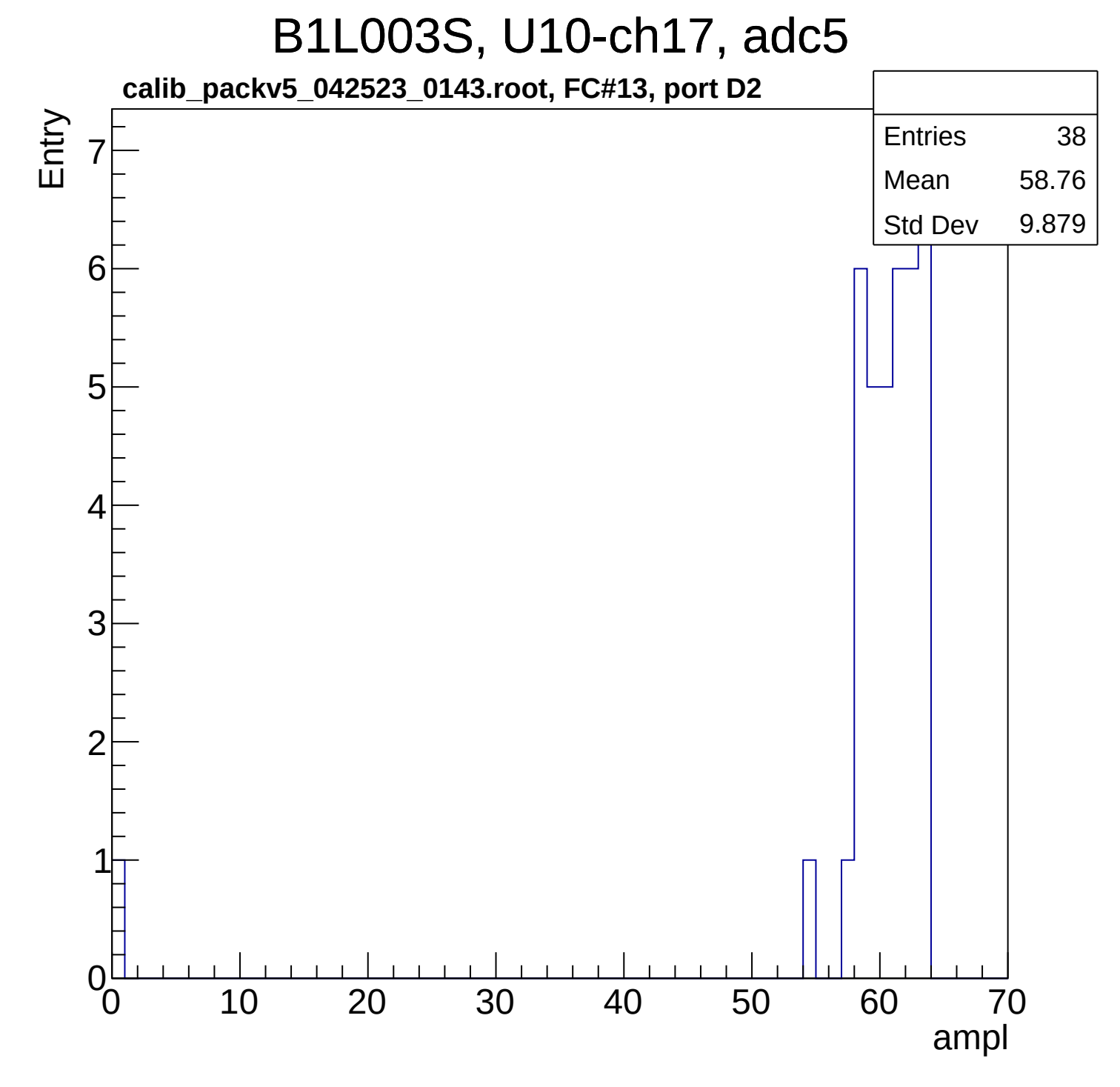
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch17, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 56.51
Std Dev 3.384
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch17, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 58.76
Std Dev 9.879
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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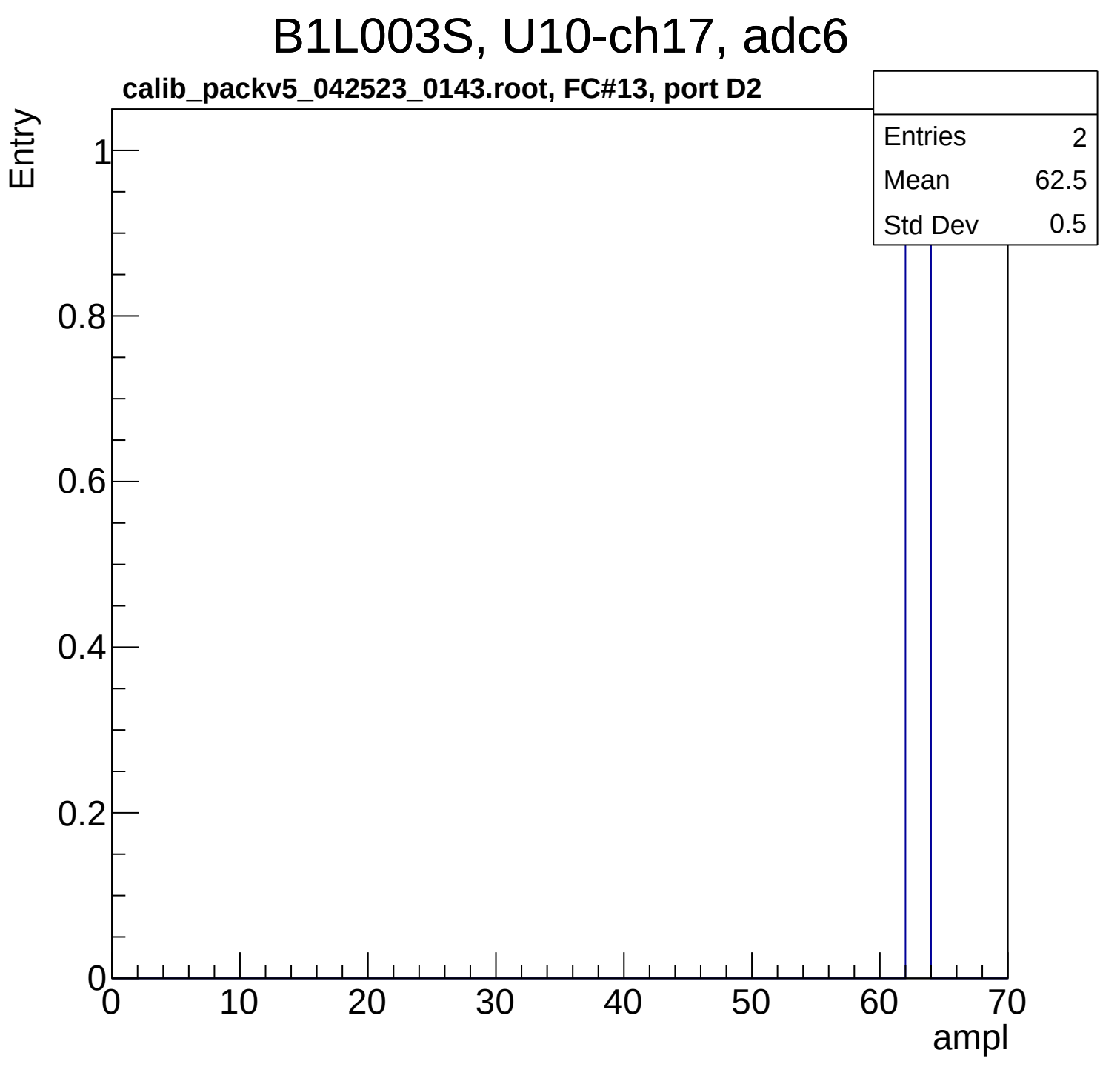
B1L003S, U10-ch17, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
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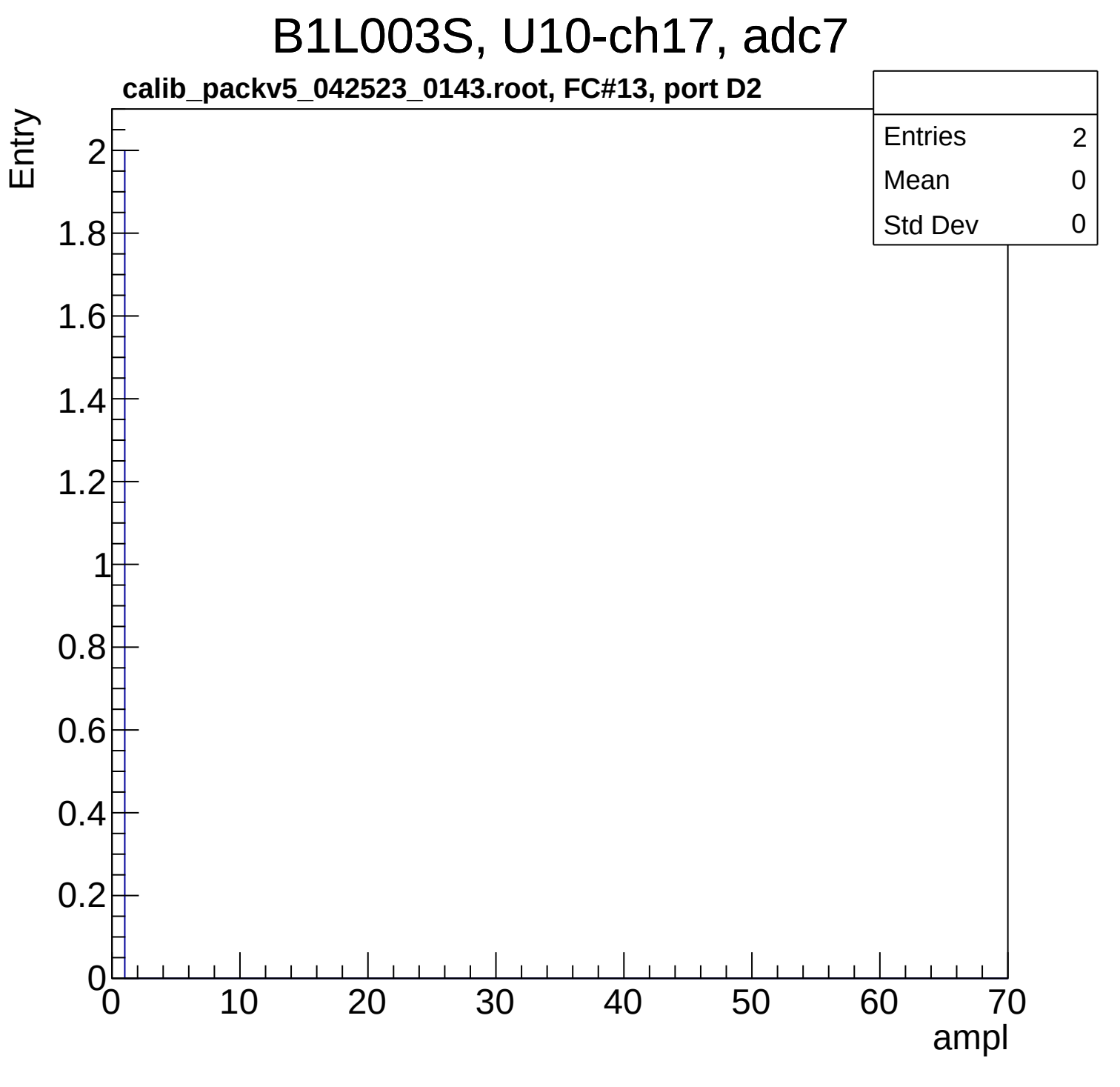
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch17, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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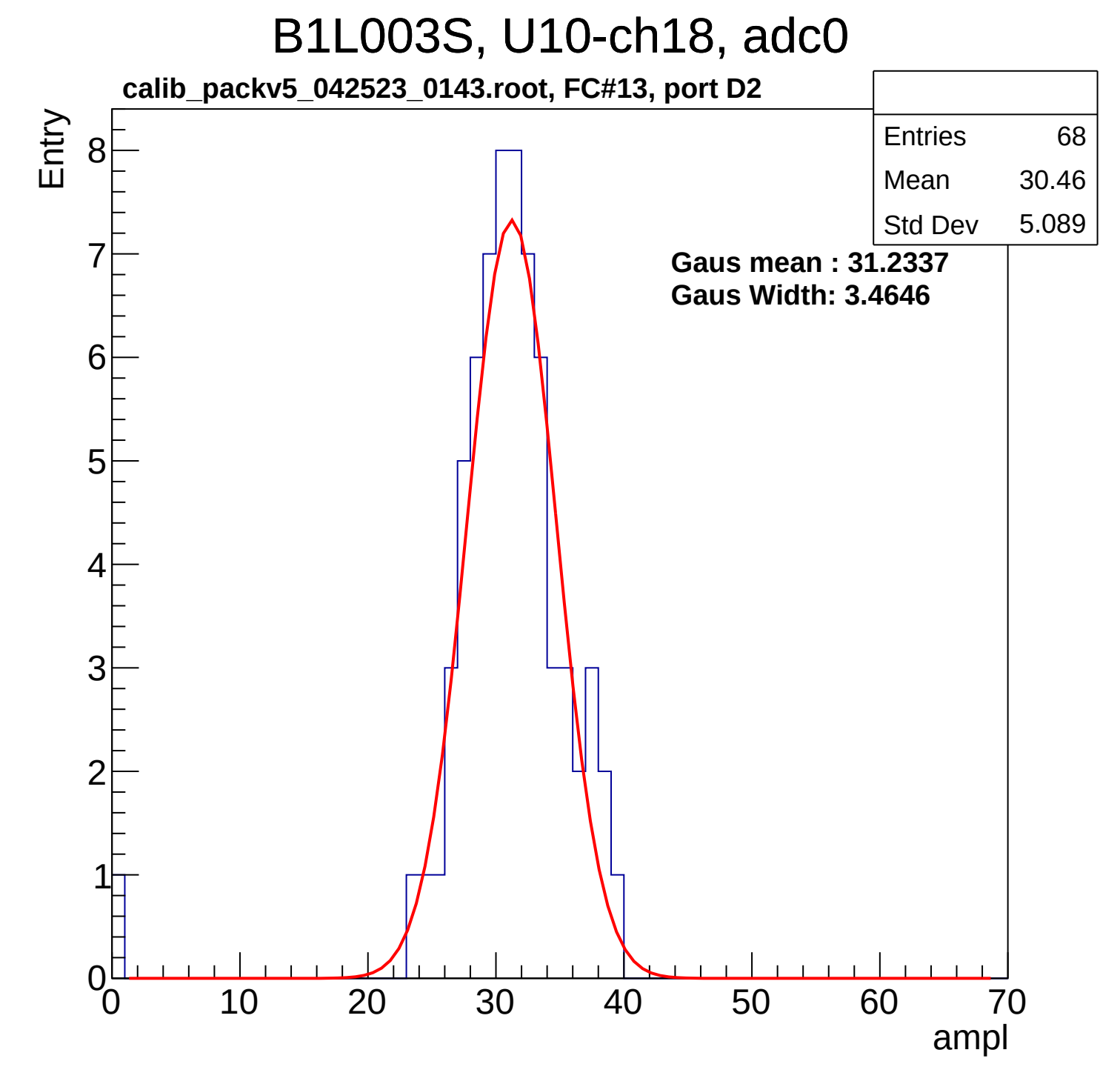
B1L003S, U10-ch18, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.46
Std Dev 5.089
Gaus mean : 31.2337
Gaus Width: 3.4646
Entry
ampl
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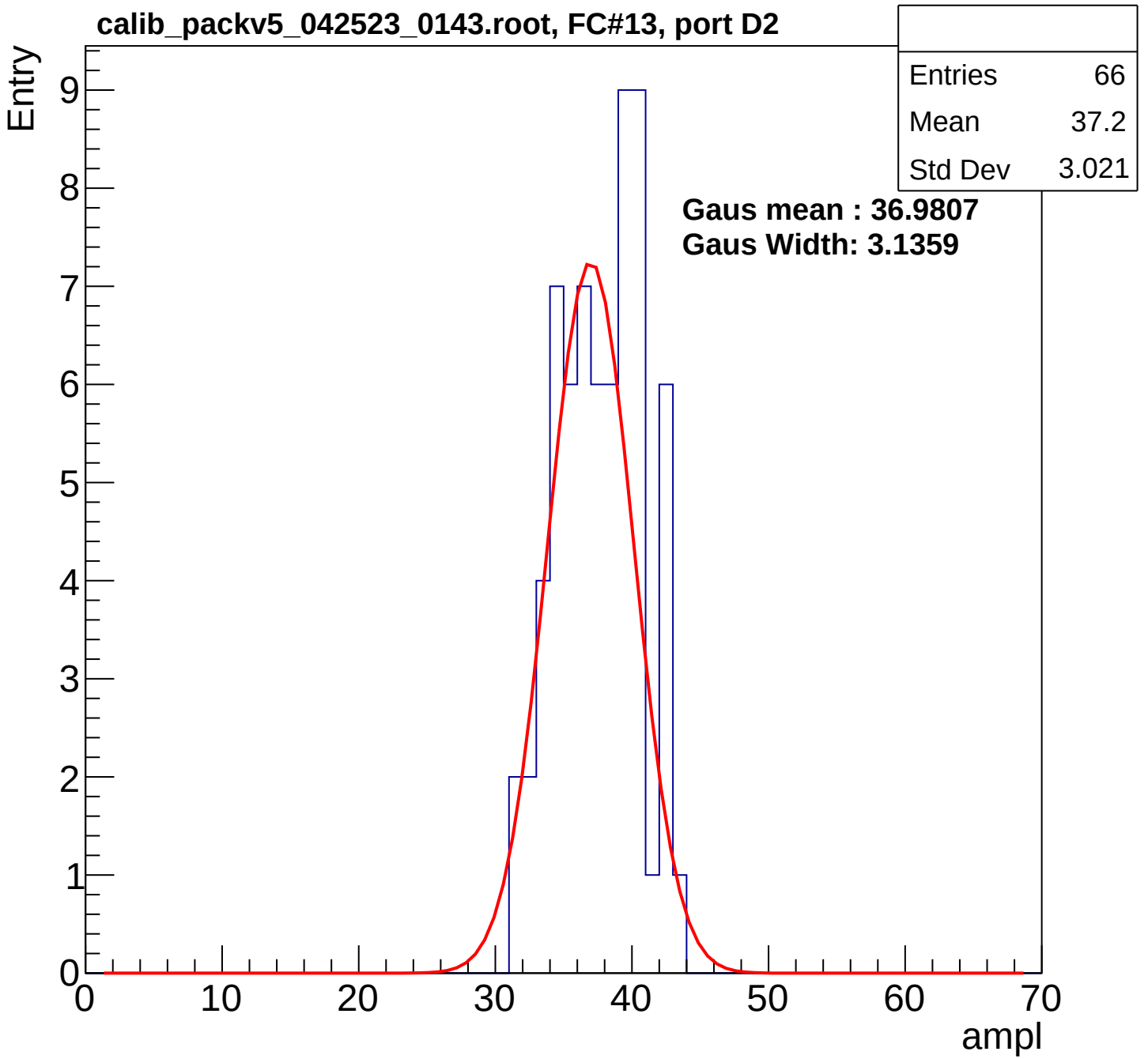
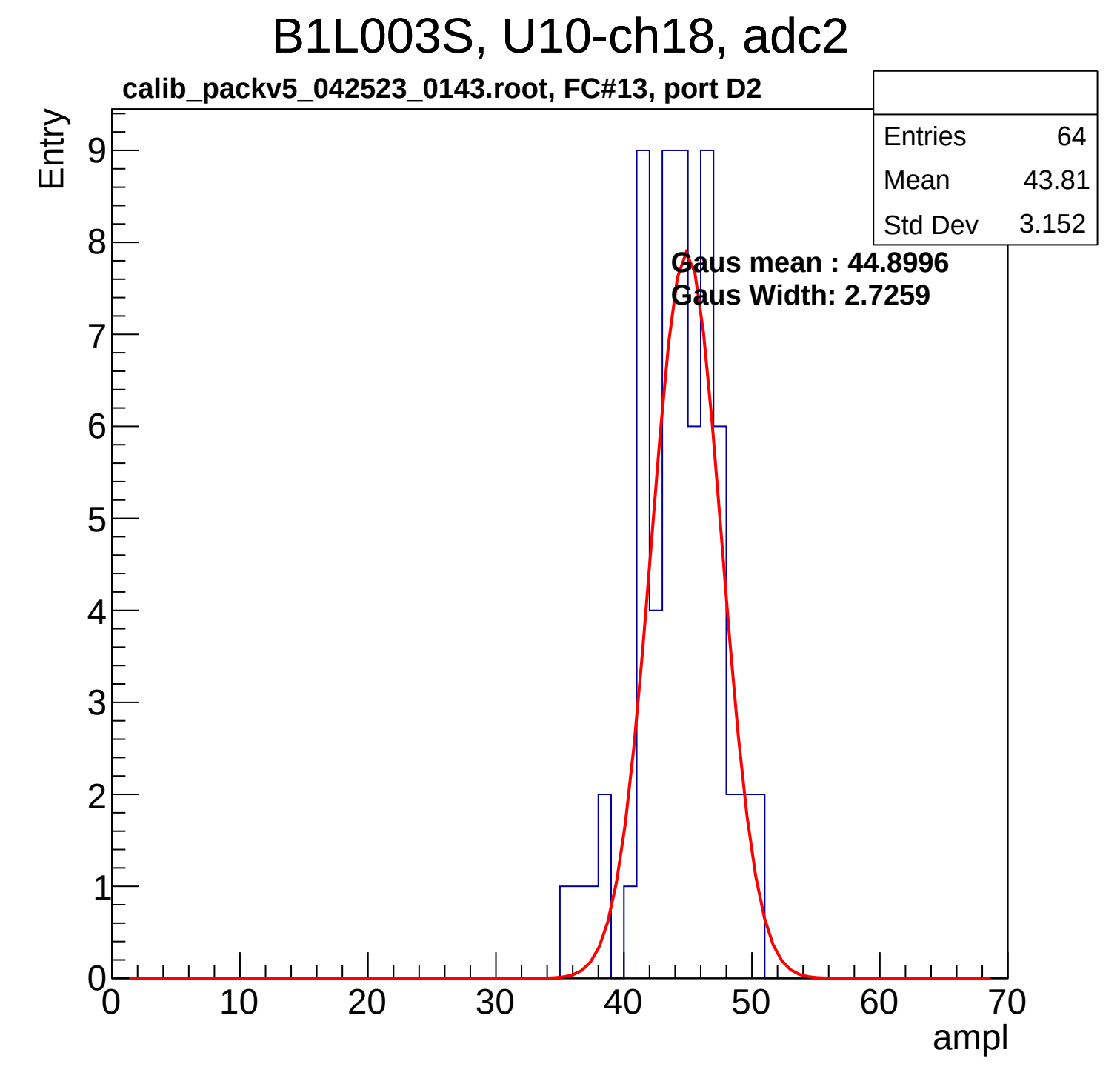
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch18, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 37.2
Std Dev 3.021
Gaus mean : 36.9807
Gaus Width: 3.1359
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch18, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.81
Std Dev 3.152
Gaus mean : 44.8996
Gaus Width: 2.7259
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
50
60
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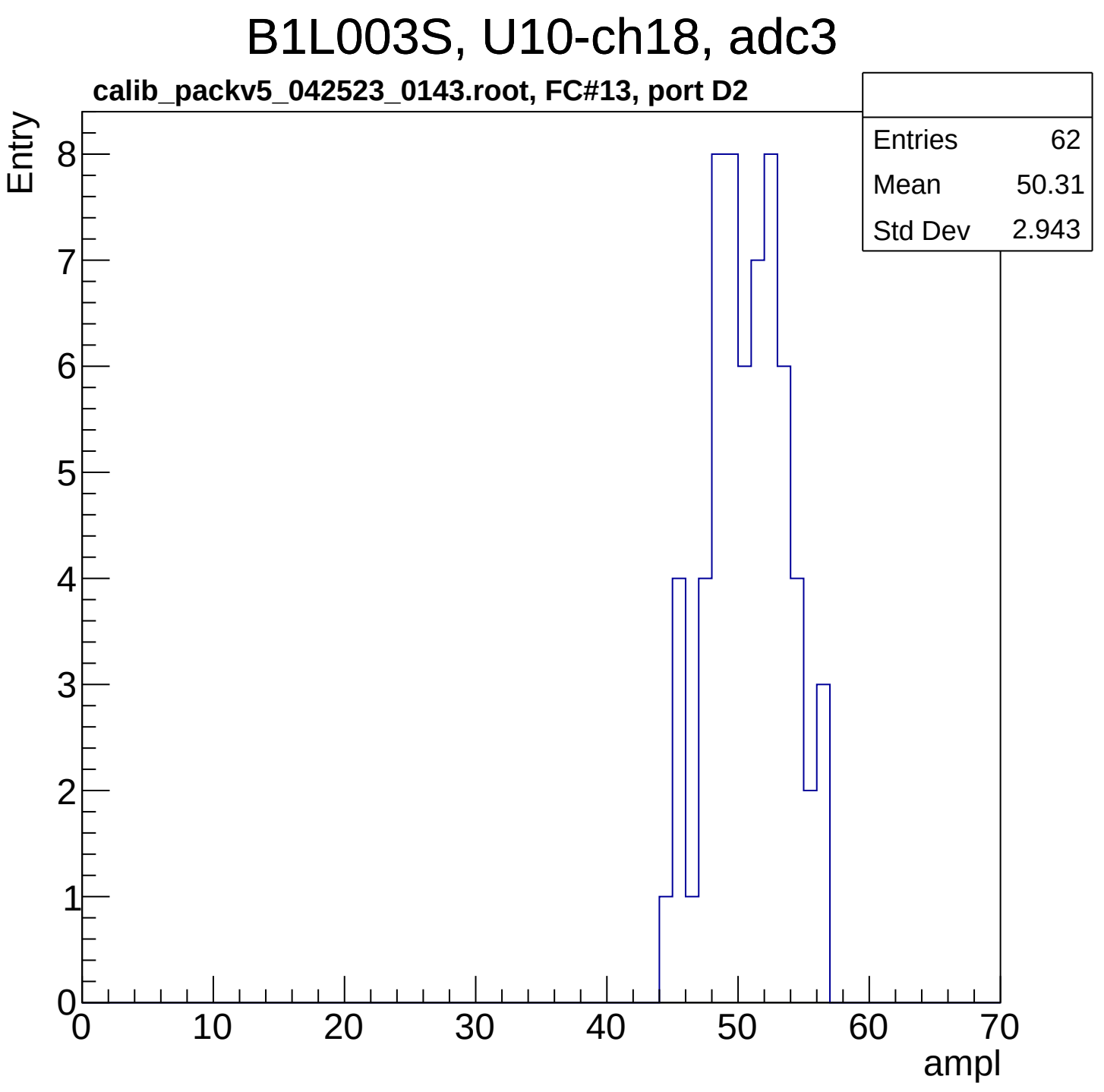
B1L003S, U10-ch18, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 50.31
Std Dev 2.943
Entry
ampl
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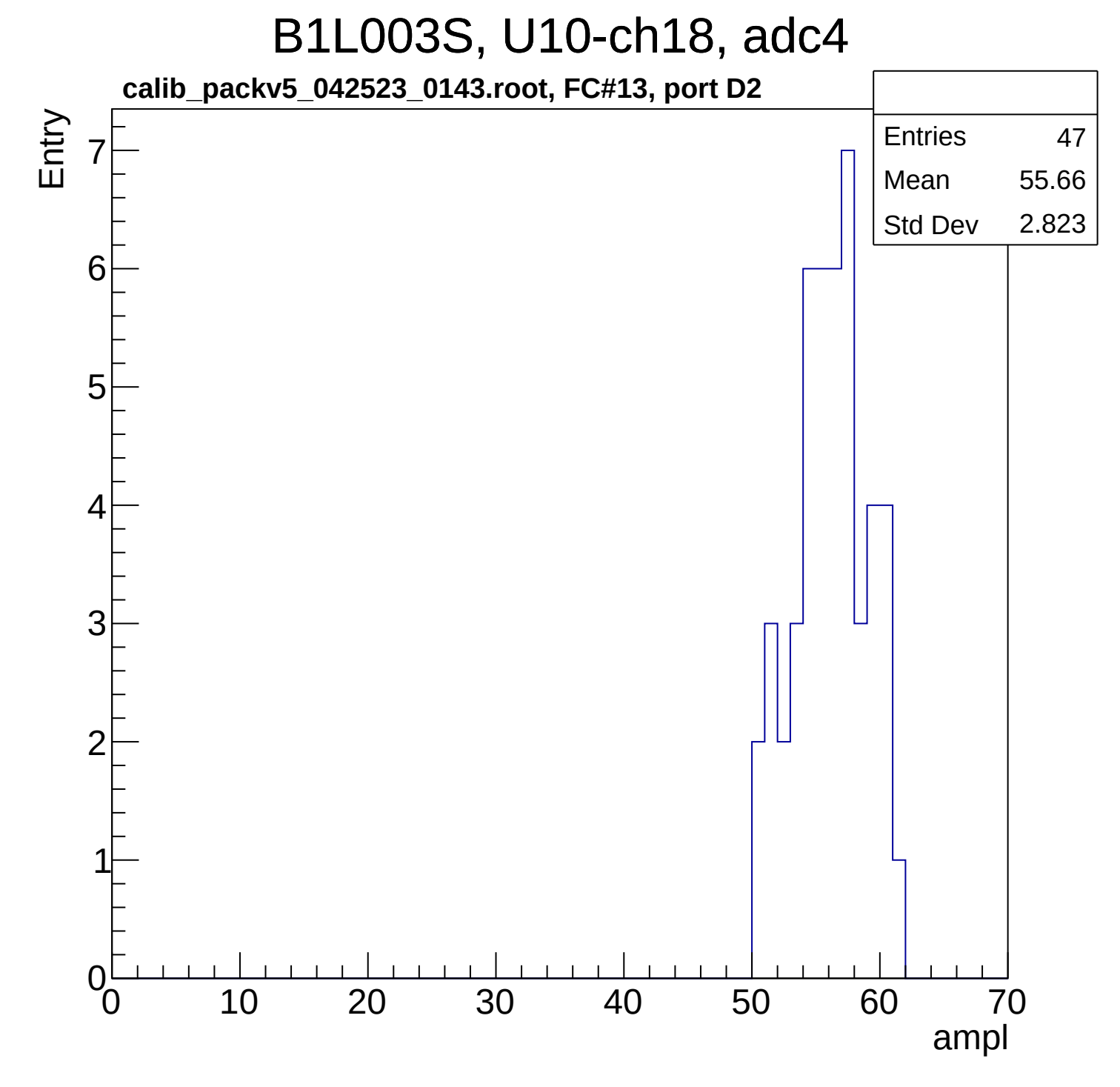
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch18, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 55.66
Std Dev 2.823
Entry
ampl
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2
3
4
5
6
7
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20
30
40
50
60
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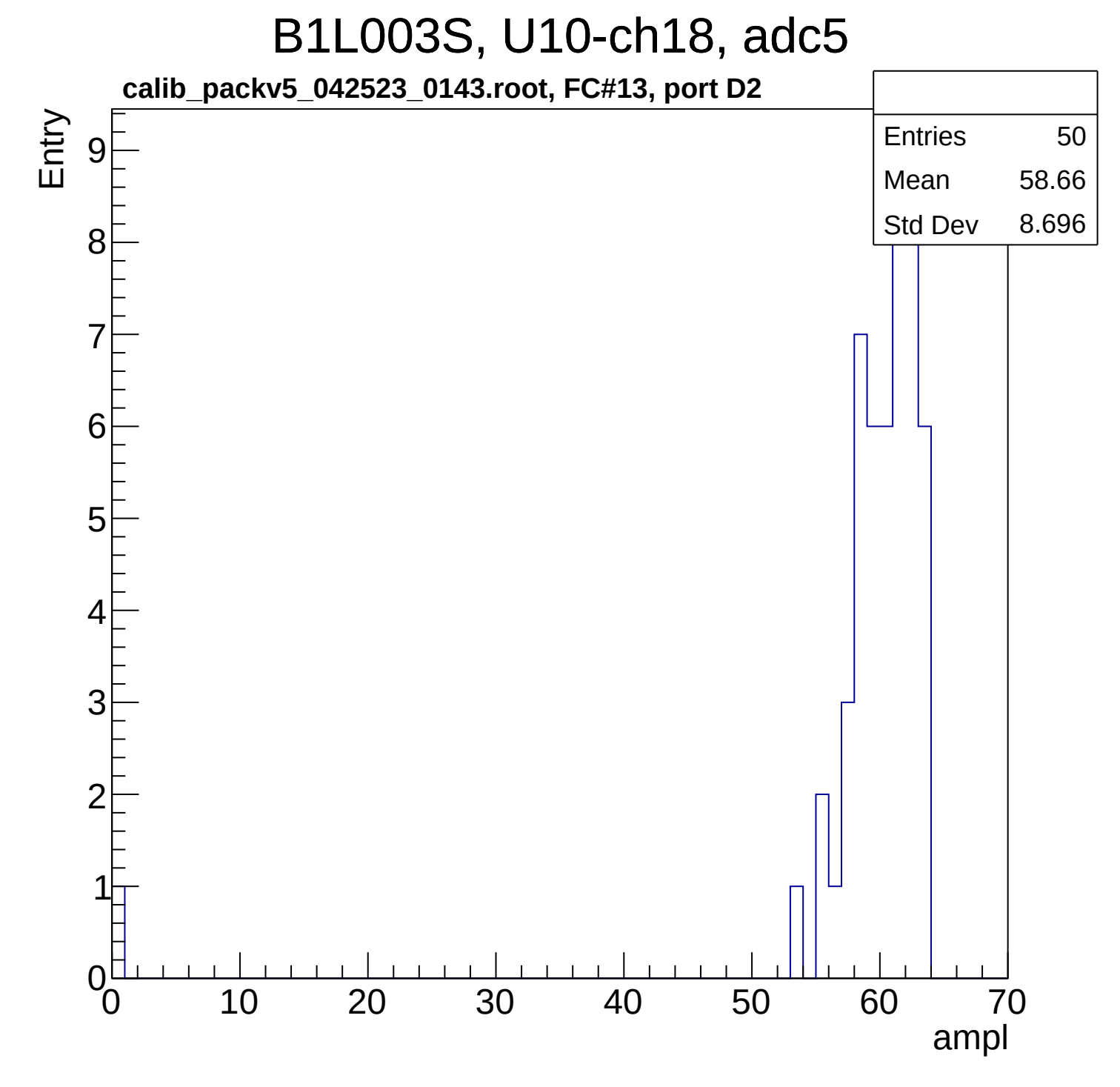
B1L003S, U10-ch18, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.66
Std Dev 8.696
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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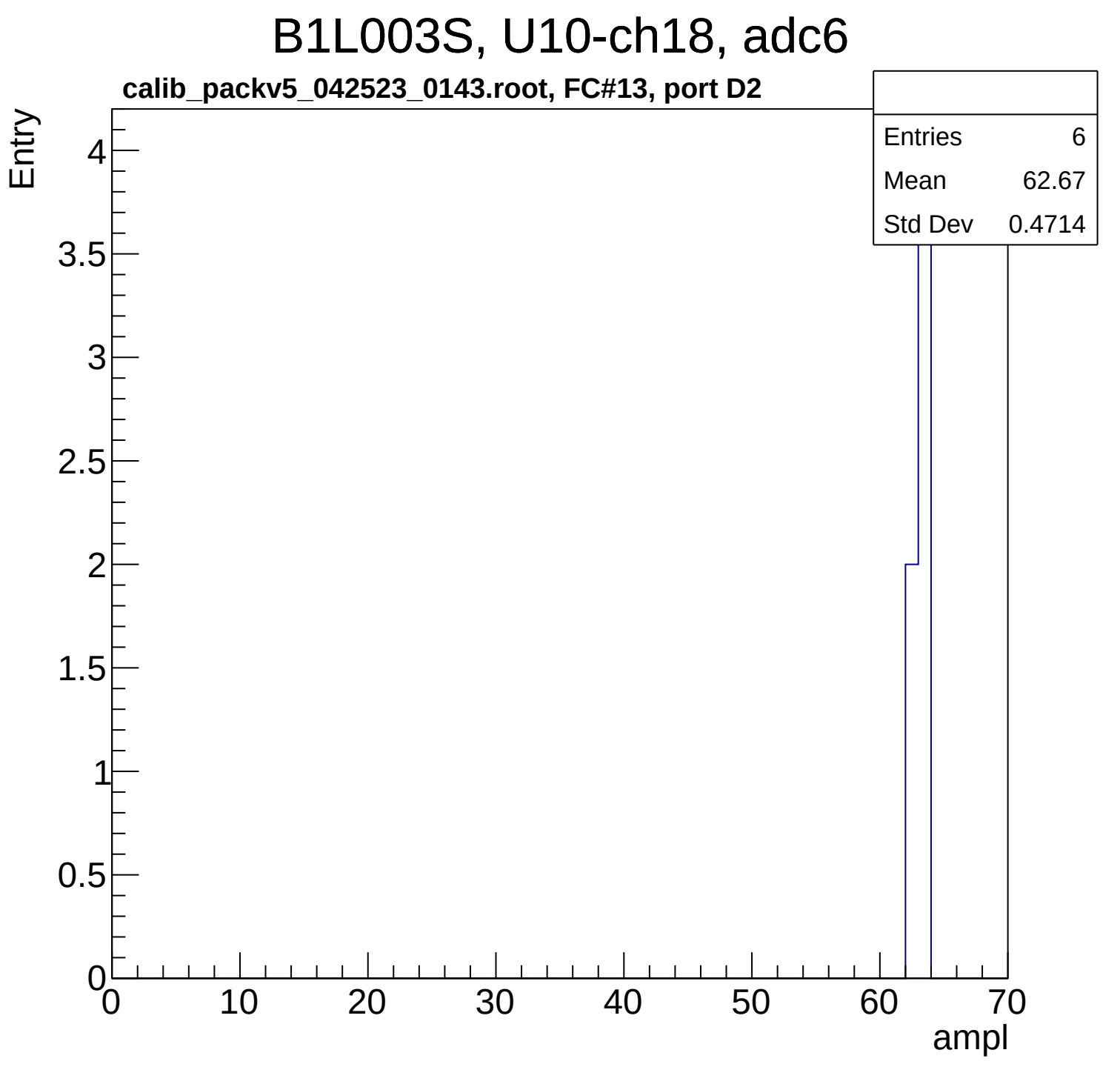
B1L003S, U10-ch18, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
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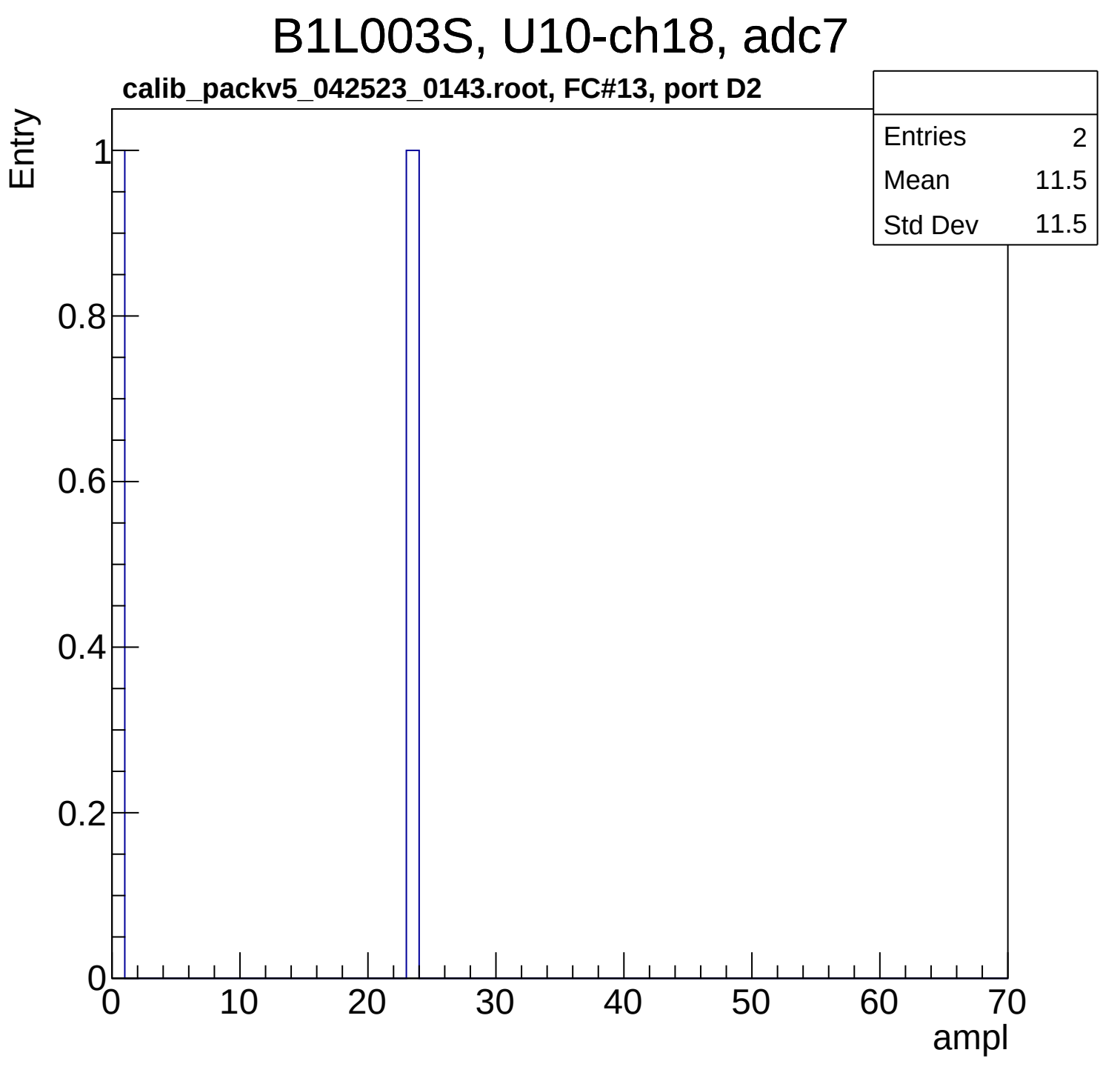
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch18, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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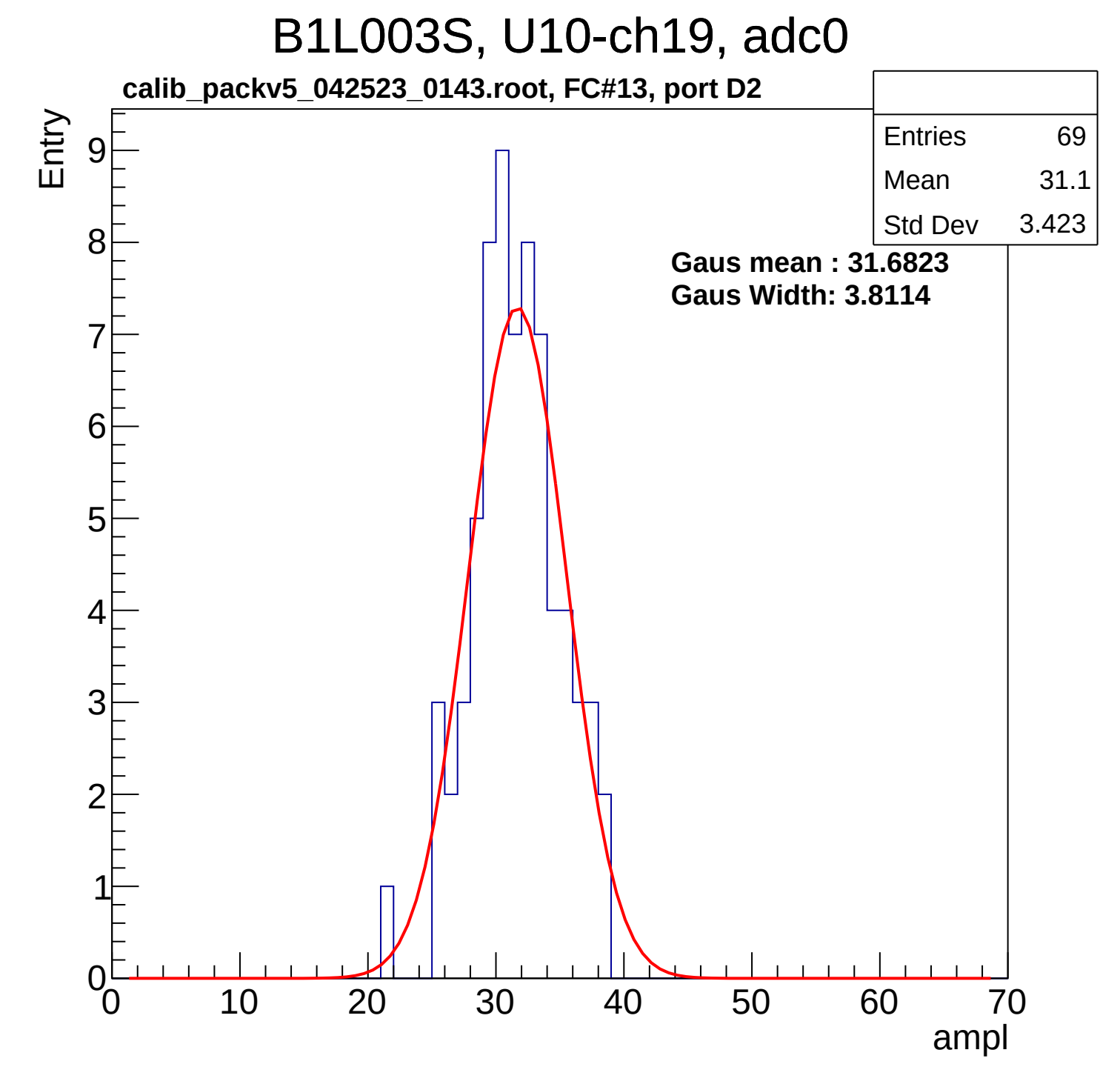
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch19, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 31.1
Std Dev 3.423
Gaus mean : 31.6823
Gaus Width: 3.8114
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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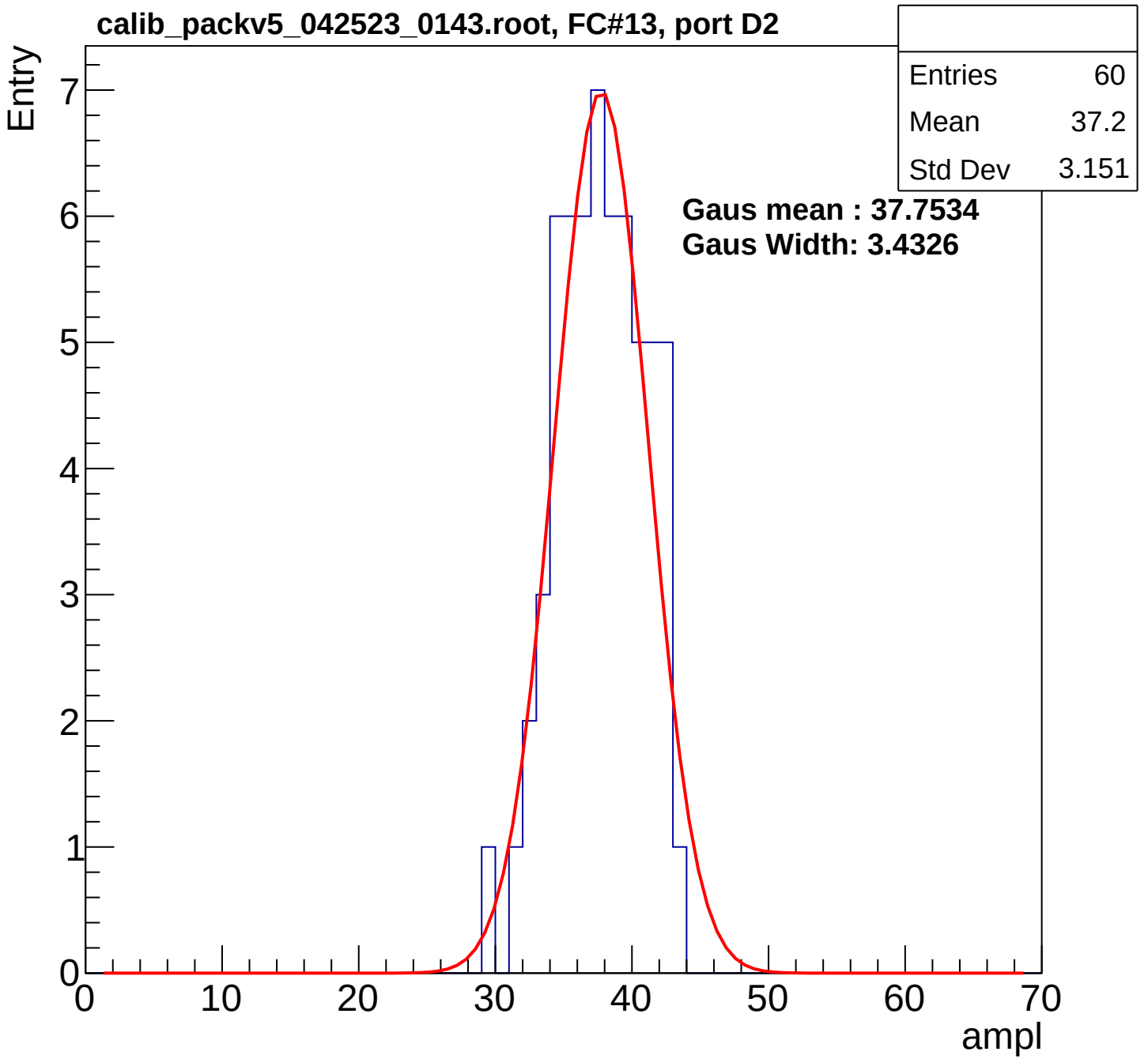
40
50
60
70


B1L003S, U10-ch19, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 37.2
Std Dev 3.151
Gaus mean : 37.7534
Gaus Width: 3.4326
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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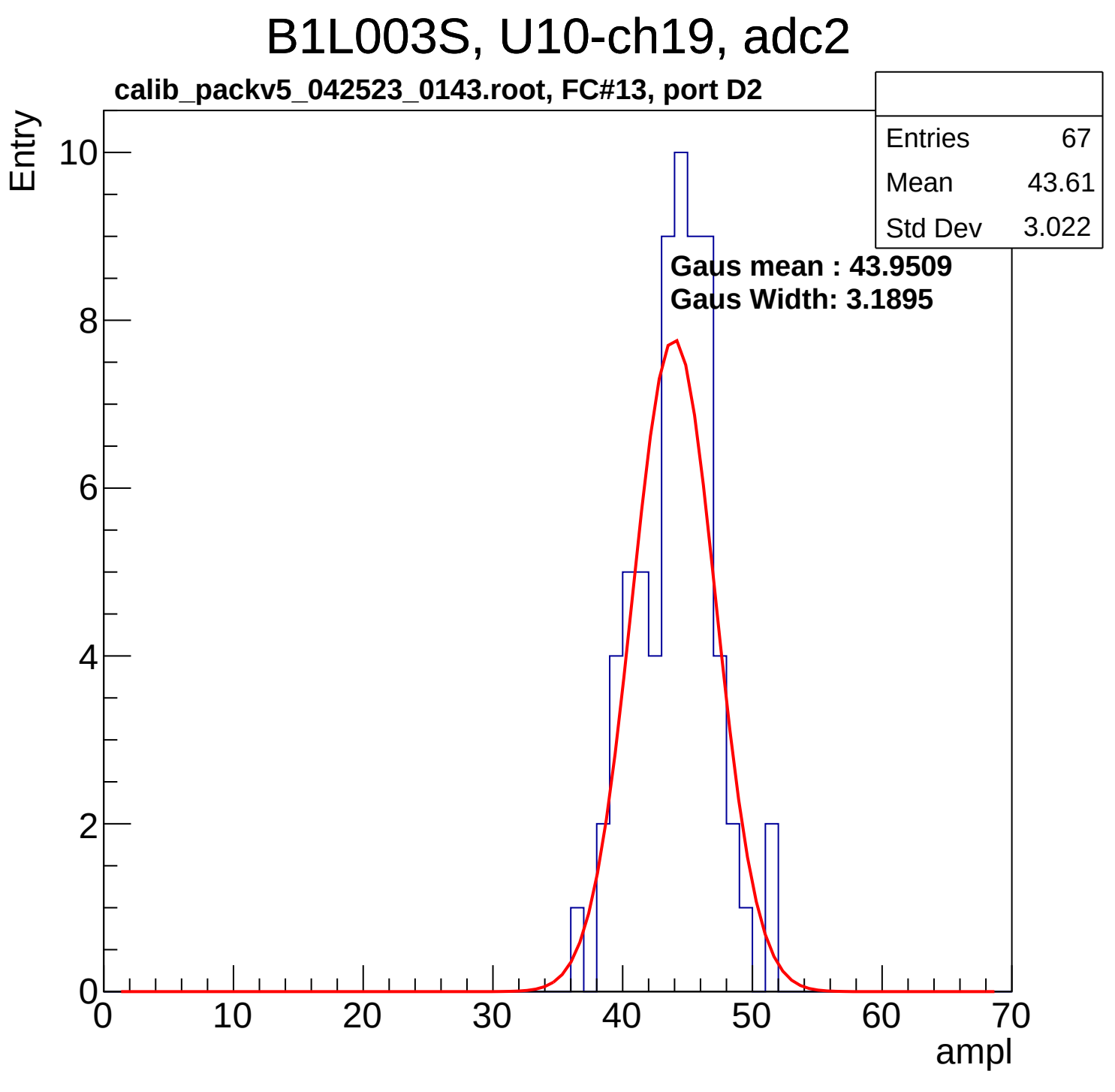
B1L003S, U10-ch19, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.61
Std Dev 3.022
Gaus mean : 43.9509
Gaus Width: 3.1895
Entry
ampl
0
2
4
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0
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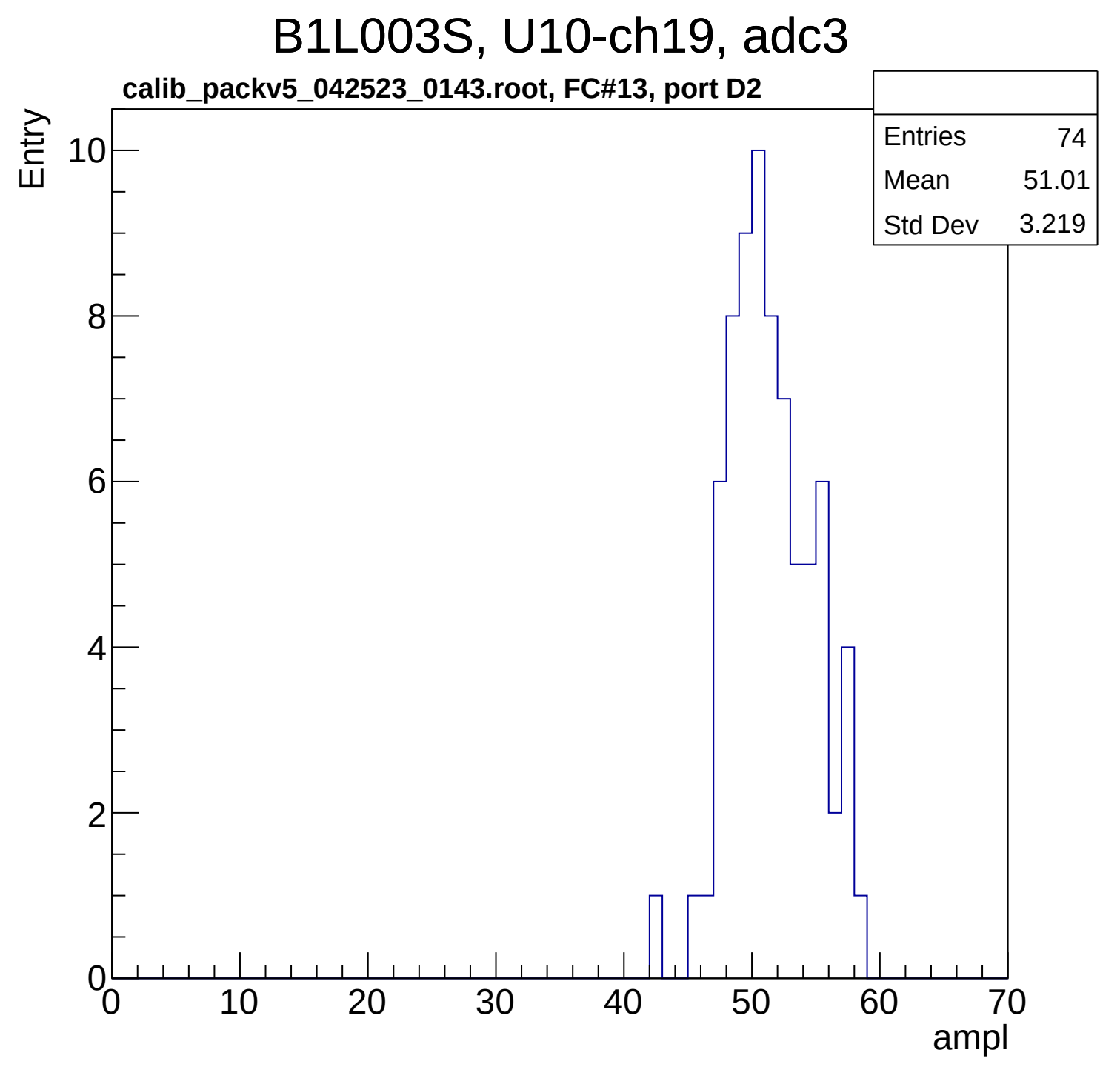
B1L003S, U10-ch19, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 51.01
Std Dev 3.219
Entry
ampl
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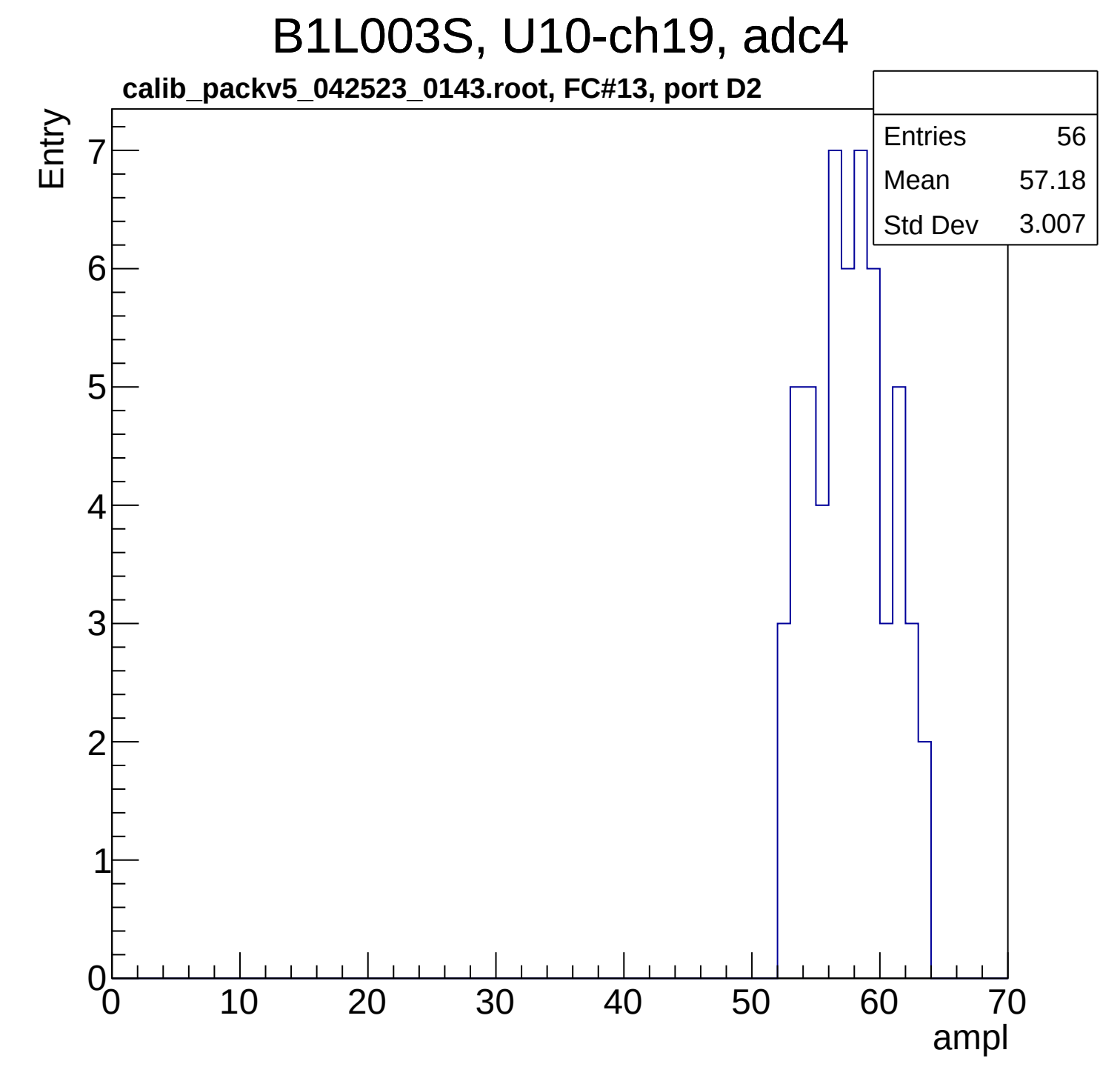
B1L003S, U10-ch19, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 57.18
Std Dev 3.007
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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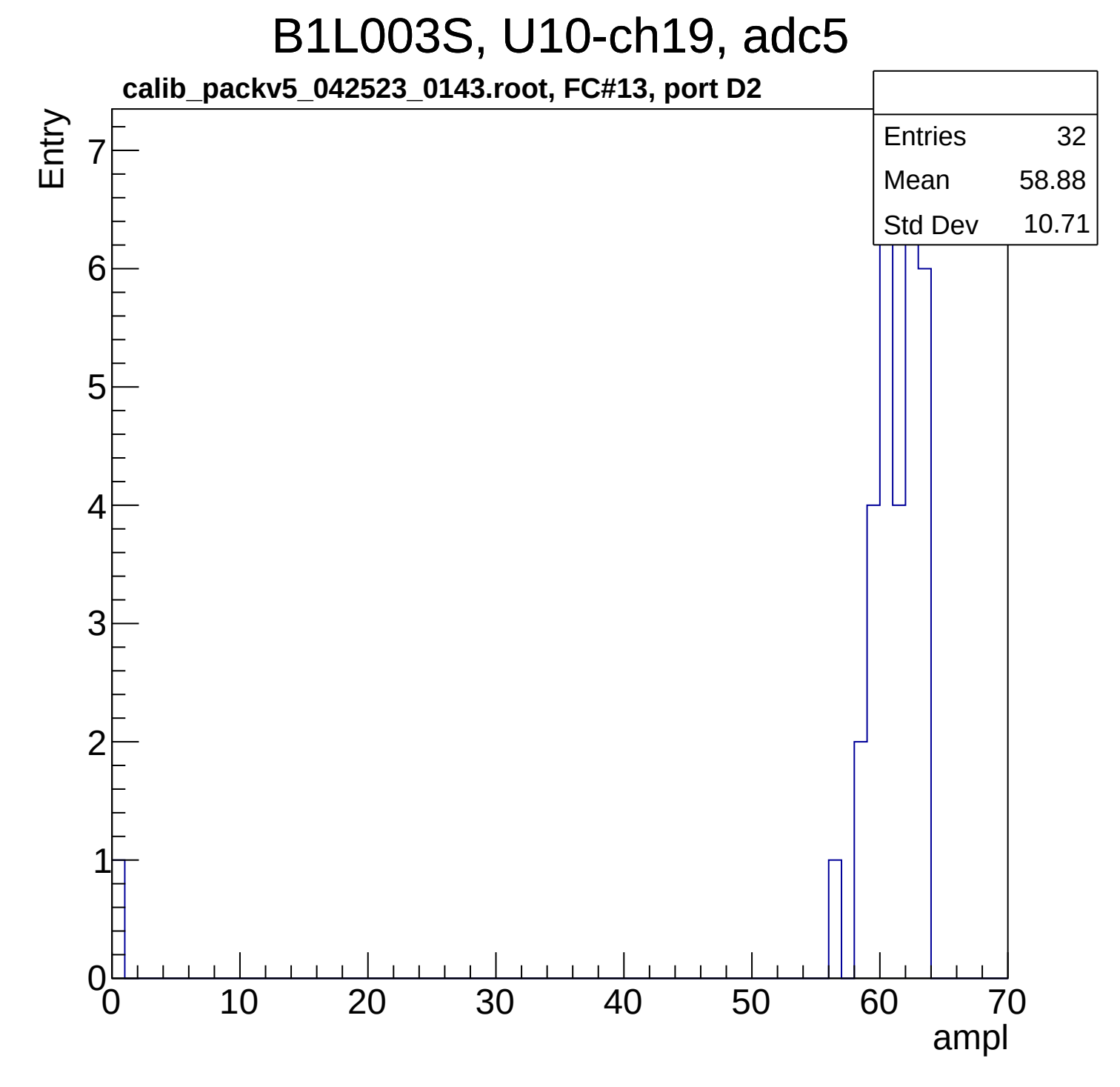
B1L003S, U10-ch19, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 58.88
Std Dev 10.71
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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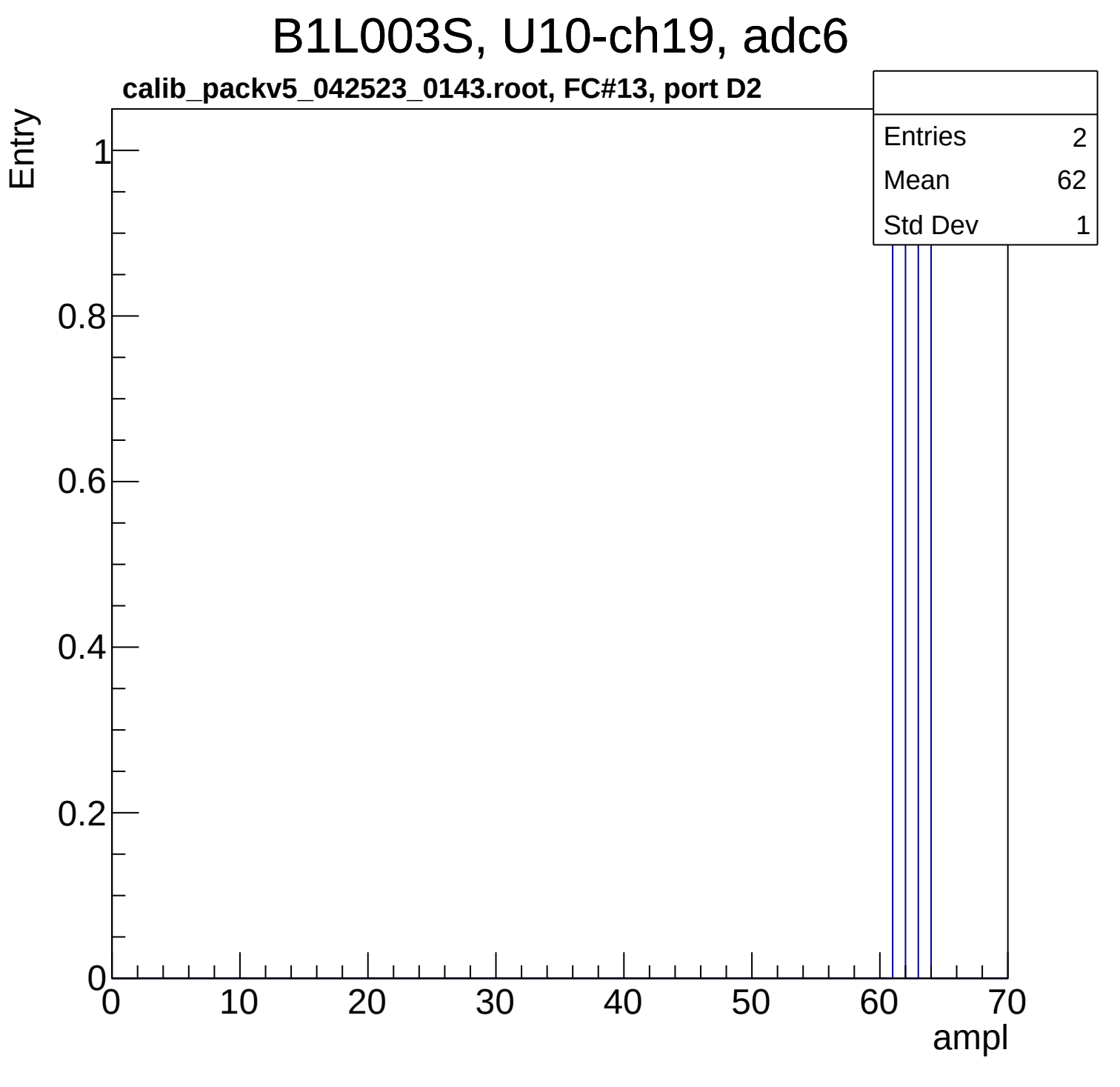
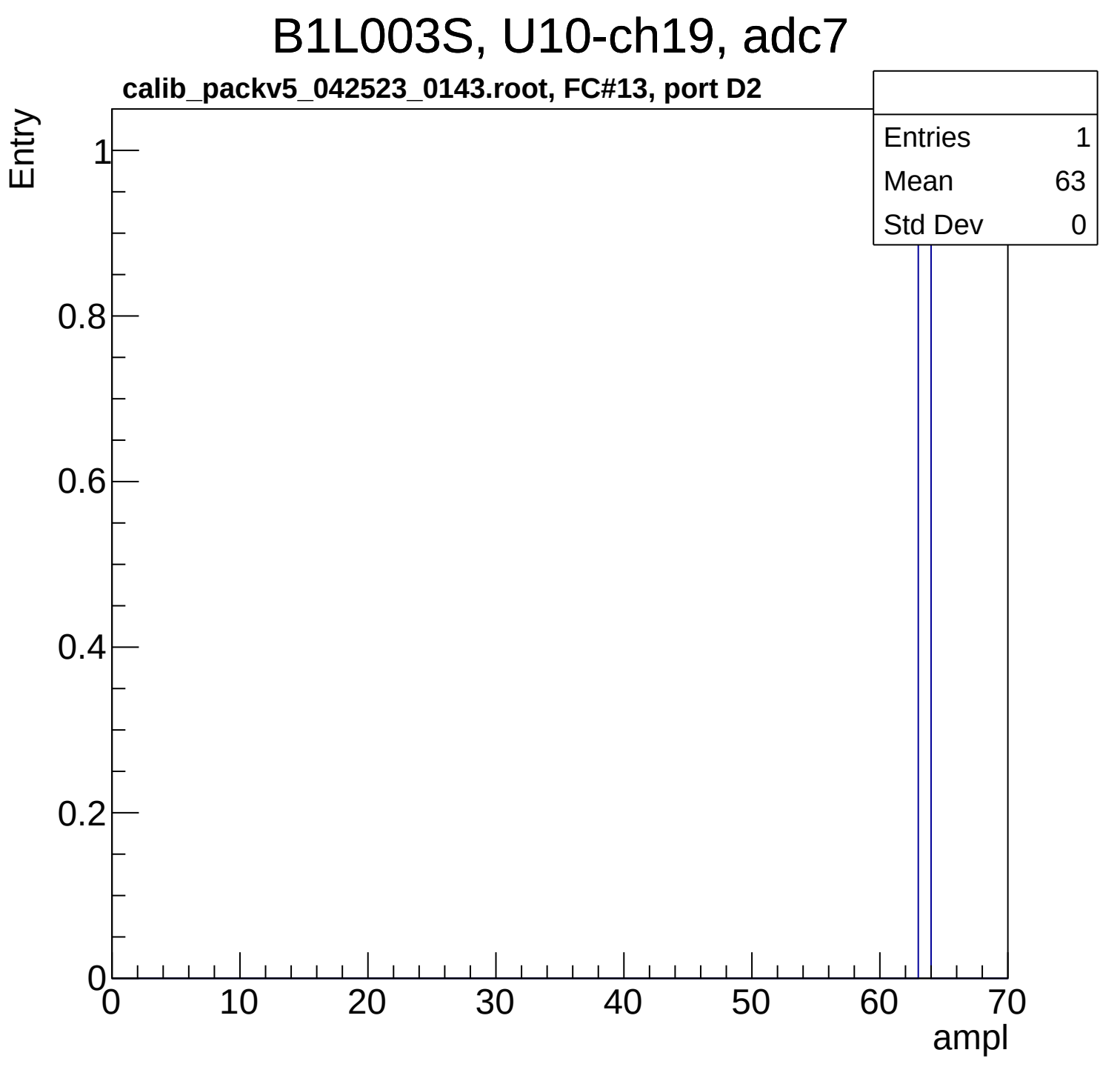
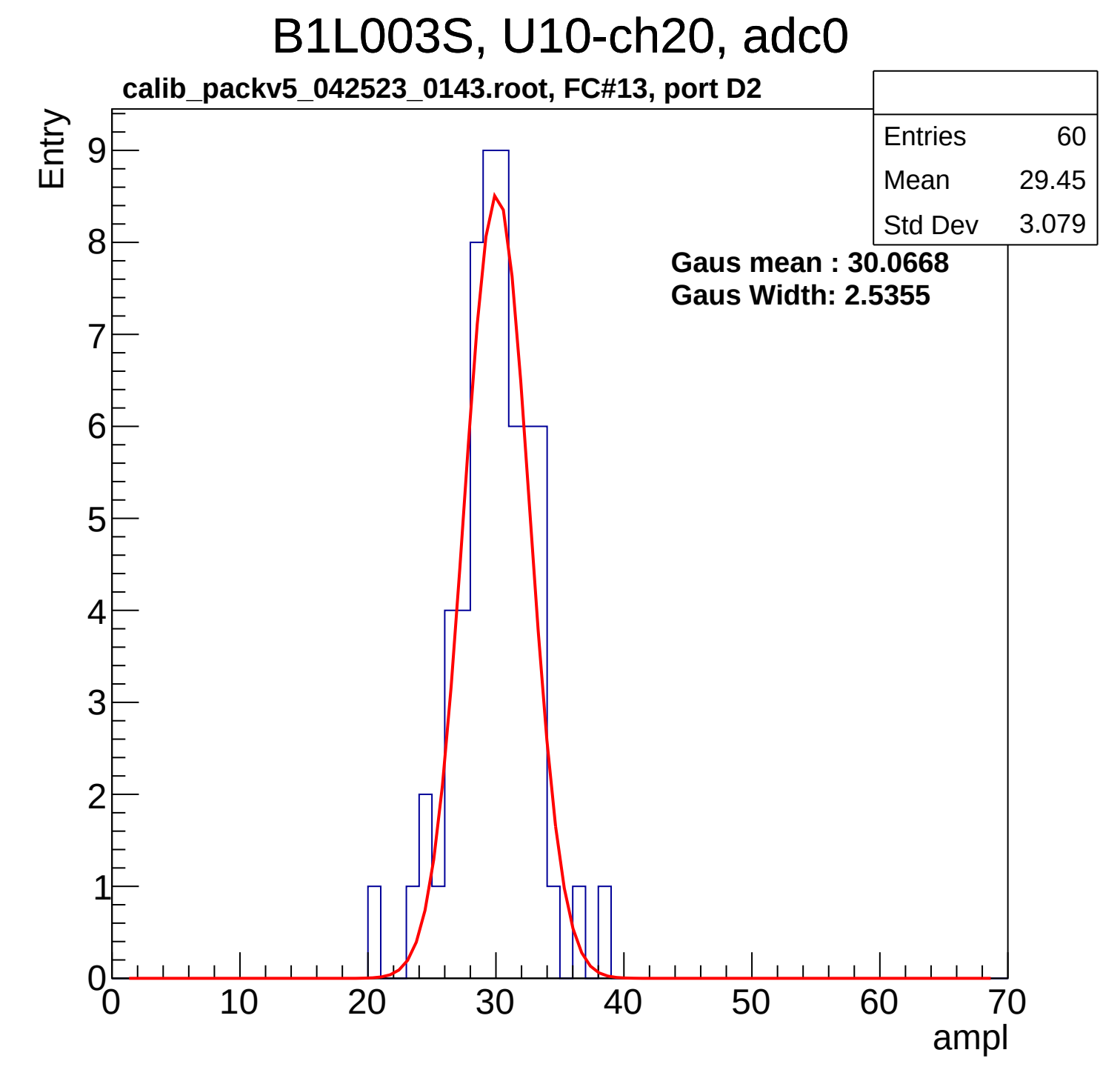

B1L003S, U10-ch19, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch19, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch20, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 29.45
Std Dev 3.079
Gaus mean : 30.0668
Gaus Width: 2.5355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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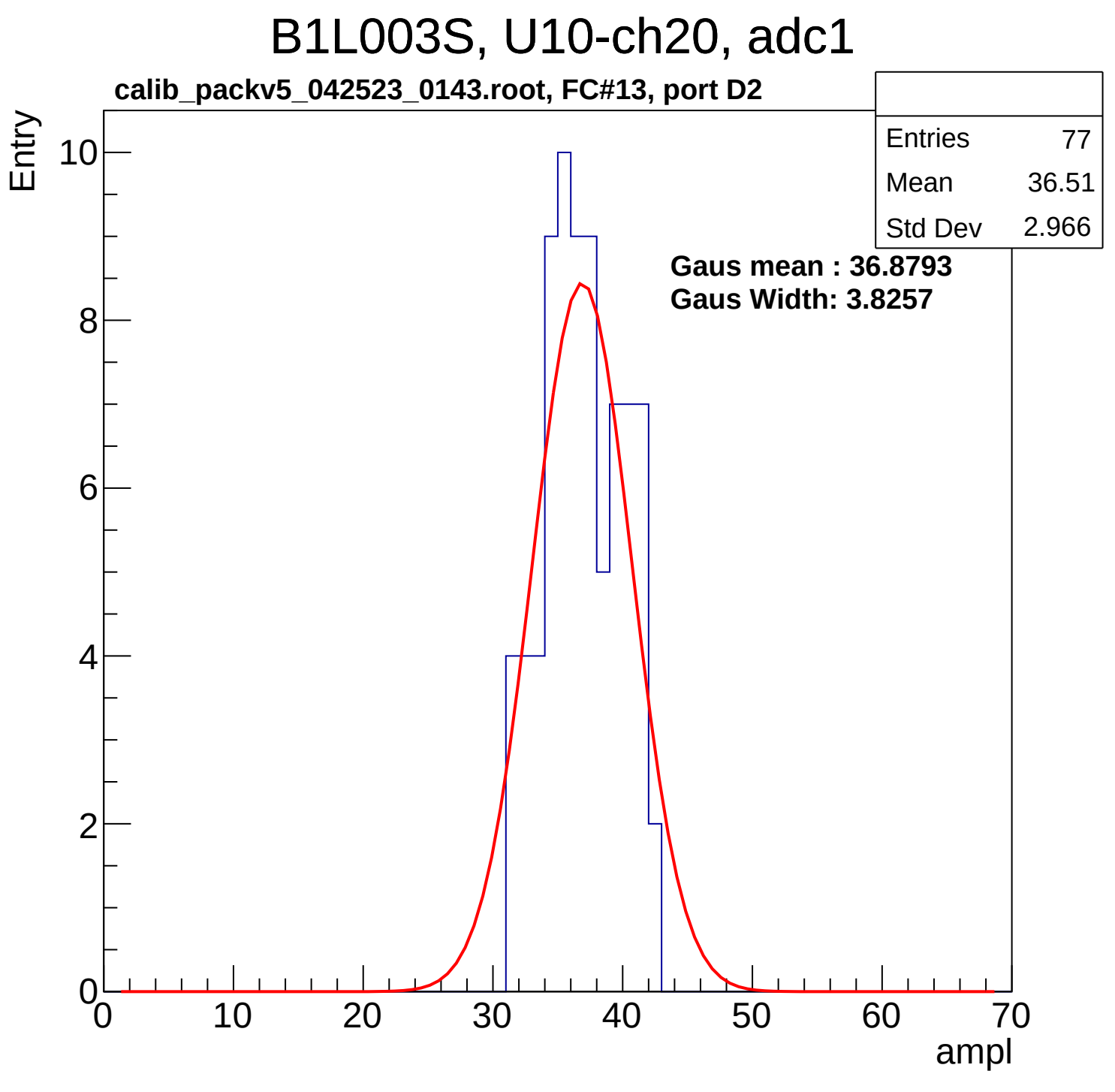
B1L003S, U10-ch20, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.51
Std Dev 2.966
Gaus mean : 36.8793
Gaus Width: 3.8257
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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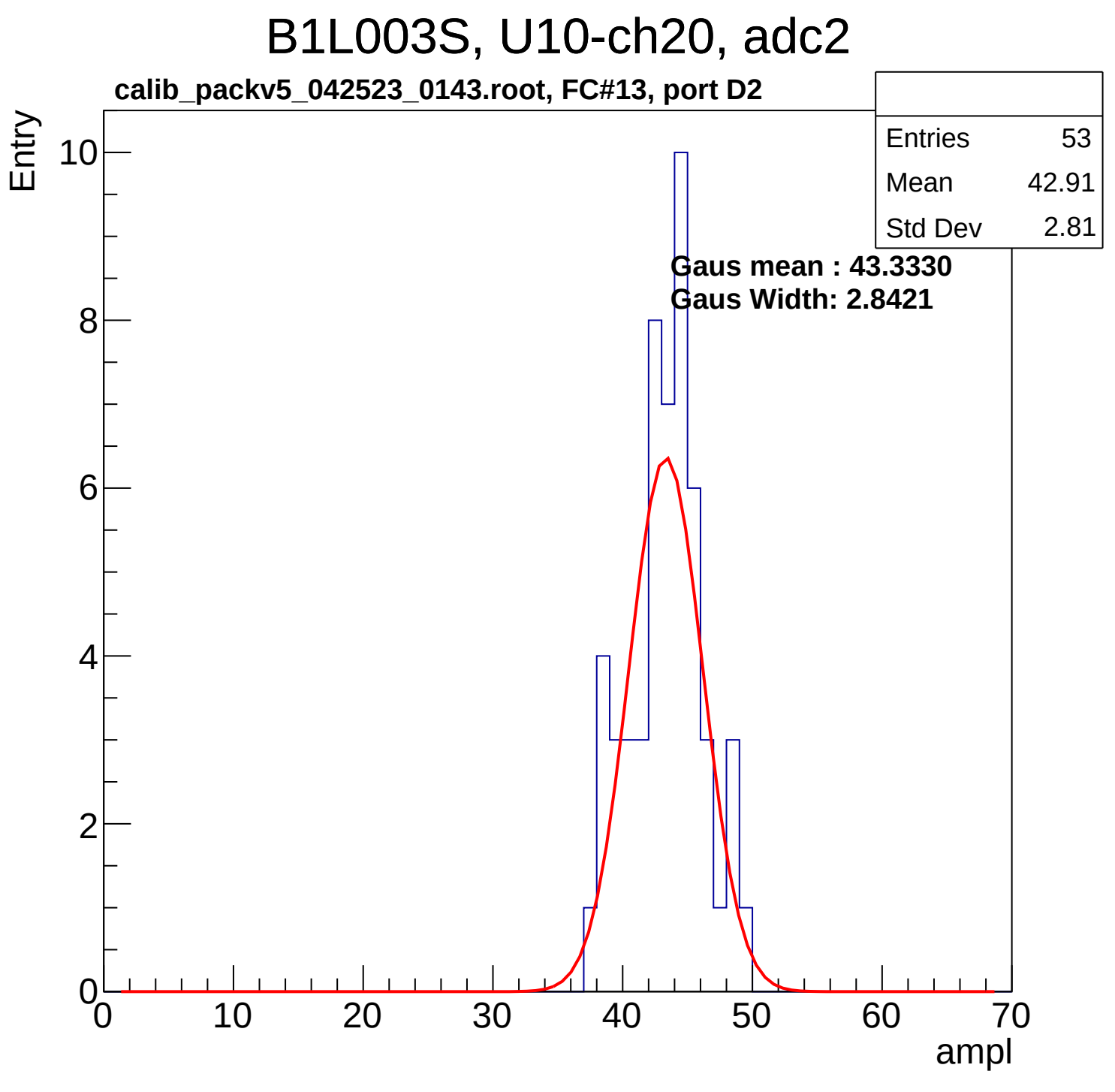
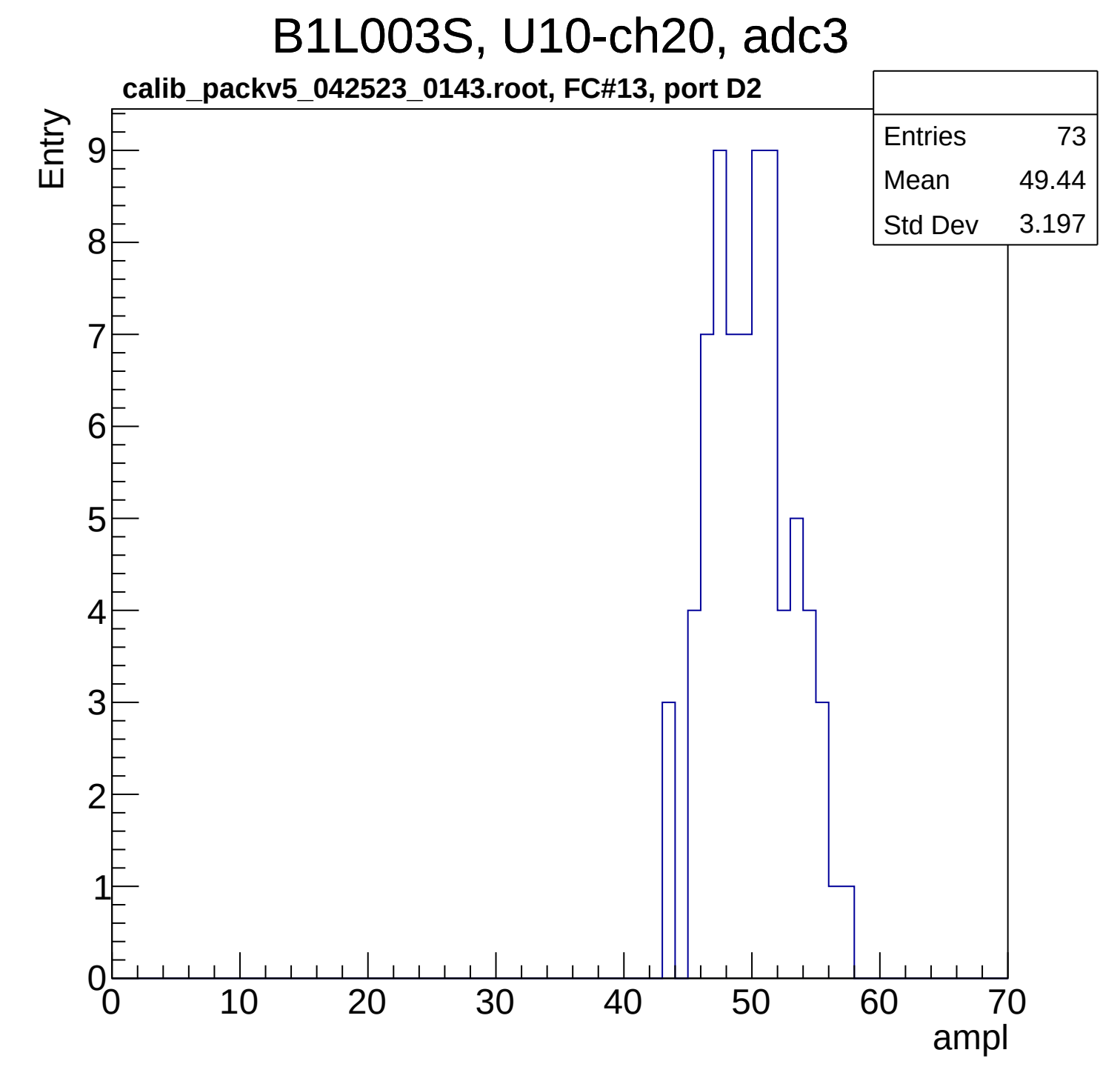
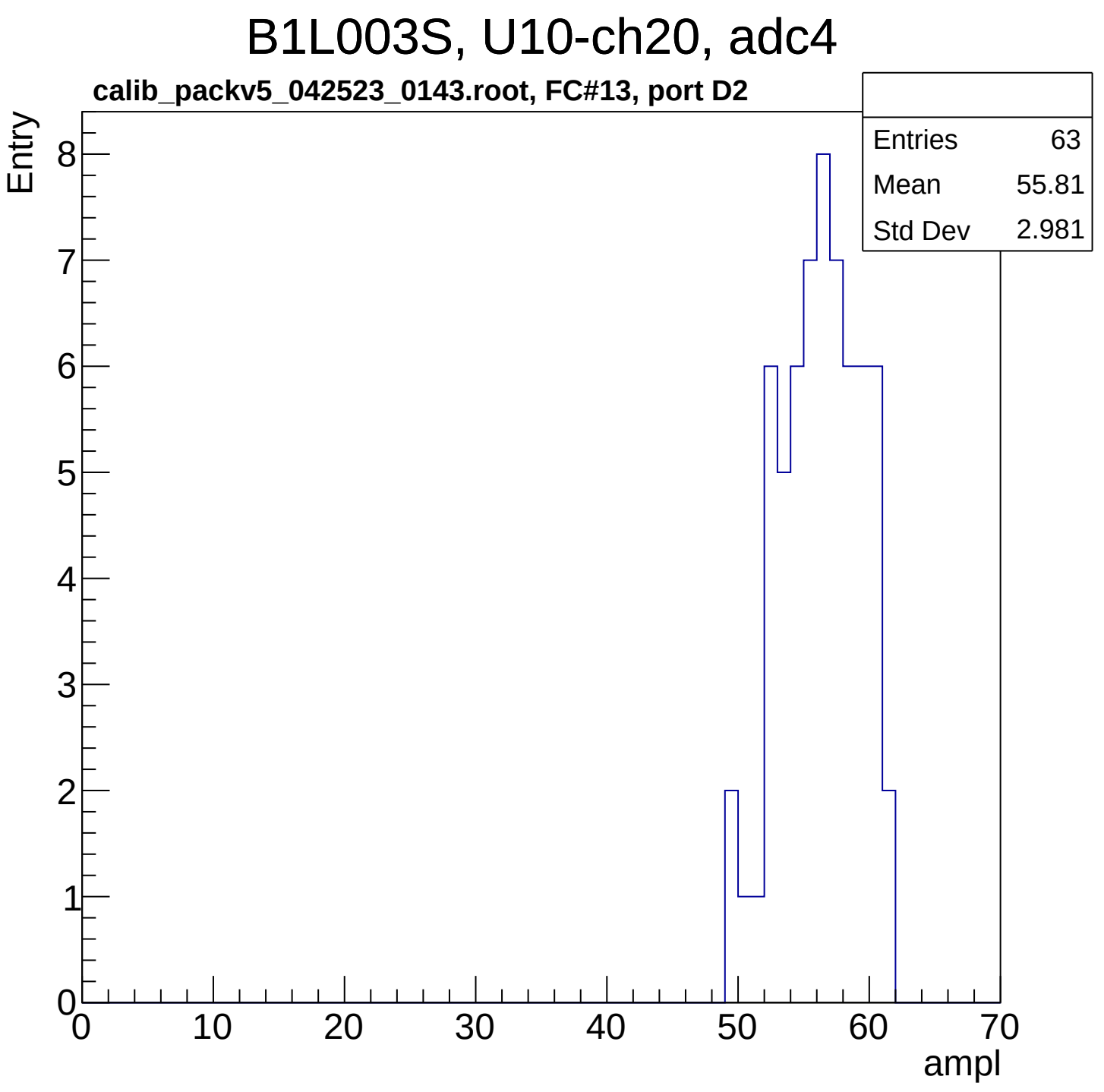
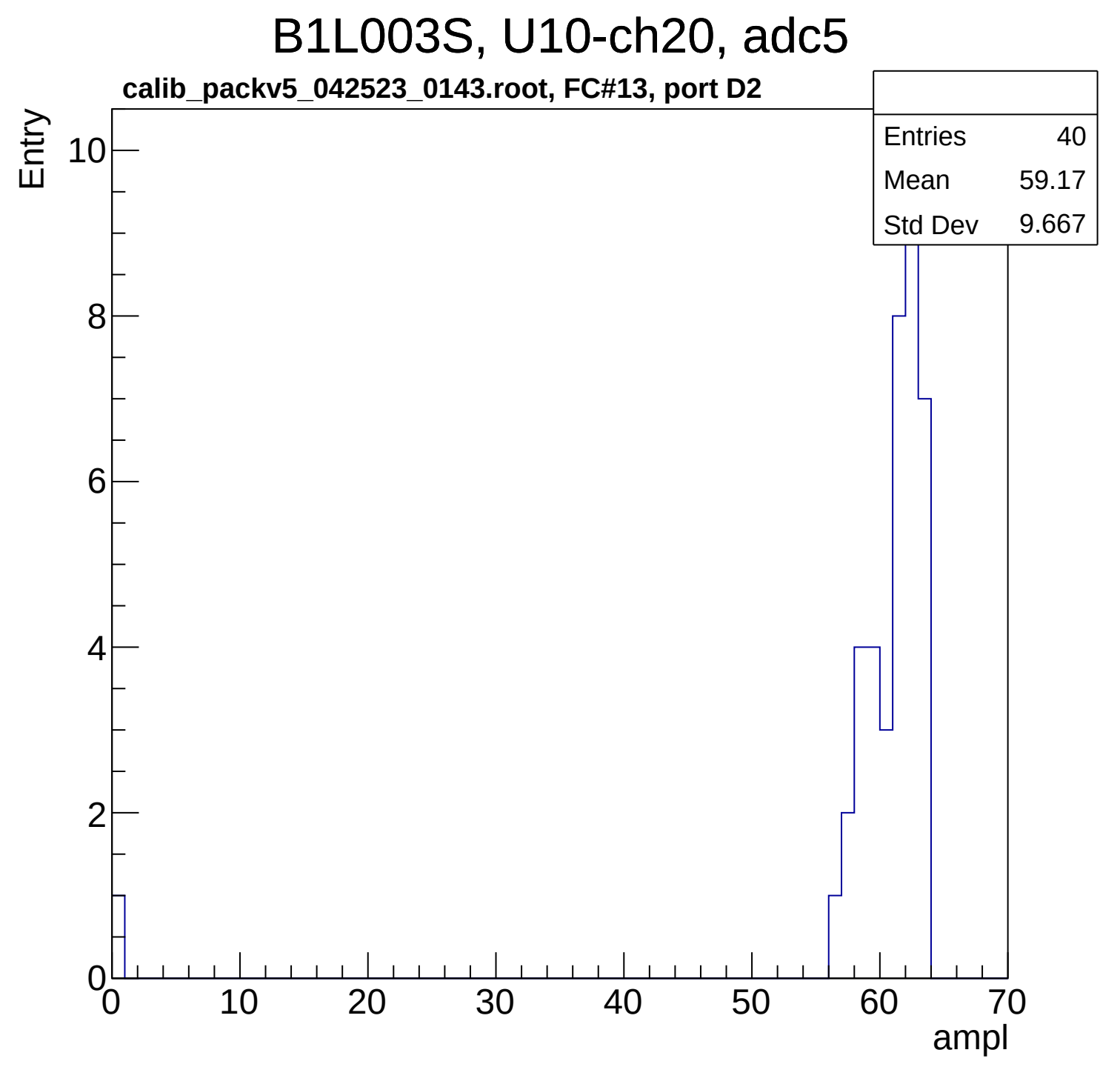
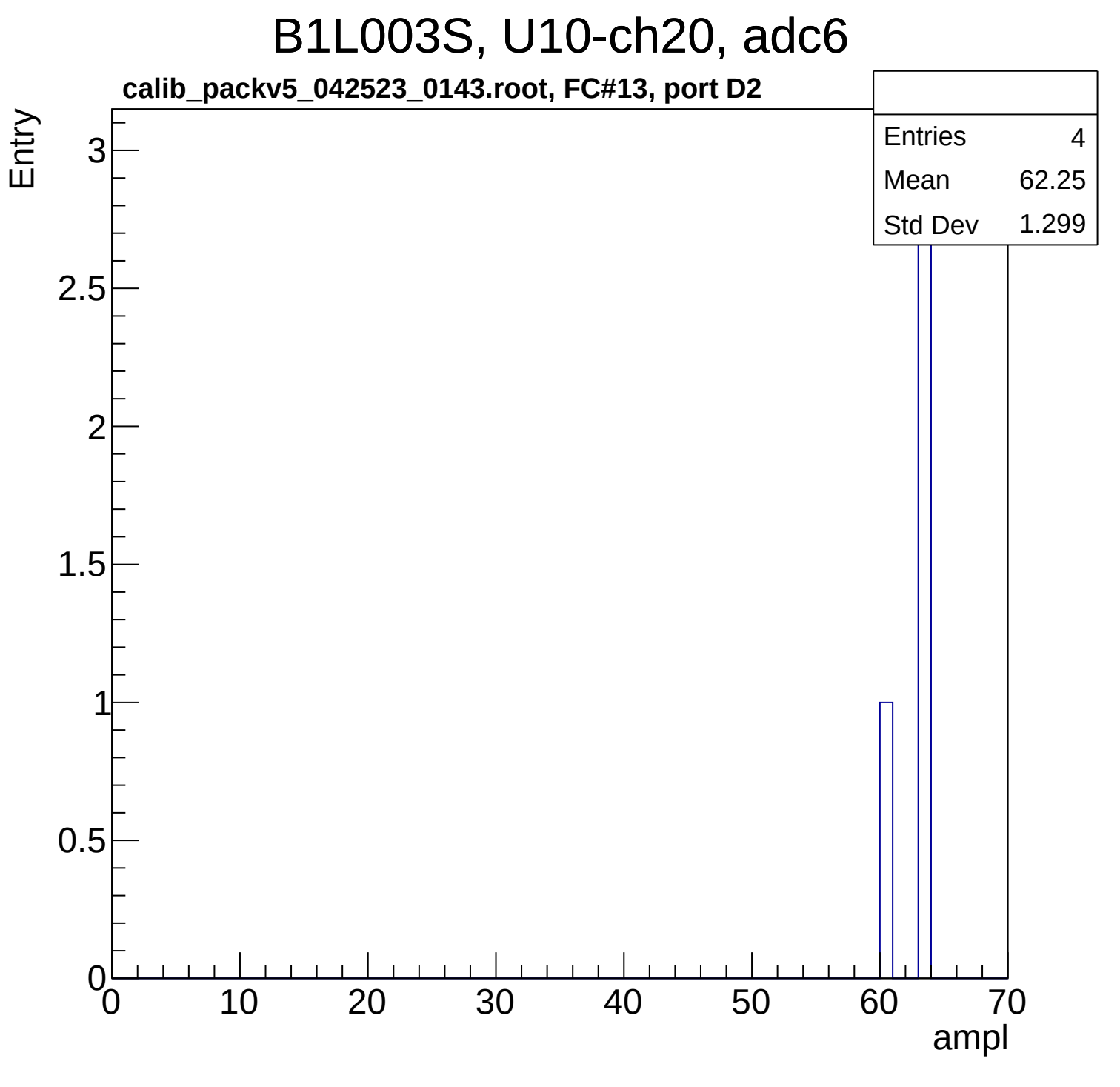
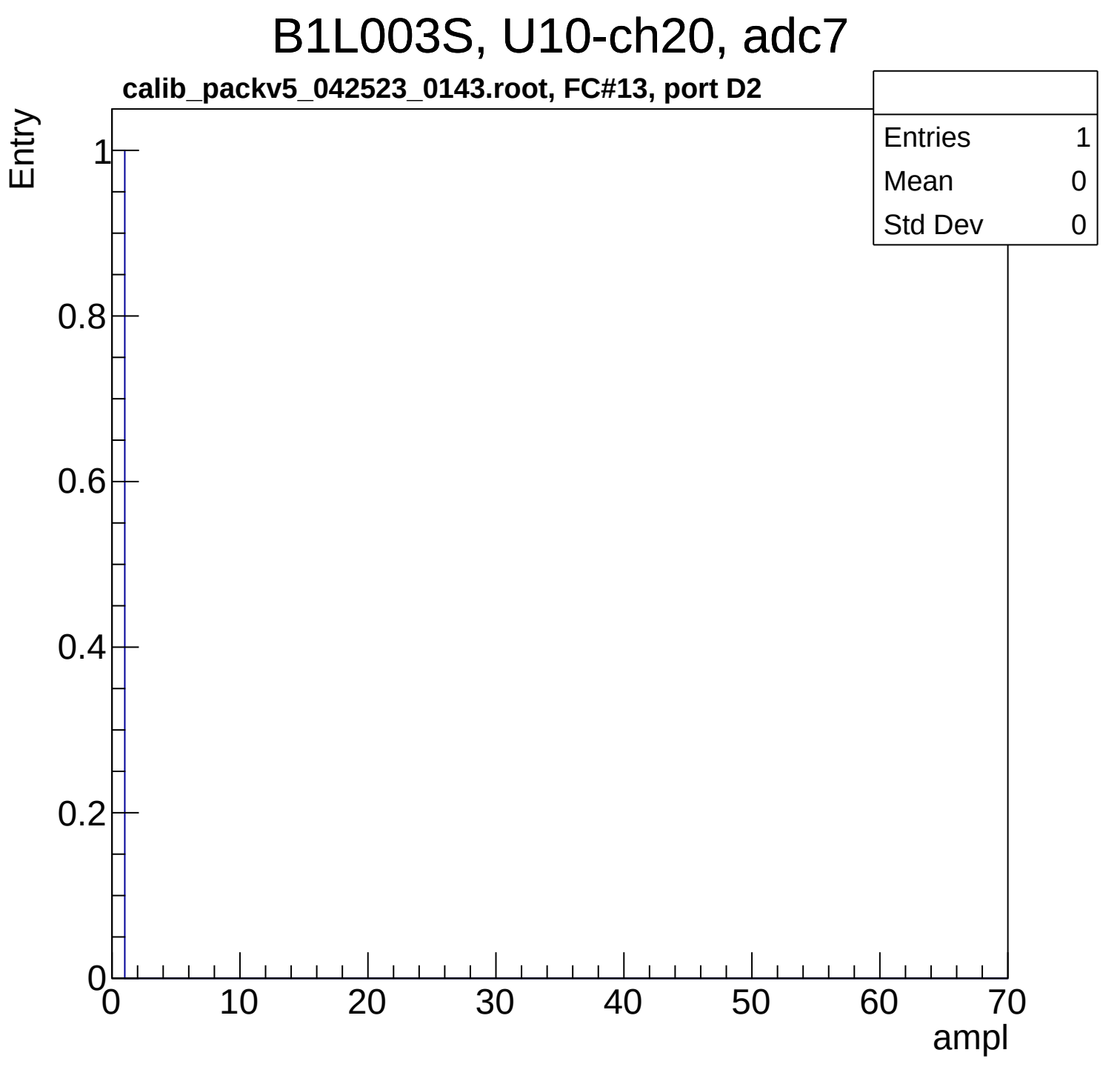

B1L003S, U10-ch20, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.91
Std Dev 2.81
Gaus mean : 43.3330
Gaus Width: 2.8421
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

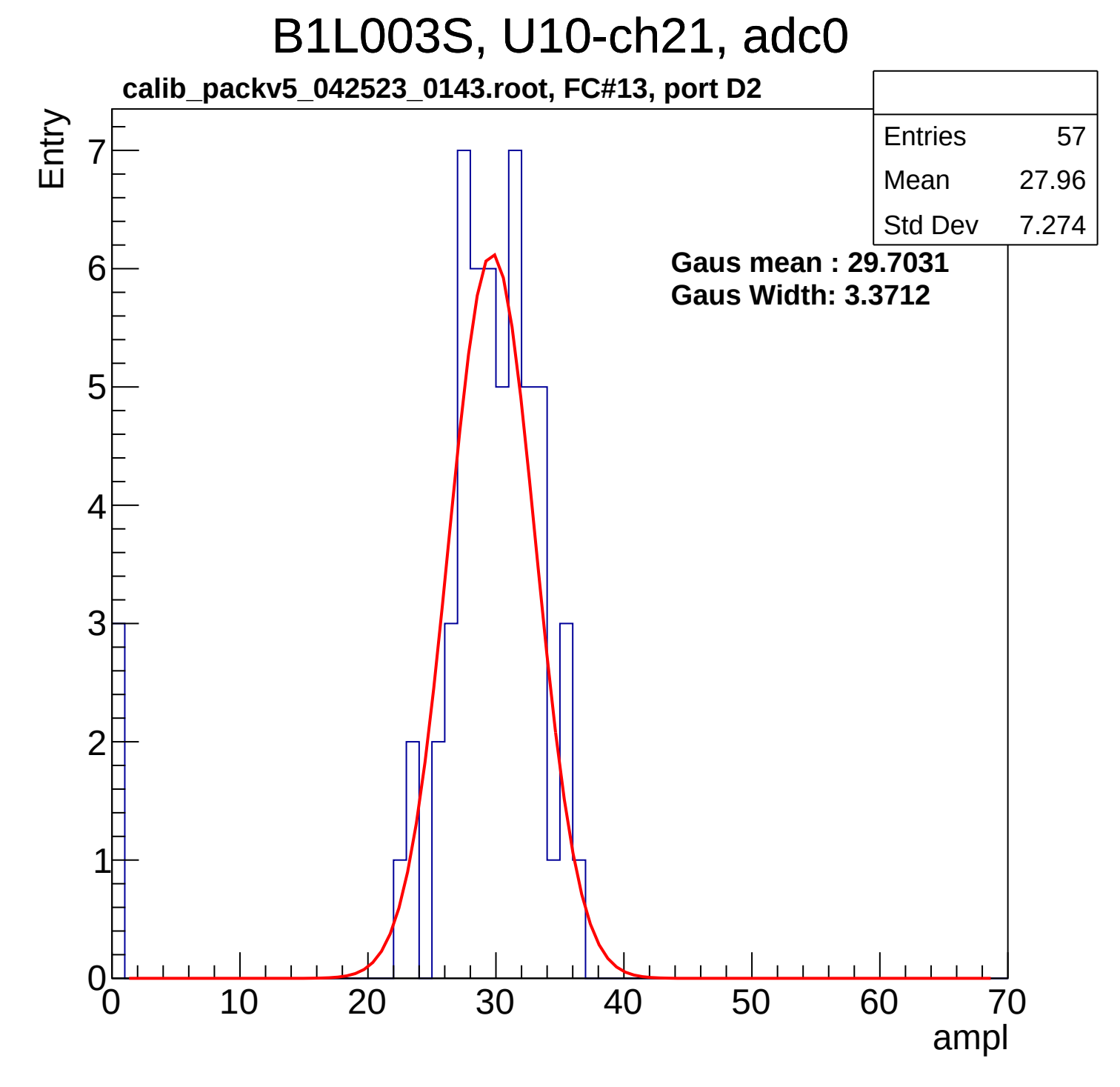

B1L003S, U10-ch20, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.44
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

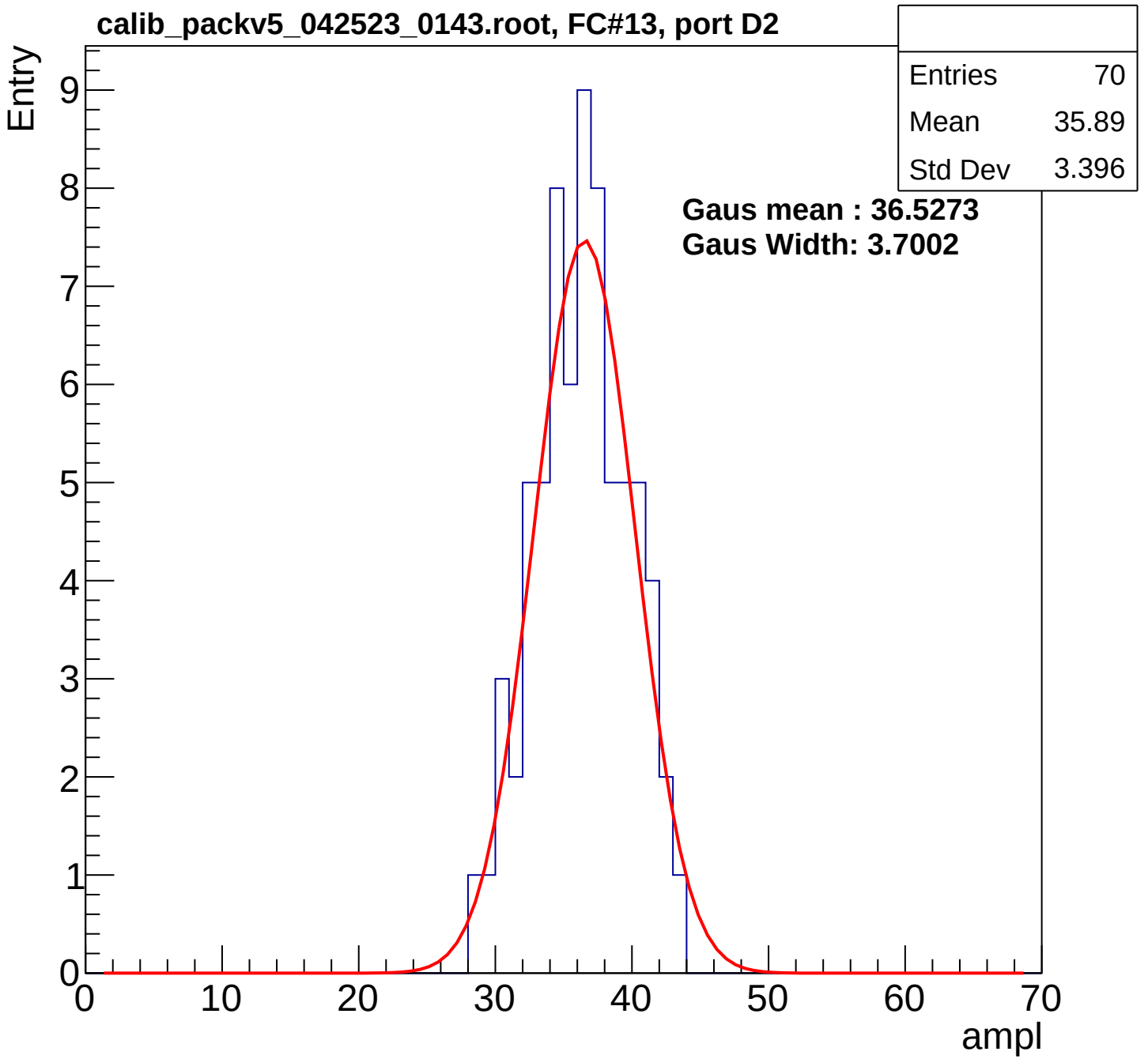

B1L003S, U10-ch20, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.81
Std Dev 2.981
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

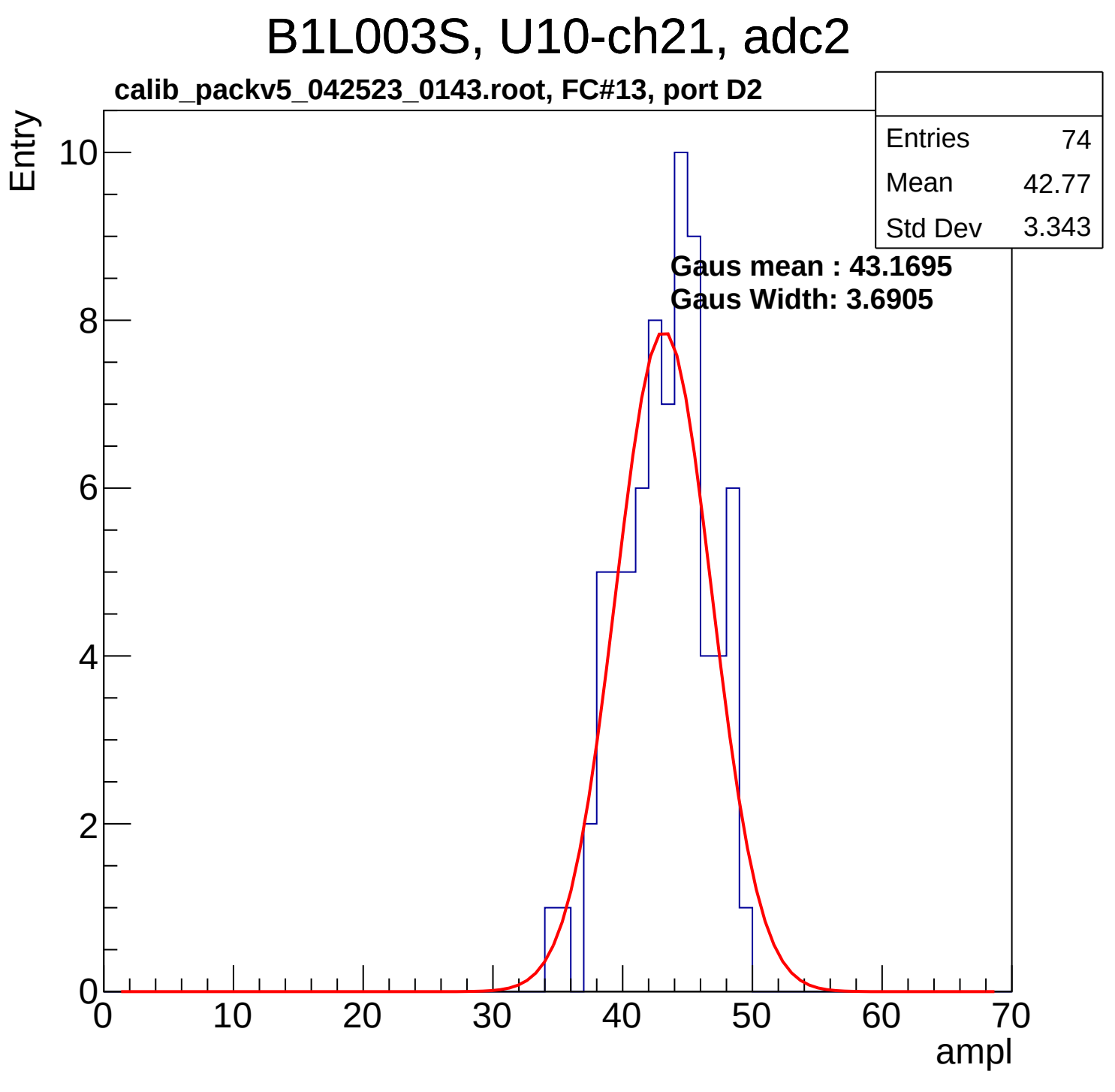

B1L003S, U10-ch20, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.17
Std Dev 9.667
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch20, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch20, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch21, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 27.96
Std Dev 7.274
Gaus mean : 29.7031
Gaus Width: 3.3712
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch21, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 35.89
Std Dev 3.396
Gaus mean : 36.5273
Gaus Width: 3.7002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch21, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 42.77
Std Dev 3.343
Gaus mean : 43.1695
Gaus Width: 3.6905
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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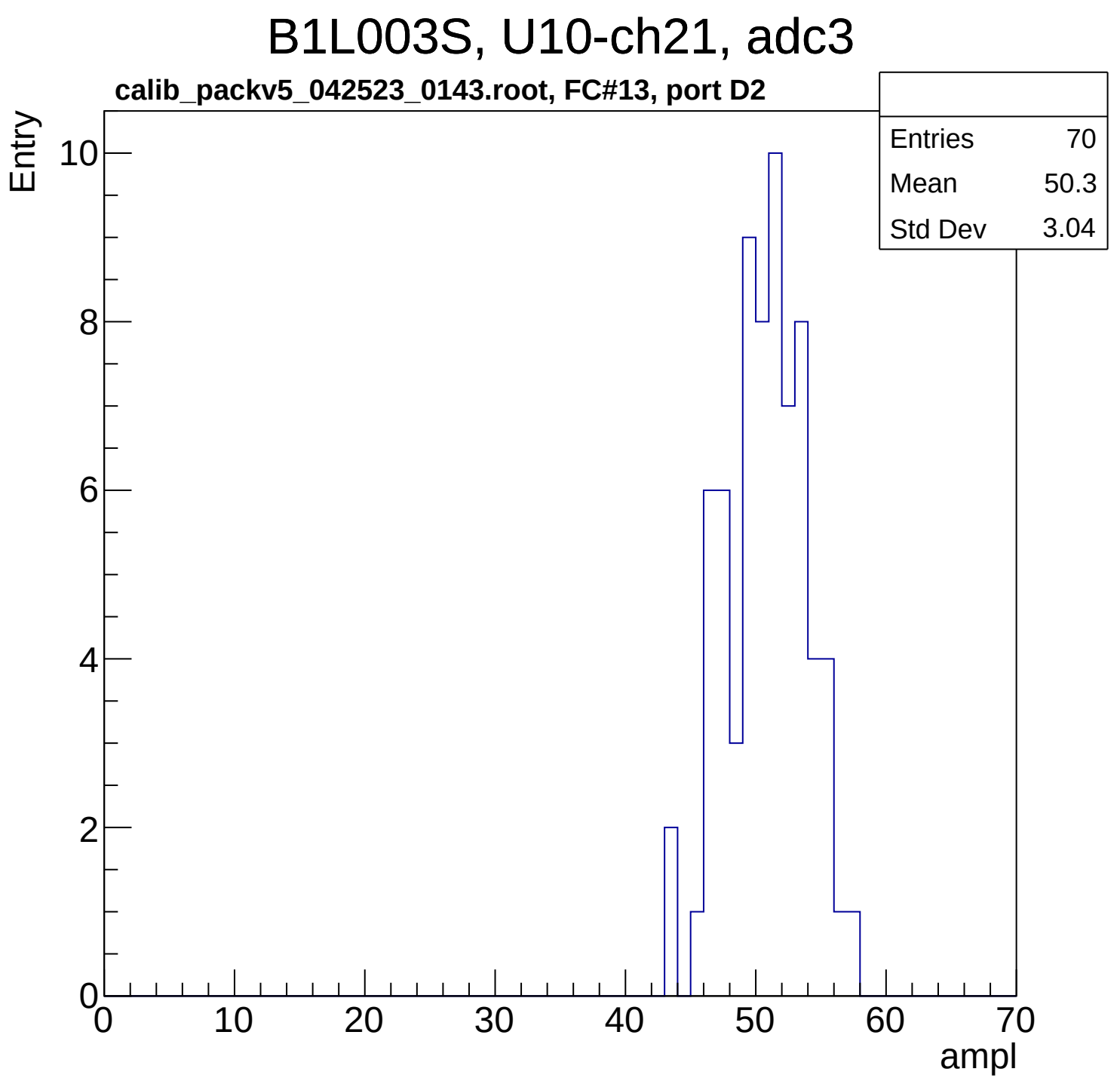
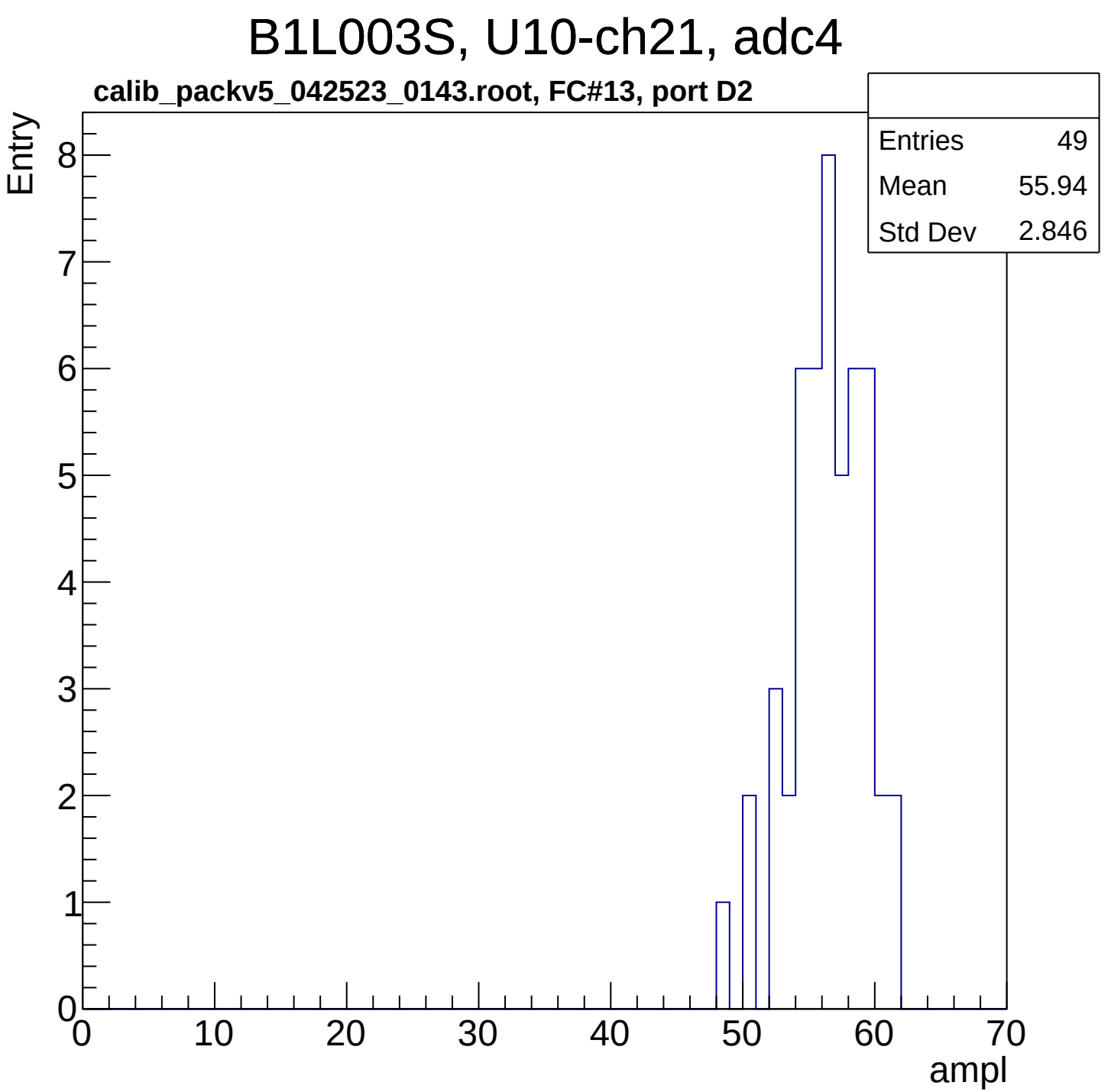

B1L003S, U10-ch21, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 50.3
Std Dev 3.04
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch21, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 55.94
Std Dev 2.846
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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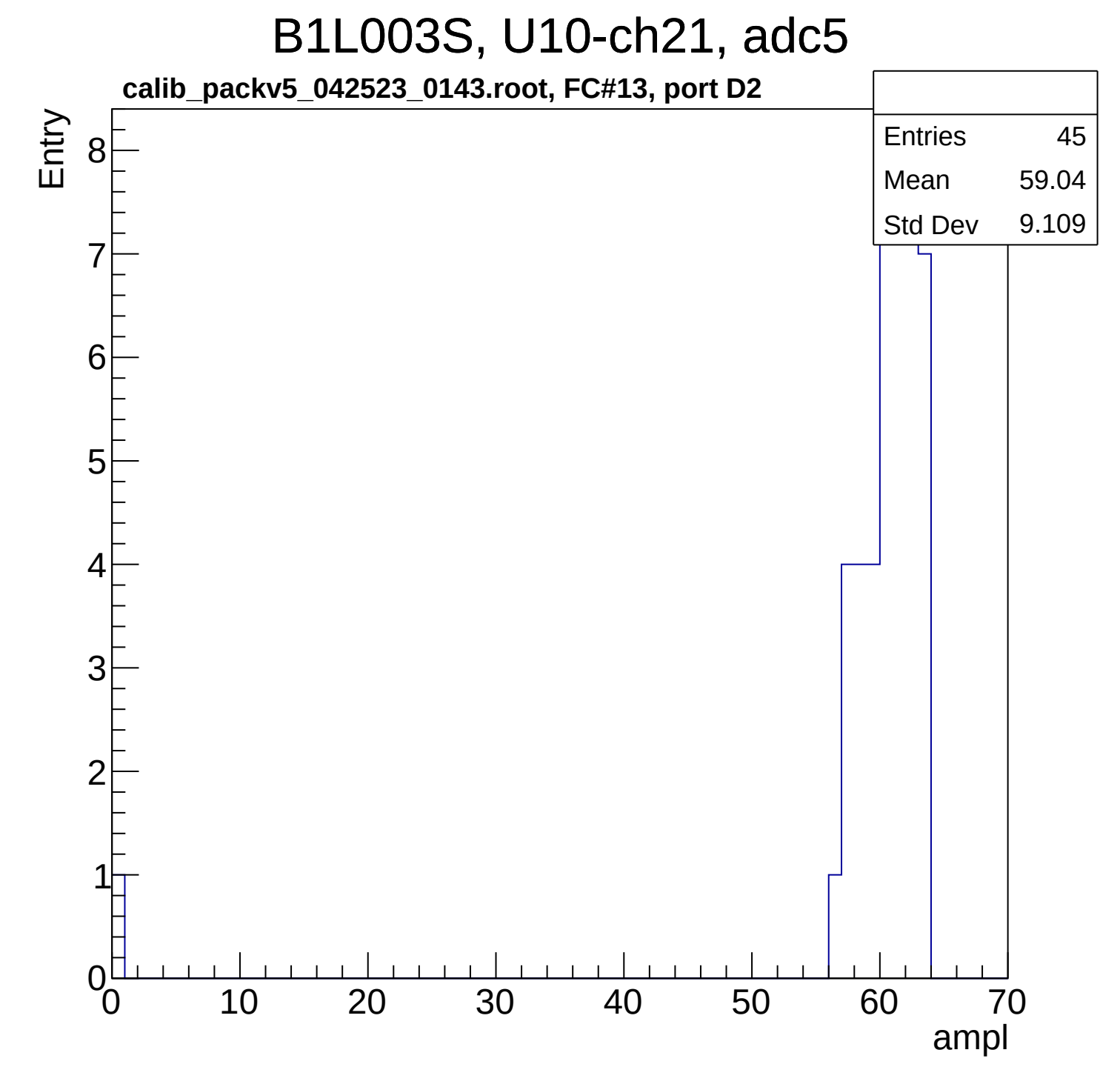
B1L003S, U10-ch21, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 59.04
Std Dev 9.109
Entry
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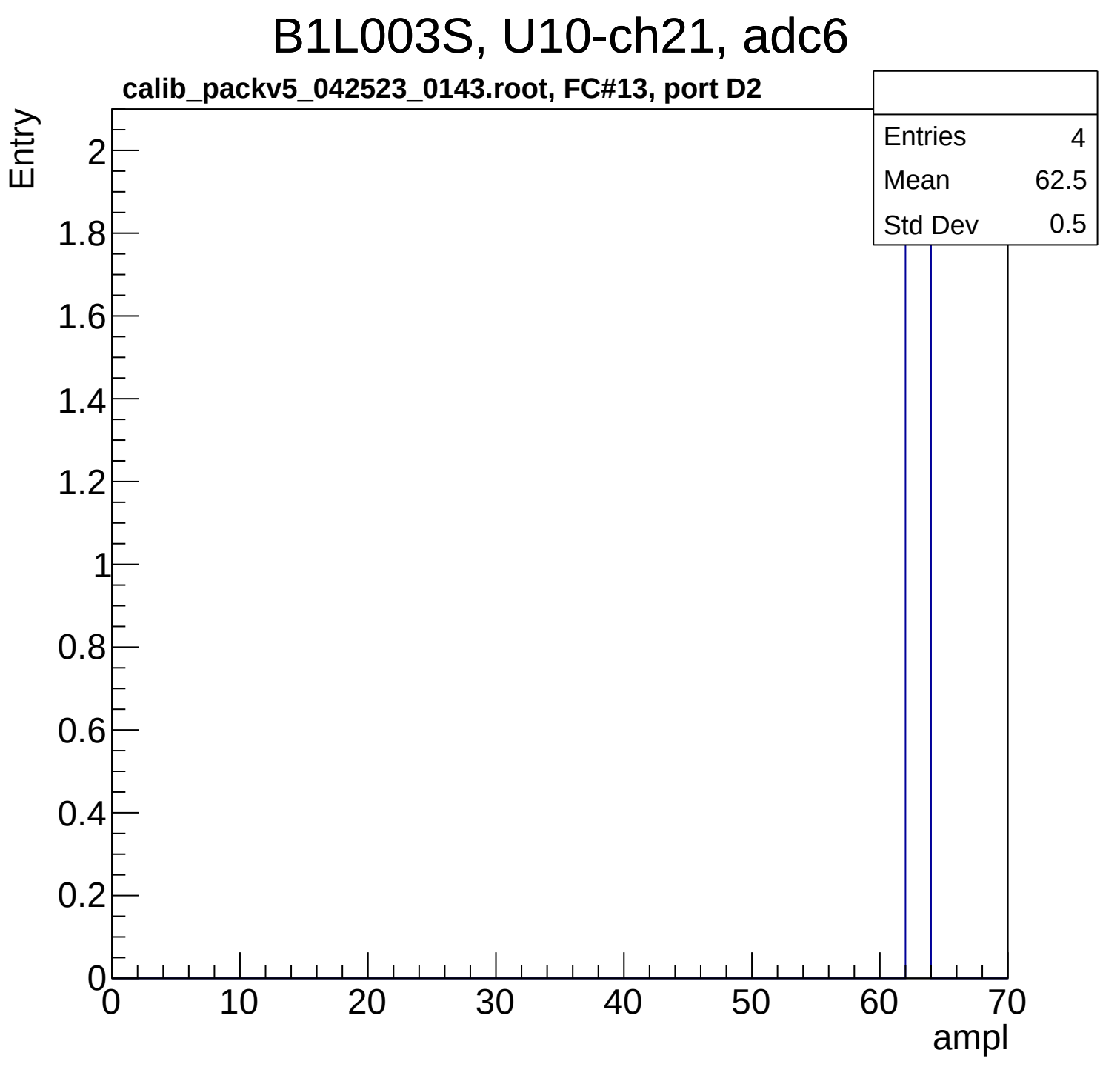
70
ampl


B1L003S, U10-ch21, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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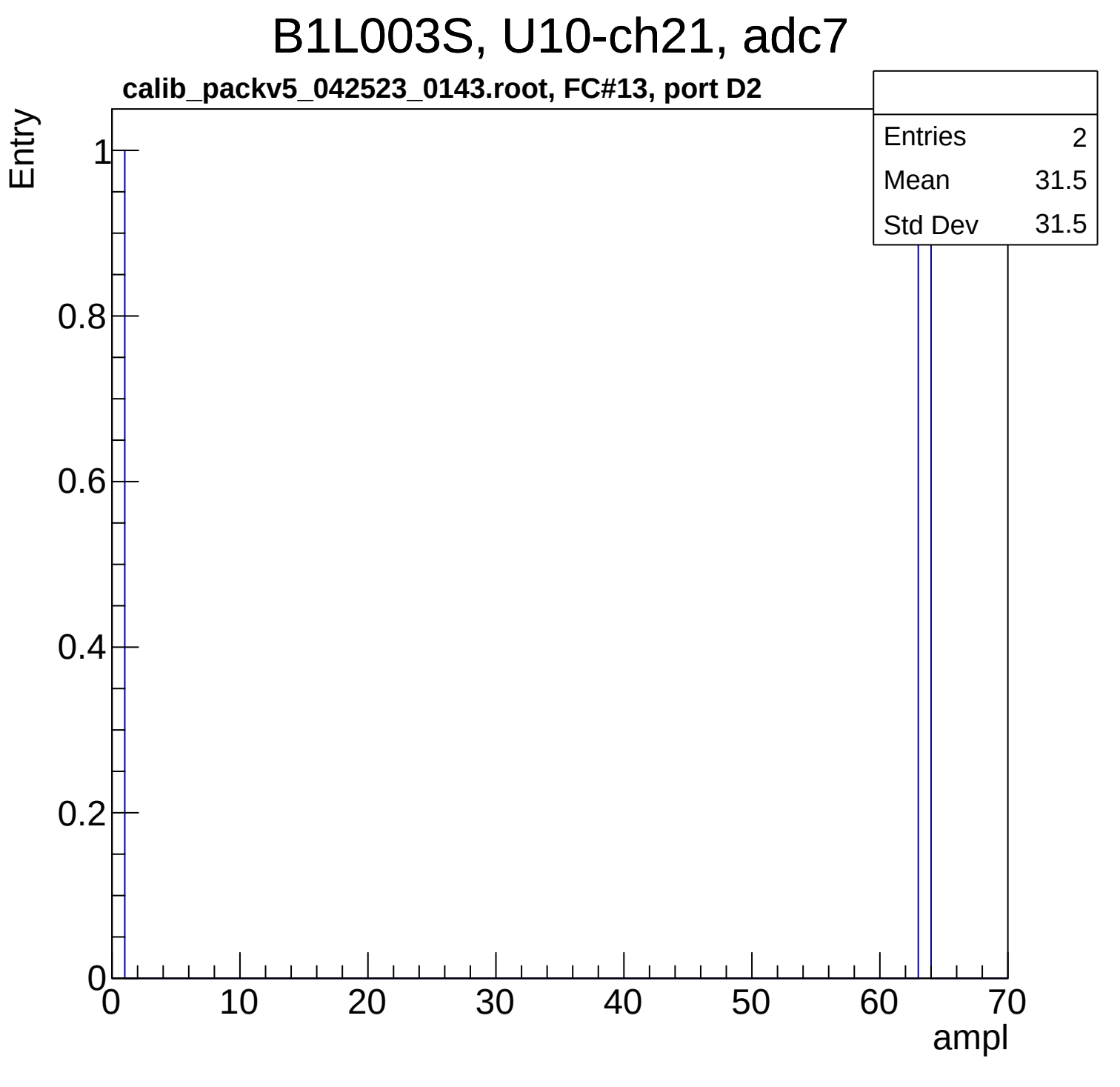
B1L003S, U10-ch21, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
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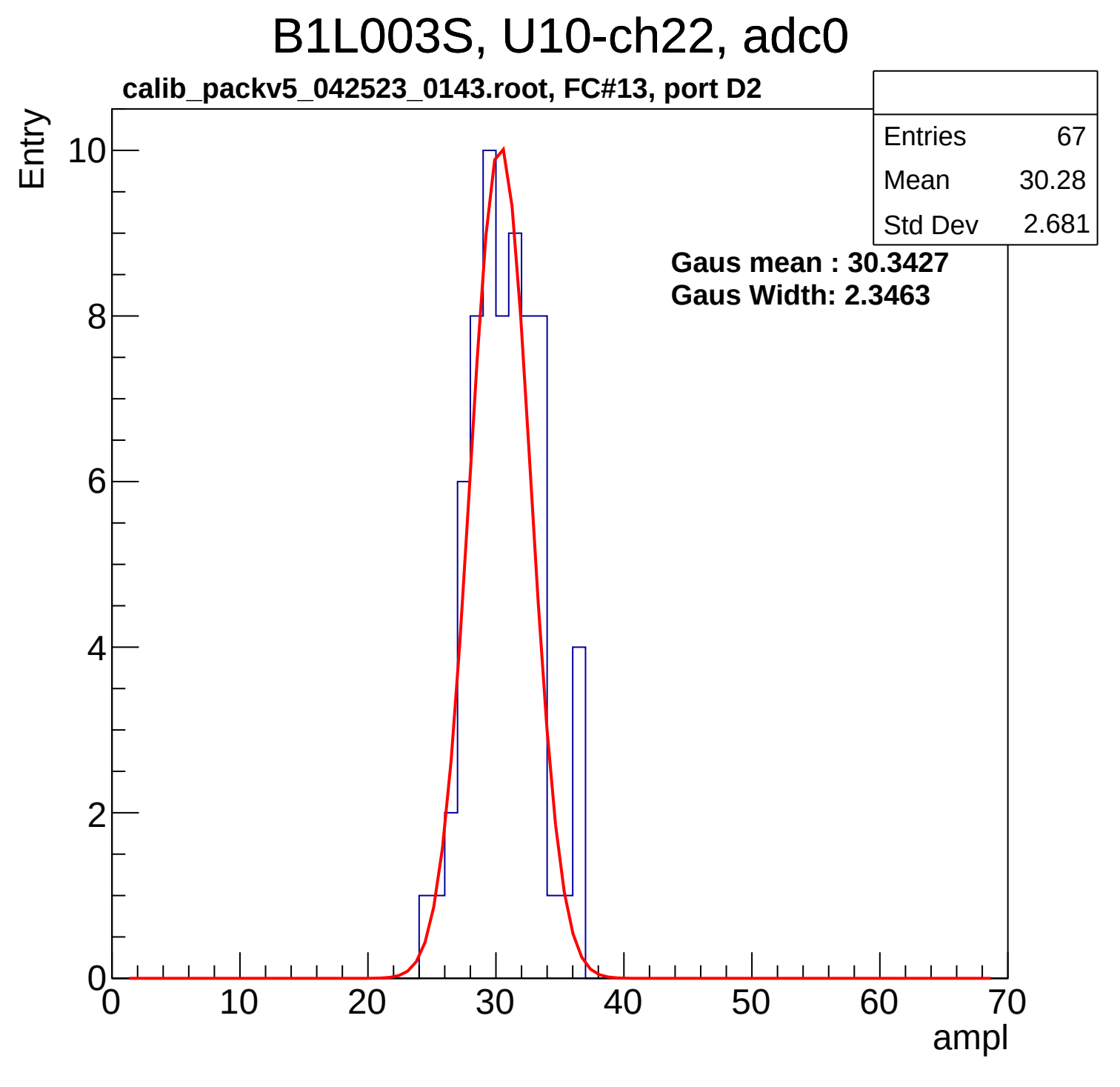
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch22, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 30.28
Std Dev 2.681
Gaus mean : 30.3427
Gaus Width: 2.3463
Entry
ampl
0
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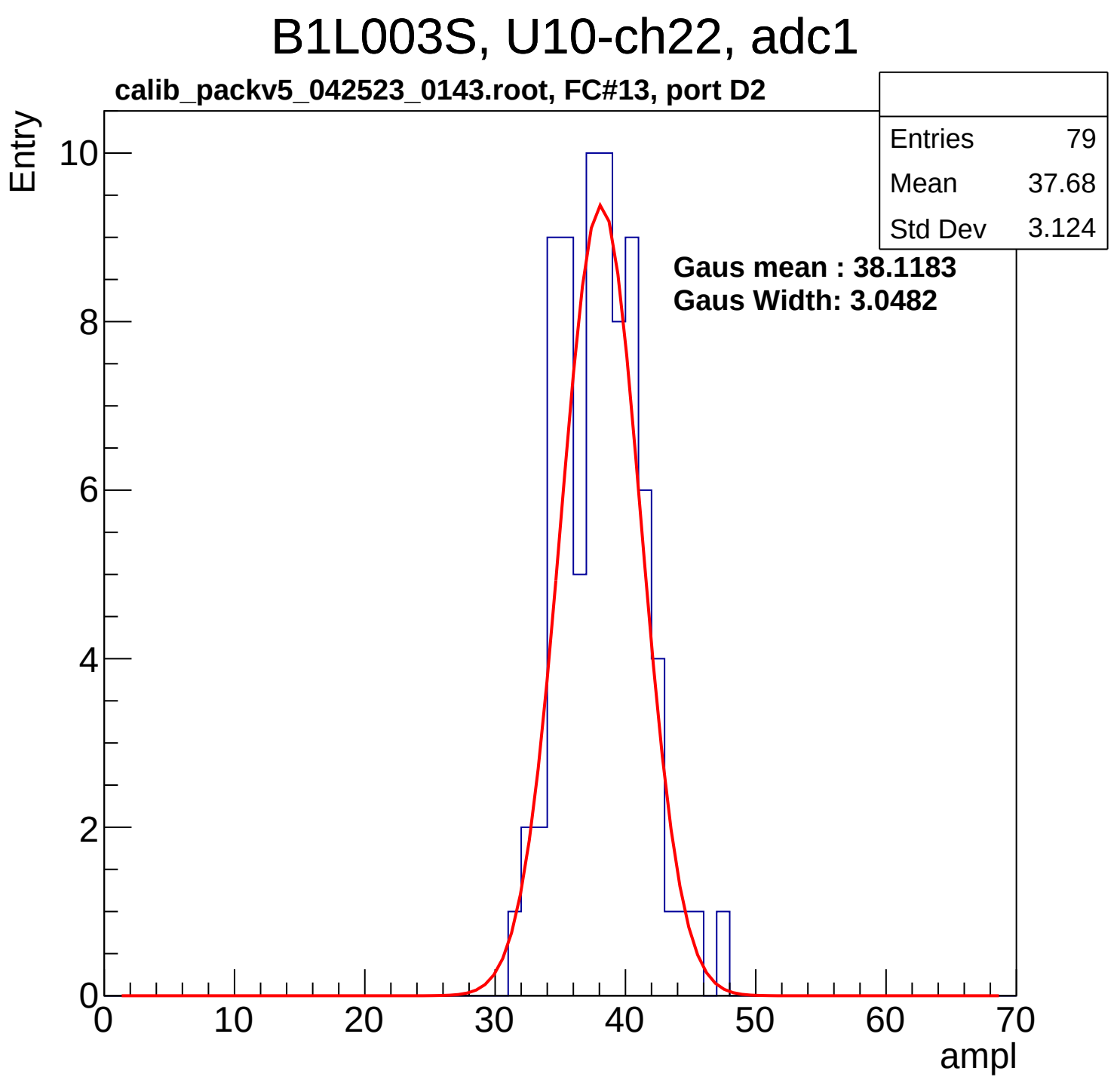
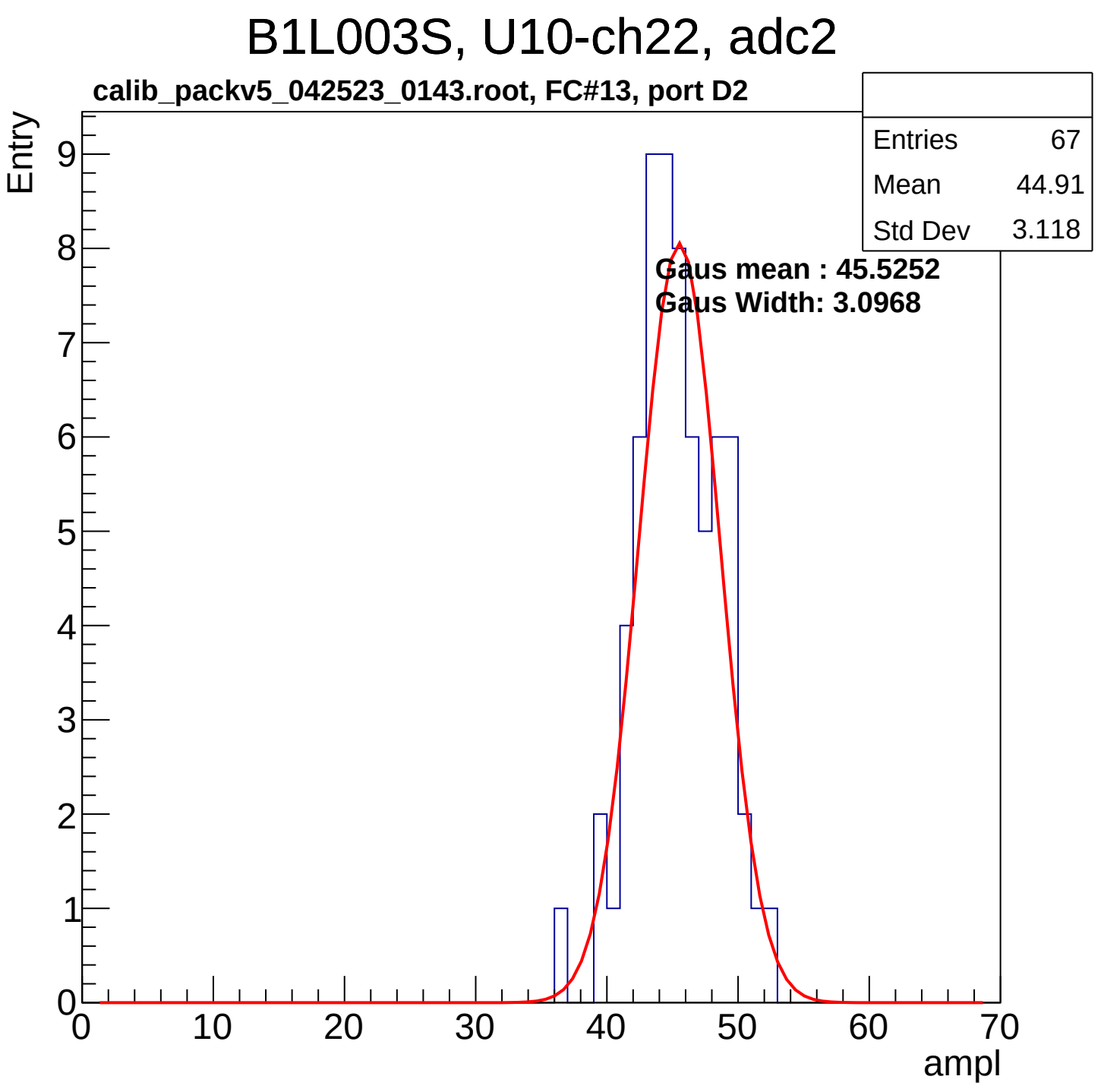
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch22, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 37.68
Std Dev 3.124
Gaus mean : 38.1183
Gaus Width: 3.0482
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch22, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 44.91
Std Dev 3.118
Gaus mean : 45.5252
Gaus Width: 3.0968
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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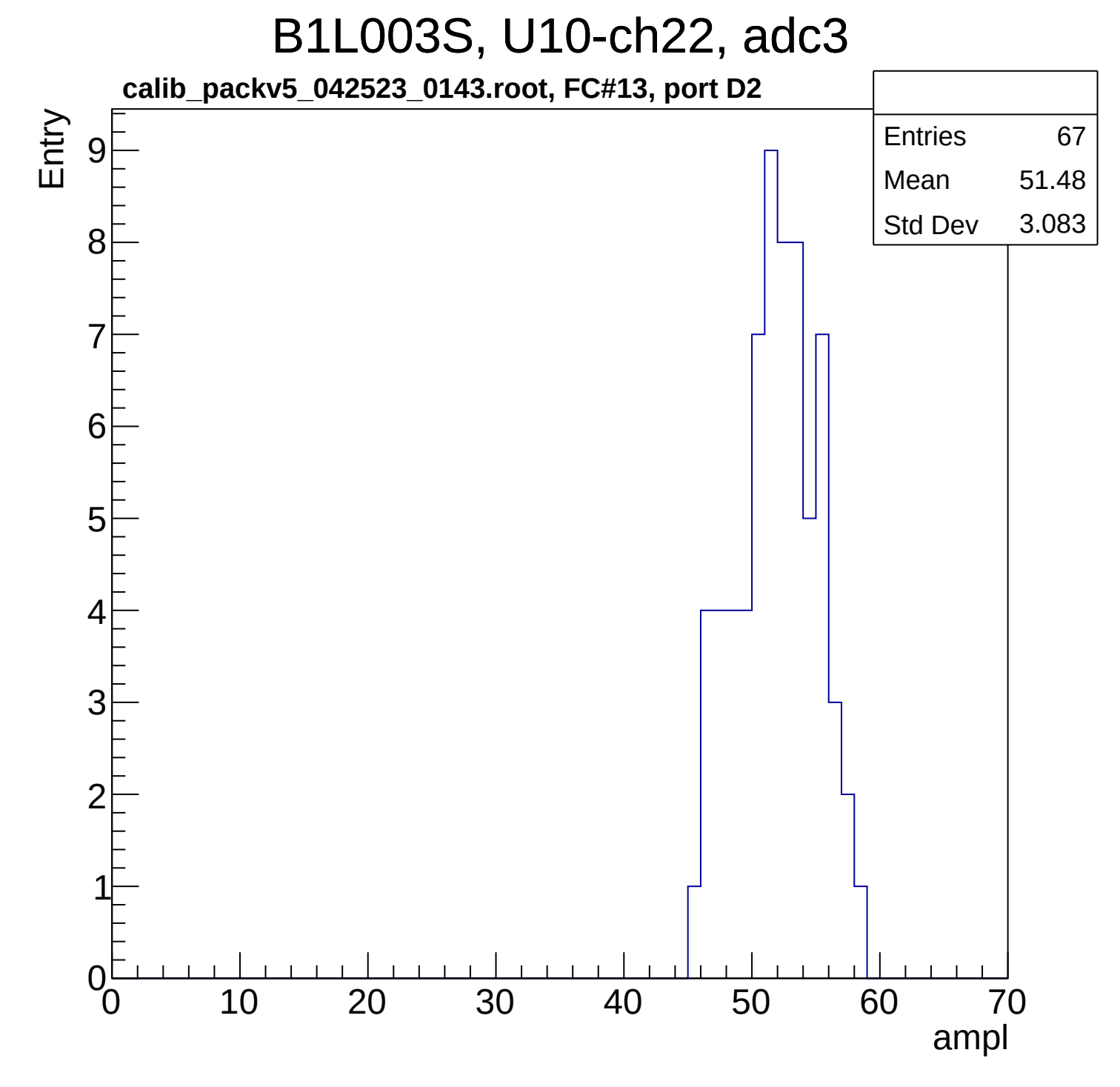
40
50
60
70


B1L003S, U10-ch22, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 51.48
Std Dev 3.083
Entry
ampl
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2
3
4
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6
7
8
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0
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60
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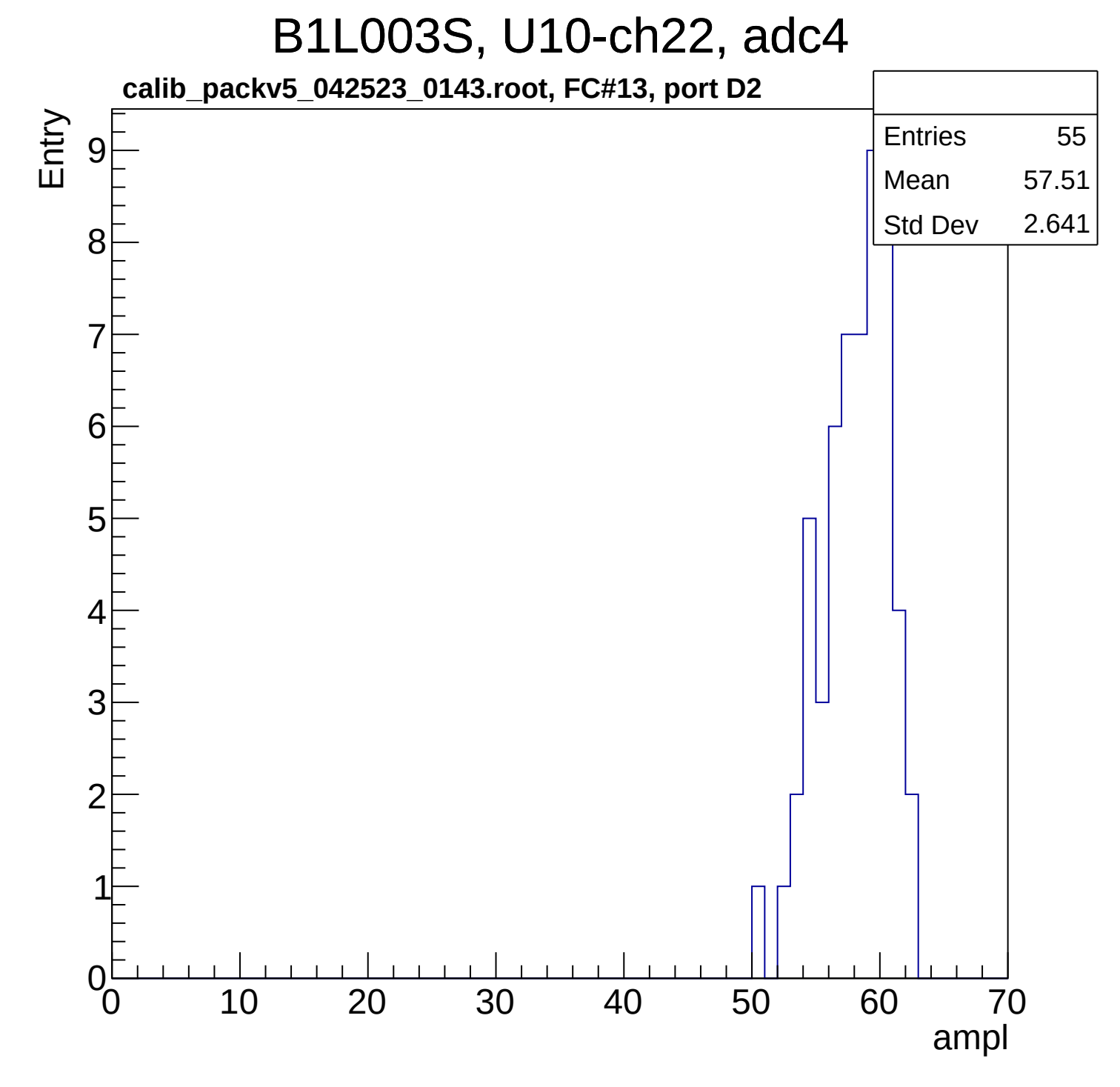
B1L003S, U10-ch22, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.51
Std Dev 2.641
Entry
ampl
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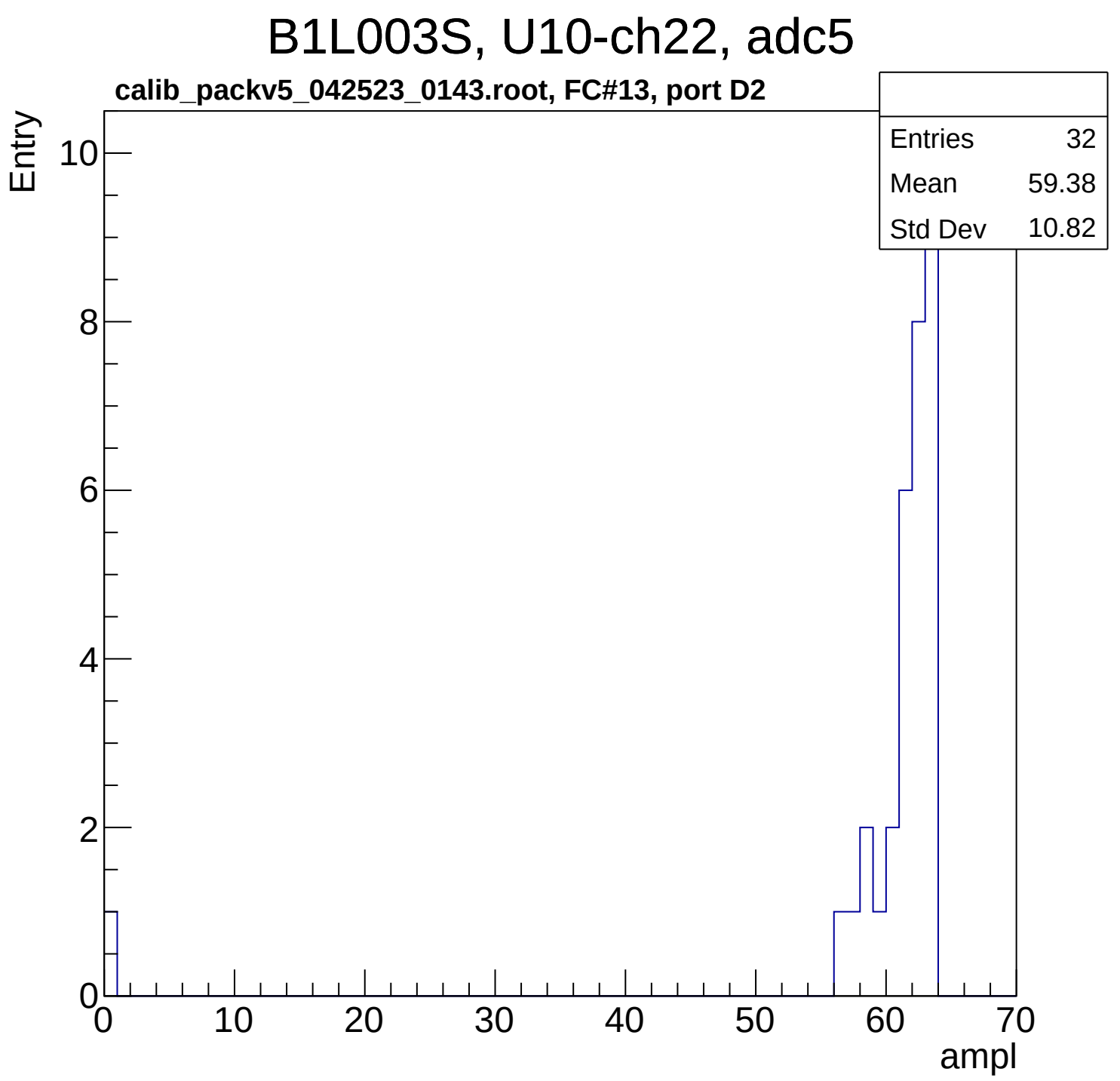
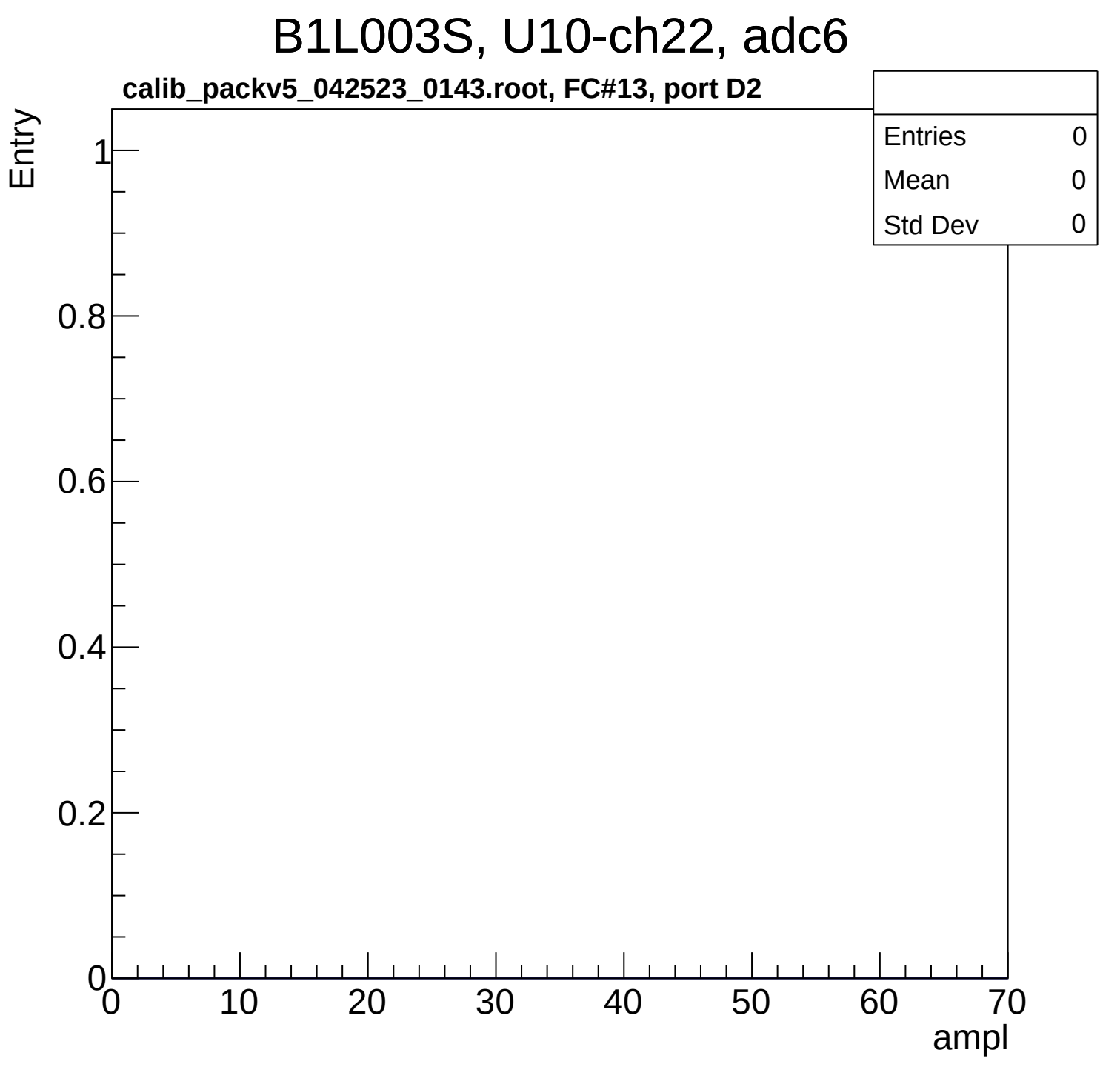
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch22, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 59.38
Std Dev 10.82
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch22, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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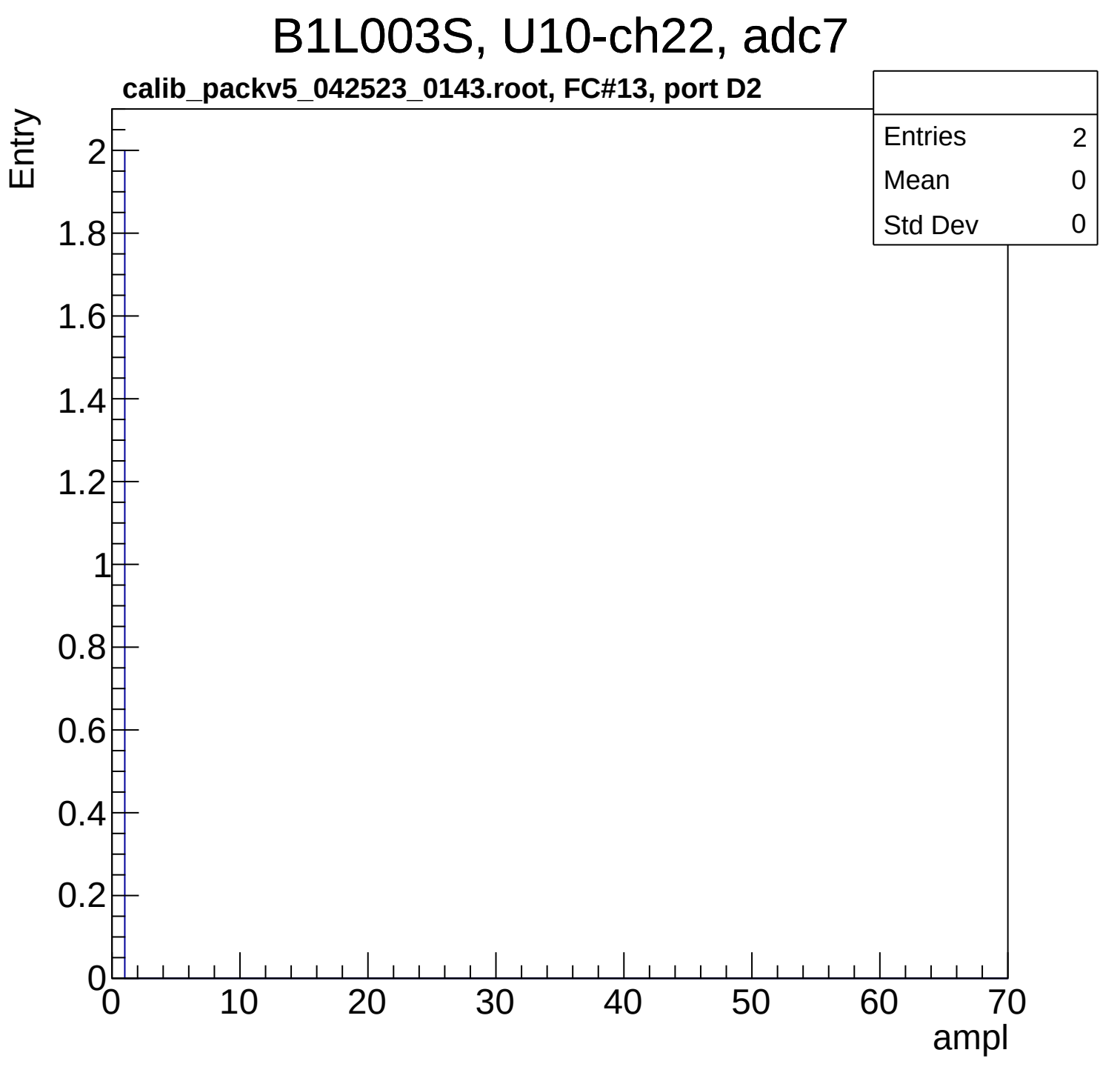
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch22, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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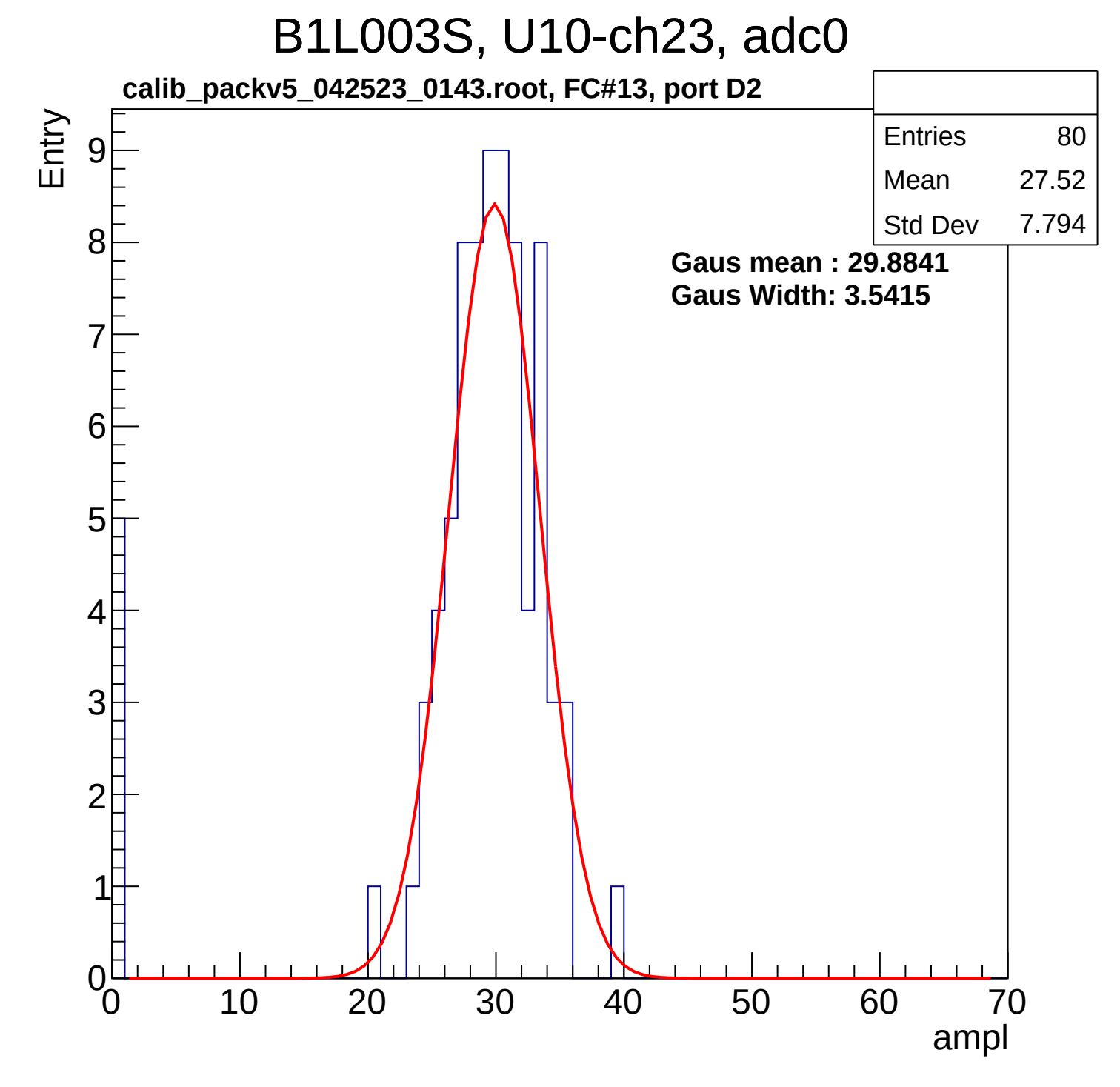
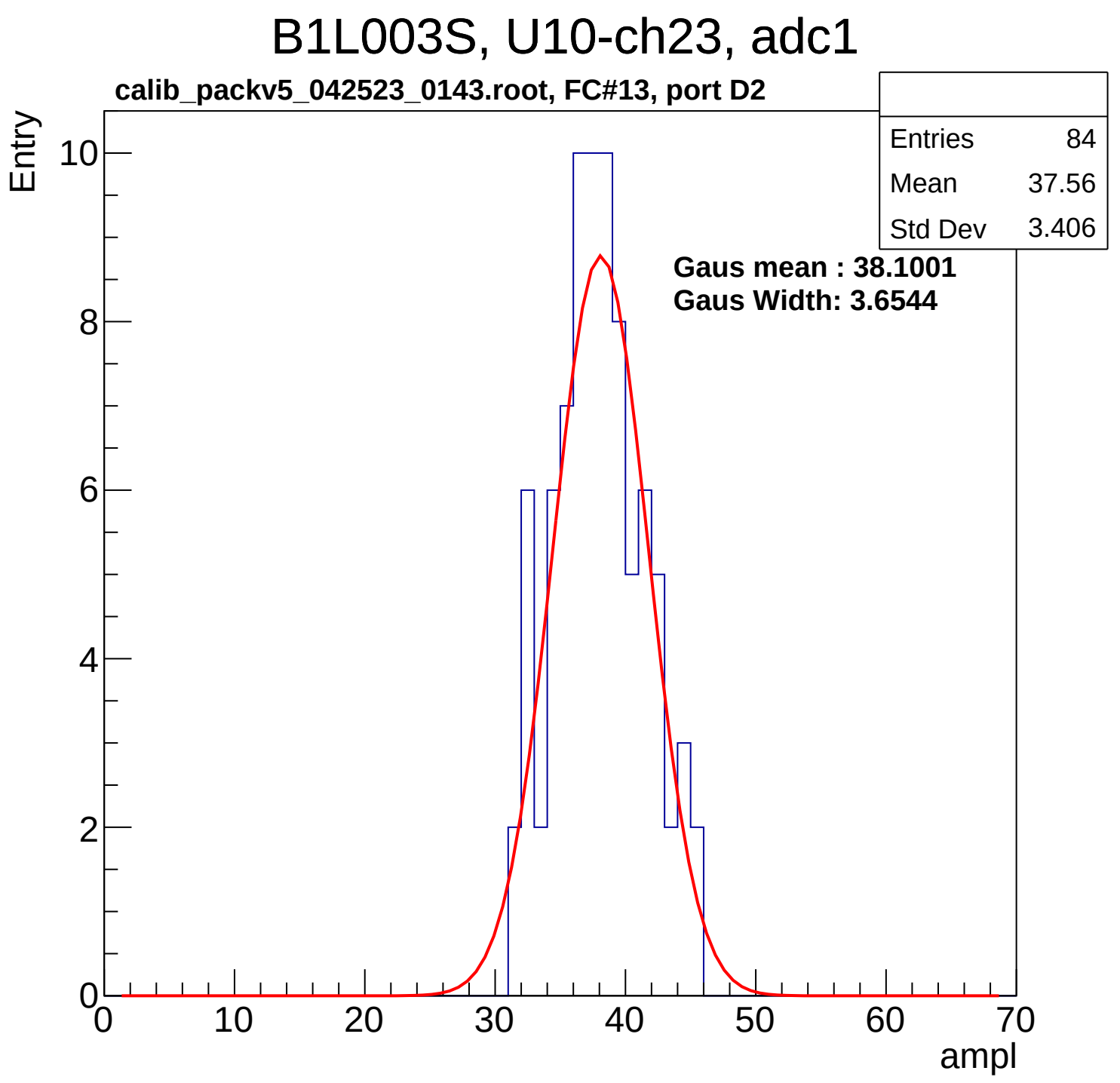

B1L003S, U10-ch23, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 27.52
Std Dev 7.794
Gaus mean : 29.8841
Gaus Width: 3.5415
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch23, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 37.56
Std Dev 3.406
Gaus mean : 38.1001
Gaus Width: 3.6544
Entry
ampl
0
2
4
6
8
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0
10
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50
60
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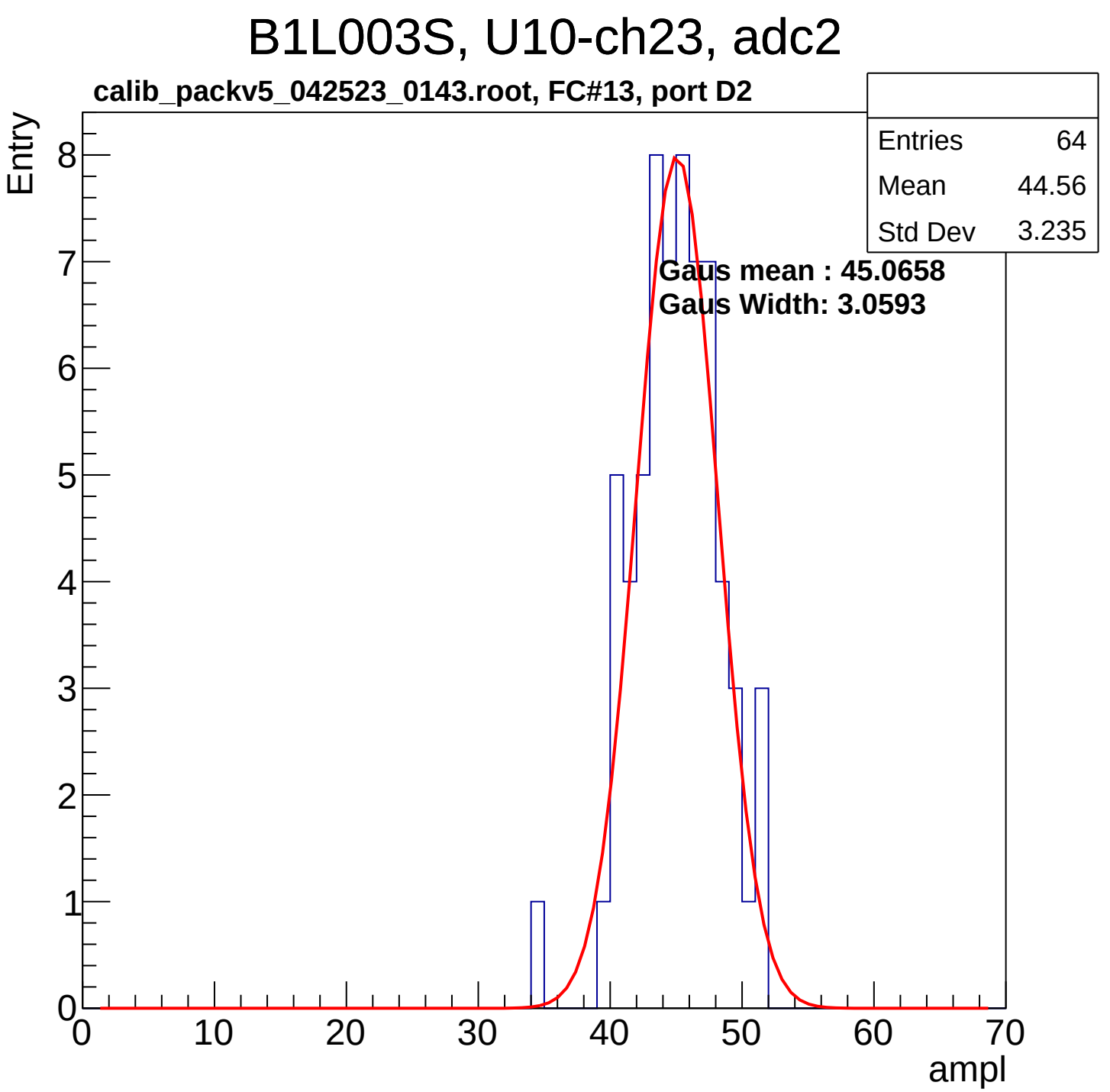
B1L003S, U10-ch23, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 44.56
Std Dev 3.235
Gaus mean : 45.0658
Gaus Width: 3.0593
Entry
ampl
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2
3
4
5
6
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8
0
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60
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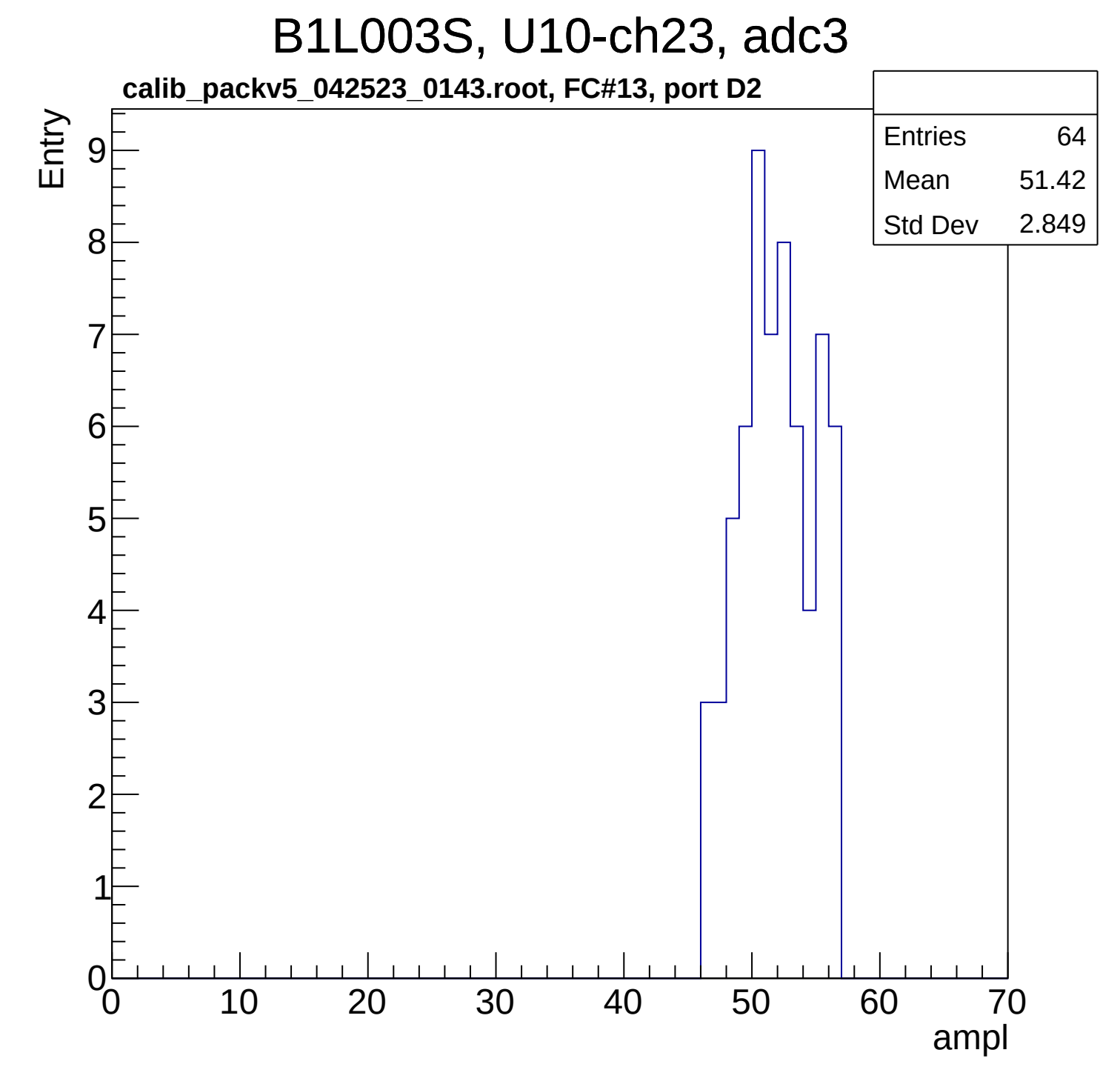
B1L003S, U10-ch23, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 51.42
Std Dev 2.849
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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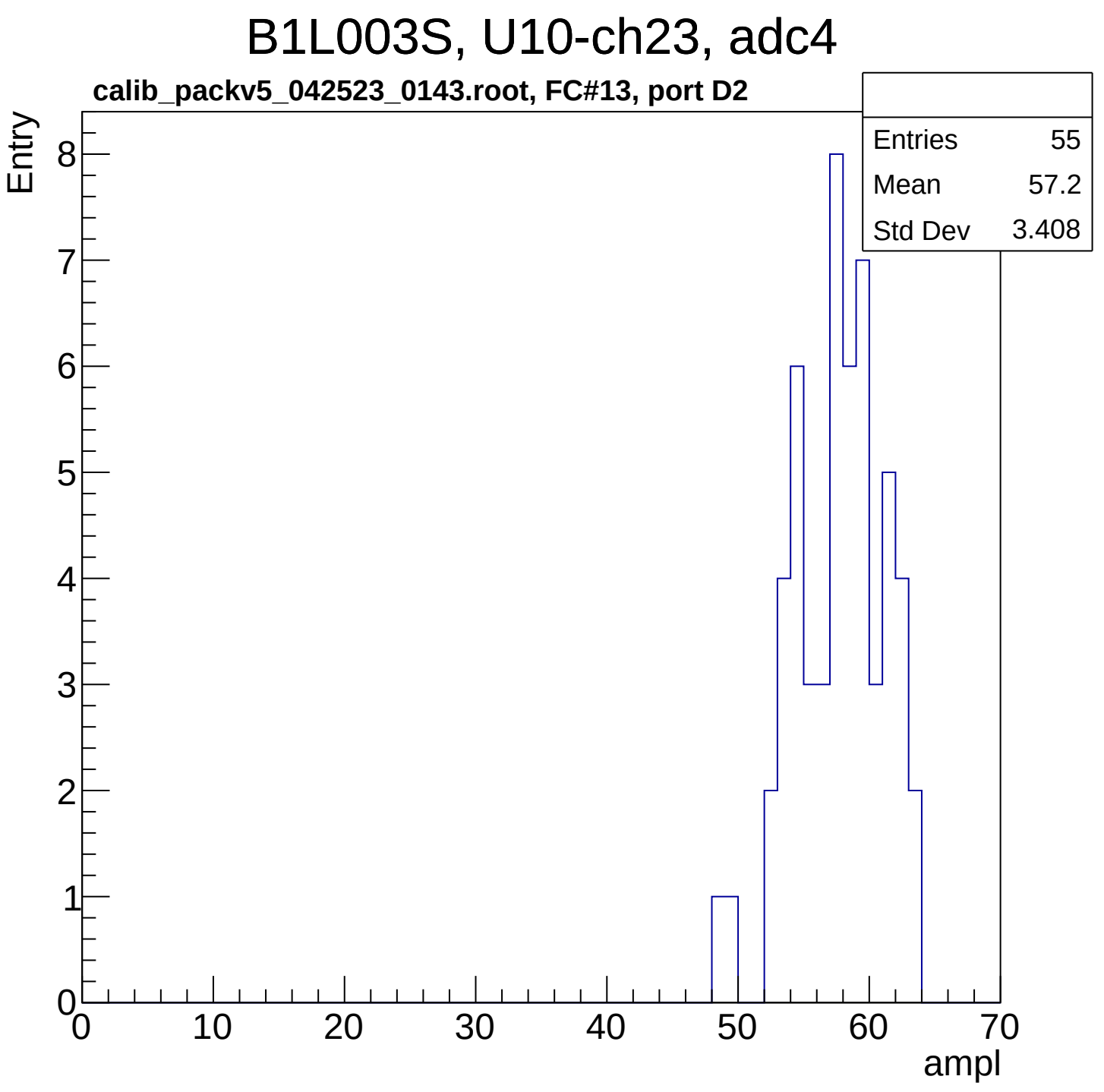
B1L003S, U10-ch23, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.2
Std Dev 3.408
Entry
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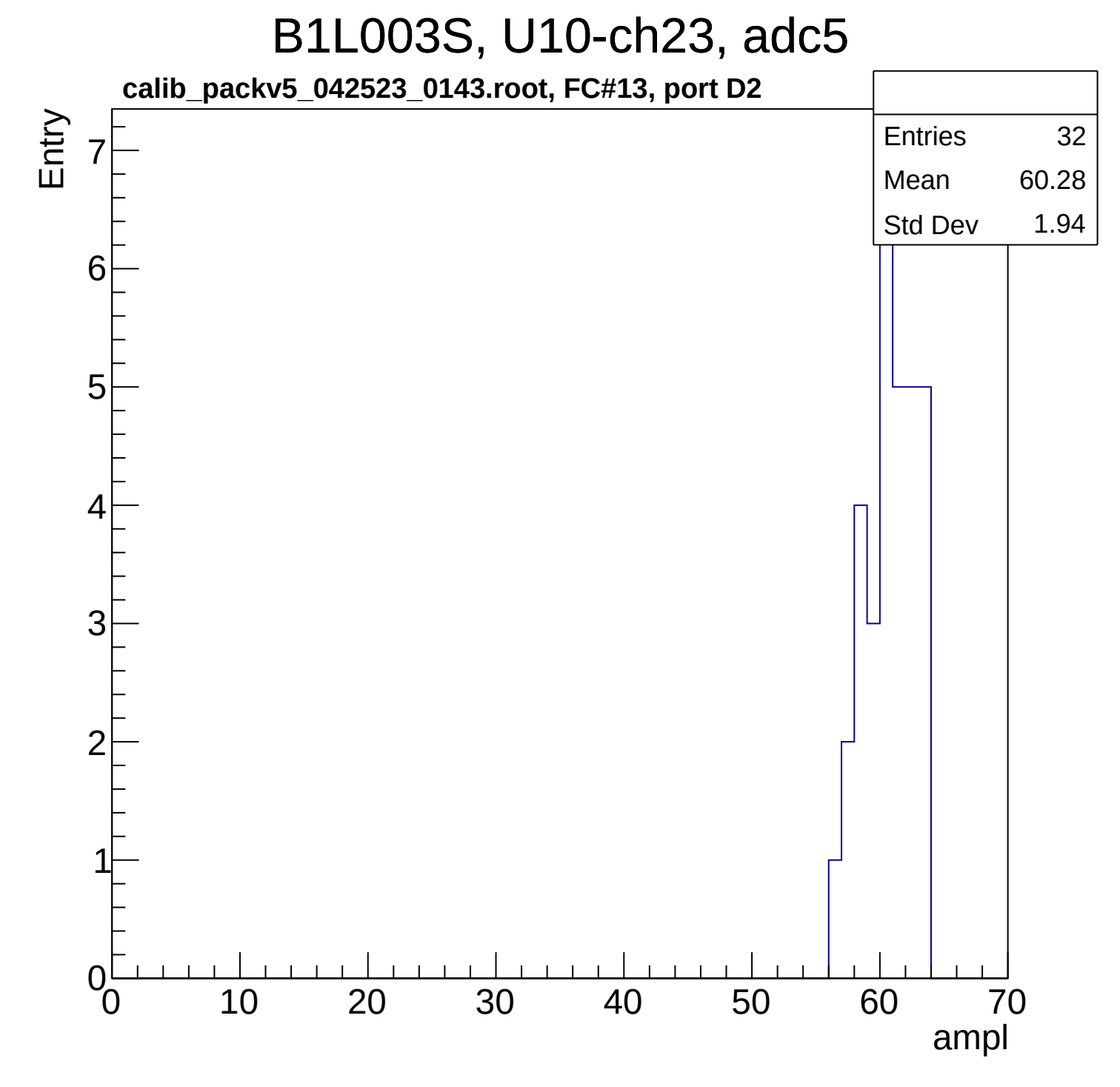
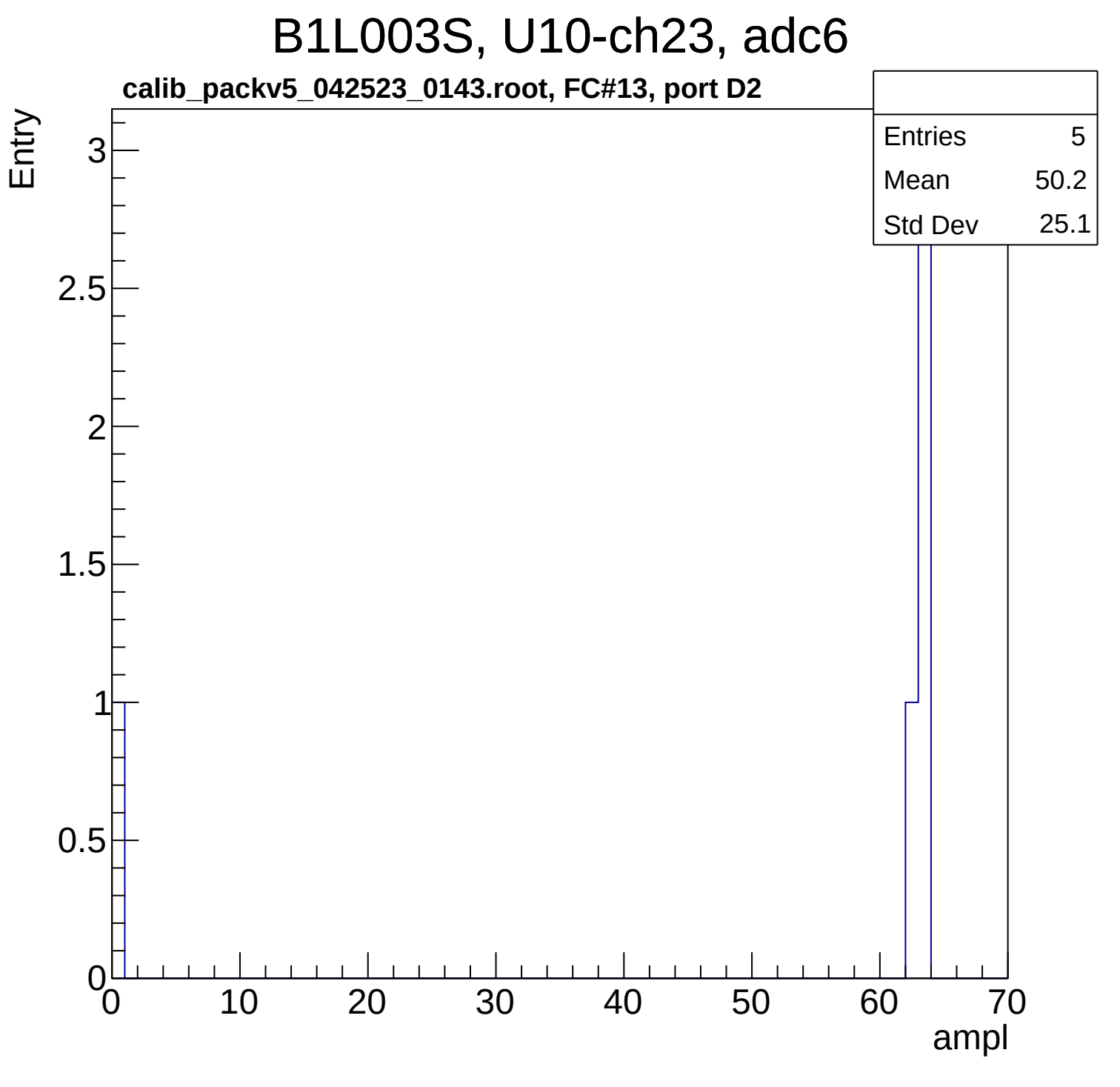
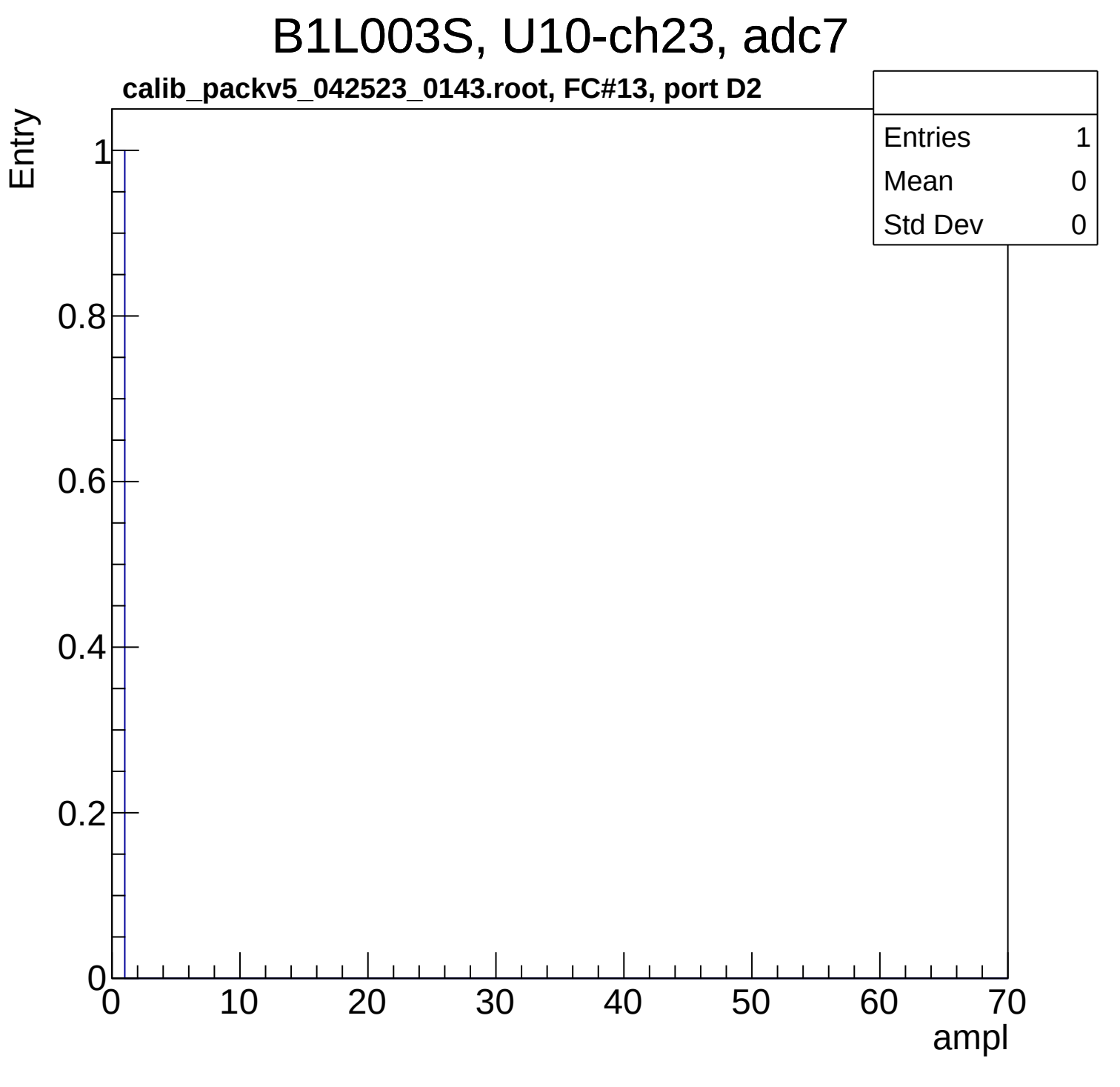
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch23, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 60.28
Std Dev 1.94
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch23, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 50.2
Std Dev 25.1
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch23, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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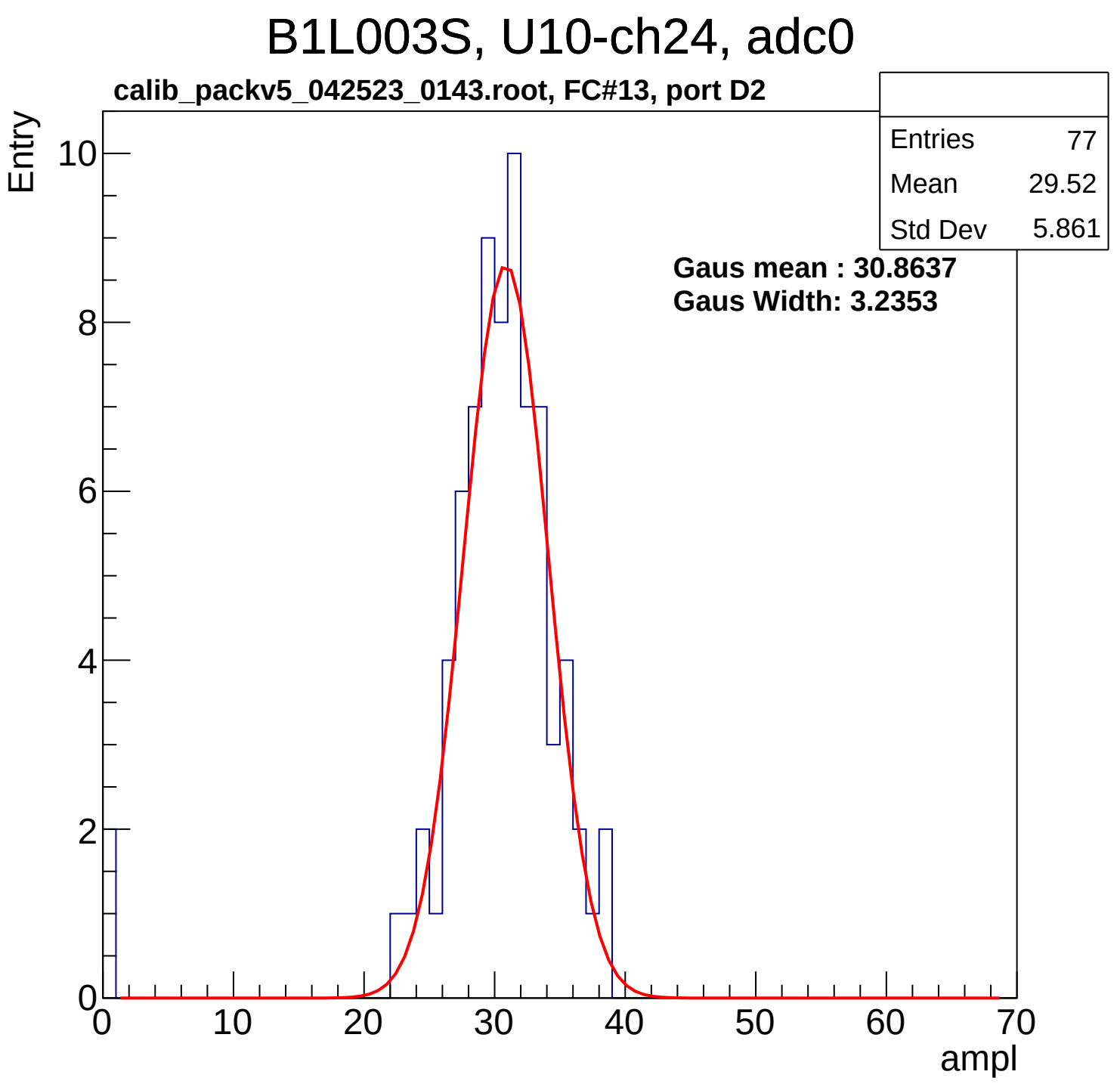
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch24, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 29.52
Std Dev 5.861
Gaus mean : 30.8637
Gaus Width: 3.2353
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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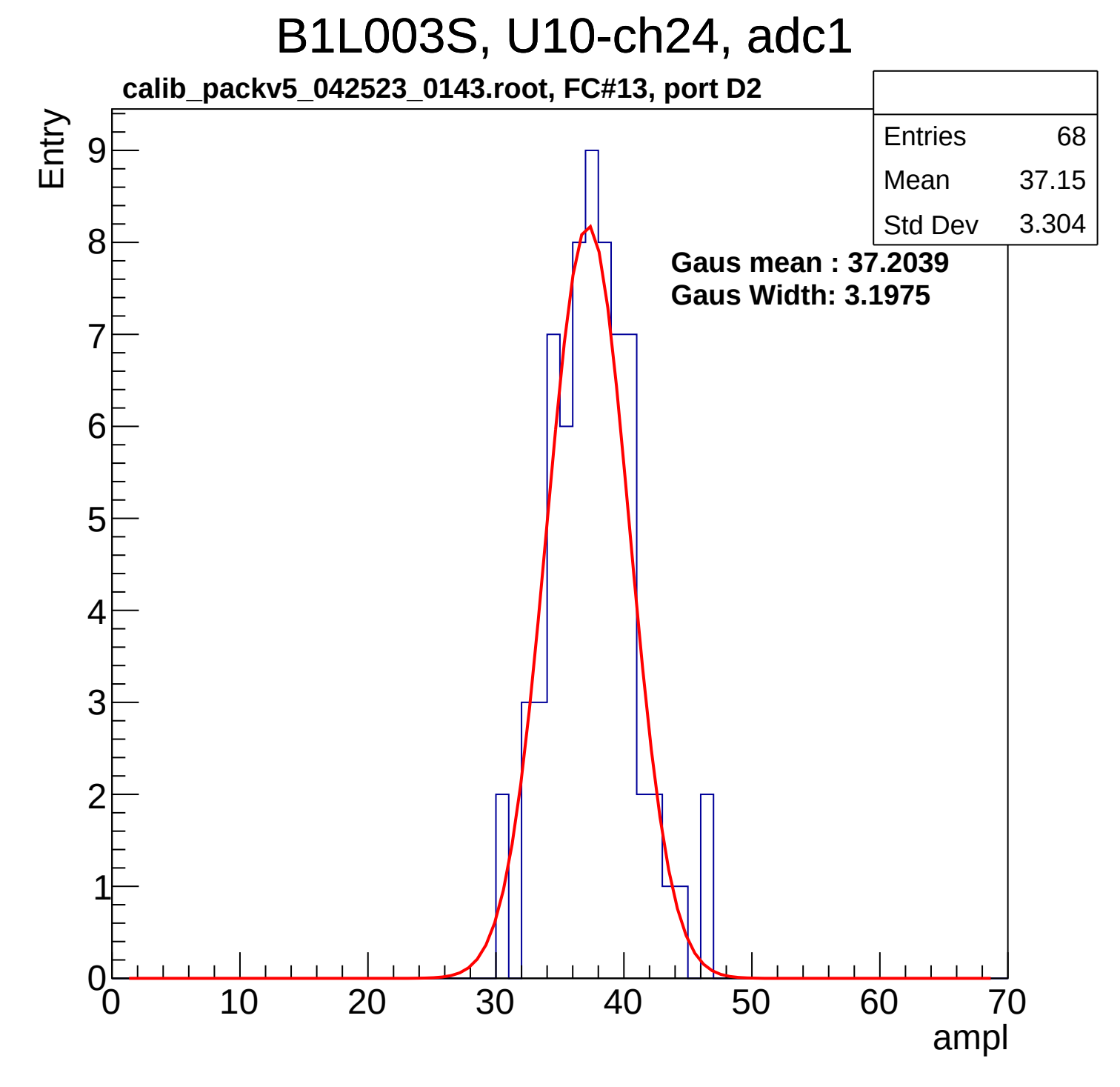
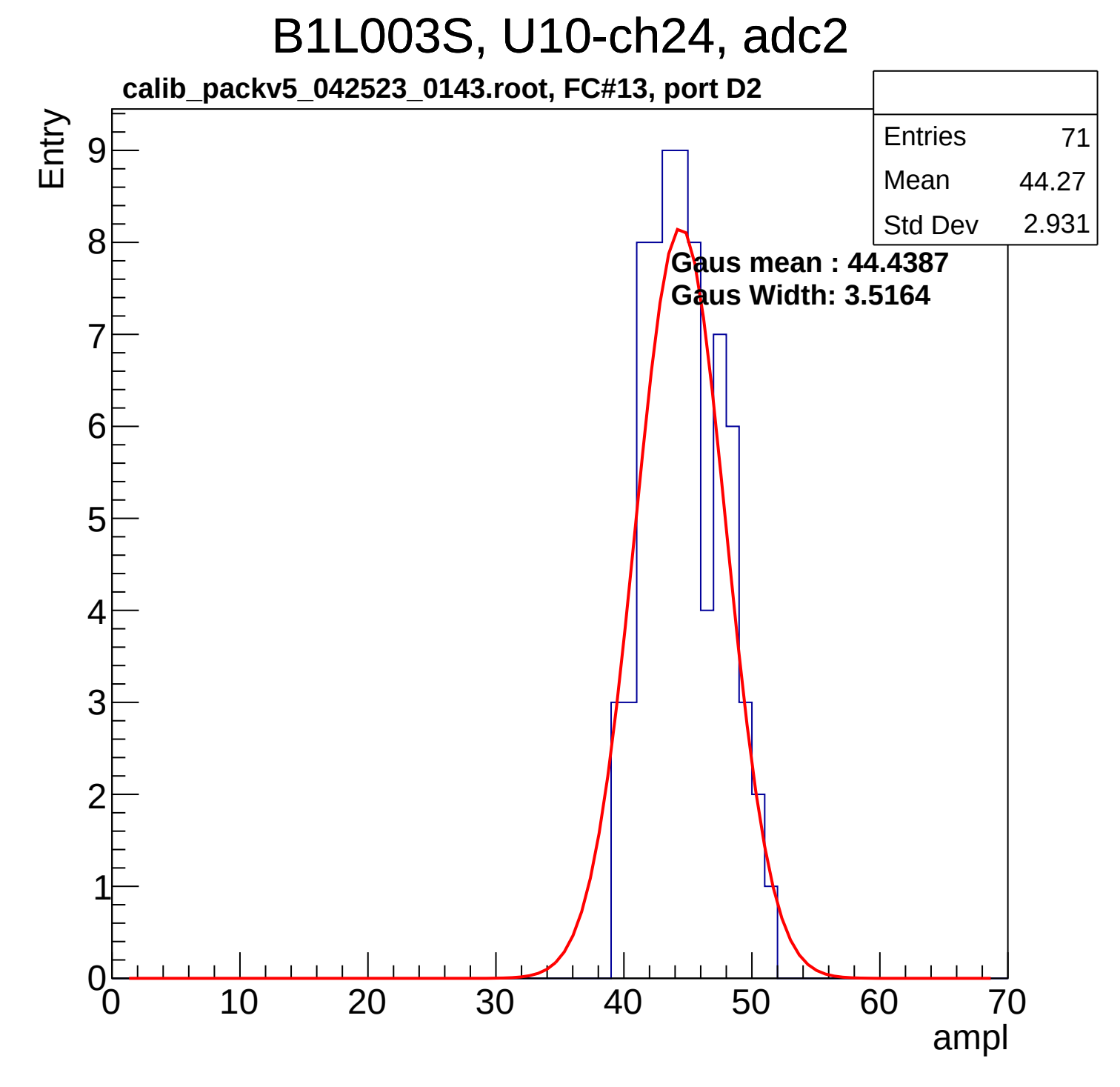

B1L003S, U10-ch24, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 37.15
Std Dev 3.304
Gaus mean : 37.2039
Gaus Width: 3.1975
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch24, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 44.27
Std Dev 2.931
Gaus mean : 44.4387
Gaus Width: 3.5164
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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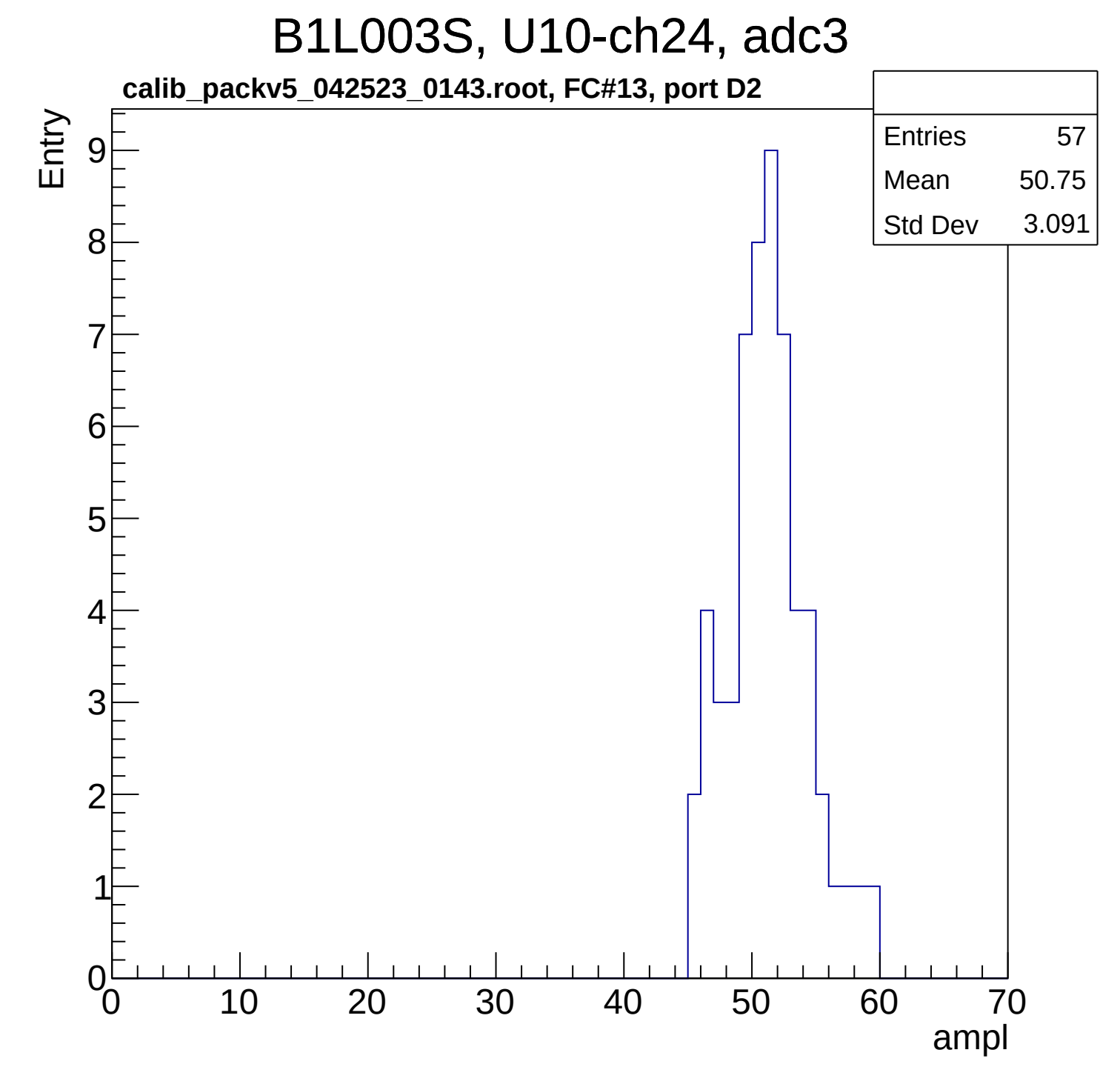
B1L003S, U10-ch24, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 50.75
Std Dev 3.091
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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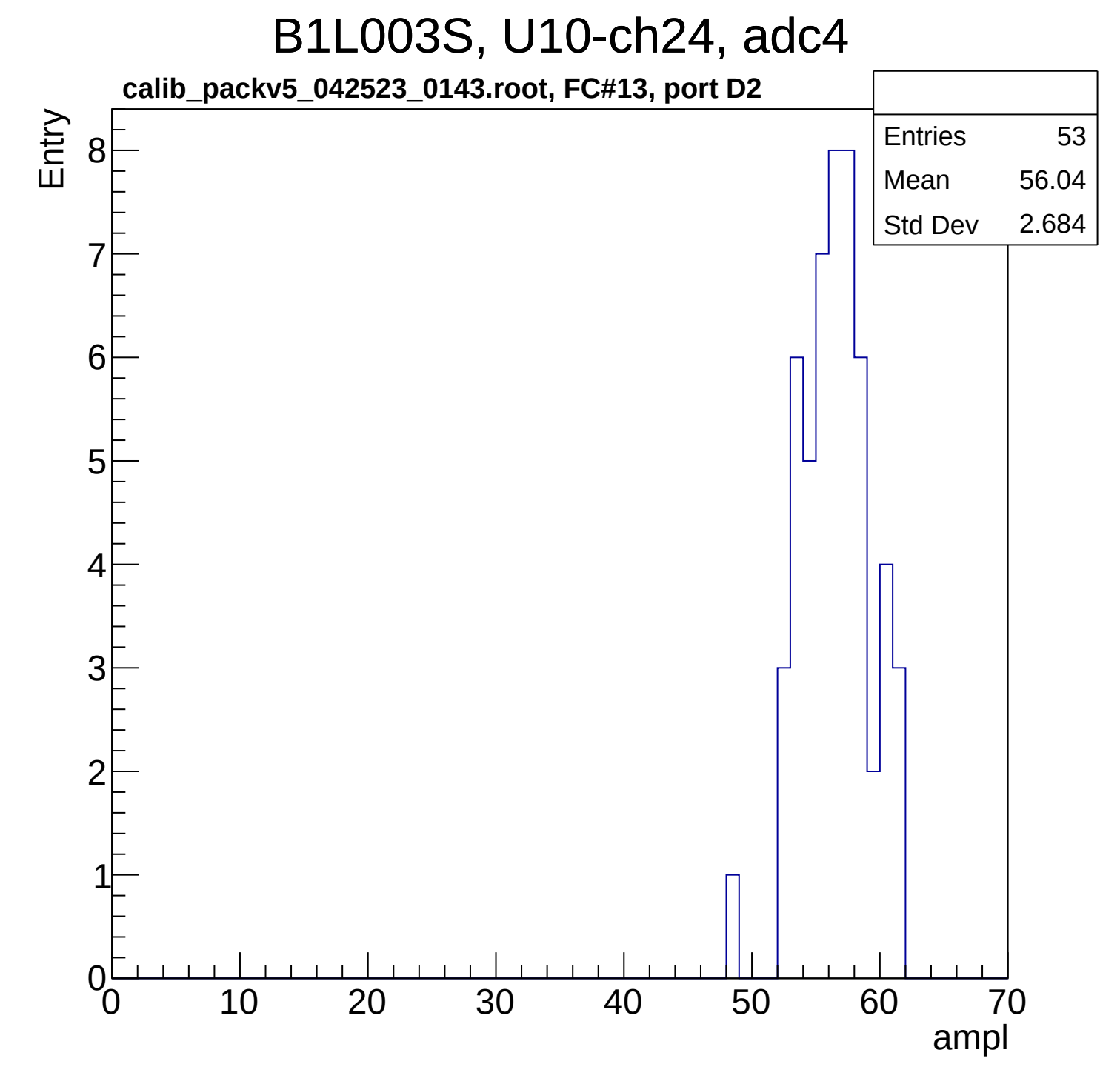
B1L003S, U10-ch24, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 56.04
Std Dev 2.684
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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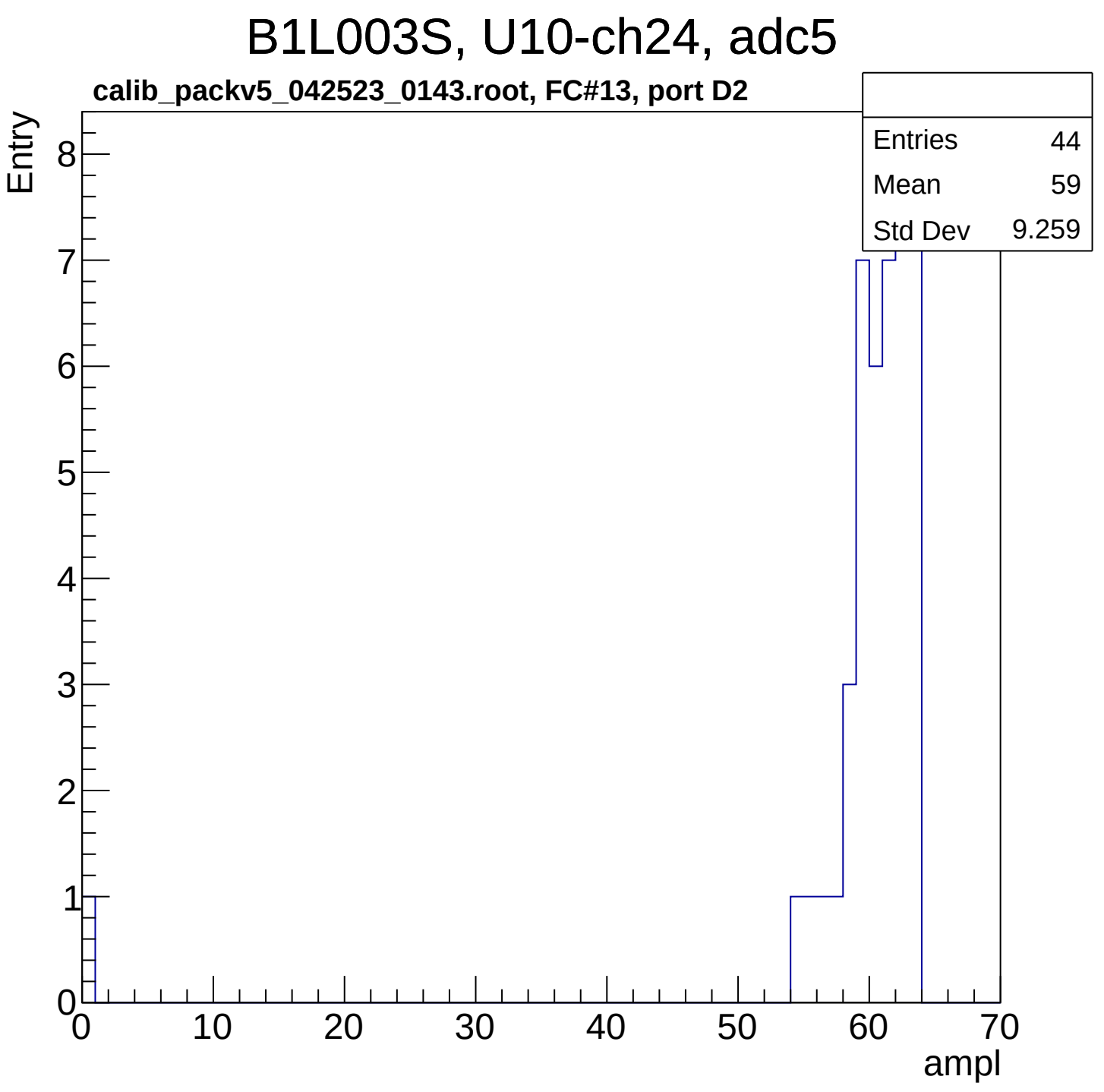
B1L003S, U10-ch24, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59
Std Dev 9.259
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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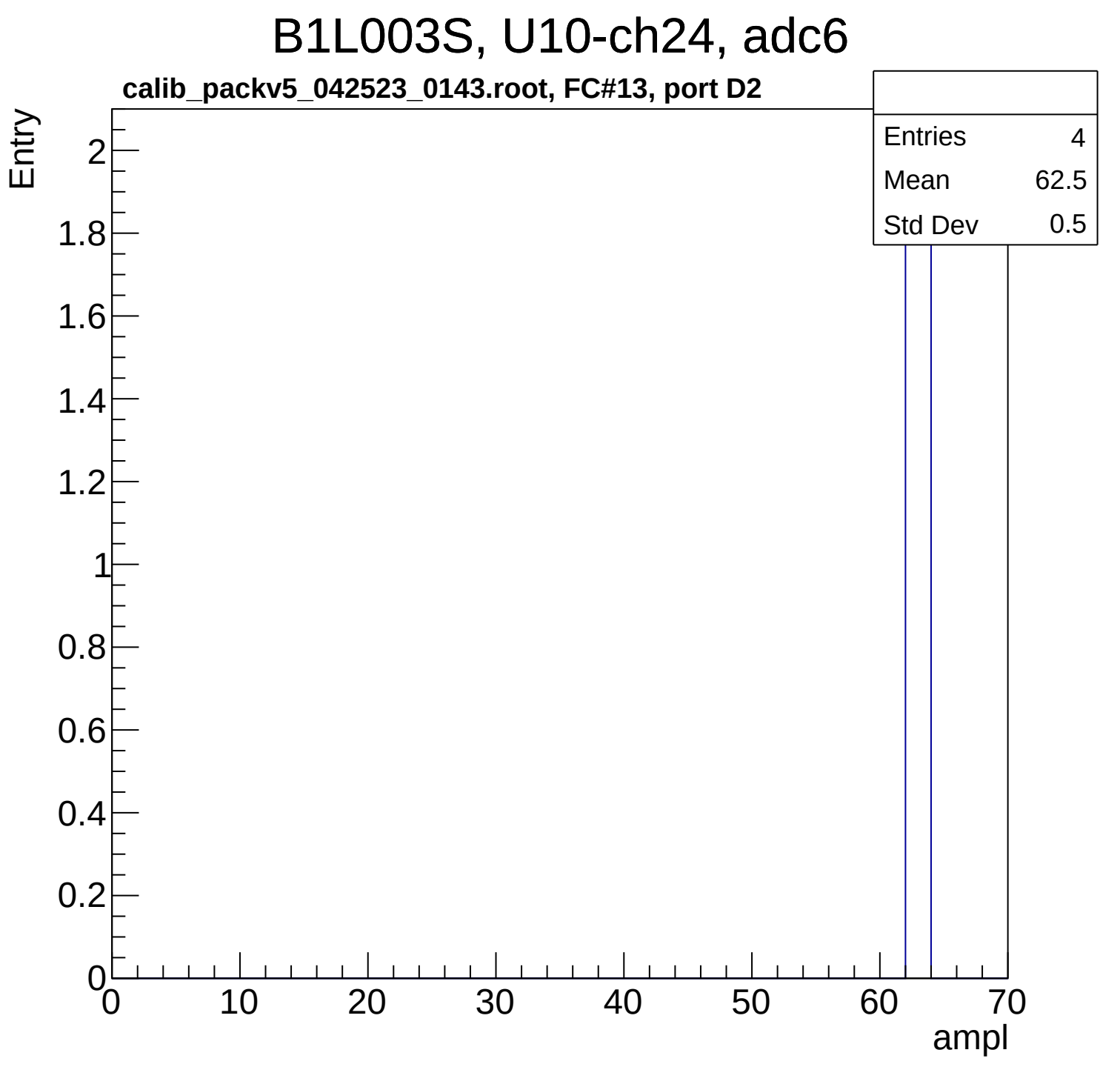
B1L003S, U10-ch24, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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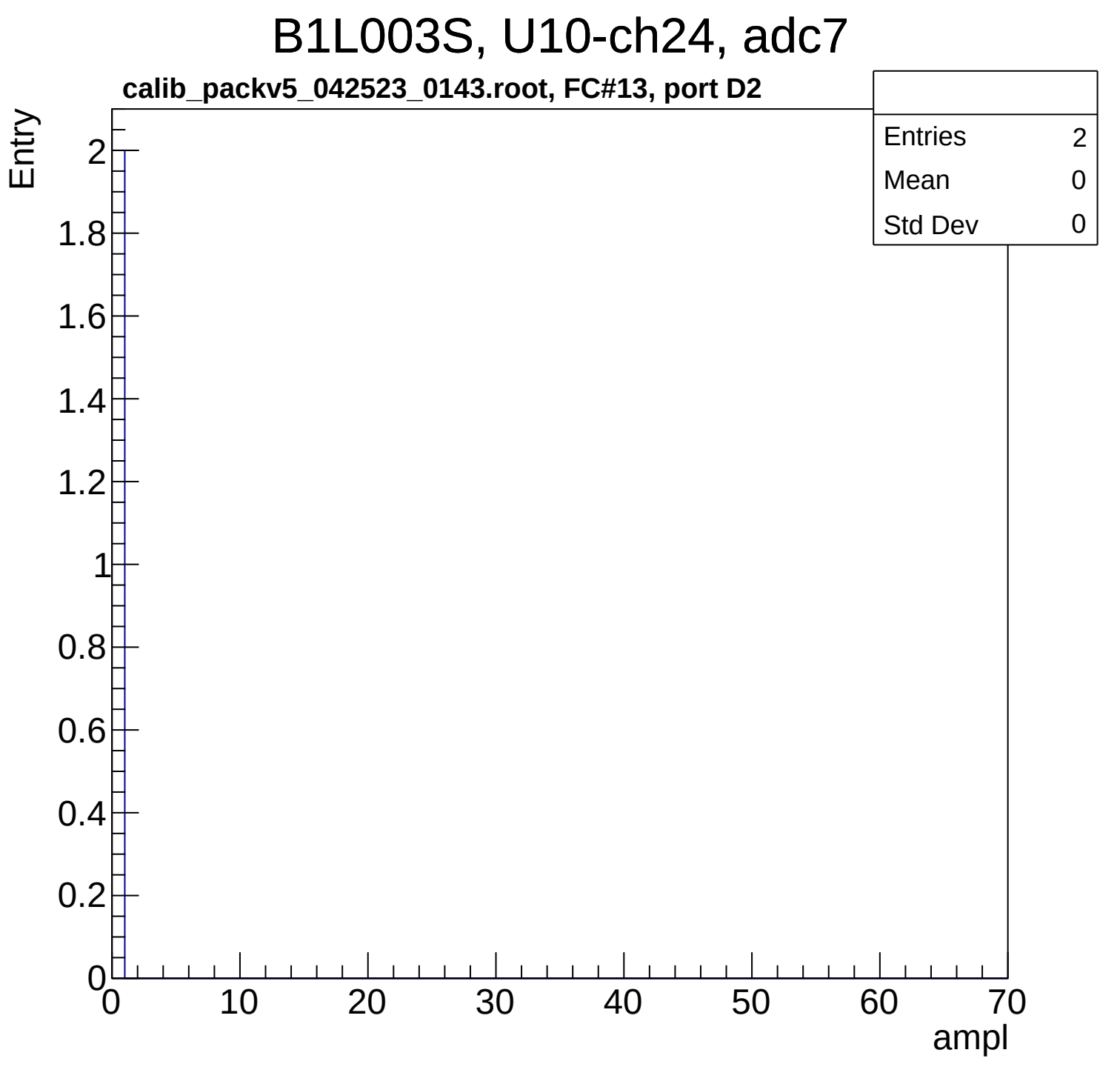
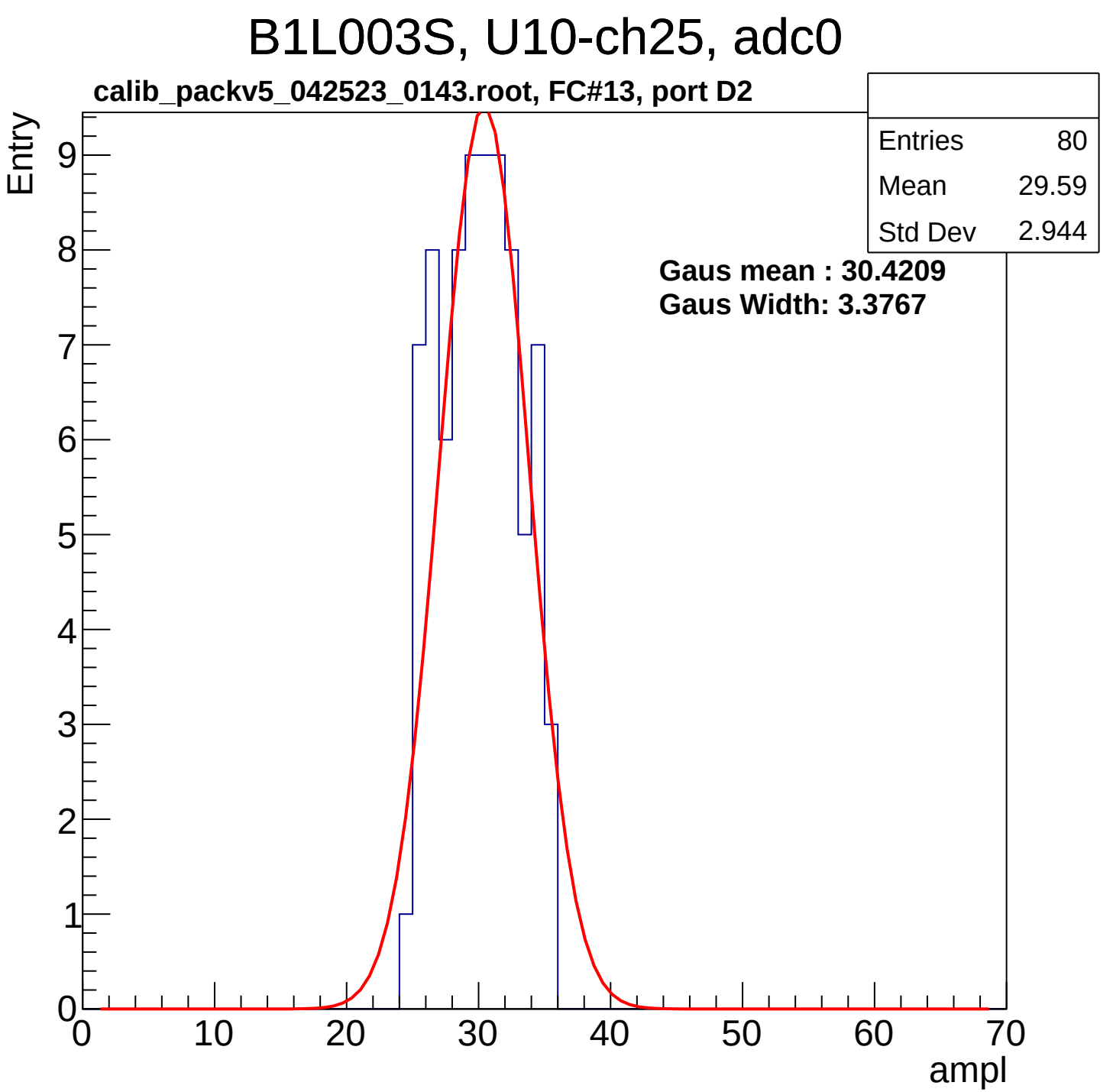
40
50
60
70


B1L003S, U10-ch24, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch25, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 29.59
Std Dev 2.944
Gaus mean : 30.4209
Gaus Width: 3.3767
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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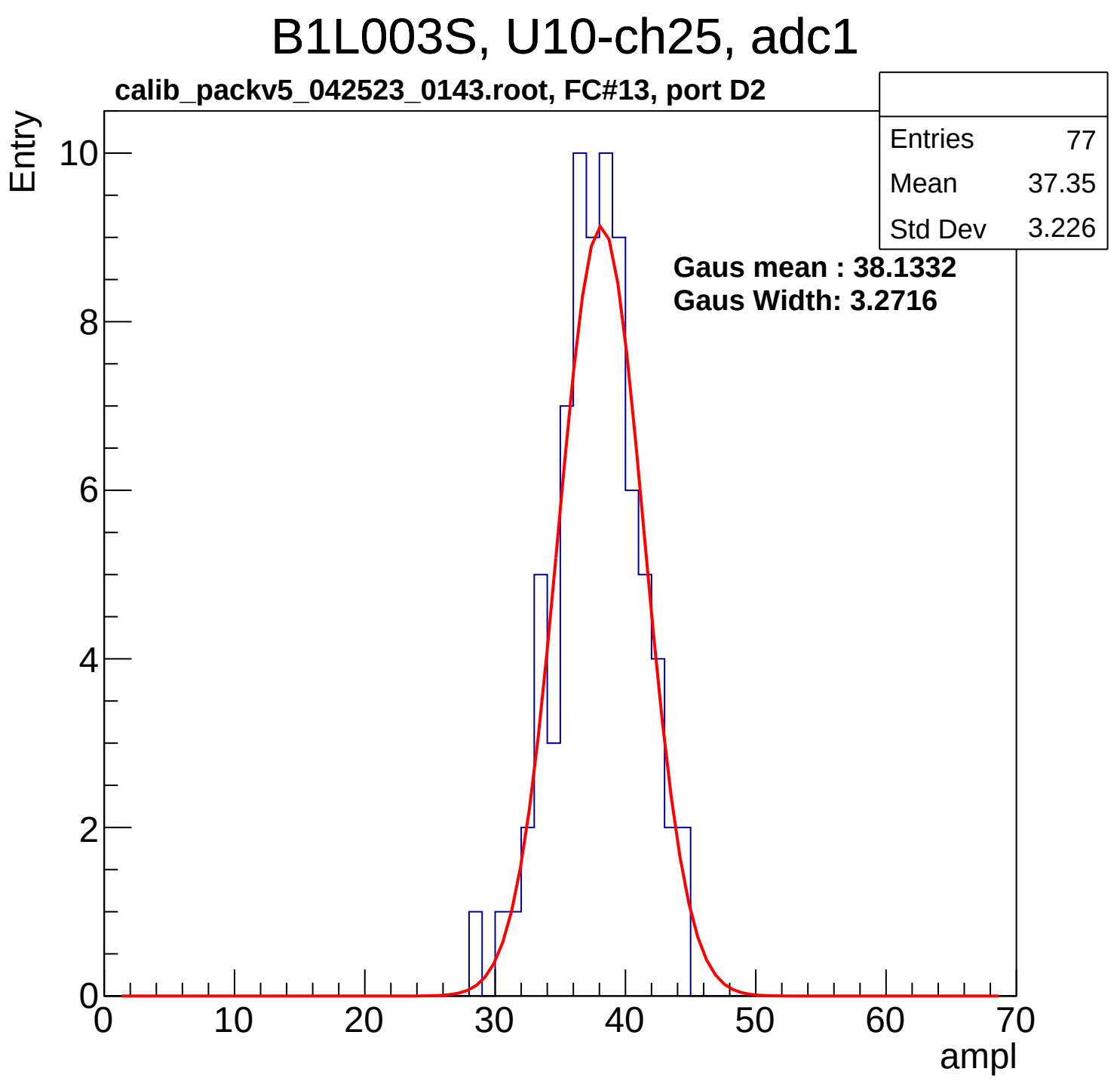
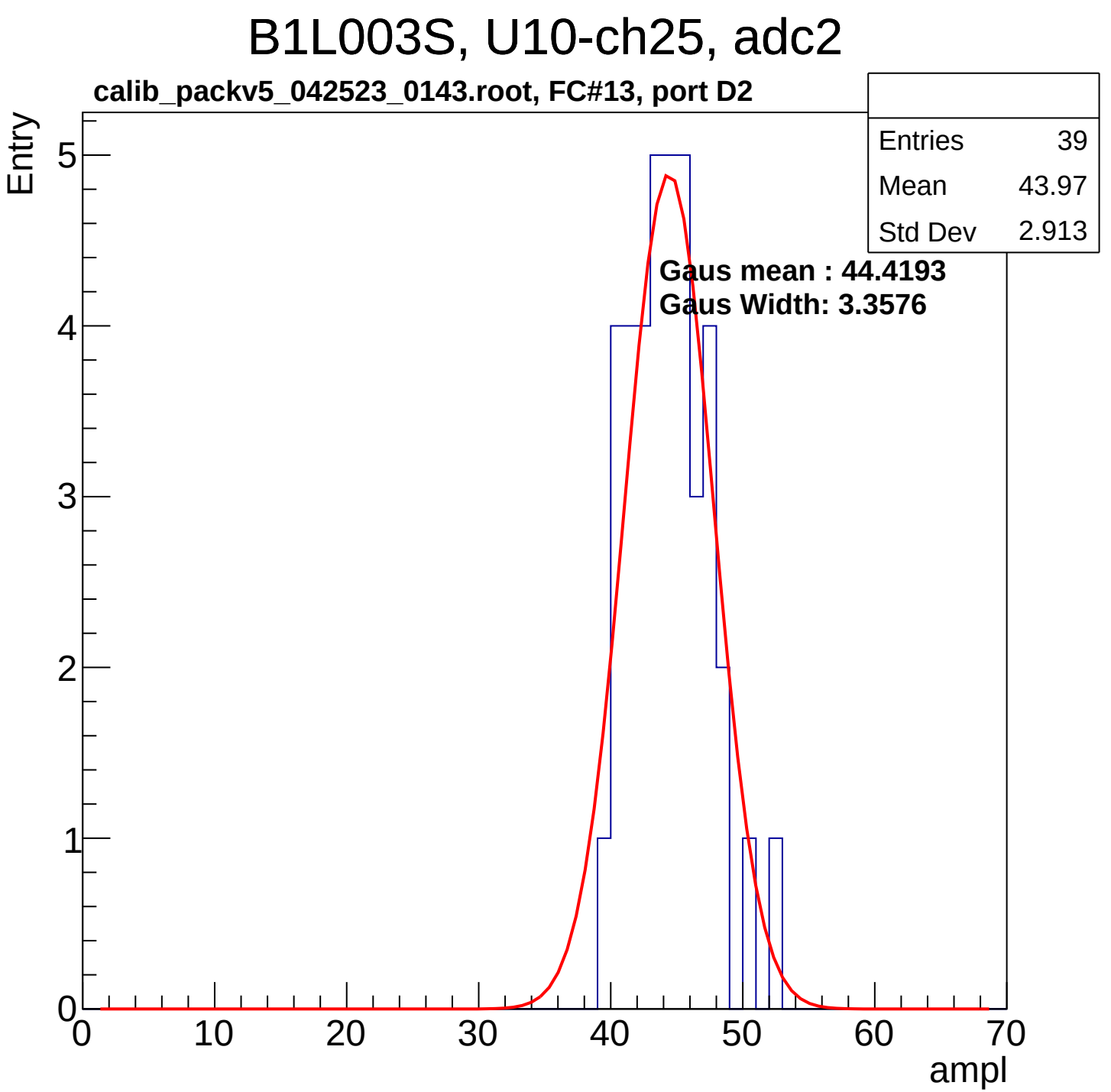

B1L003S, U10-ch25, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 37.35
Std Dev 3.226
Gaus mean : 38.1332
Gaus Width: 3.2716
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch25, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 43.97
Std Dev 2.913
Gaus mean : 44.4193
Gaus Width: 3.3576
Entry
ampl
0
1
2
3
4
5
0
10
20
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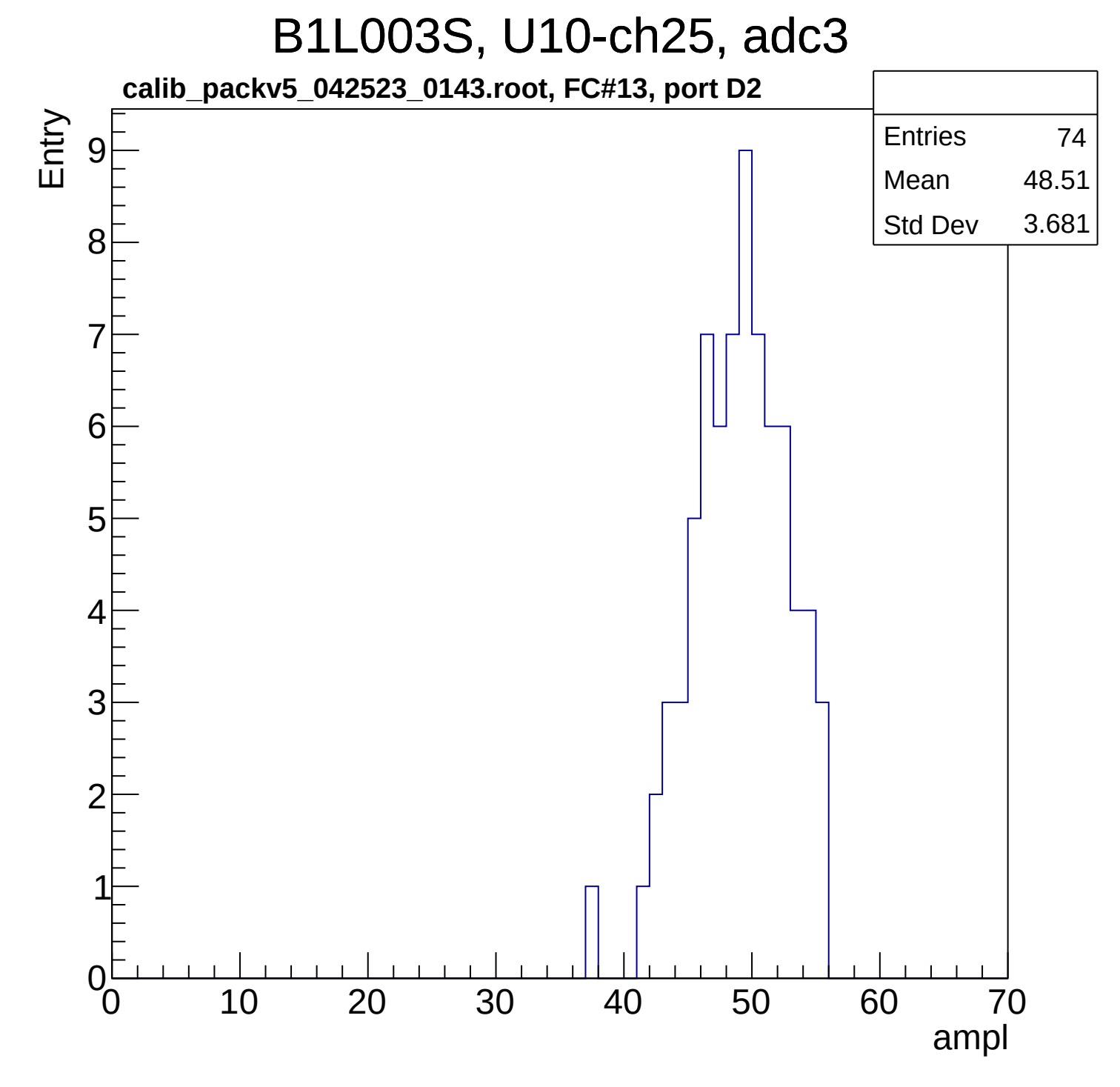
40
50
60
70


B1L003S, U10-ch25, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.51
Std Dev 3.681
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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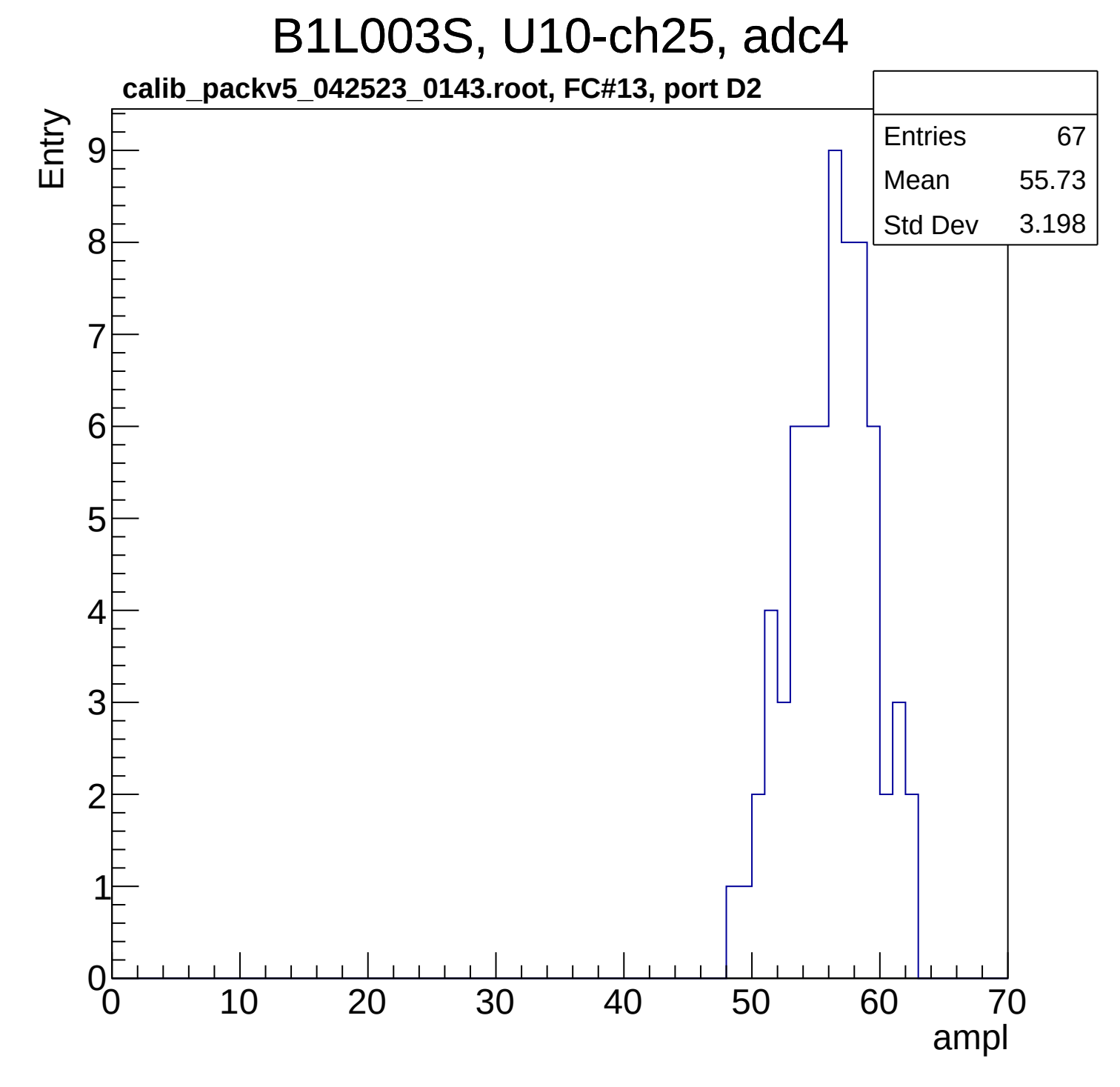
B1L003S, U10-ch25, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 55.73
Std Dev 3.198
Entry
ampl
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5
6
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0
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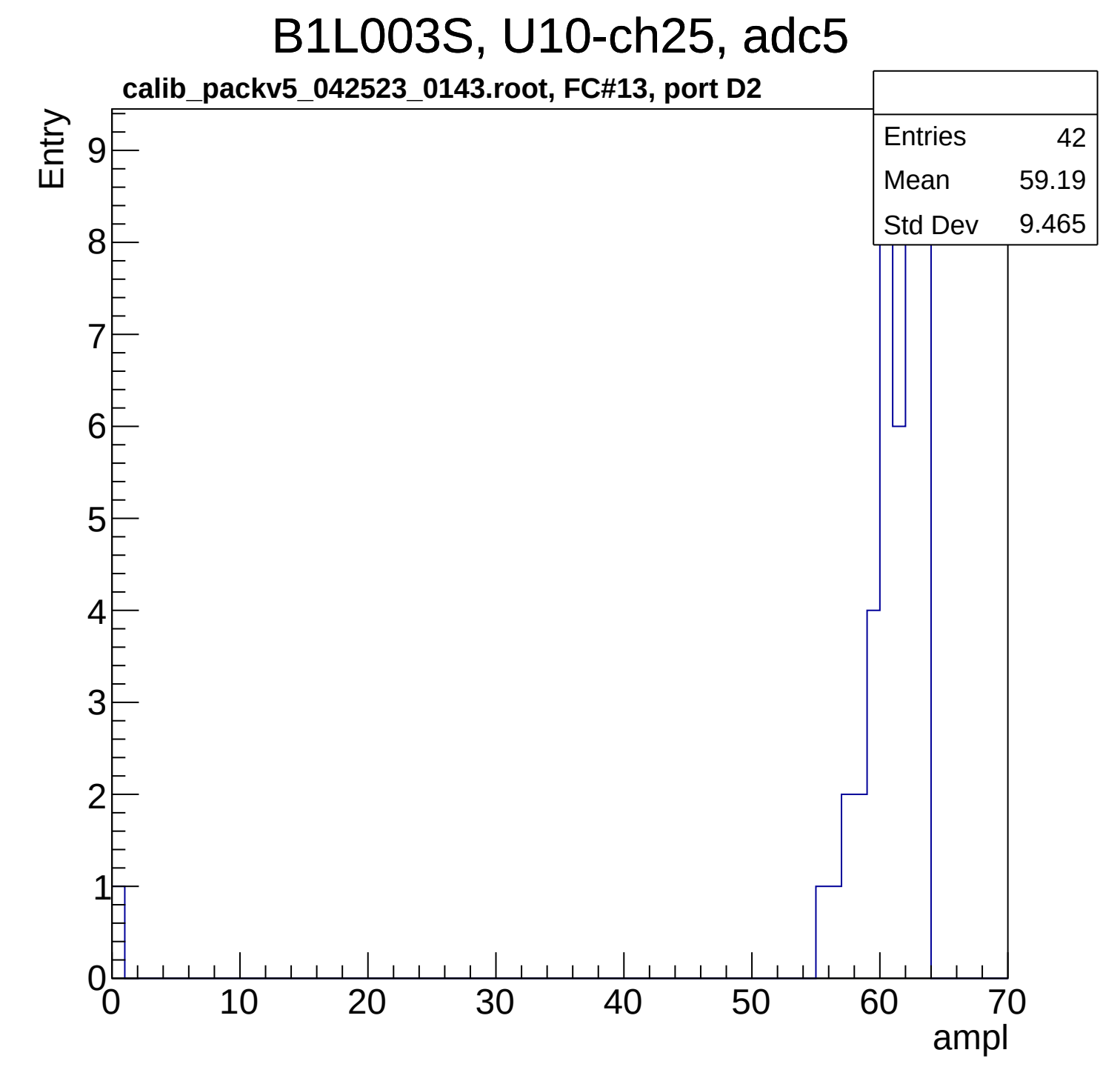
B1L003S, U10-ch25, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 59.19
Std Dev 9.465
Entry
ampl
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3
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7
8
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0
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60
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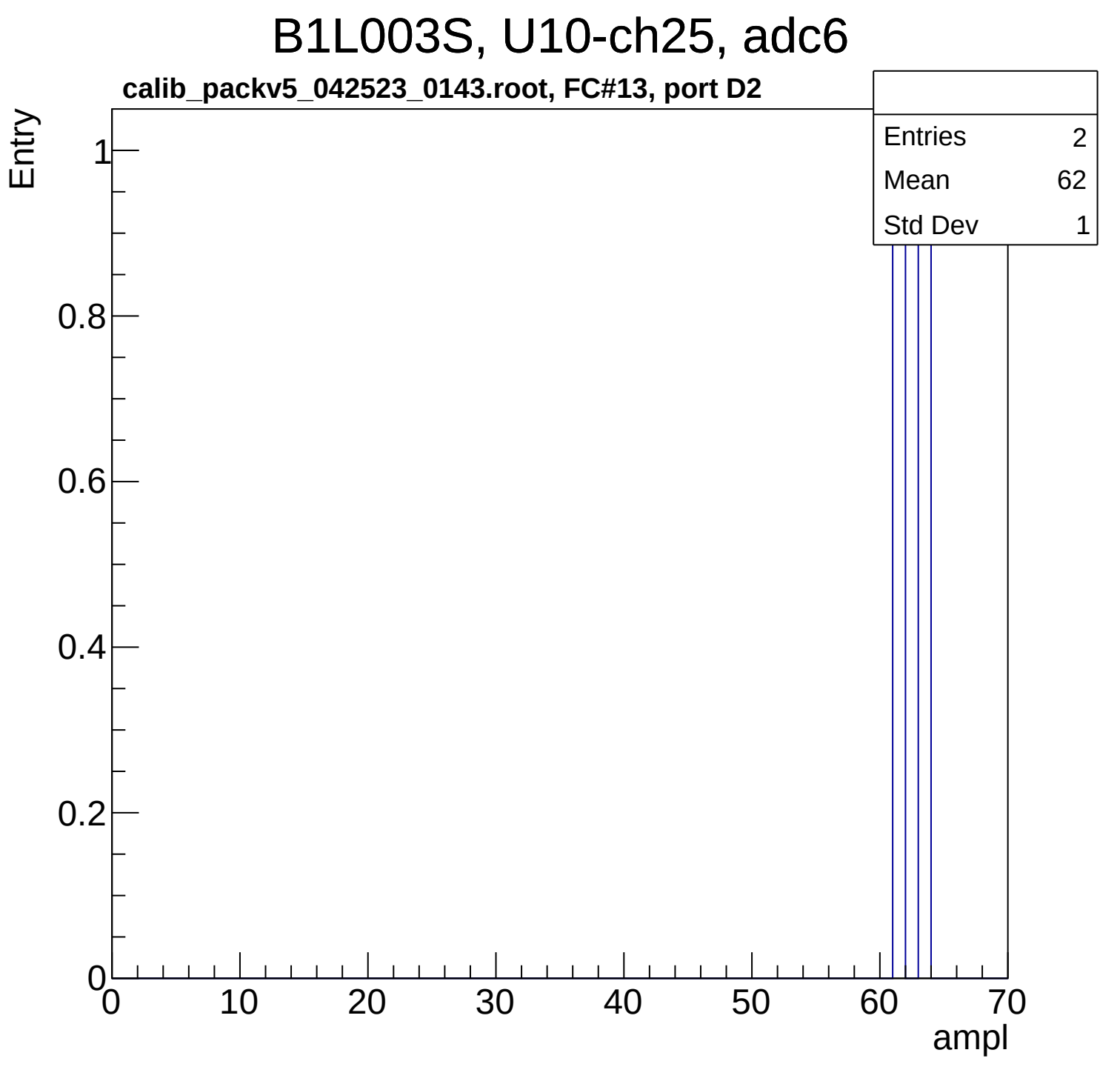
B1L003S, U10-ch25, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62
Std Dev 1
Entry
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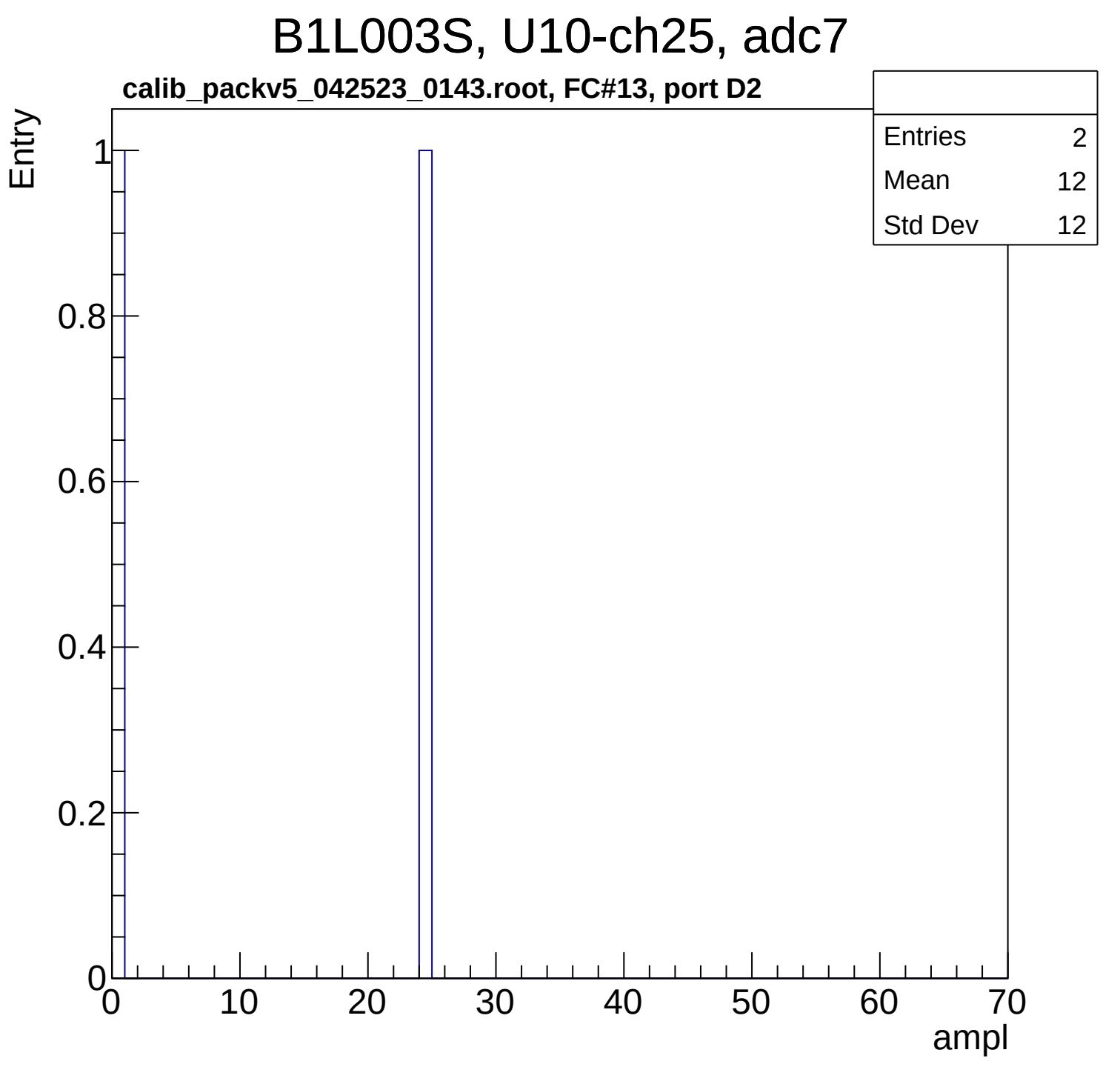
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch25, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12
Std Dev 12
Entry
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0.6
0.4
0.2
0
0
10
20
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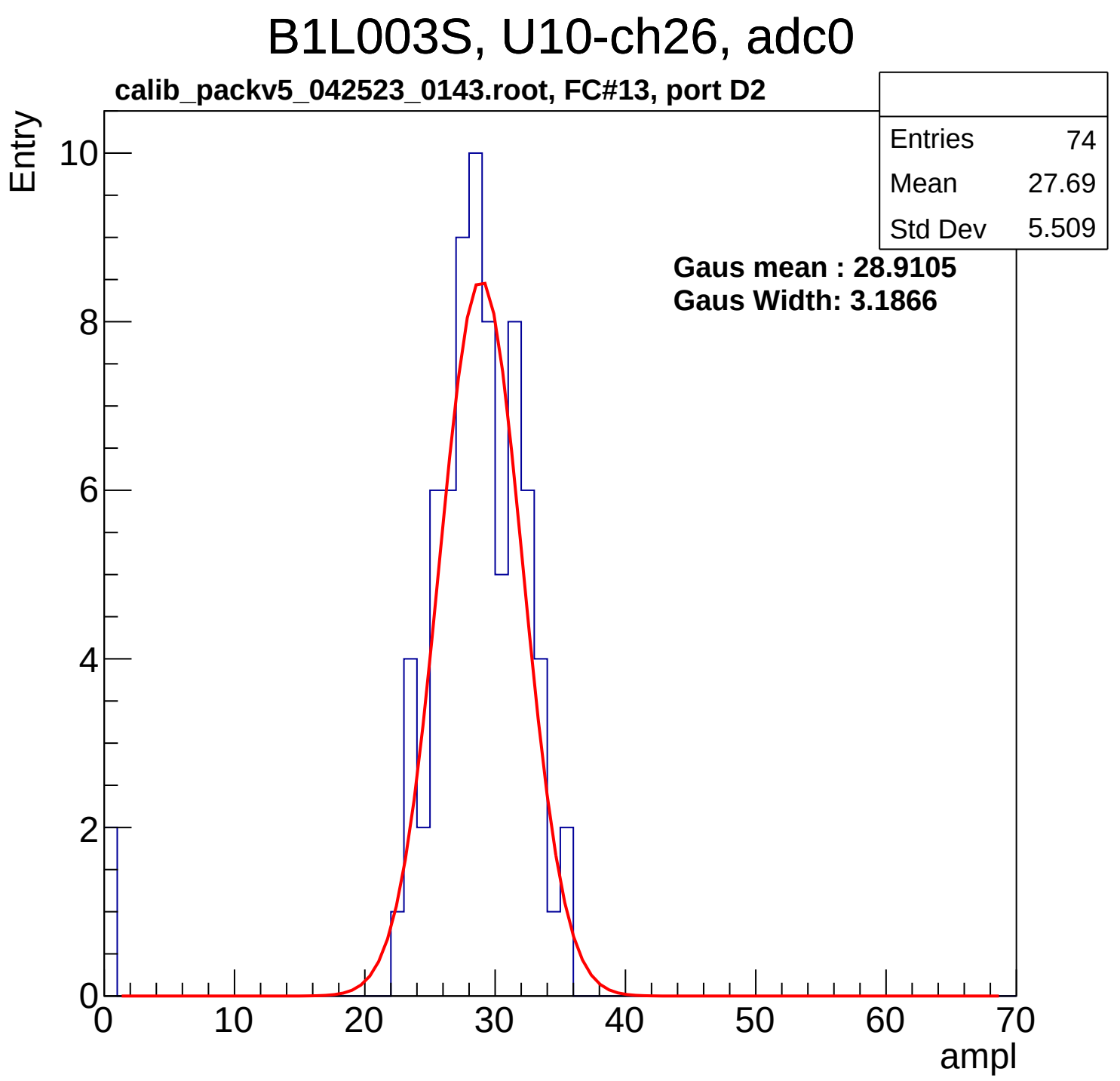
50
60
70
ampl


B1L003S, U10-ch26, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 27.69
Std Dev 5.509
Gaus mean : 28.9105
Gaus Width: 3.1866
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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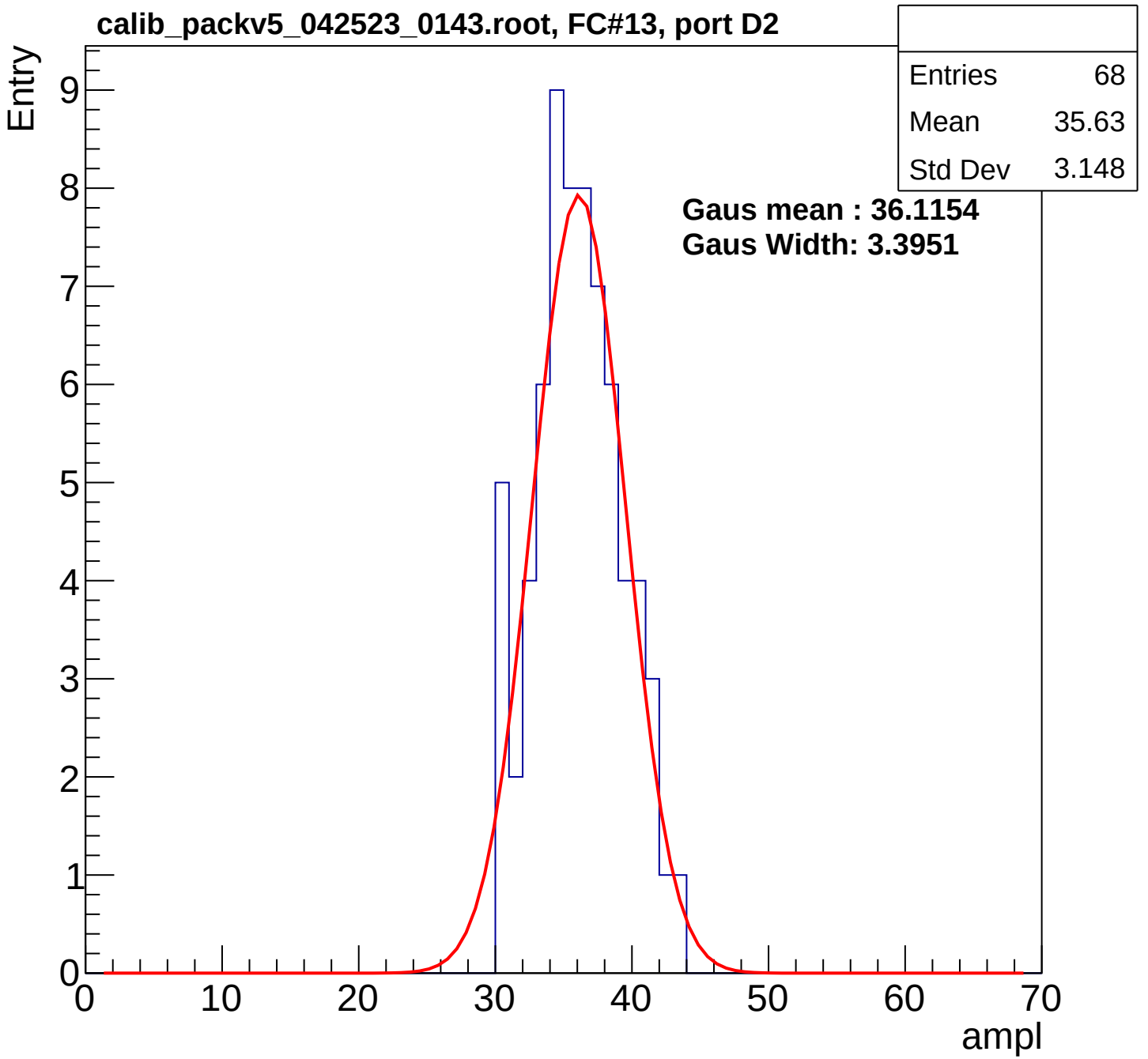
B1L003S, U10-ch26, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.63
Std Dev 3.148
Gaus mean : 36.1154
Gaus Width: 3.3951
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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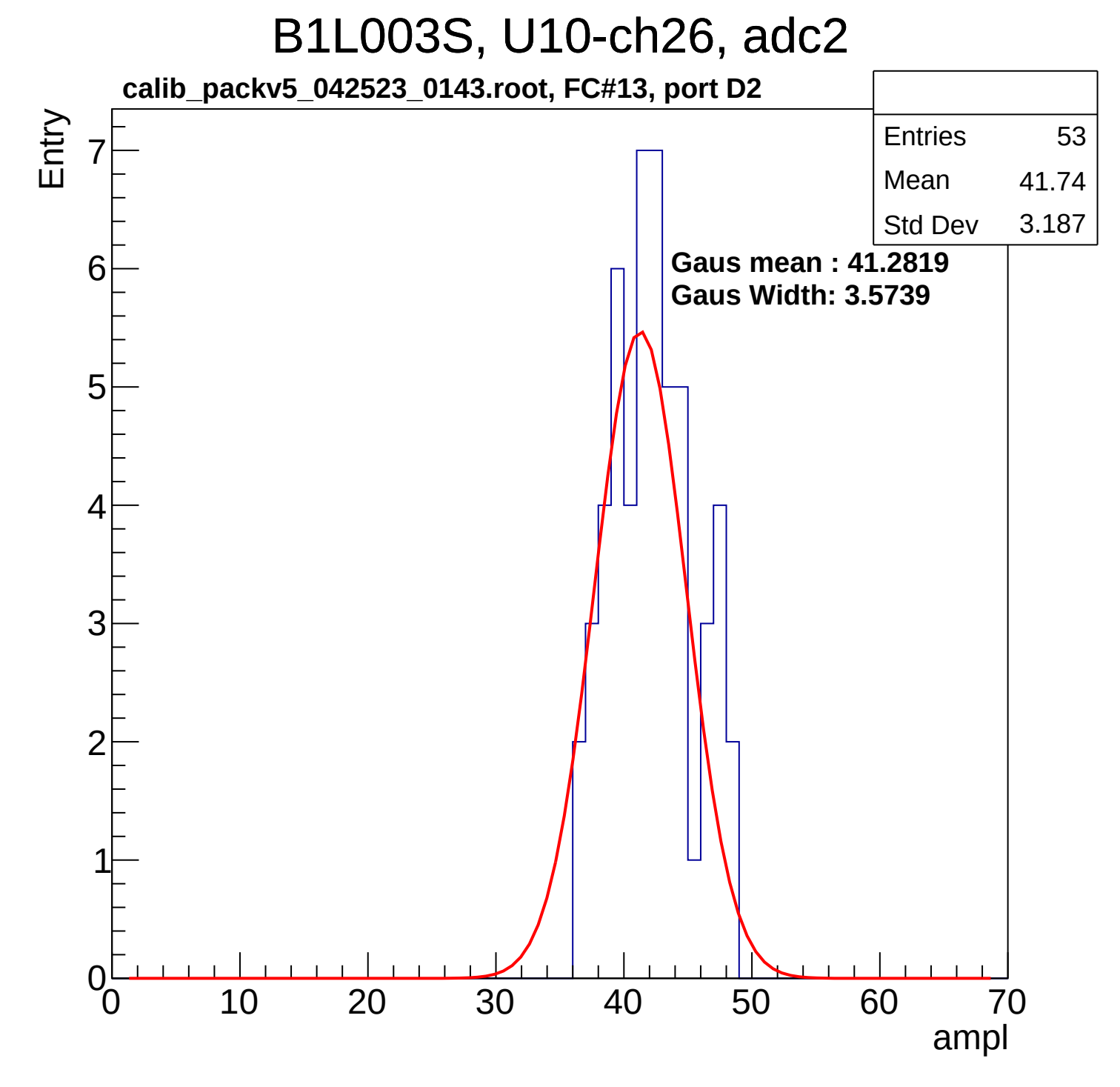
B1L003S, U10-ch26, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 41.74
Std Dev 3.187
Gaus mean : 41.2819
Gaus Width: 3.5739
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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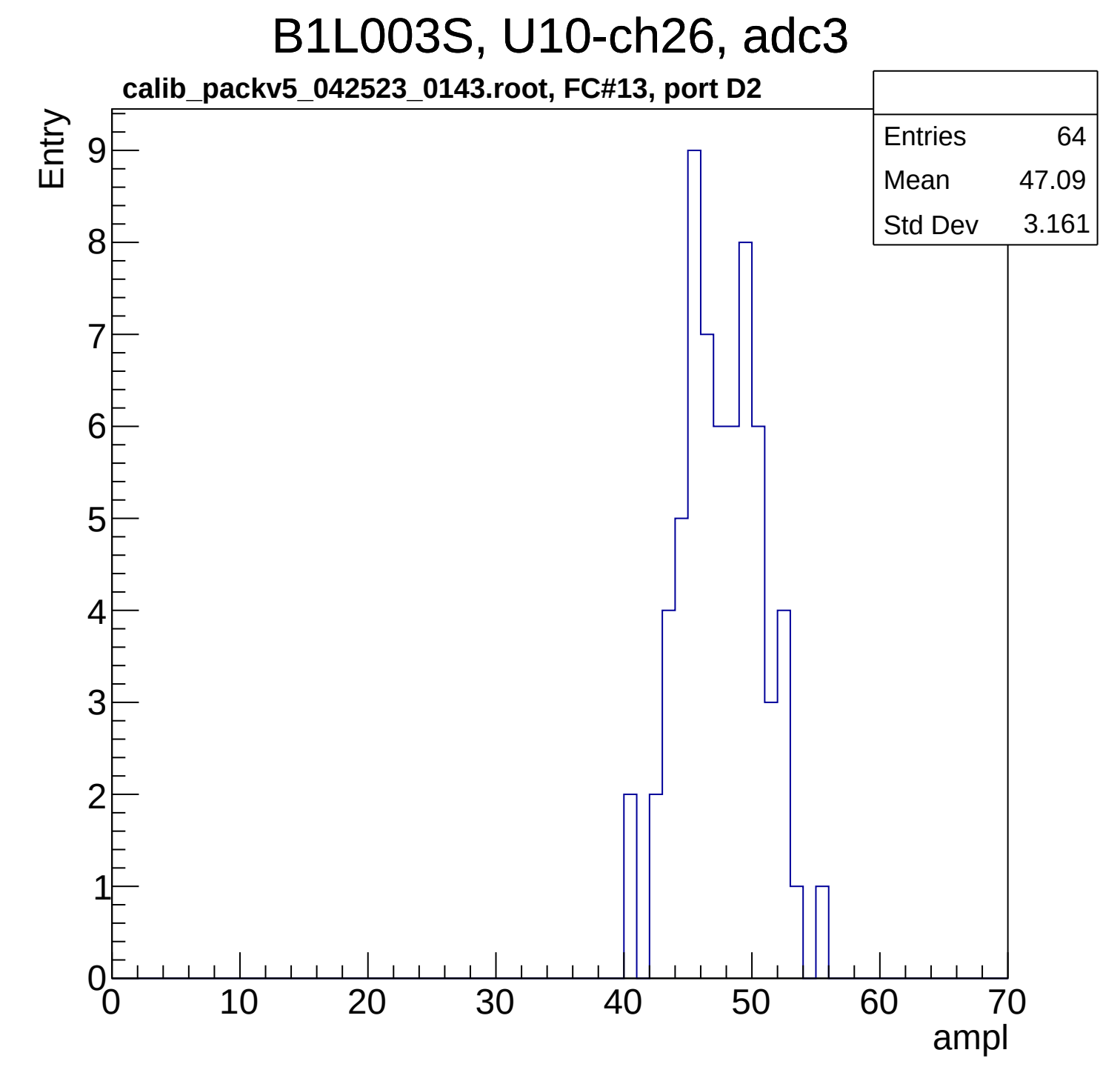
B1L003S, U10-ch26, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.09
Std Dev 3.161
Entry
ampl
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10
20
30
40
50
60
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1
2
3
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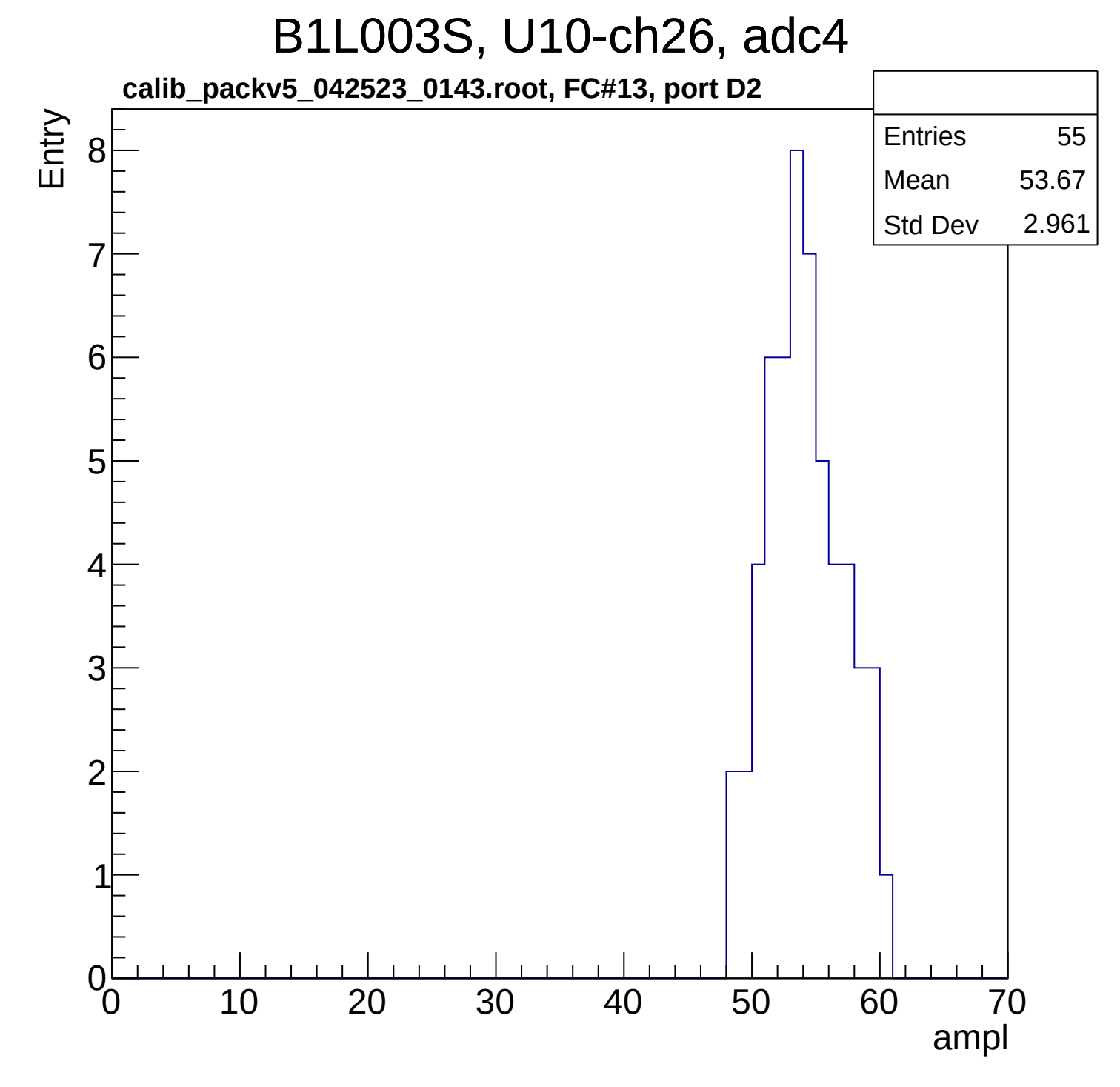
6
7
8
9


B1L003S, U10-ch26, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.67
Std Dev 2.961
Entry
ampl
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1
2
3
4
5
6
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0
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20
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40
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60
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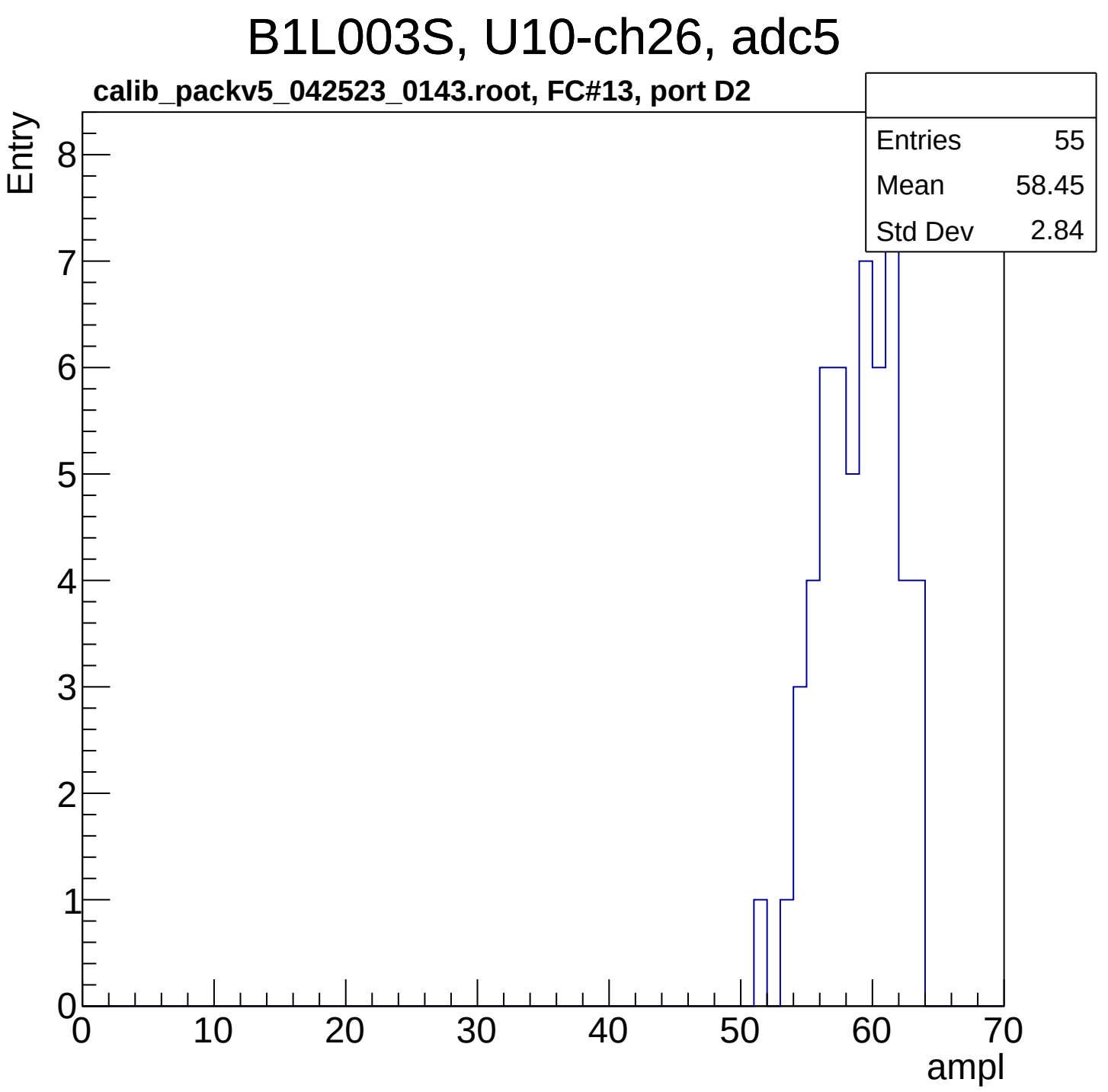
B1L003S, U10-ch26, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.45
Std Dev 2.84
Entry
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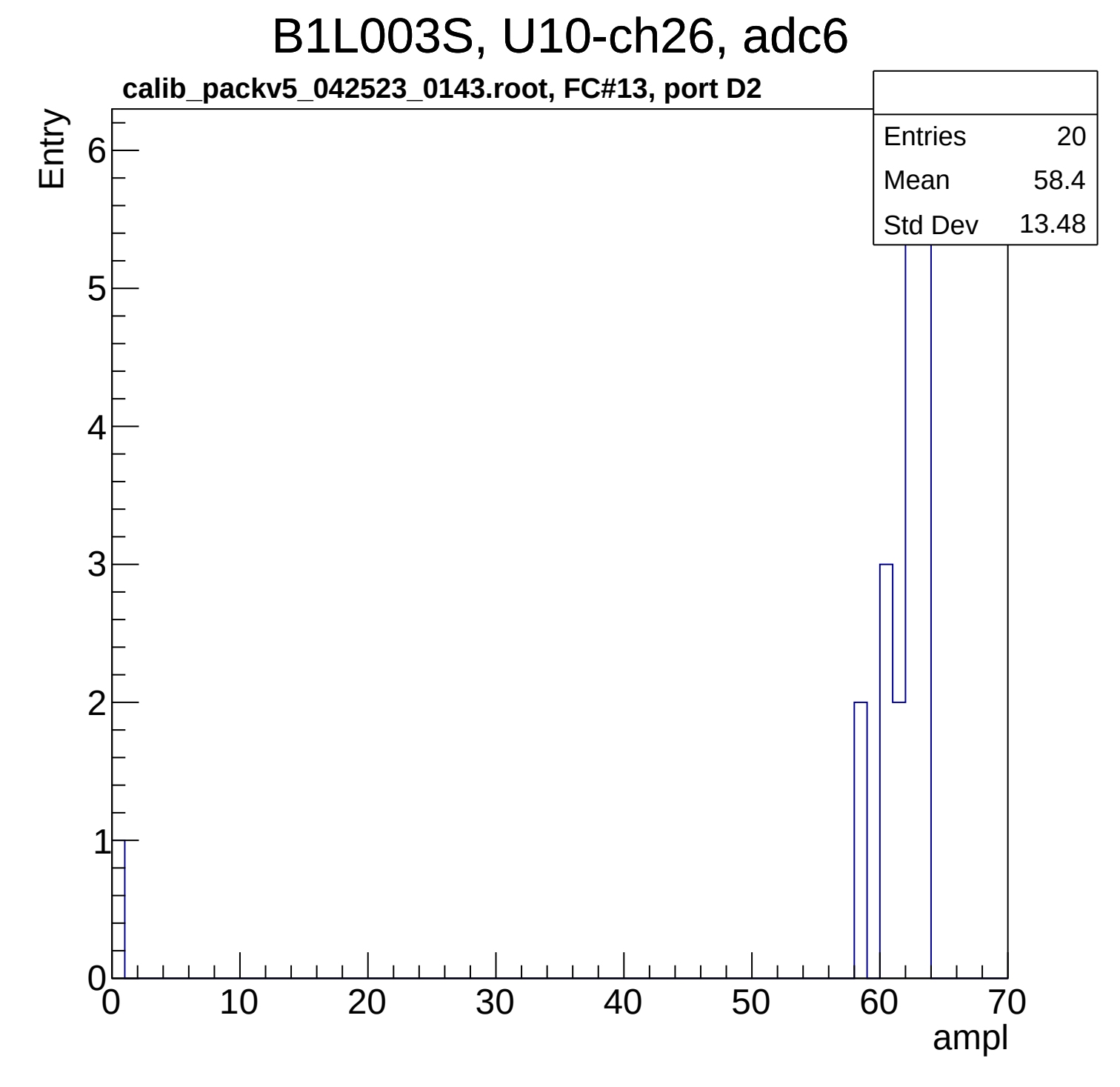
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch26, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 58.4
Std Dev 13.48
Entry
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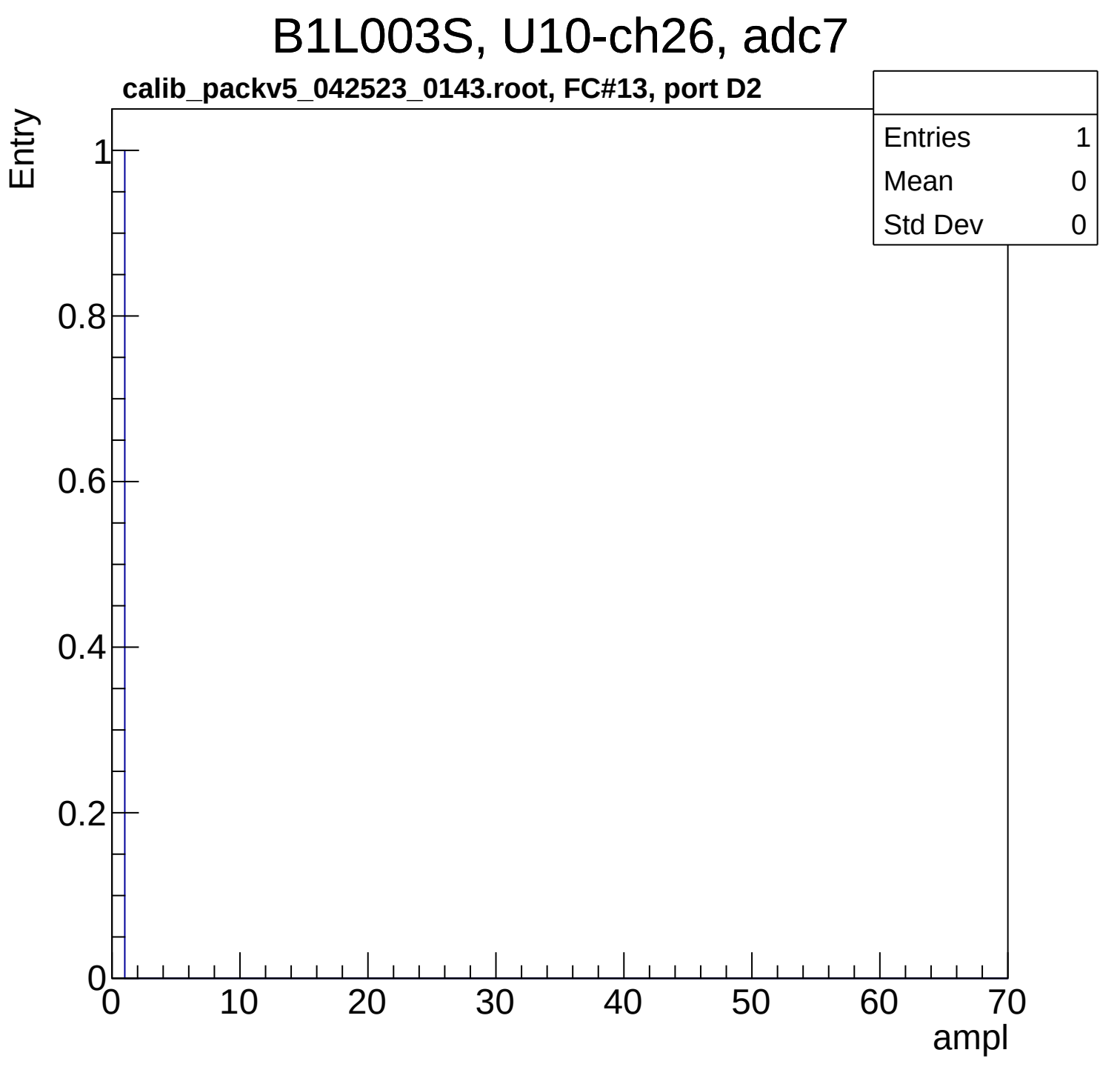
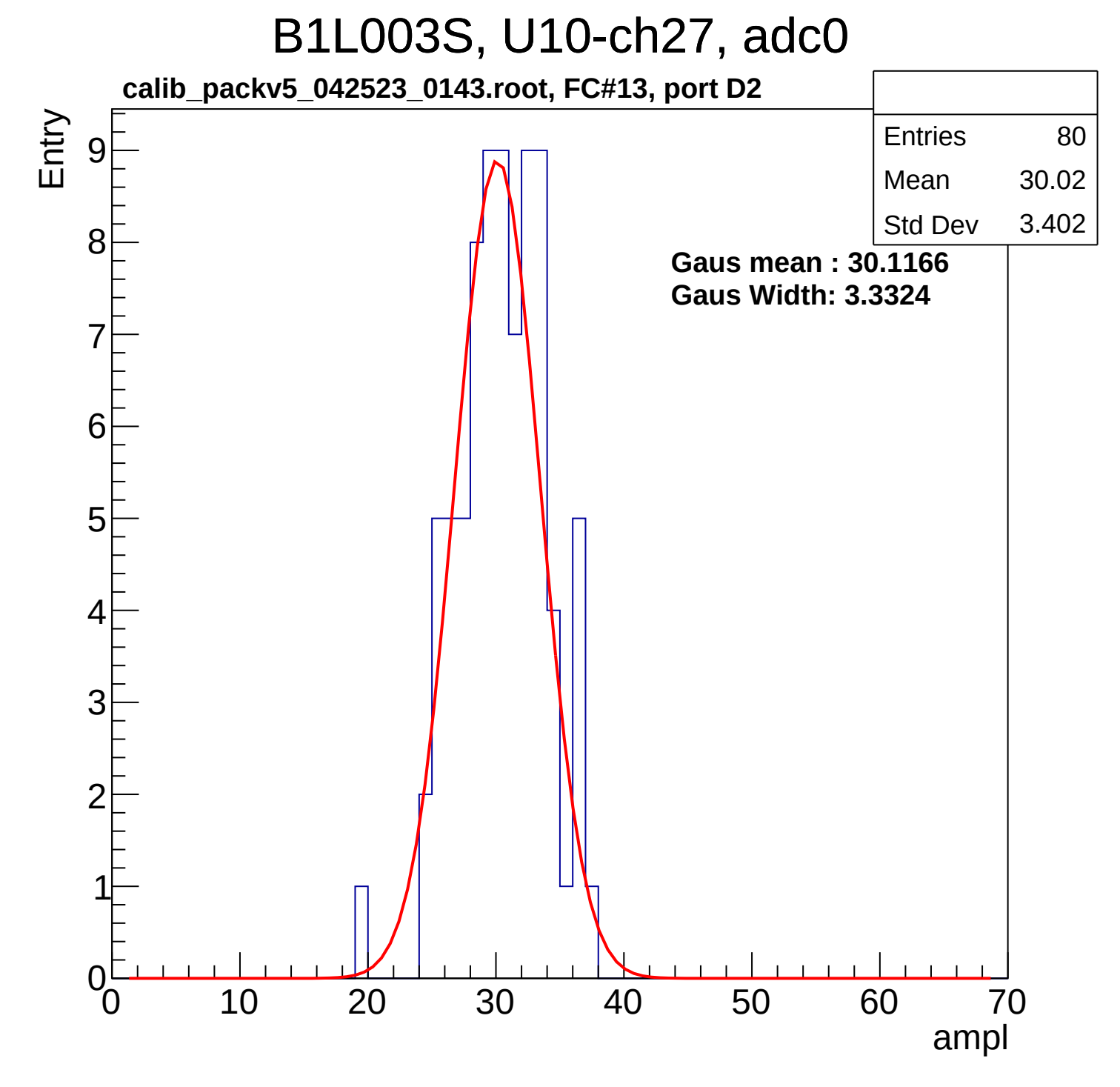
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch26, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch27, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 30.02
Std Dev 3.402
Gaus mean : 30.1166
Gaus Width: 3.3324
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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40
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60
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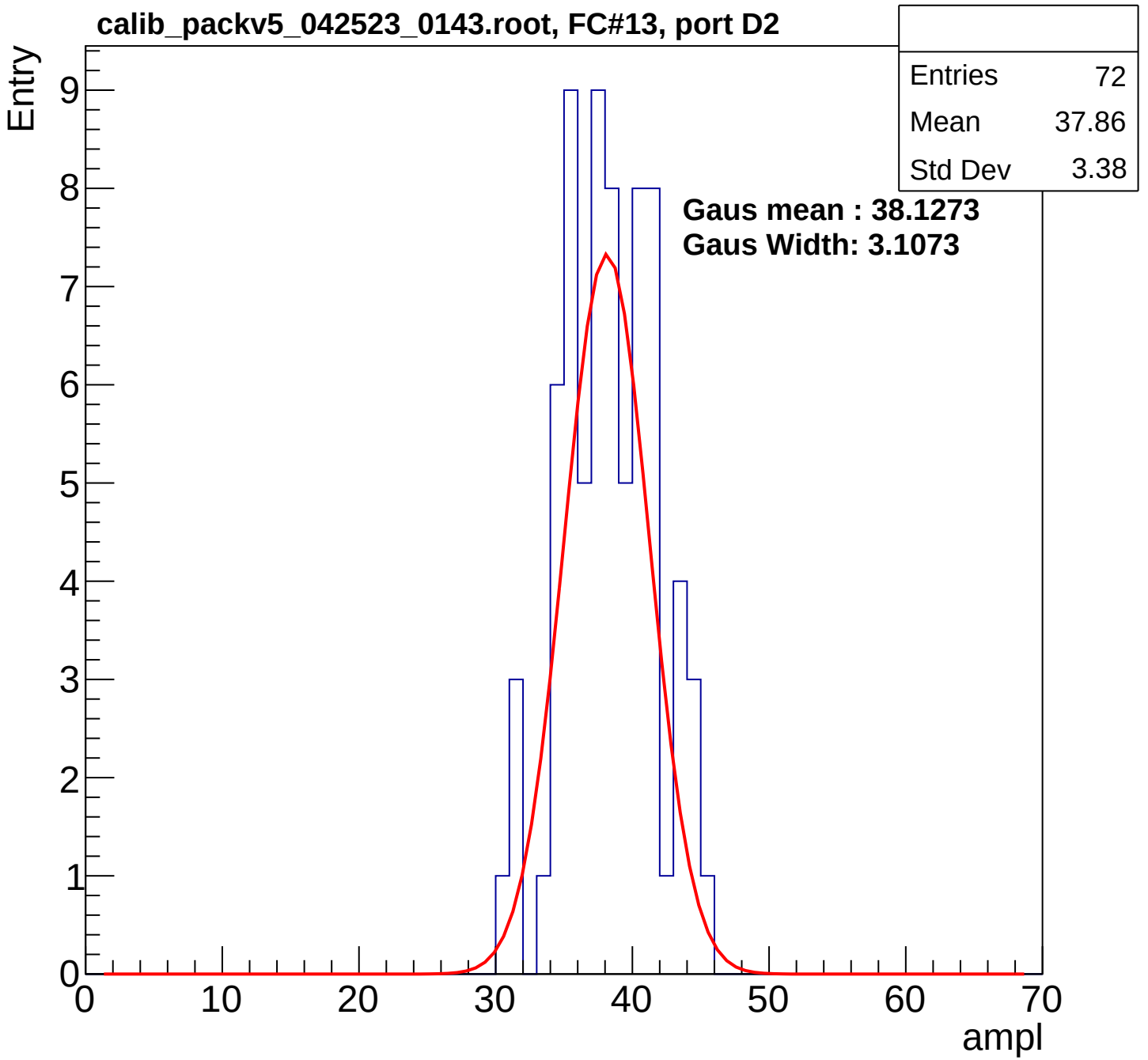
B1L003S, U10-ch27, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 37.86
Std Dev 3.38
Gaus mean : 38.1273
Gaus Width: 3.1073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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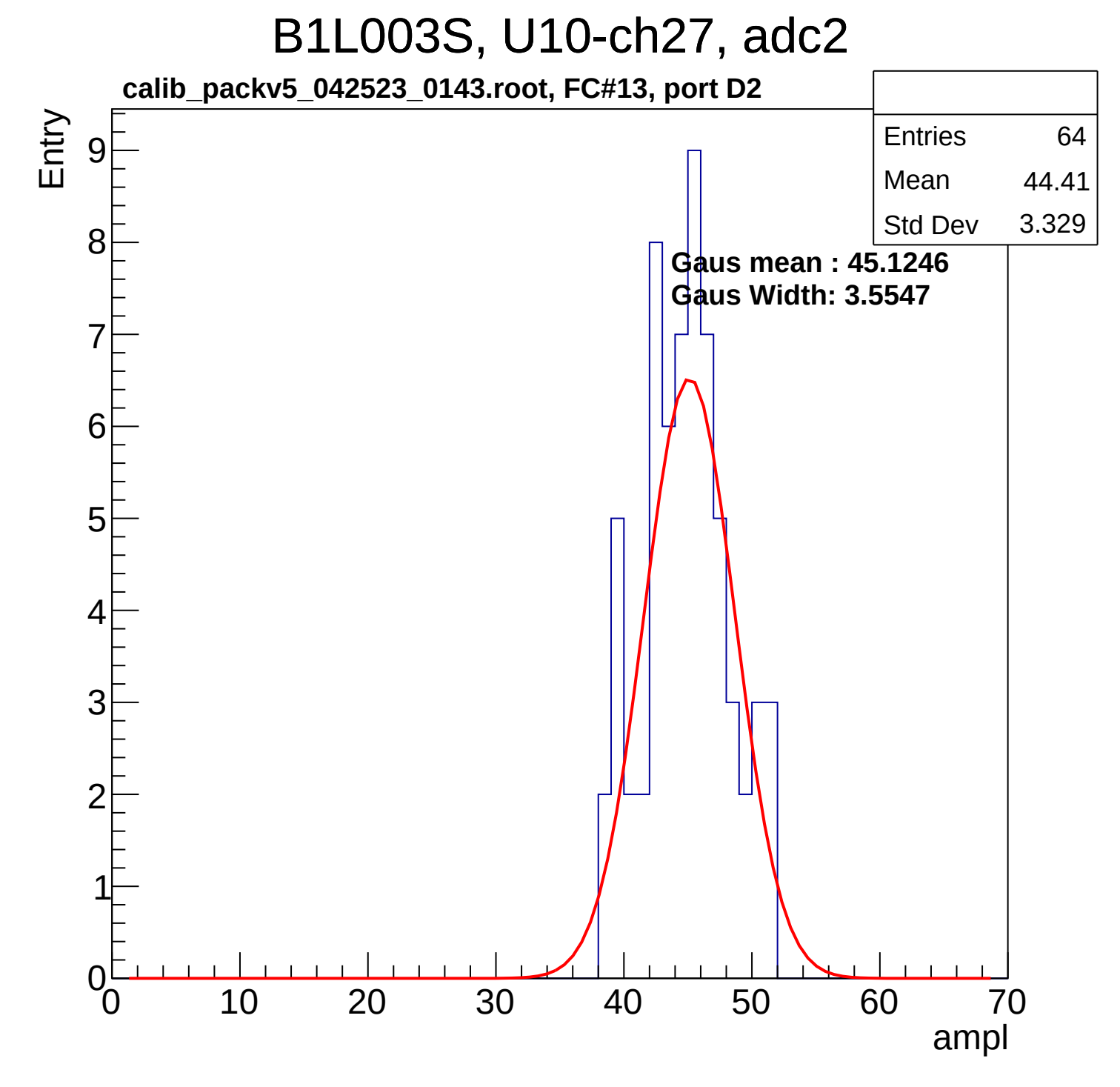
B1L003S, U10-ch27, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 44.41
Std Dev 3.329
Gaus mean : 45.1246
Gaus Width: 3.5547
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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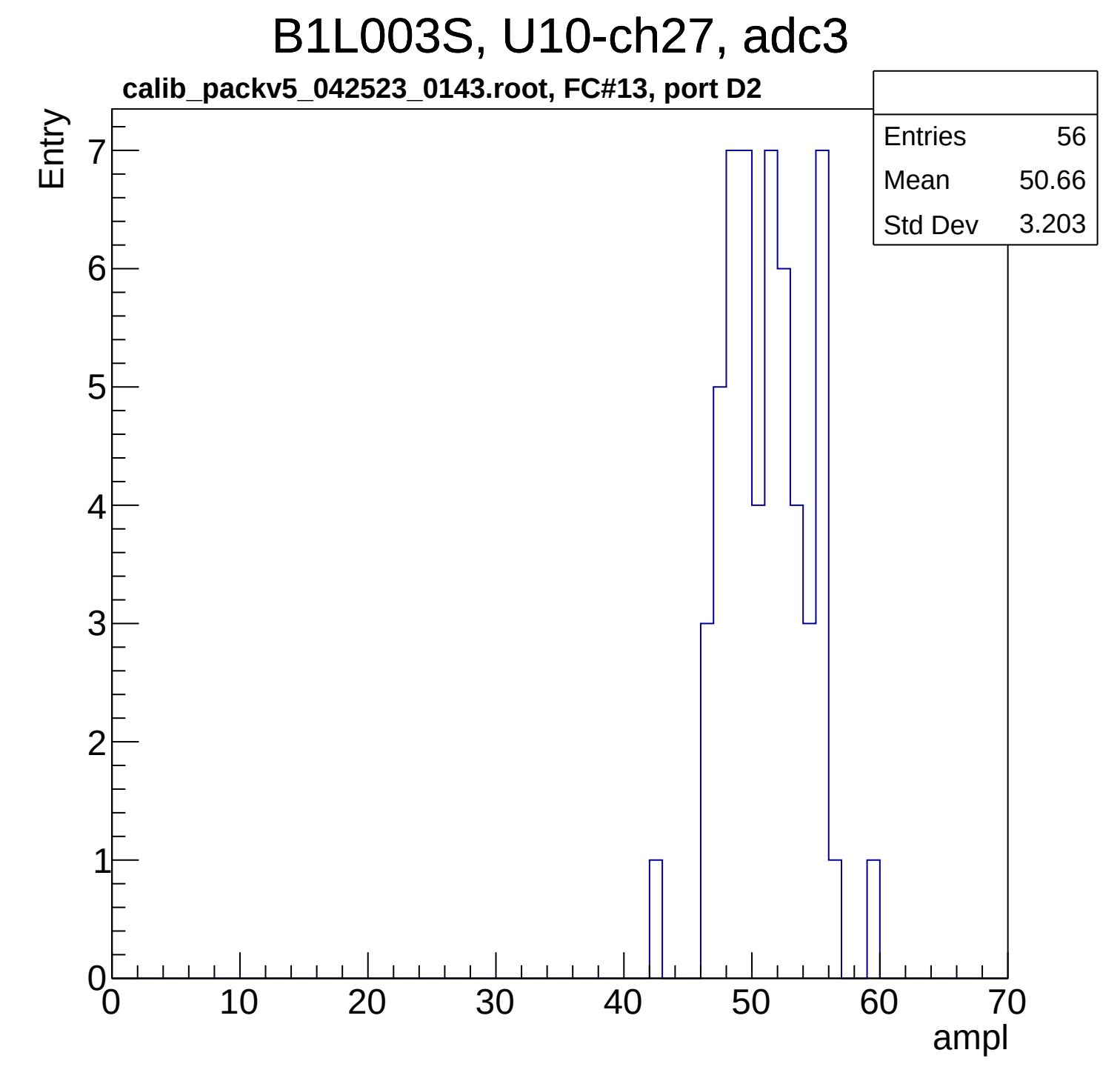
B1L003S, U10-ch27, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 50.66
Std Dev 3.203
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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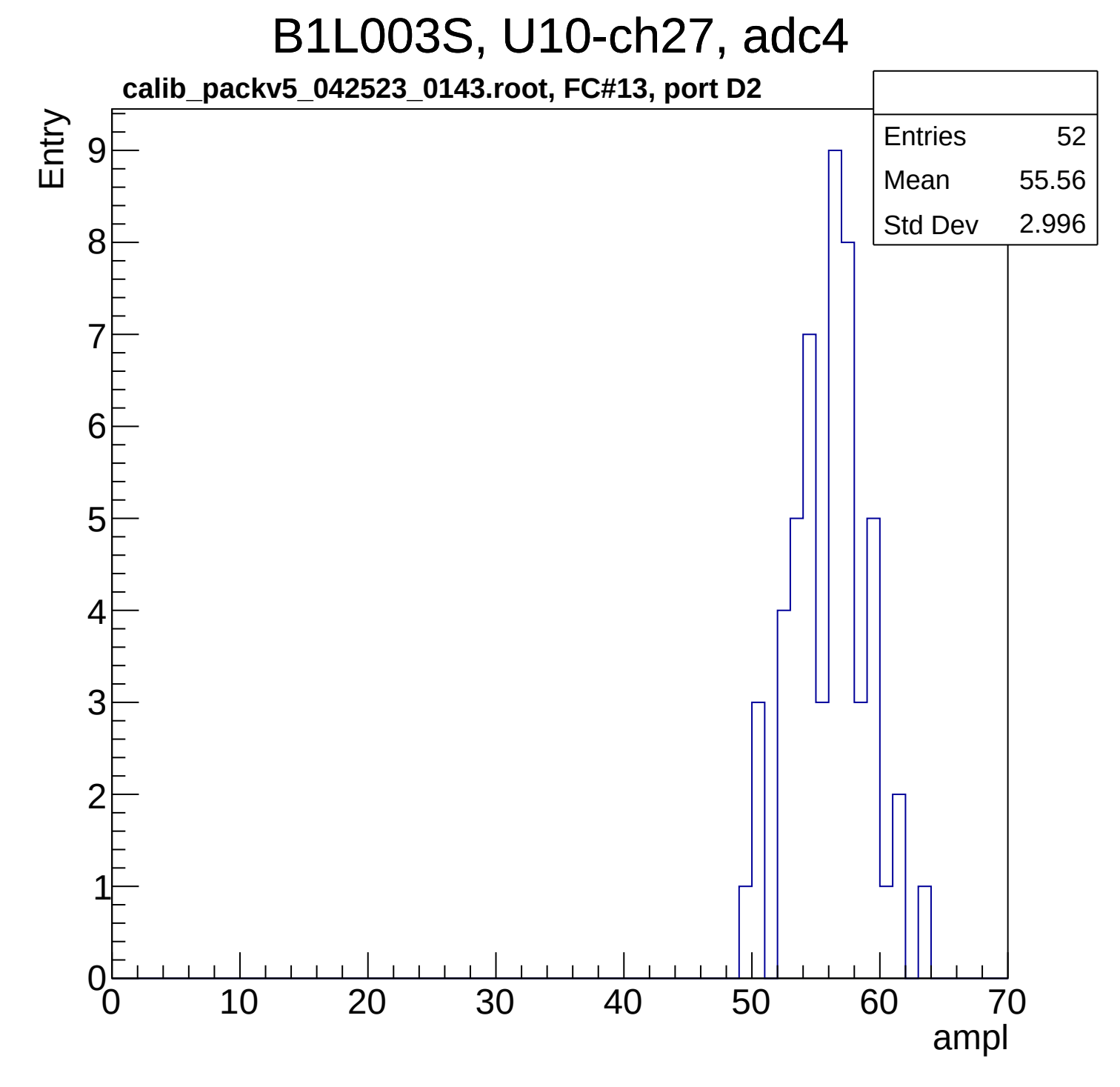
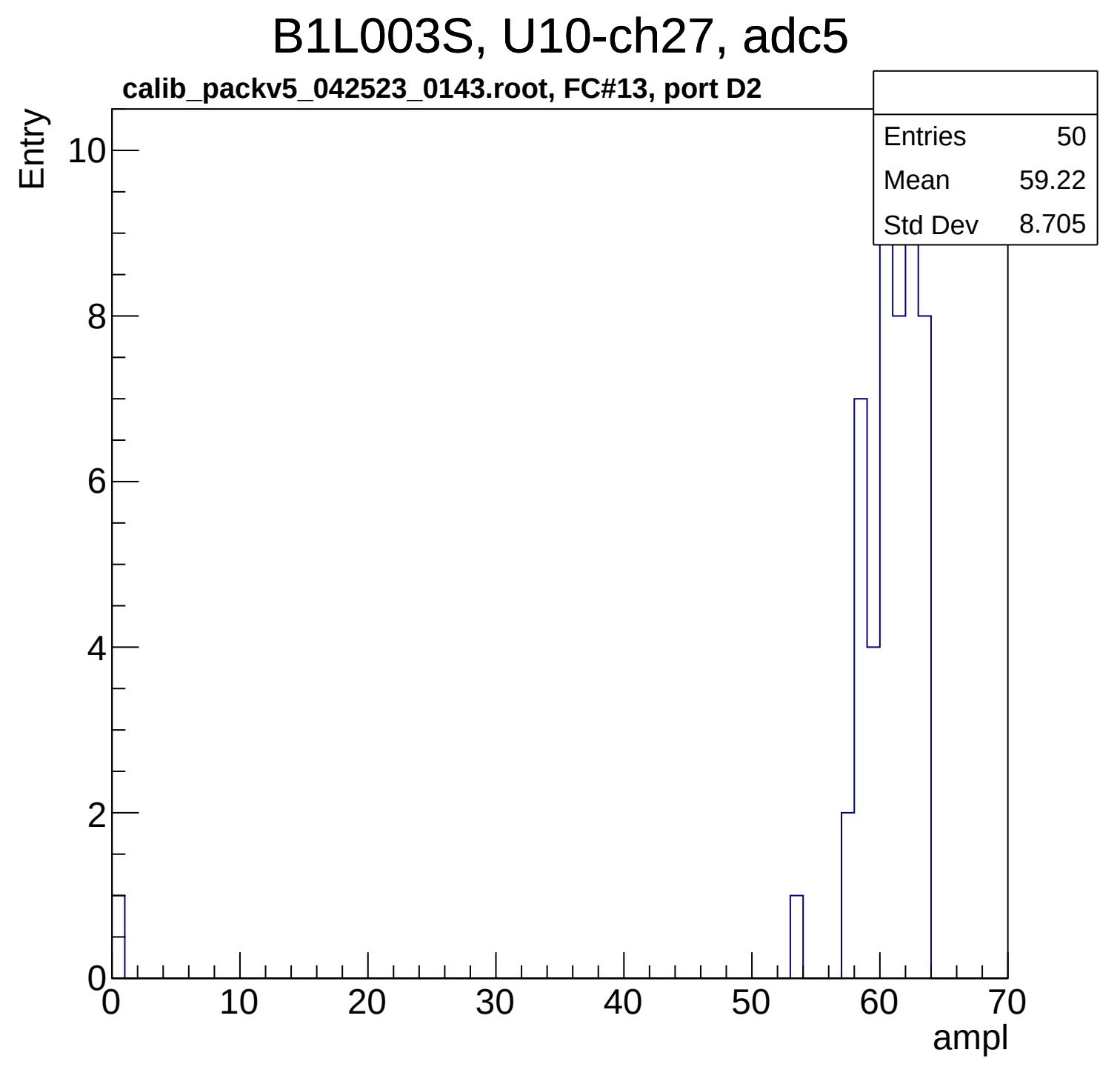
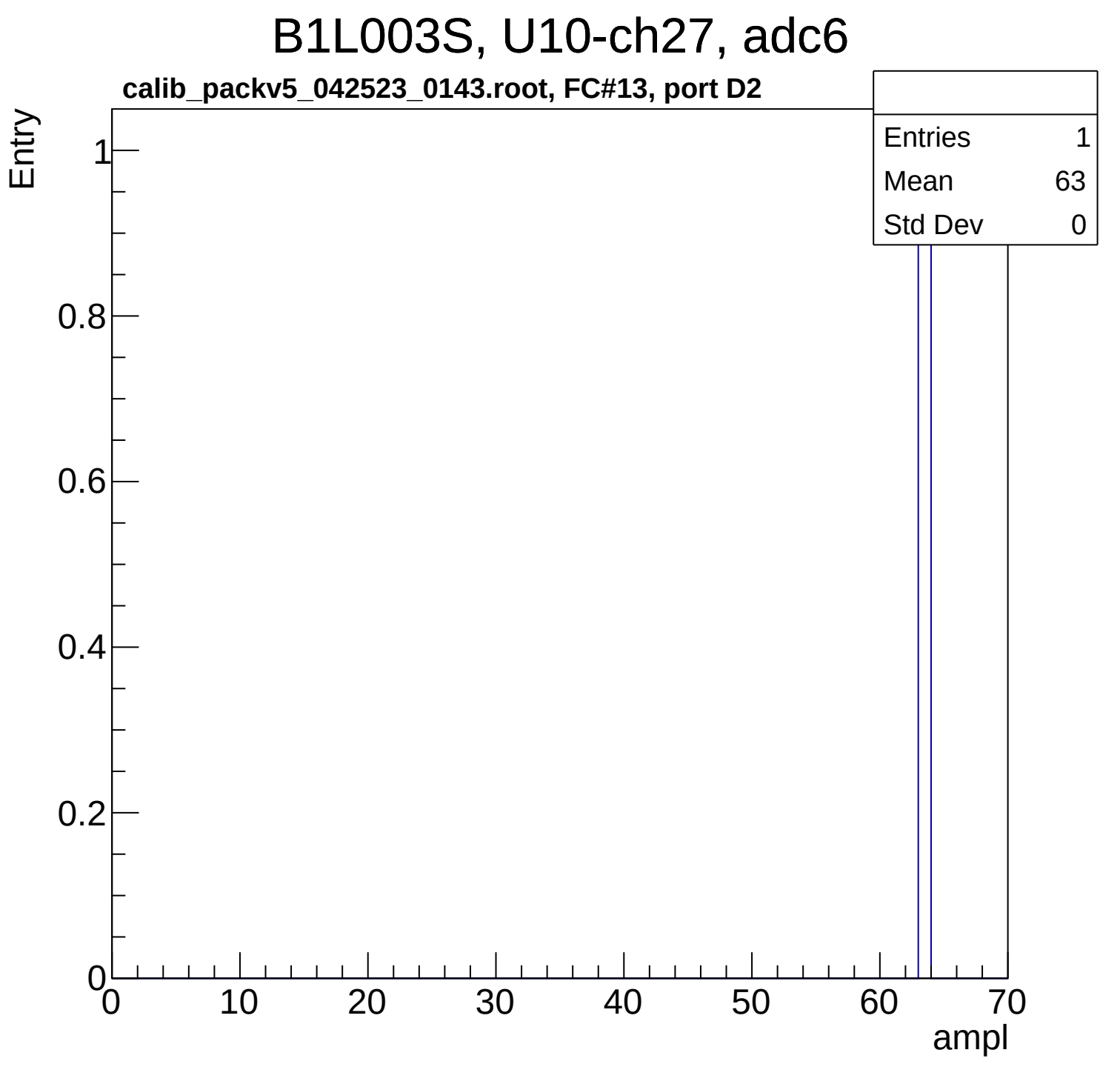
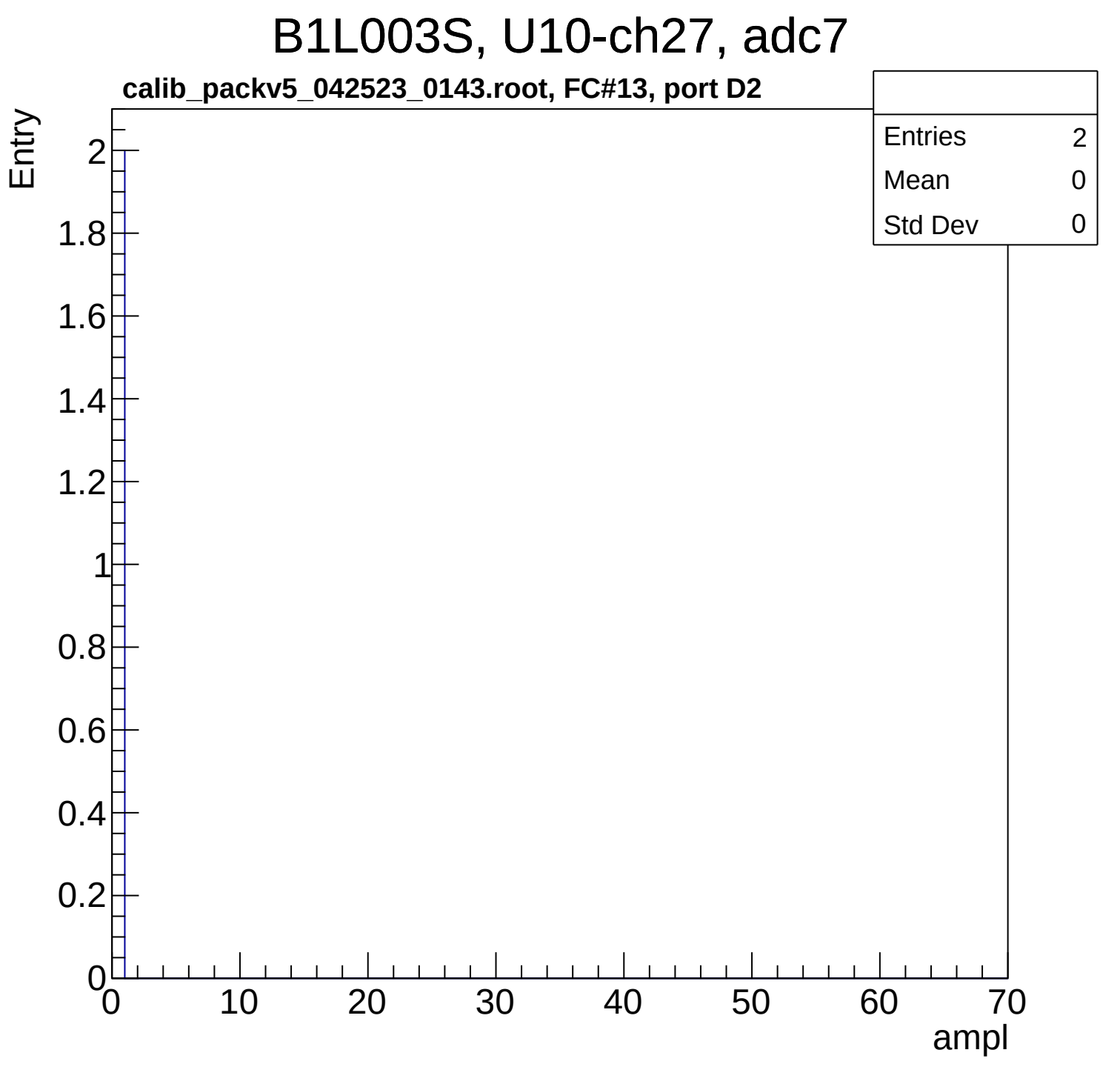

B1L003S, U10-ch27, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.56
Std Dev 2.996
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch27, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.22
Std Dev 8.705
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch27, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch27, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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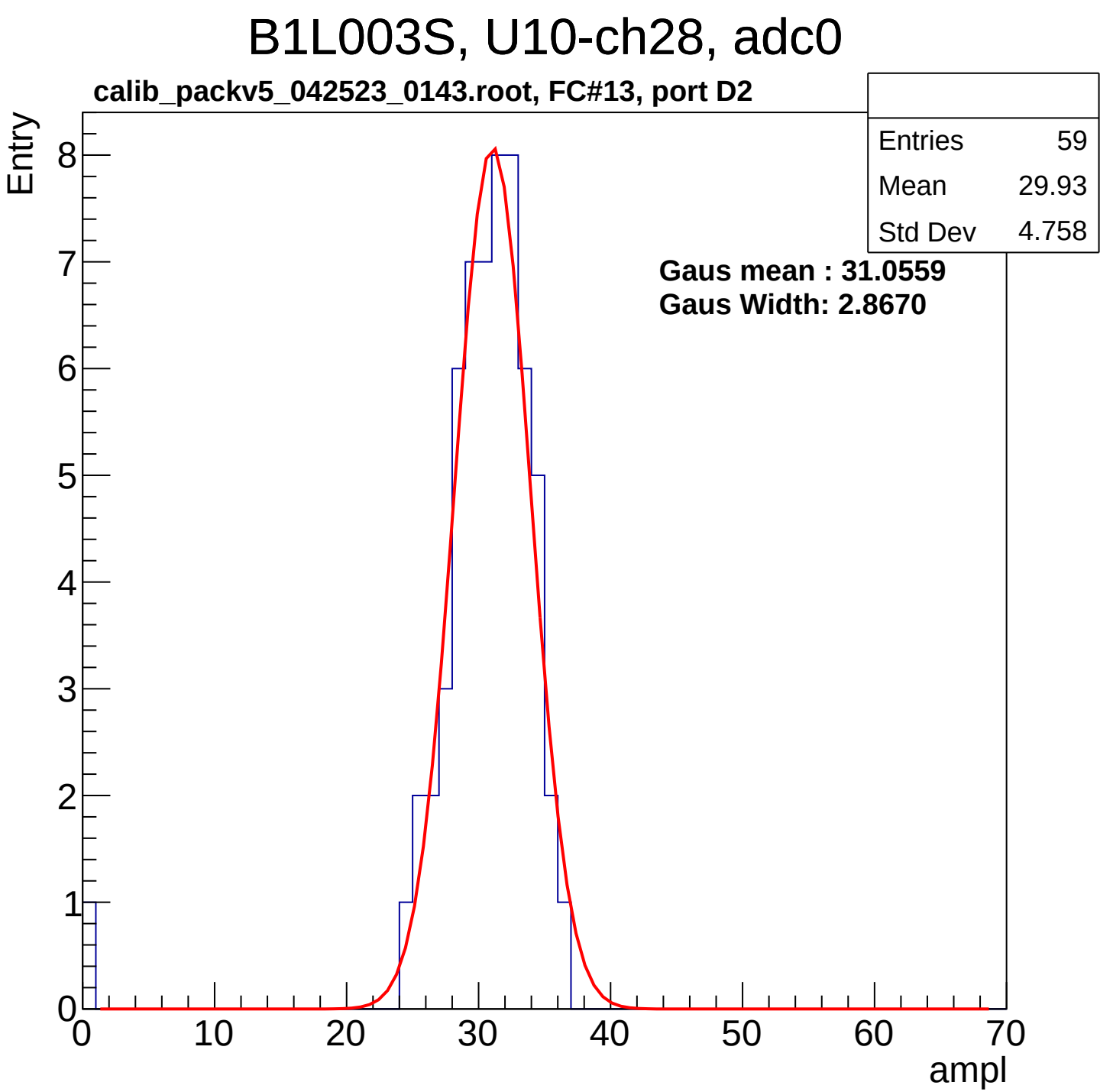
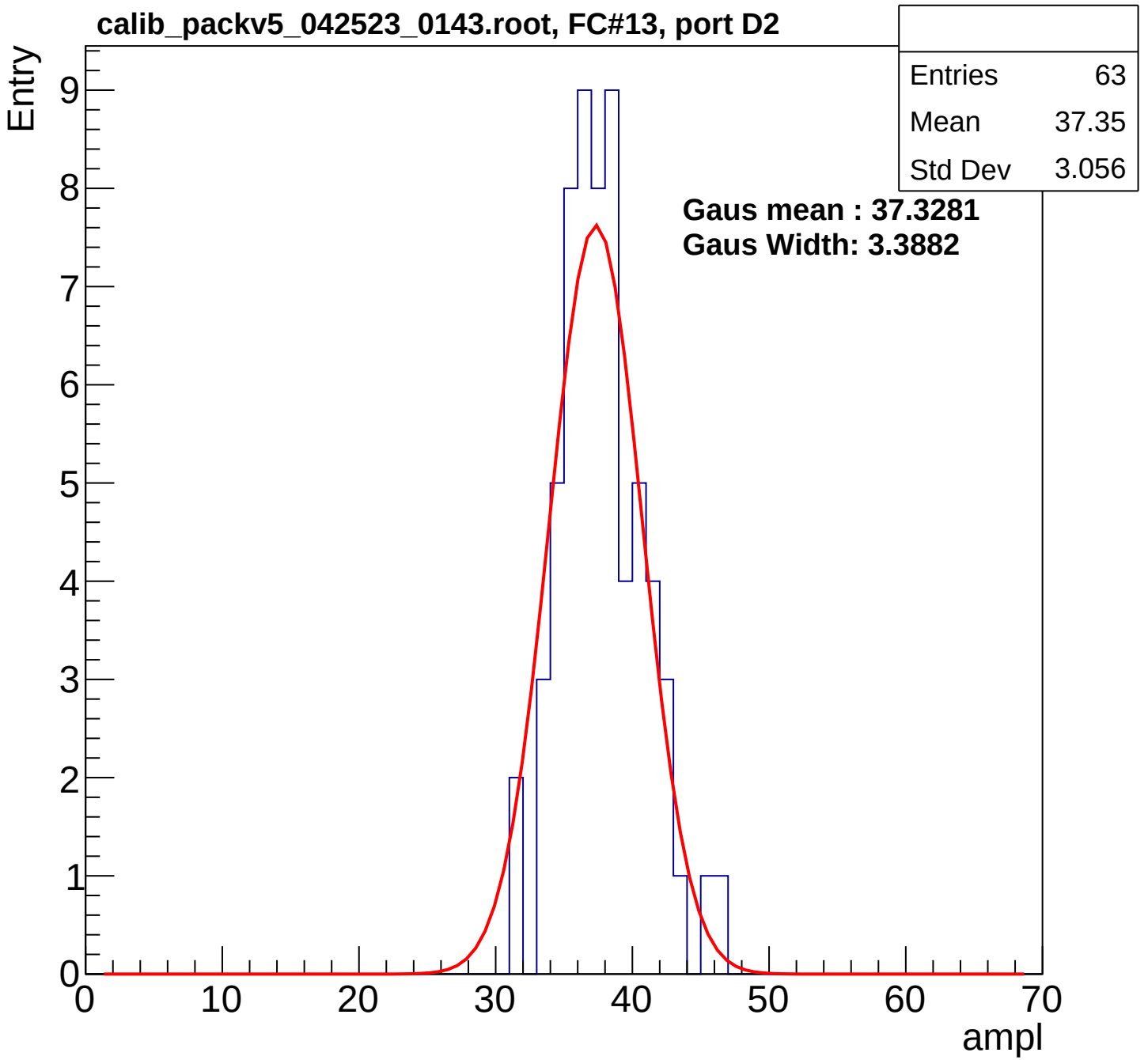

B1L003S, U10-ch28, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 29.93
Std Dev 4.758
Gaus mean : 31.0559
Gaus Width: 2.8670
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch28, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 37.35
Std Dev 3.056
Gaus mean : 37.3281
Gaus Width: 3.3882
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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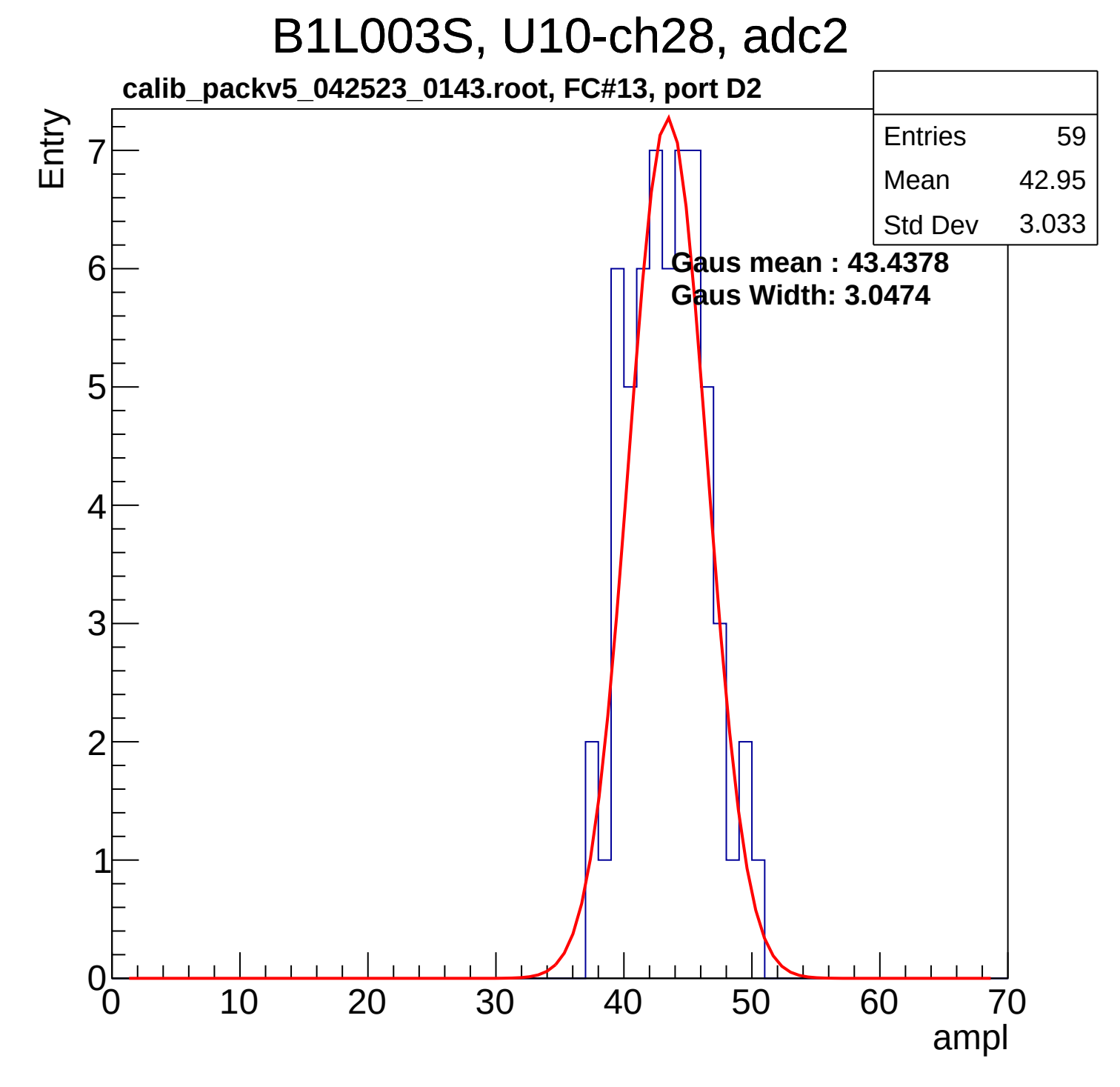
B1L003S, U10-ch28, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.95
Std Dev 3.033
Gaus mean : 43.4378
Gaus Width: 3.0474
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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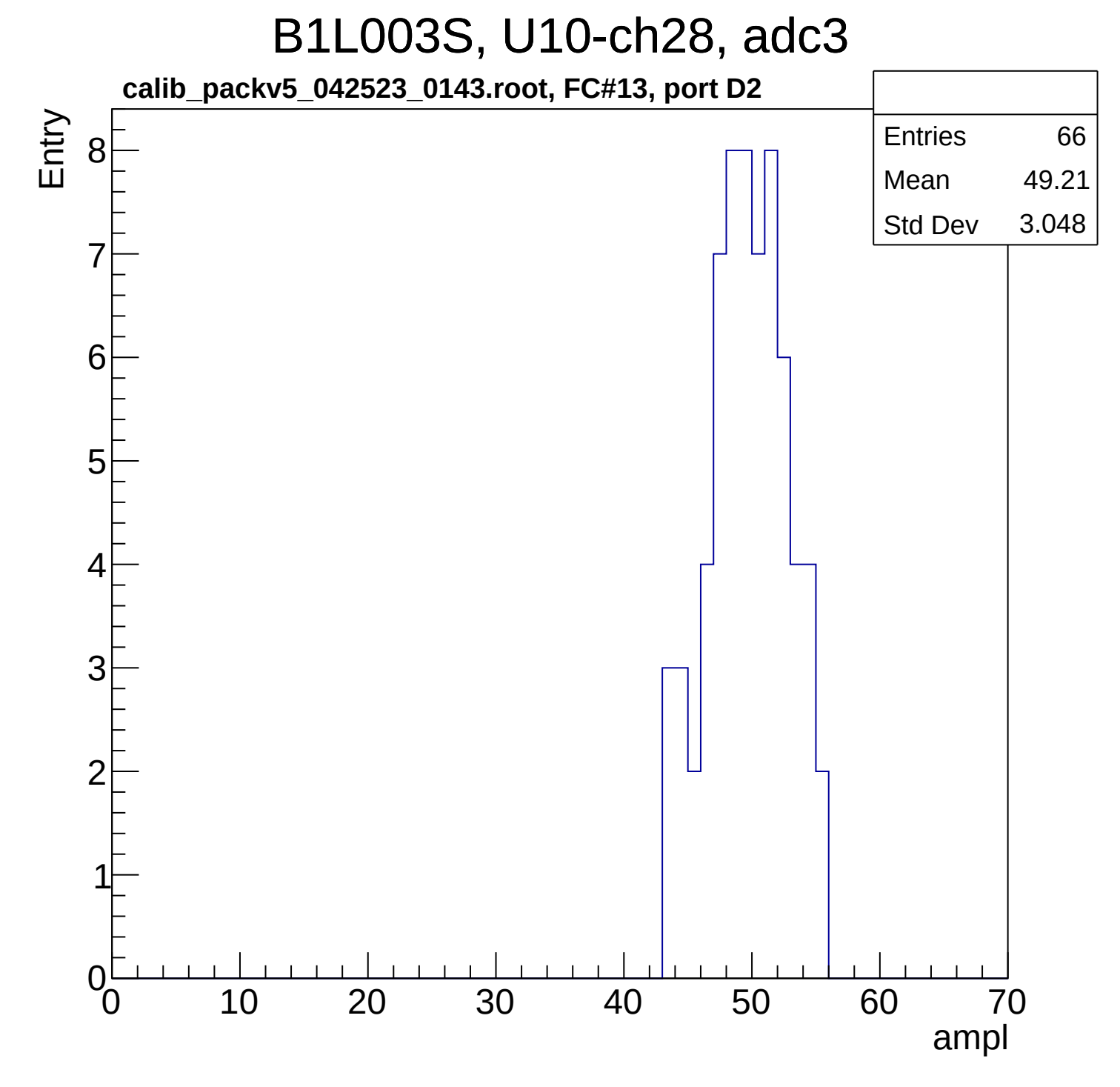
B1L003S, U10-ch28, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.21
Std Dev 3.048
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
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60
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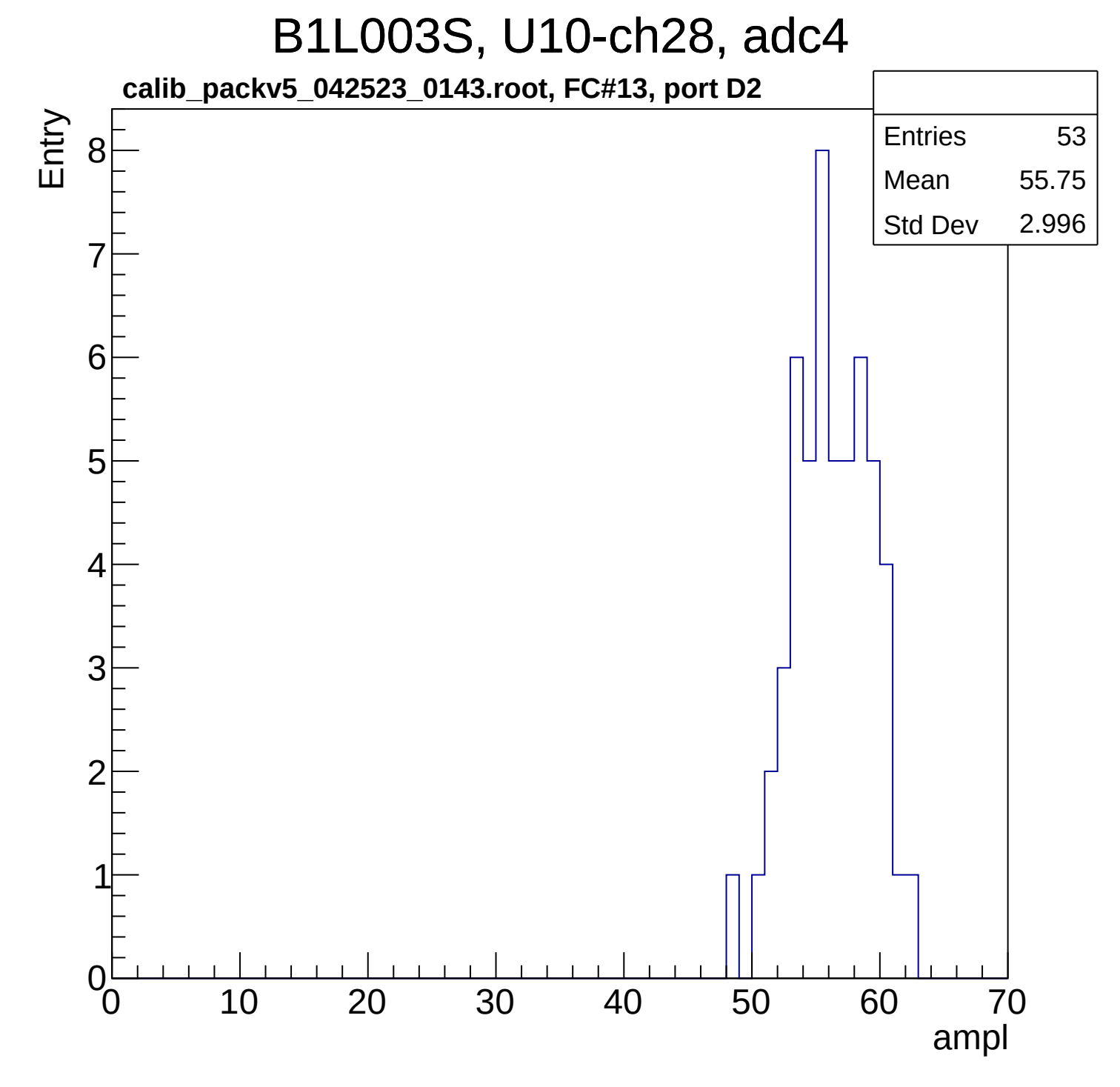
B1L003S, U10-ch28, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.75
Std Dev 2.996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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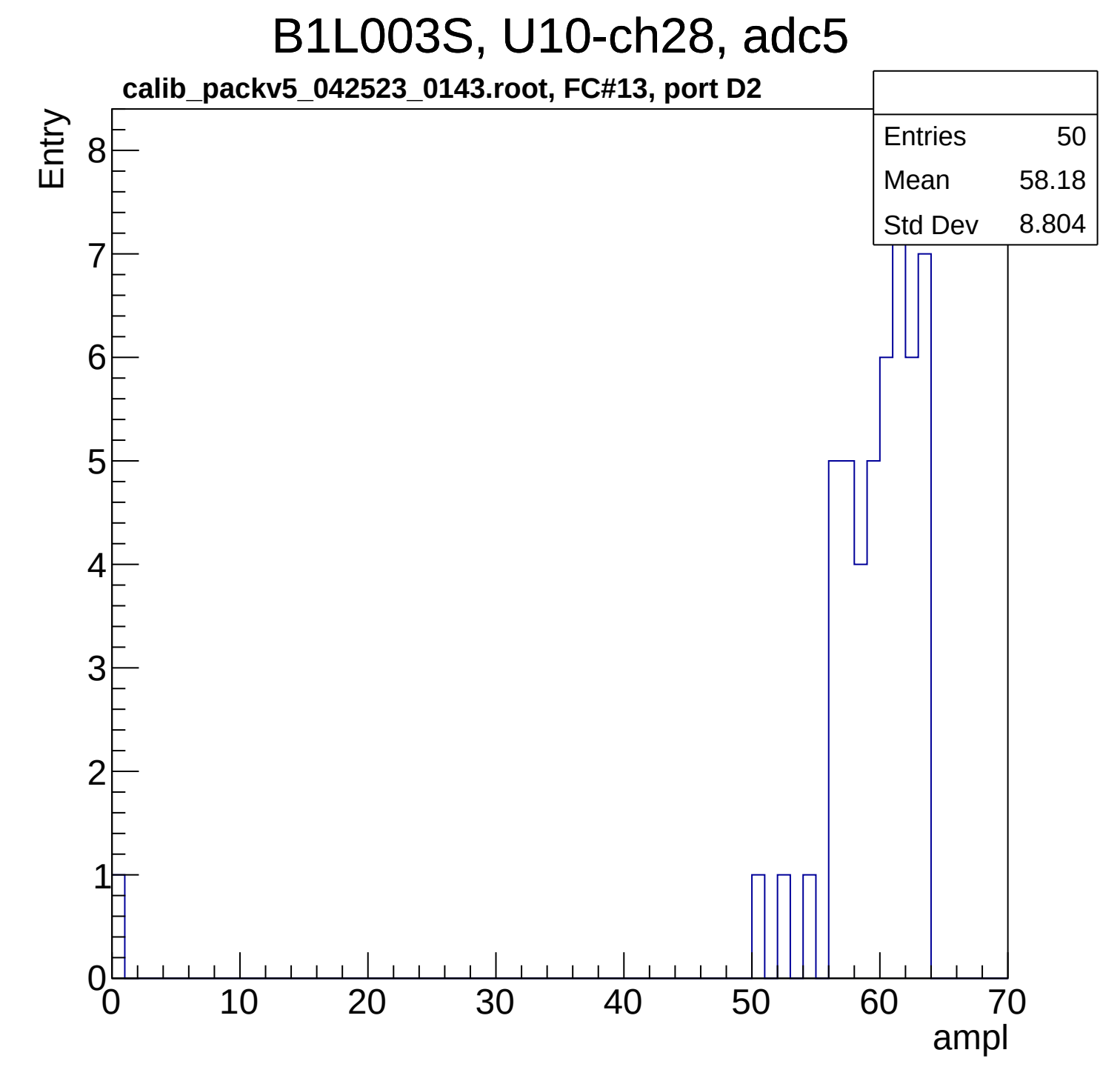
B1L003S, U10-ch28, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.18
Std Dev 8.804
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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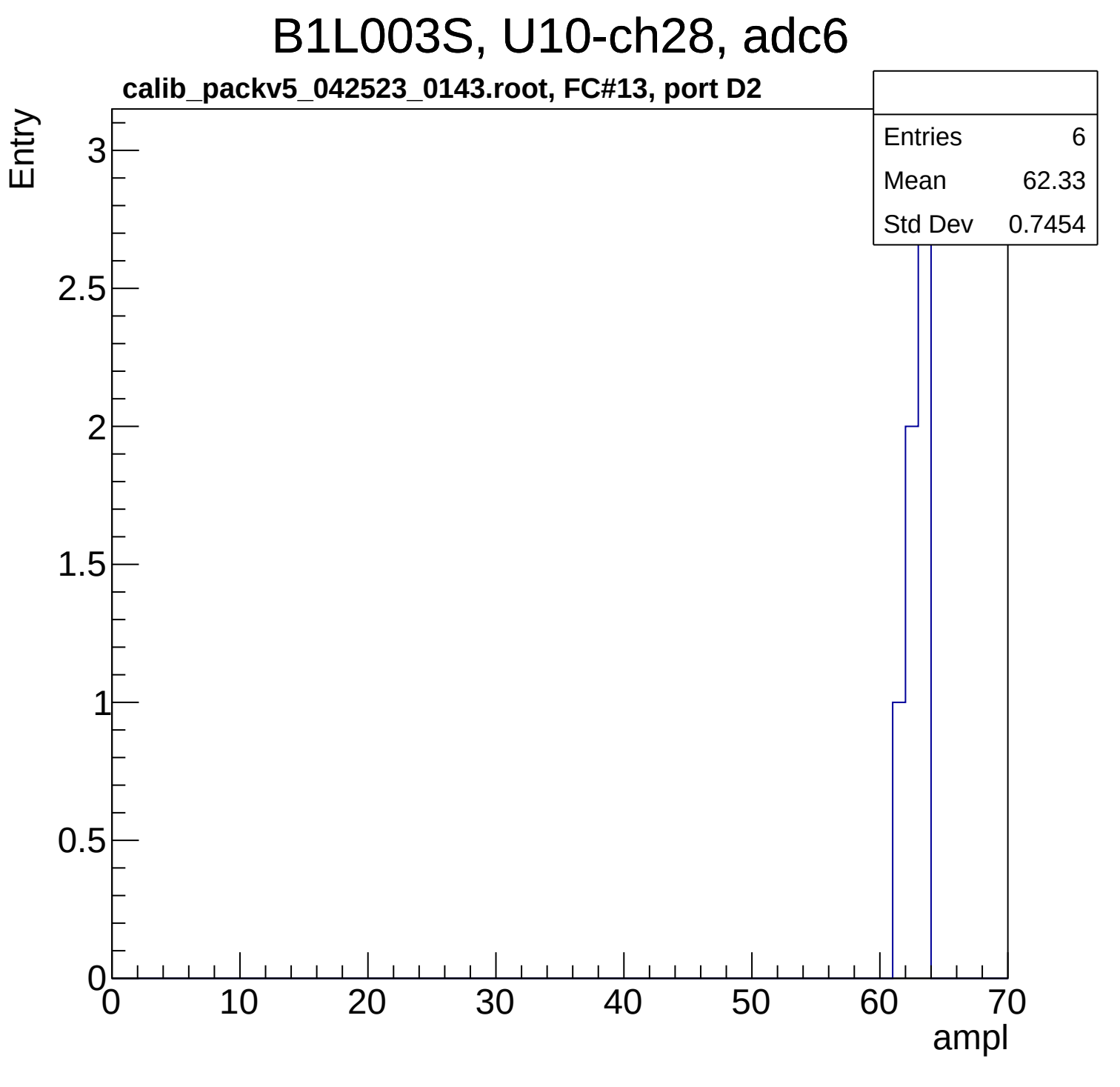
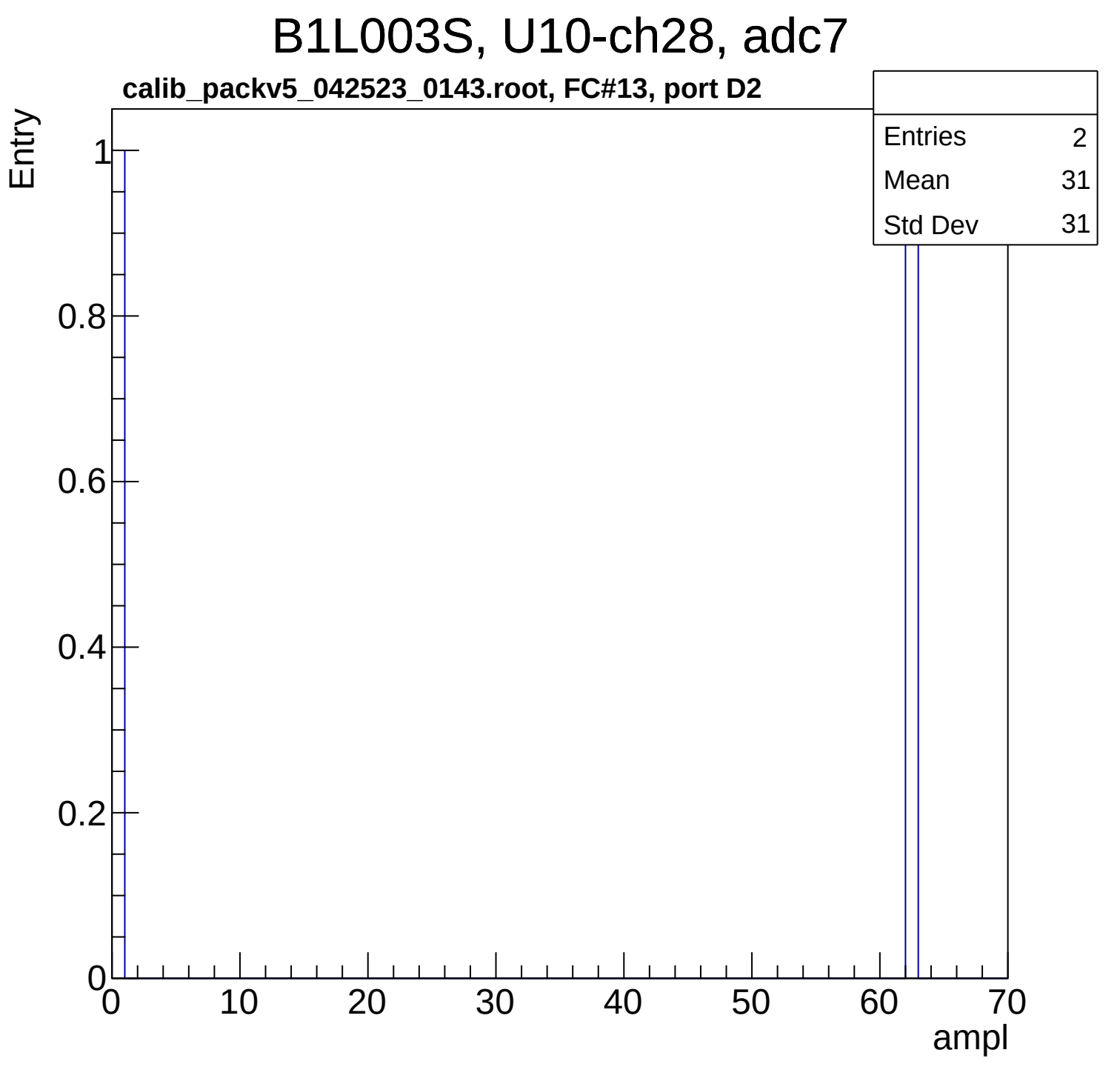
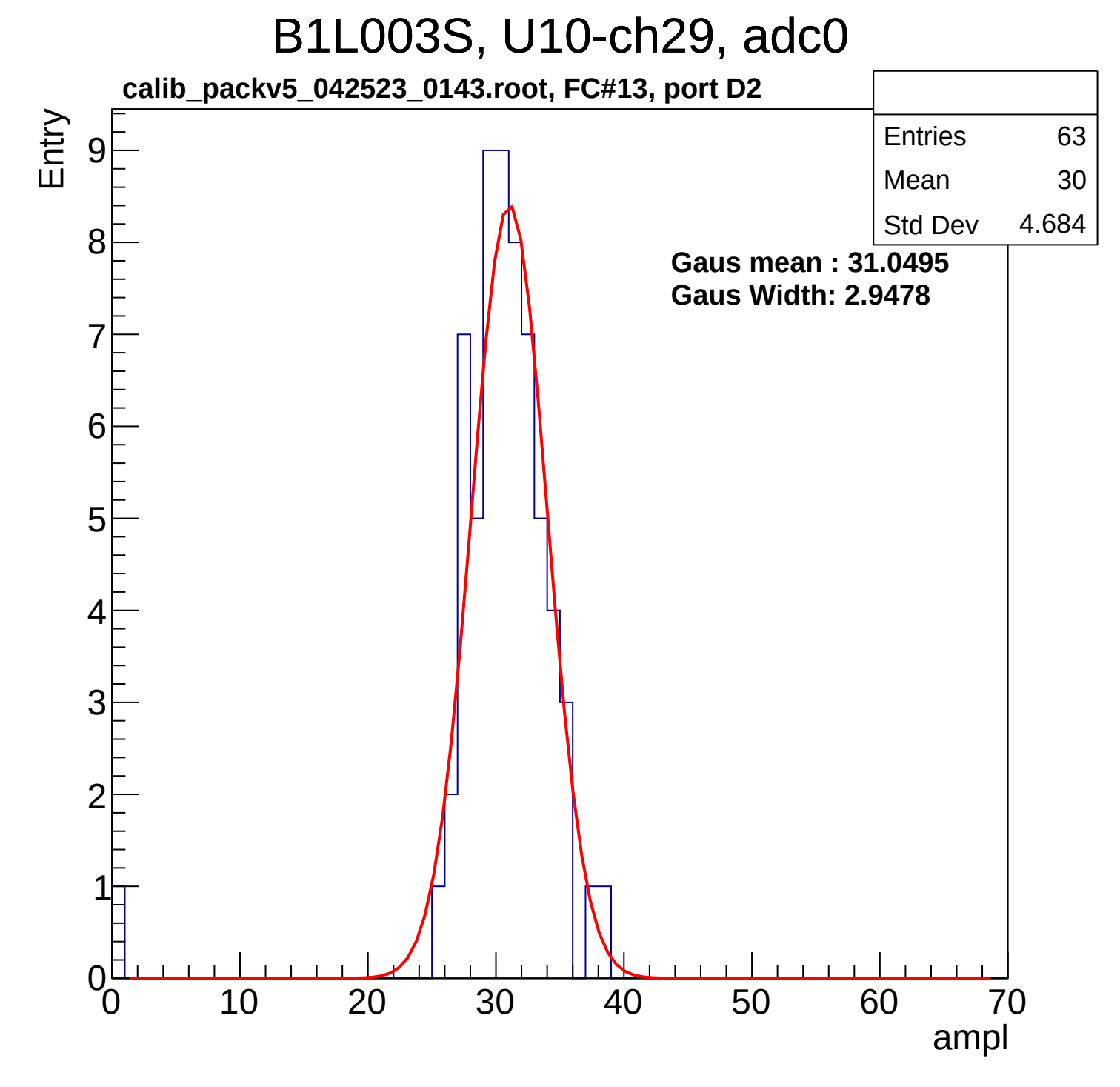
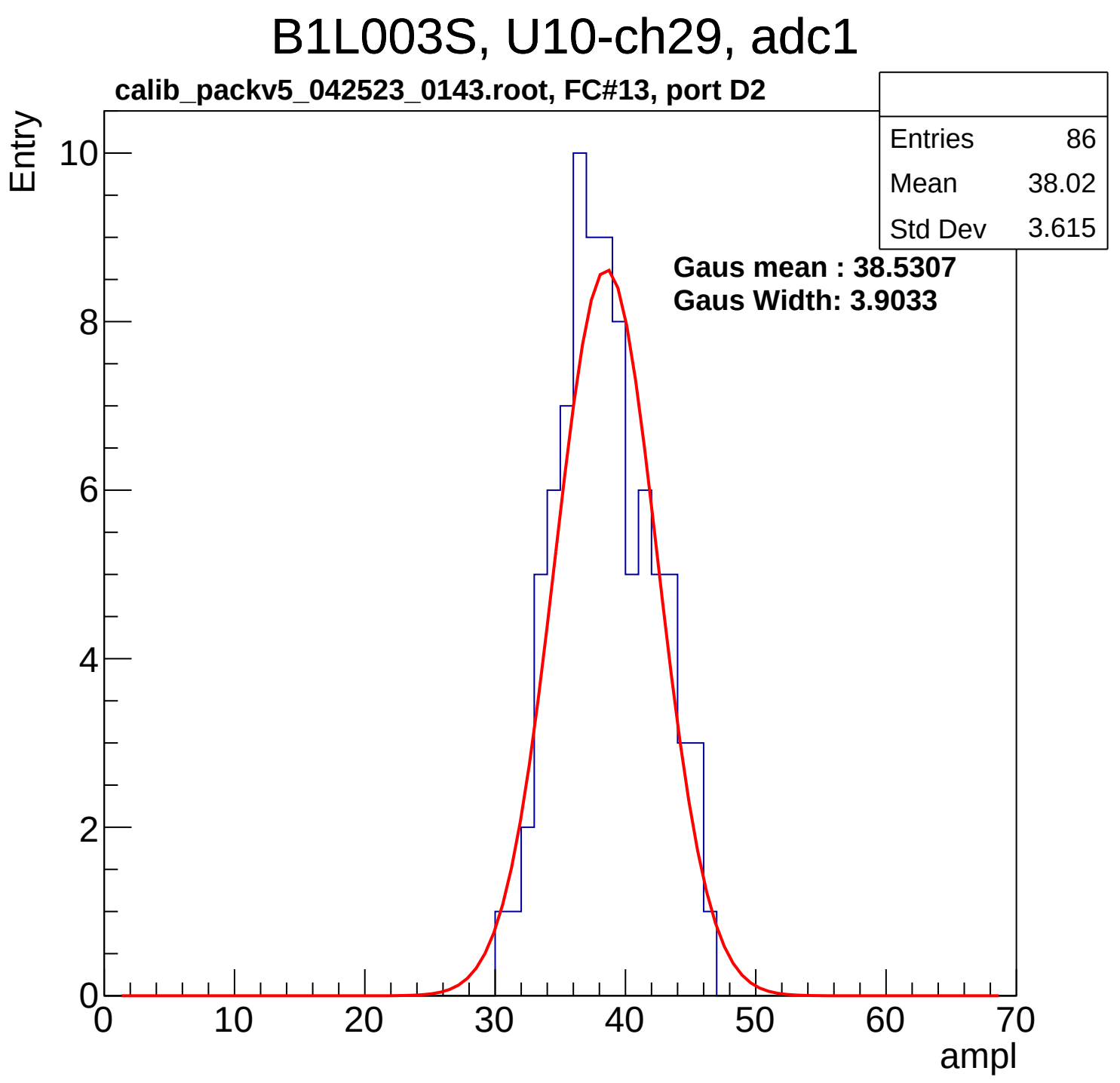
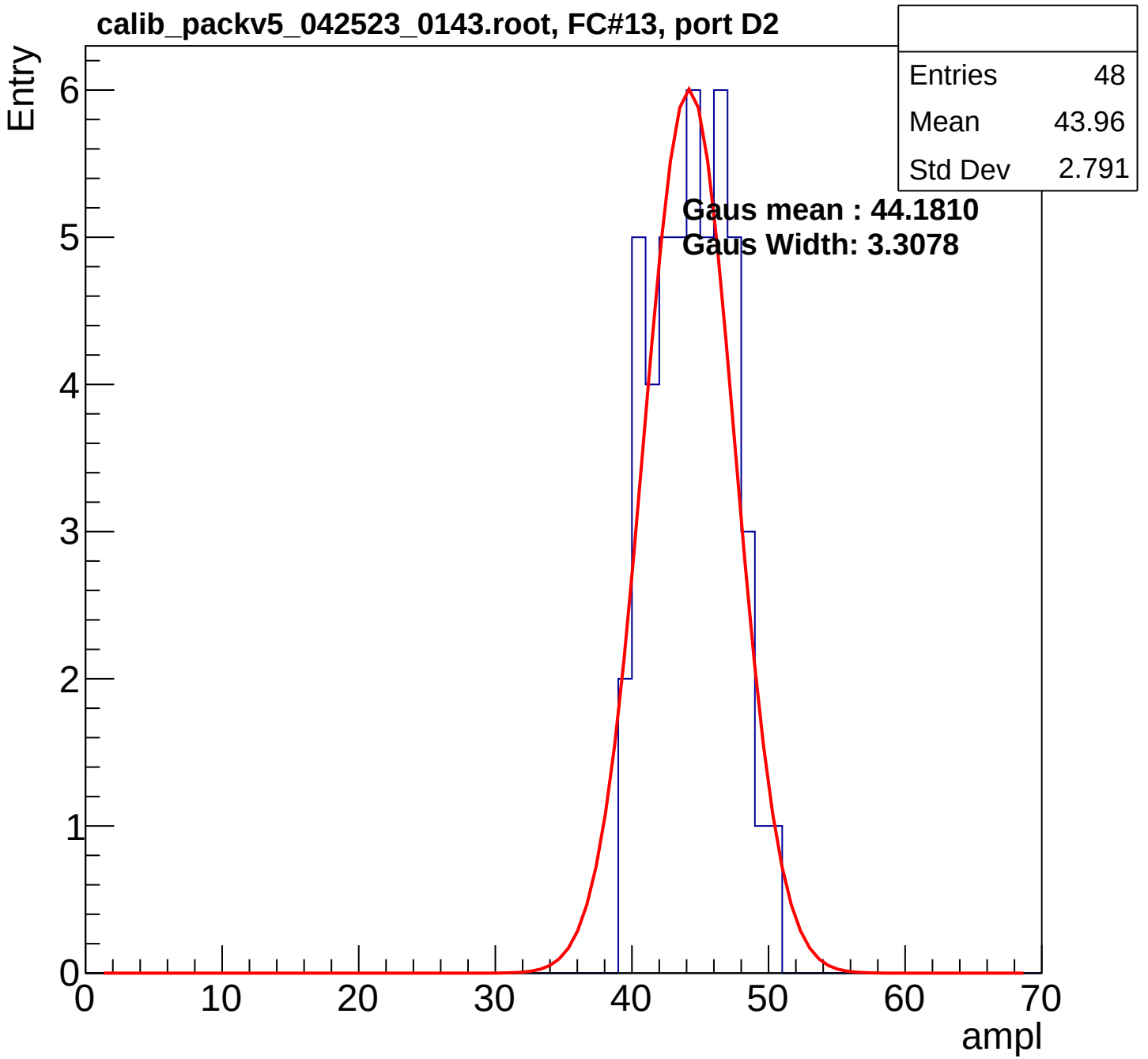

B1L003S, U10-ch28, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch28, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch29, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 30
Std Dev 4.684
Gaus mean : 31.0495
Gaus Width: 2.9478
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch29, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 38.02
Std Dev 3.615
Gaus mean : 38.5307
Gaus Width: 3.9033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch29, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 43.96
Std Dev 2.791
Gaus mean : 44.1810
Gaus Width: 3.3078
Entry
ampl
0
1
2
3
4
5
6
0
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20
30
40
50
60
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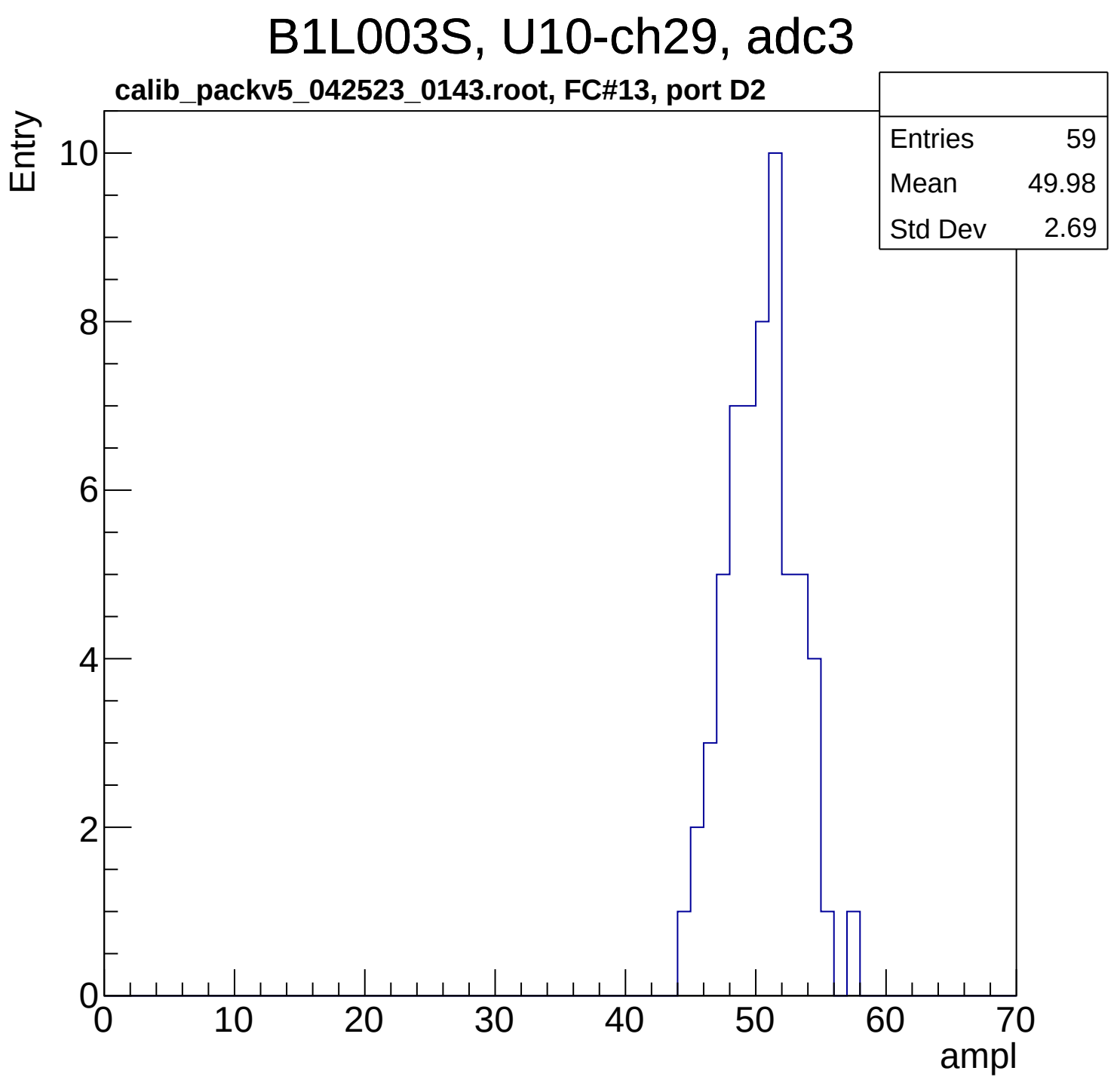
B1L003S, U10-ch29, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.98
Std Dev 2.69
Entry
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4
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10
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10
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30
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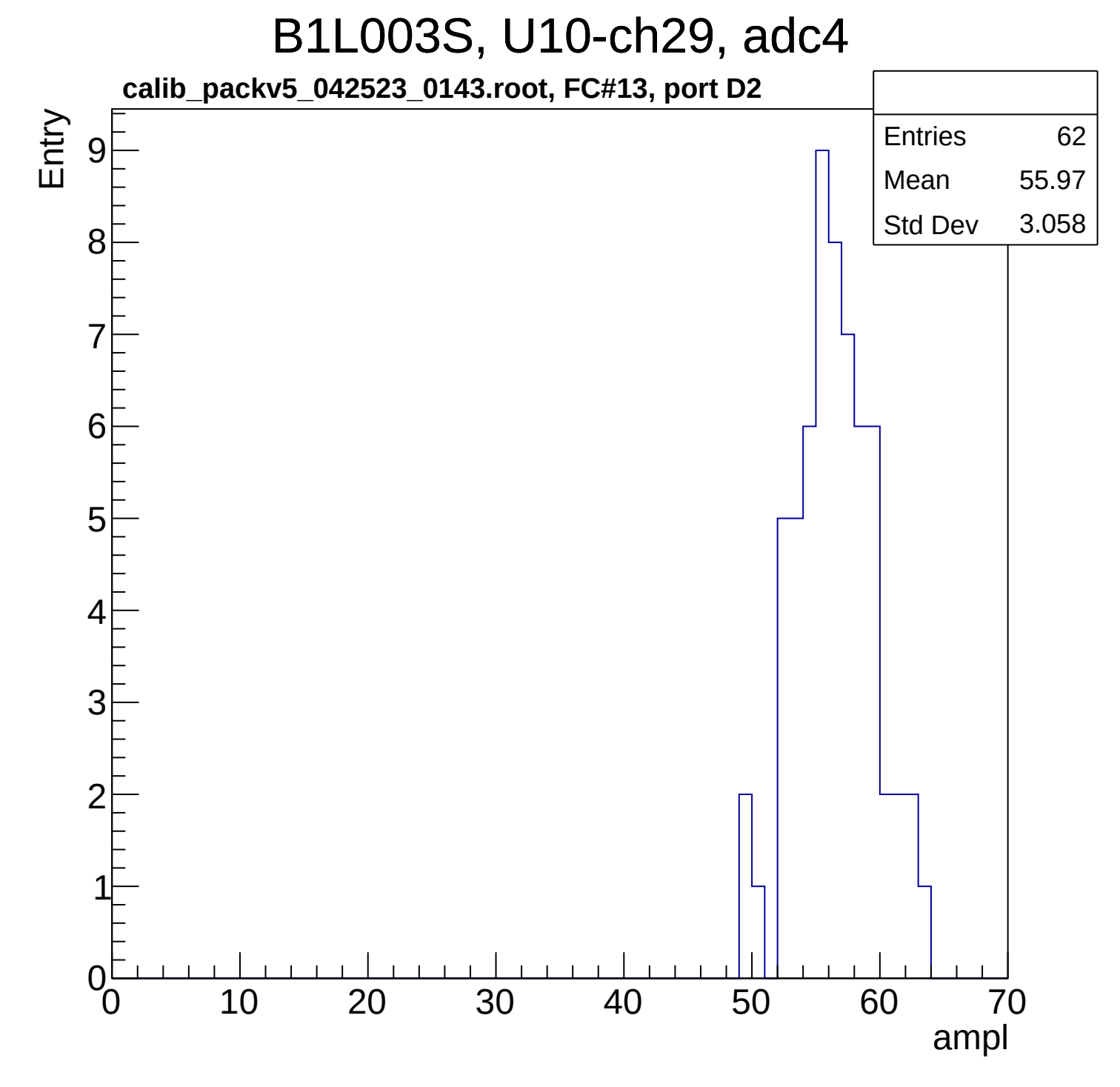
50
60
70
ampl


B1L003S, U10-ch29, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 55.97
Std Dev 3.058
Entry
ampl
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2
3
4
5
6
7
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9
0
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60
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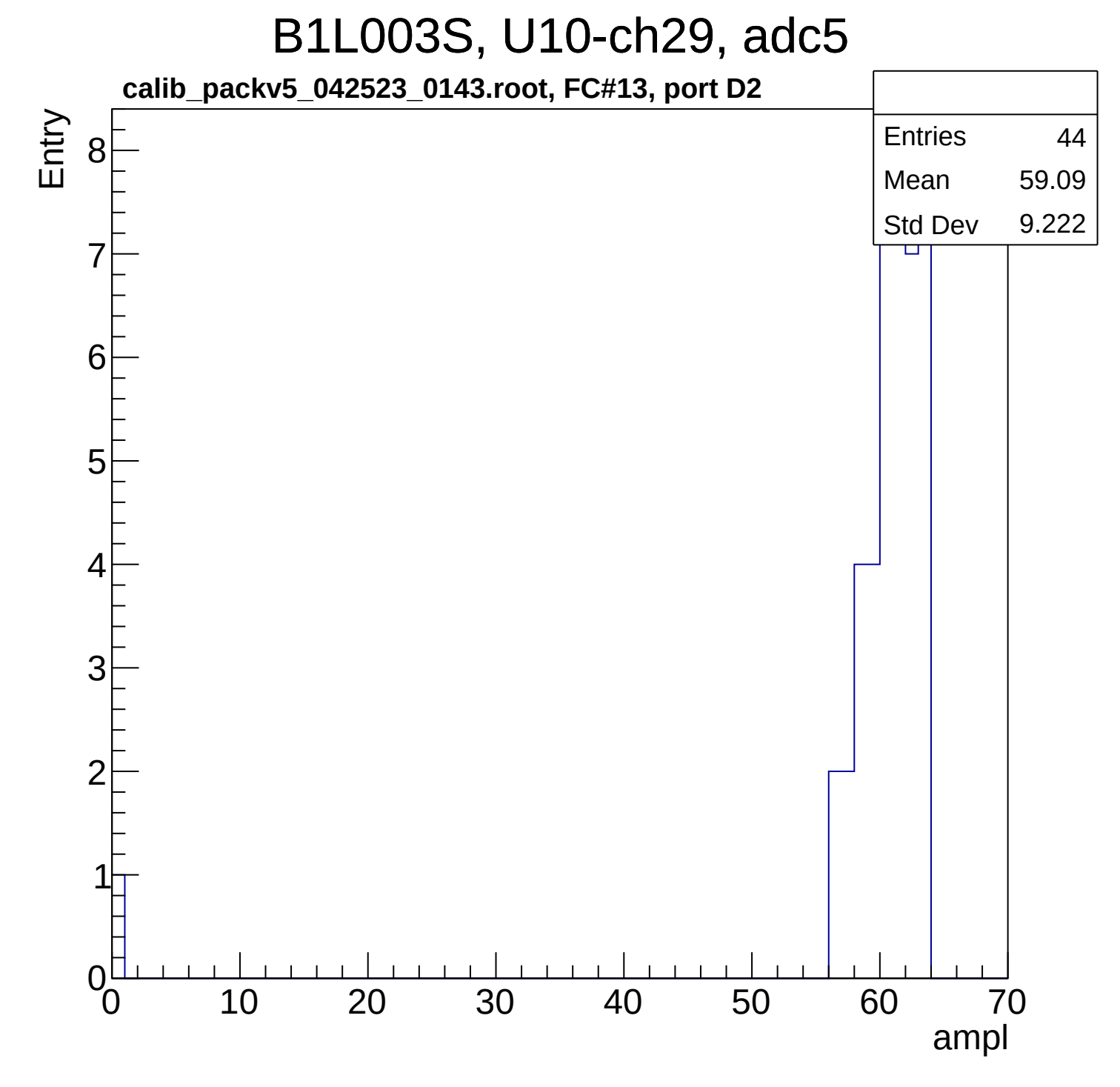
B1L003S, U10-ch29, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.09
Std Dev 9.222
Entry
ampl
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2
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4
5
6
7
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0
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60
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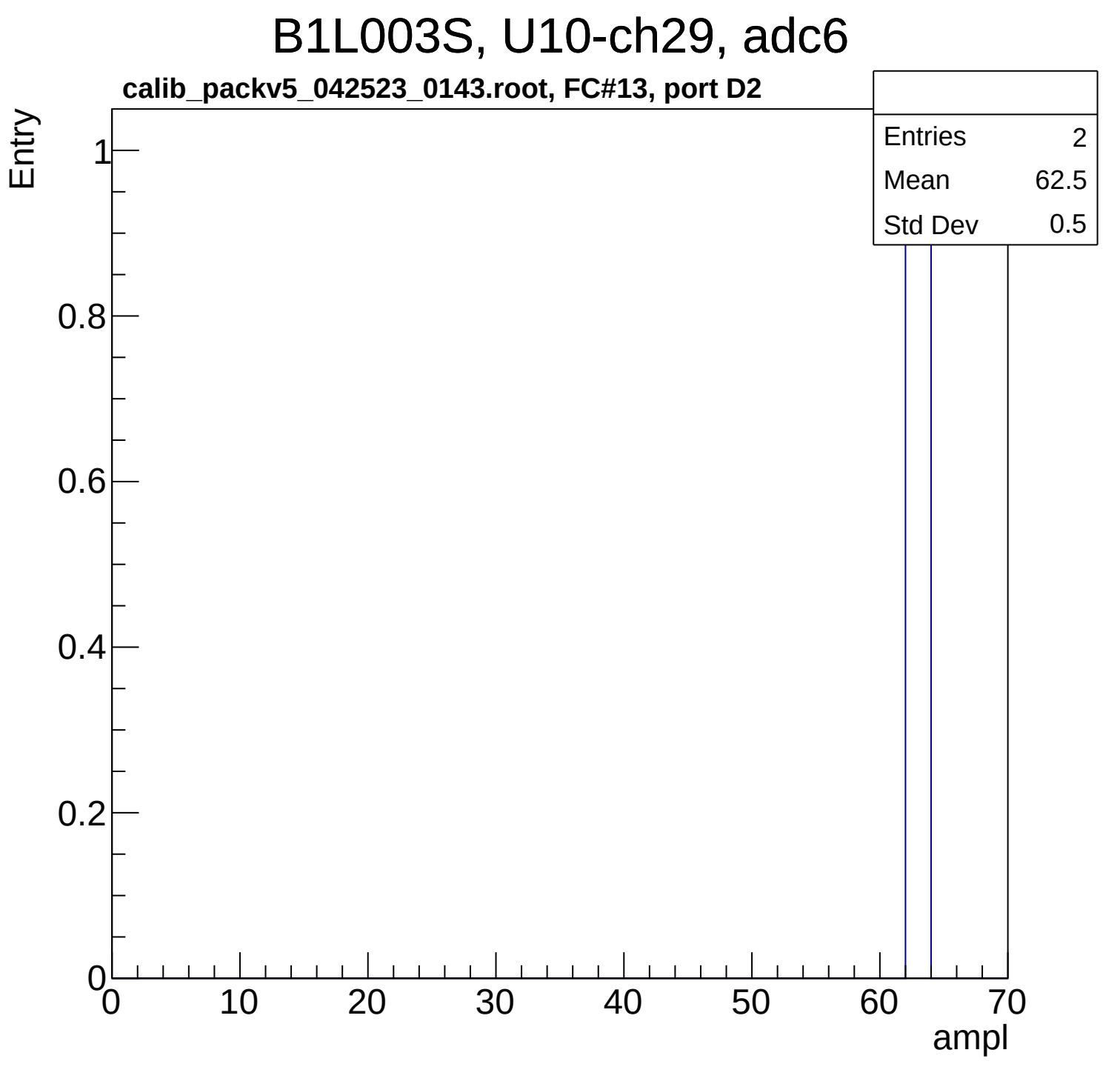
B1L003S, U10-ch29, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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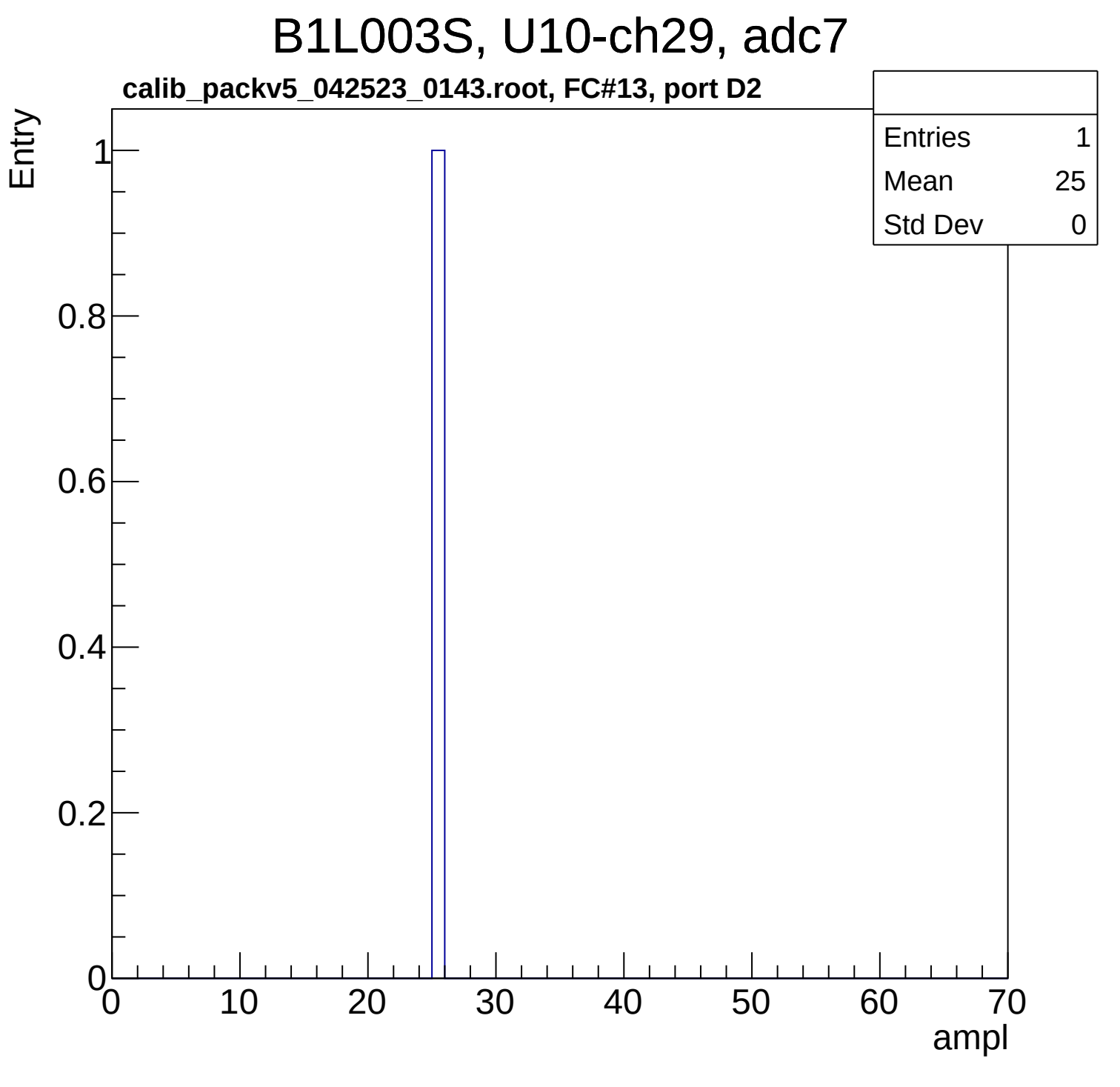
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch29, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 25
Std Dev 0
Entry
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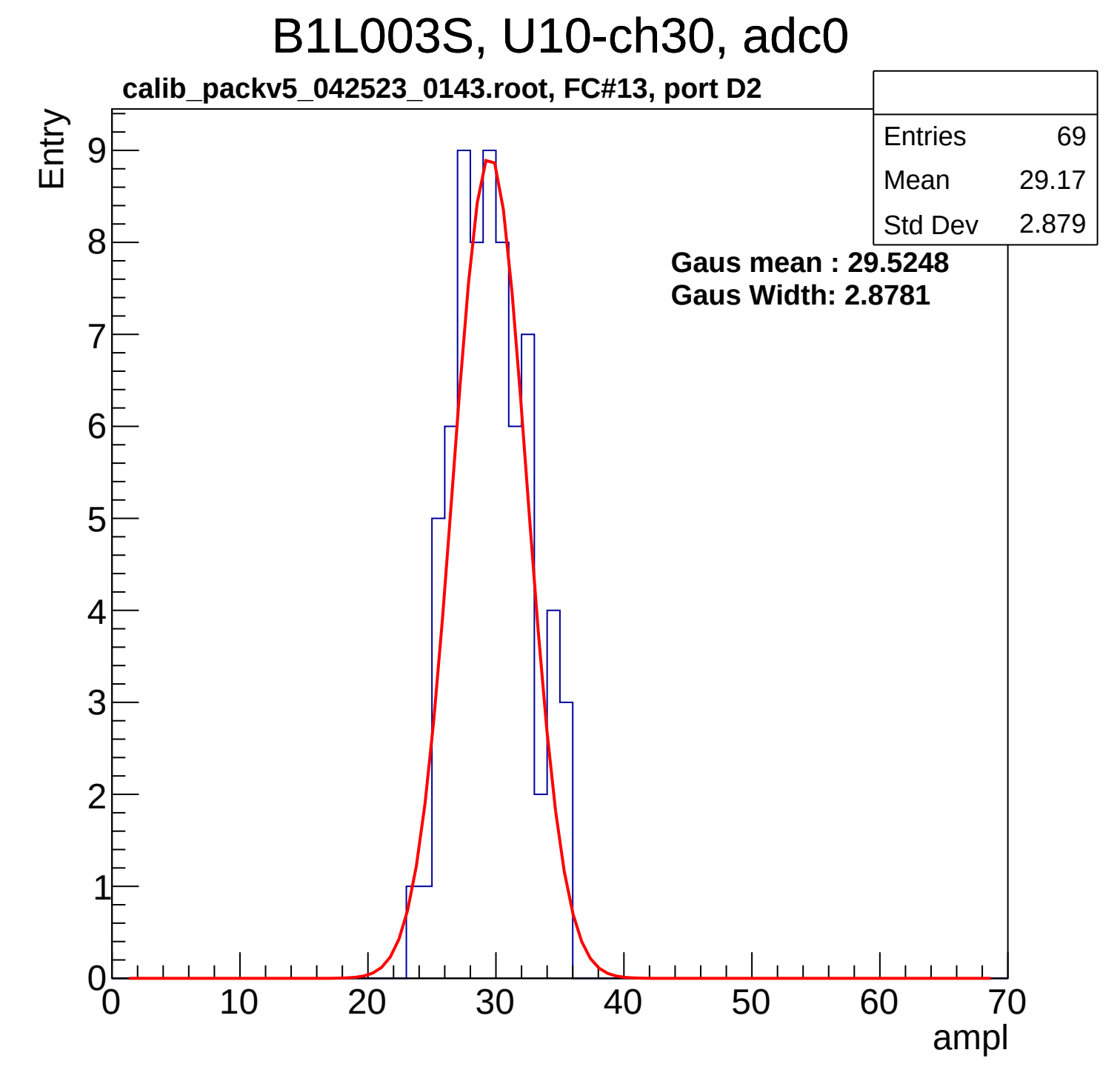
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch30, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 29.17
Std Dev 2.879
Gaus mean : 29.5248
Gaus Width: 2.8781
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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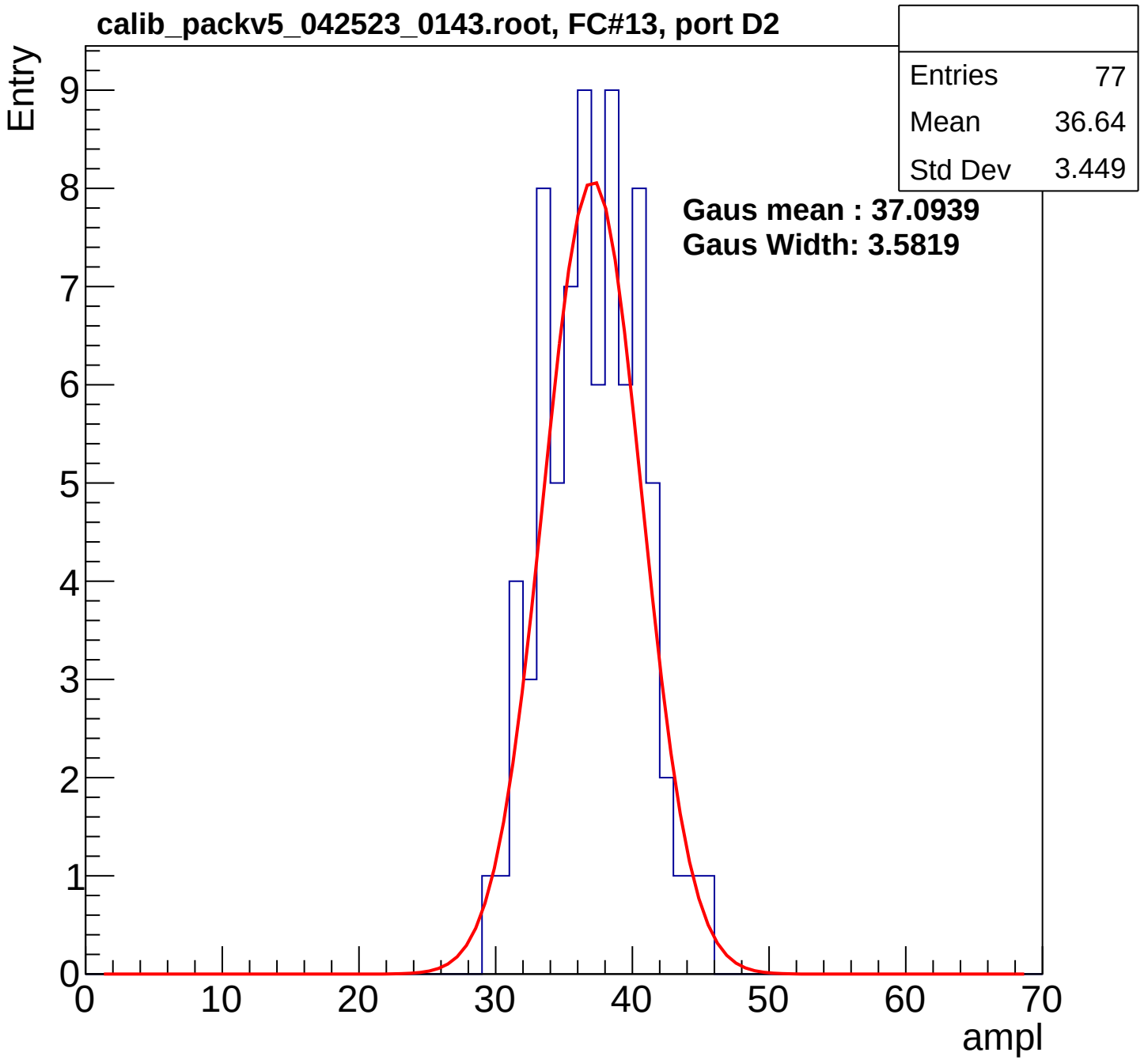
B1L003S, U10-ch30, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.64
Std Dev 3.449
Gaus mean : 37.0939
Gaus Width: 3.5819
Entry
ampl
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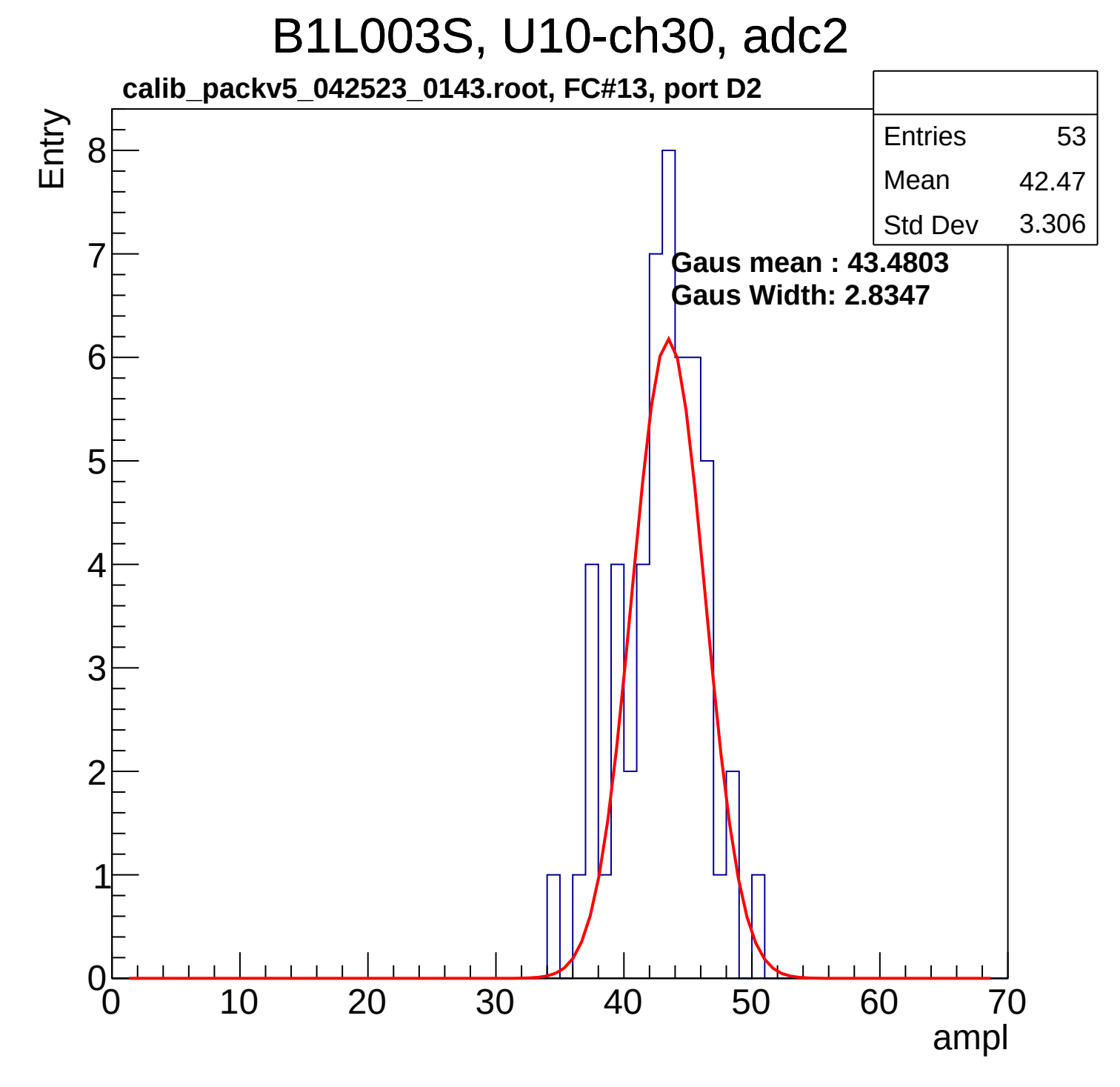
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch30, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.47
Std Dev 3.306
Gaus mean : 43.4803
Gaus Width: 2.8347
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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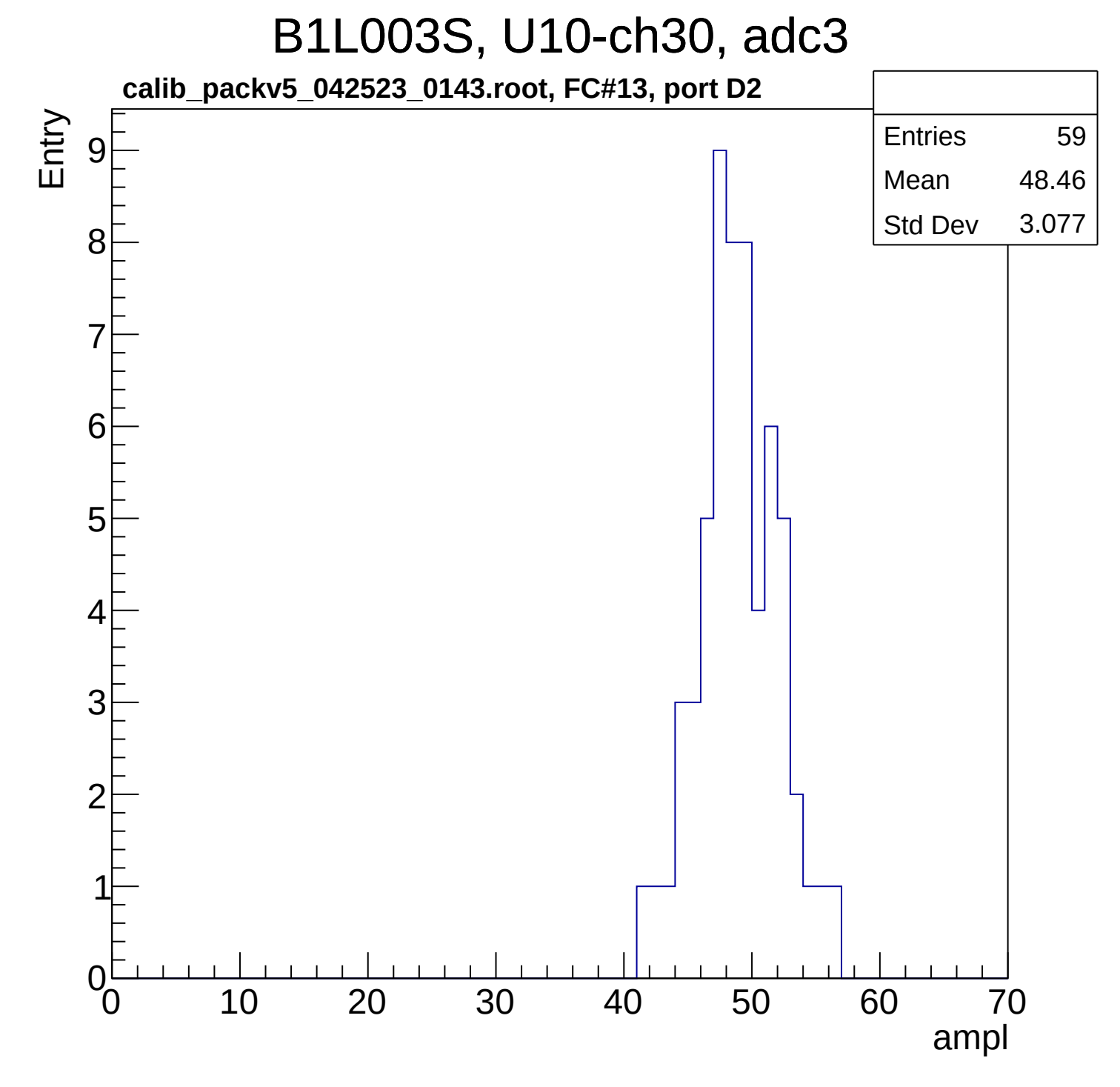
B1L003S, U10-ch30, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48.46
Std Dev 3.077
Entry
ampl
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4
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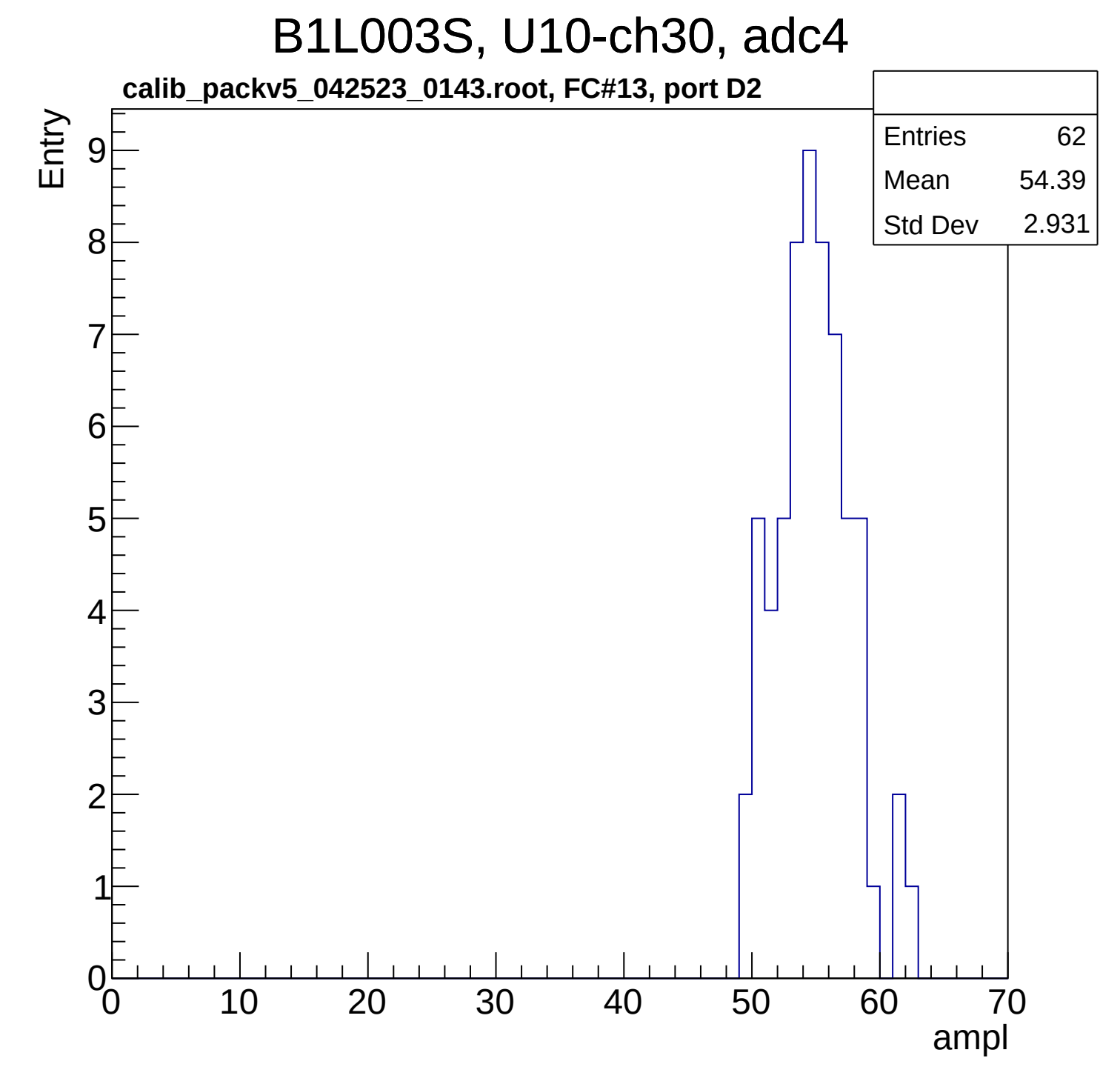
B1L003S, U10-ch30, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 54.39
Std Dev 2.931
Entry
ampl
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3
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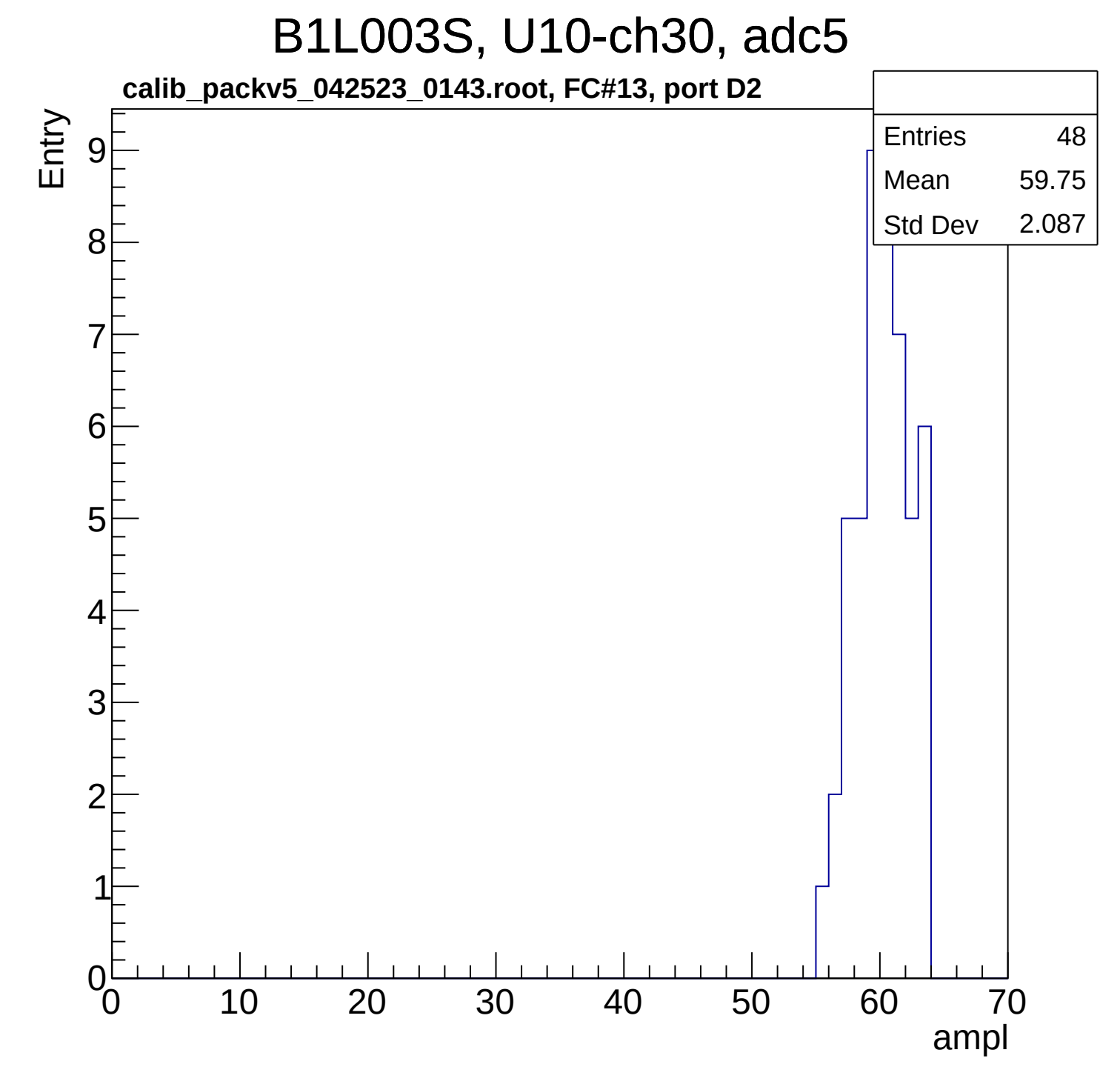
B1L003S, U10-ch30, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.75
Std Dev 2.087
Entry
ampl
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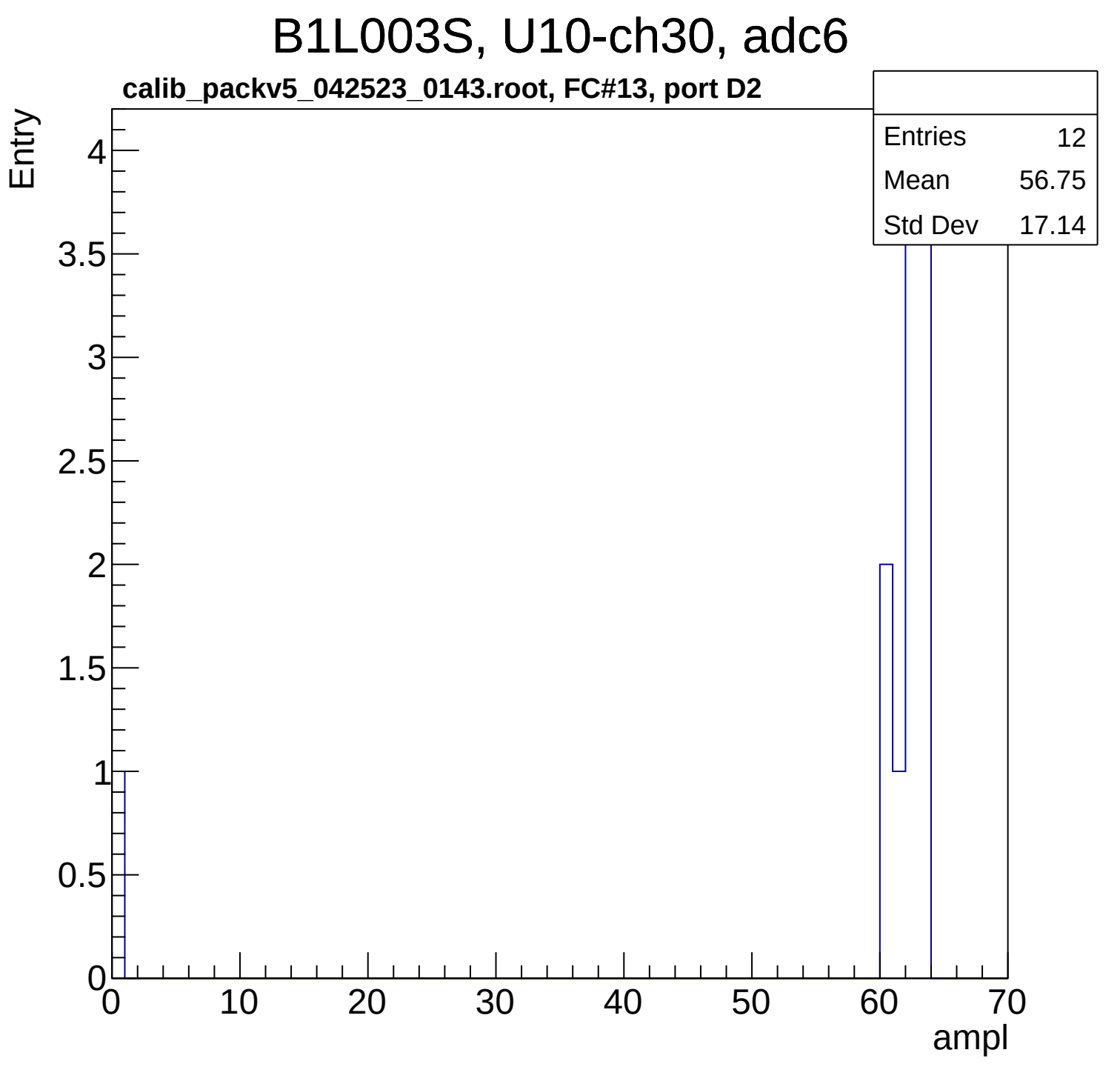
B1L003S, U10-ch30, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.75
Std Dev 17.14
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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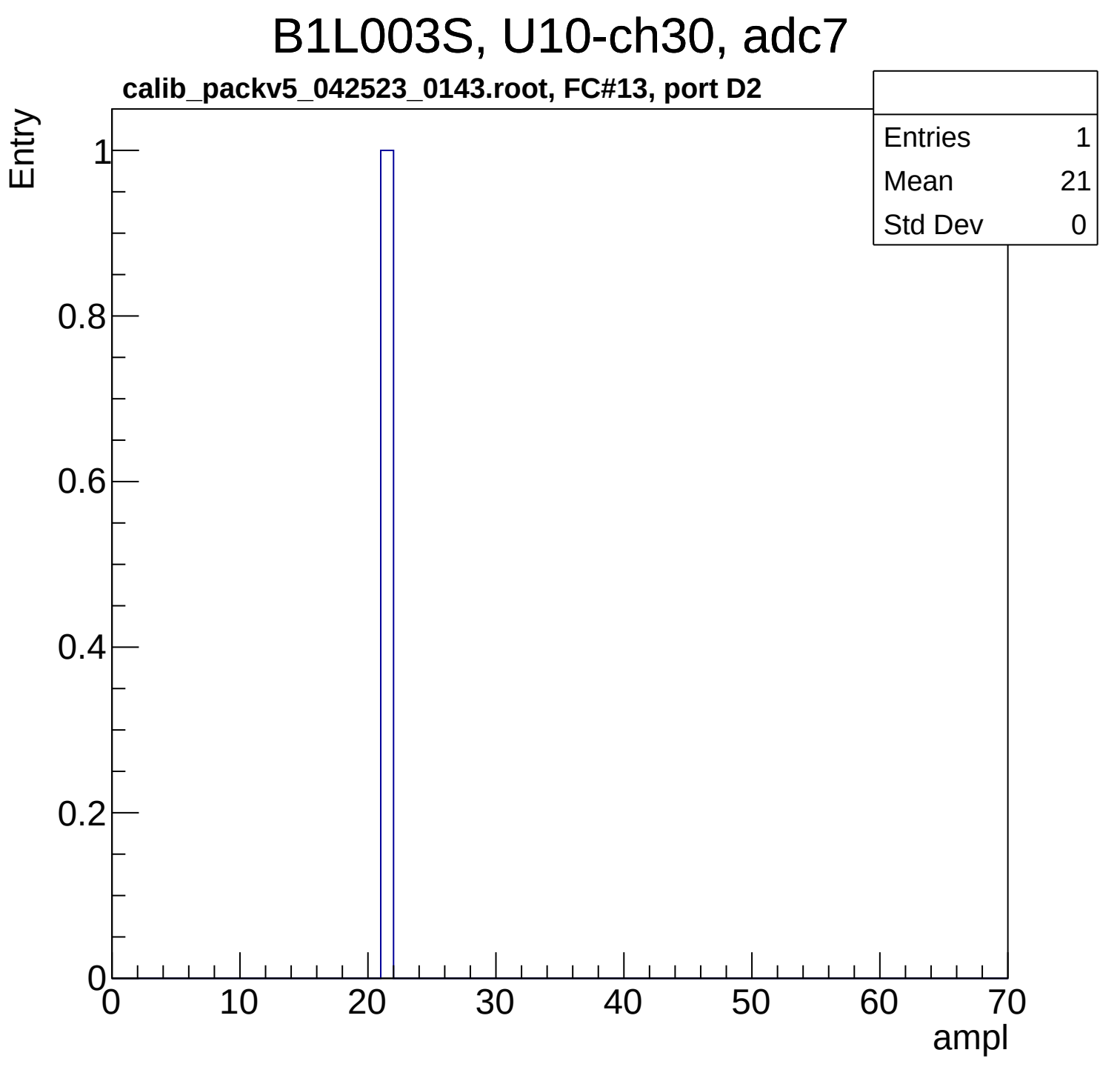
B1L003S, U10-ch30, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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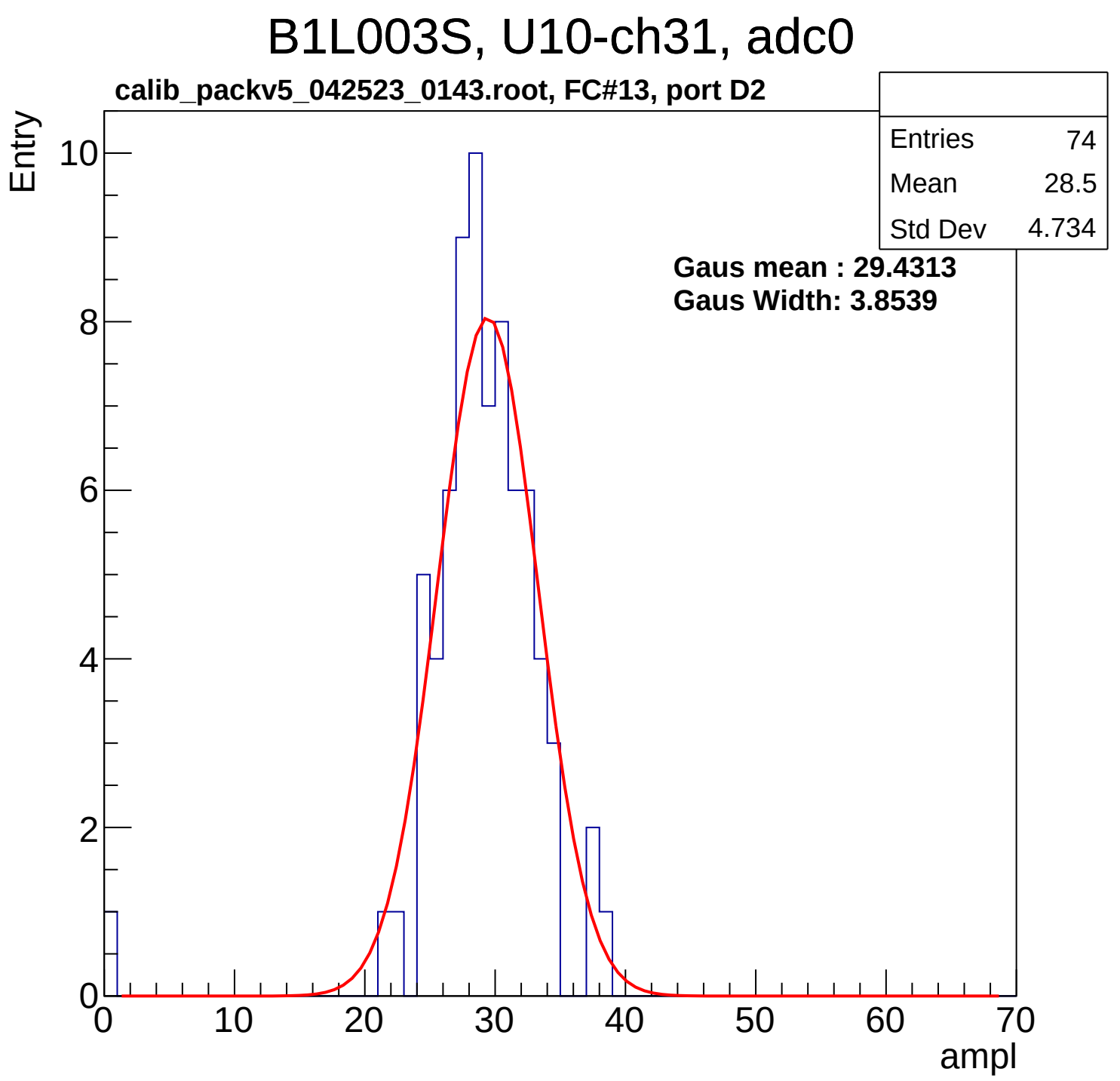
60
70
ampl


B1L003S, U10-ch31, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 28.5
Std Dev 4.734
Gaus mean : 29.4313
Gaus Width: 3.8539
Entry
ampl
0
2
4
6
8
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0
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60
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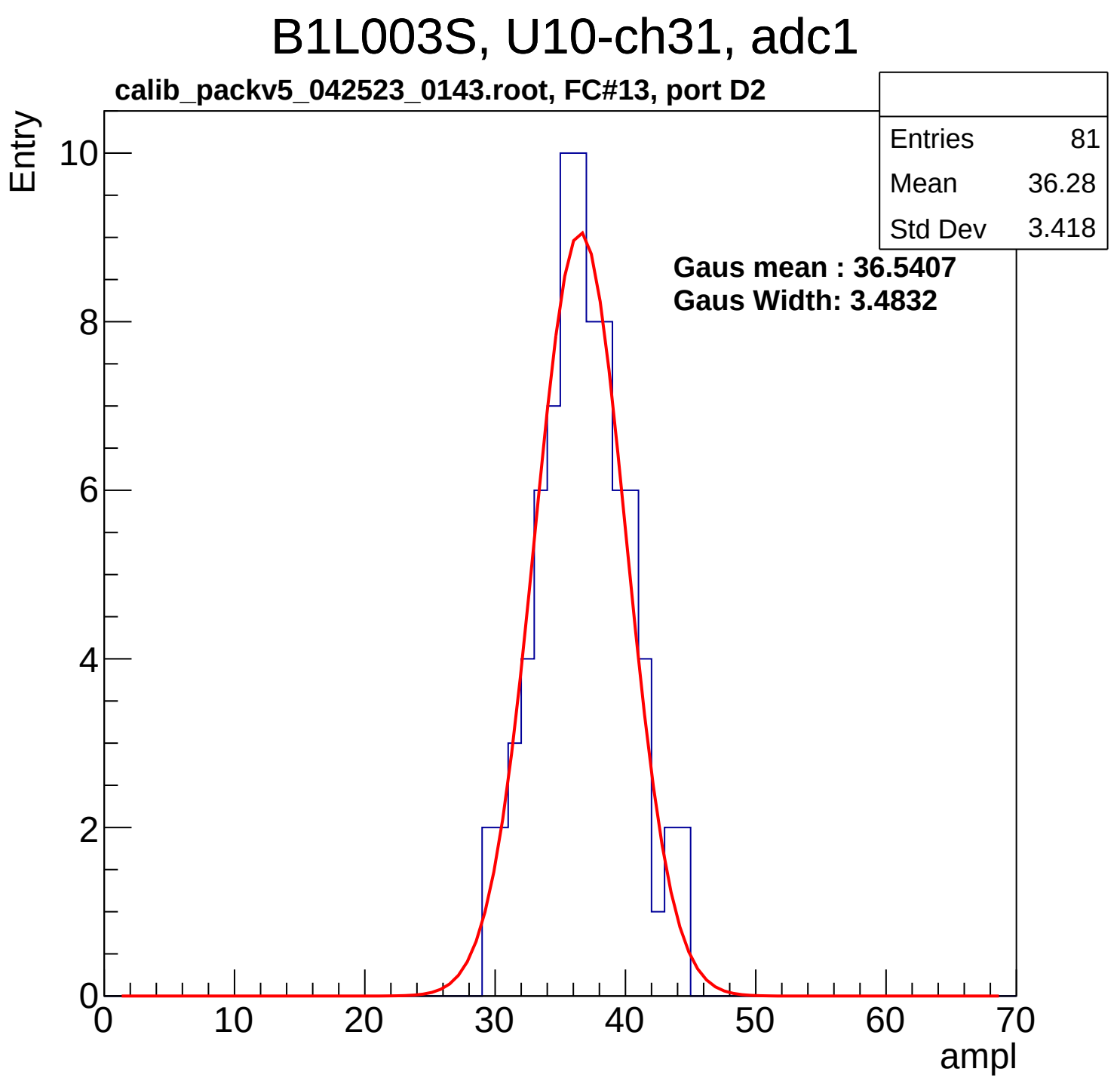
B1L003S, U10-ch31, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 36.28
Std Dev 3.418
Gaus mean : 36.5407
Gaus Width: 3.4832
Entry
ampl
0
2
4
6
8
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0
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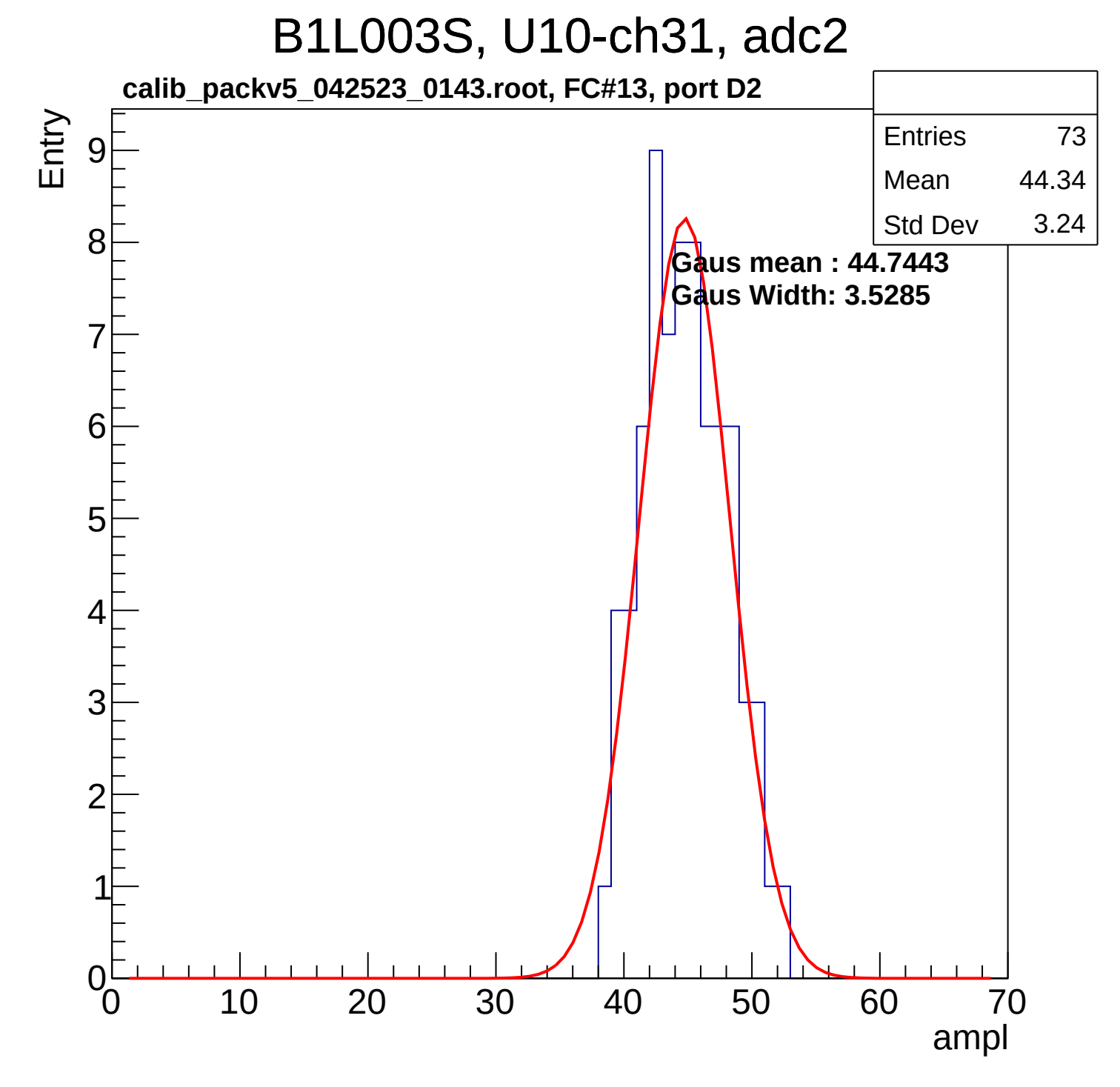
B1L003S, U10-ch31, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 44.34
Std Dev 3.24
Gaus mean : 44.7443
Gaus Width: 3.5285
Entry
ampl
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4
5
6
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0
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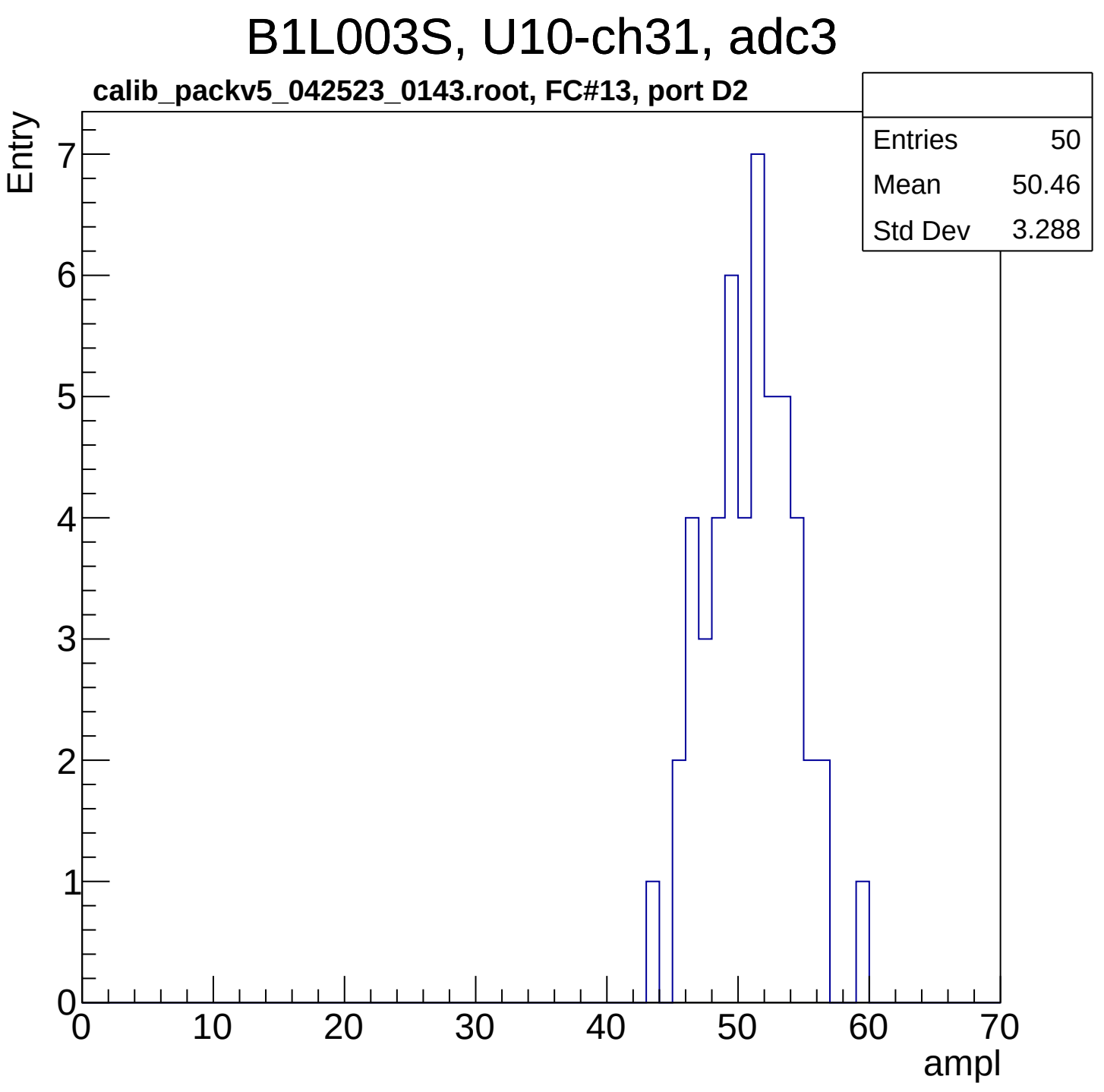
B1L003S, U10-ch31, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 50.46
Std Dev 3.288
Entry
ampl
0
1
2
3
4
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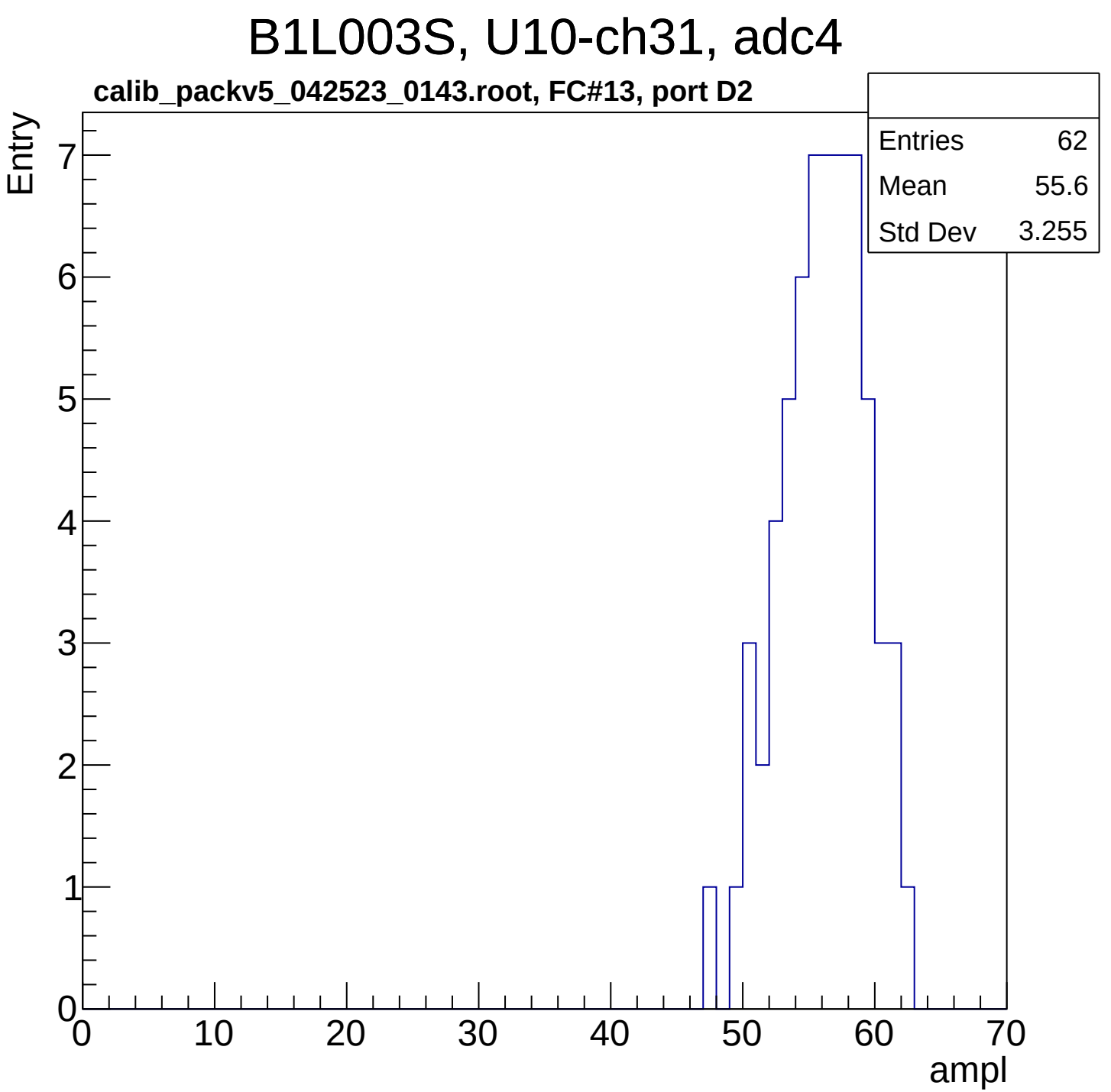
B1L003S, U10-ch31, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 55.6
Std Dev 3.255
Entry
ampl
0
1
2
3
4
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0
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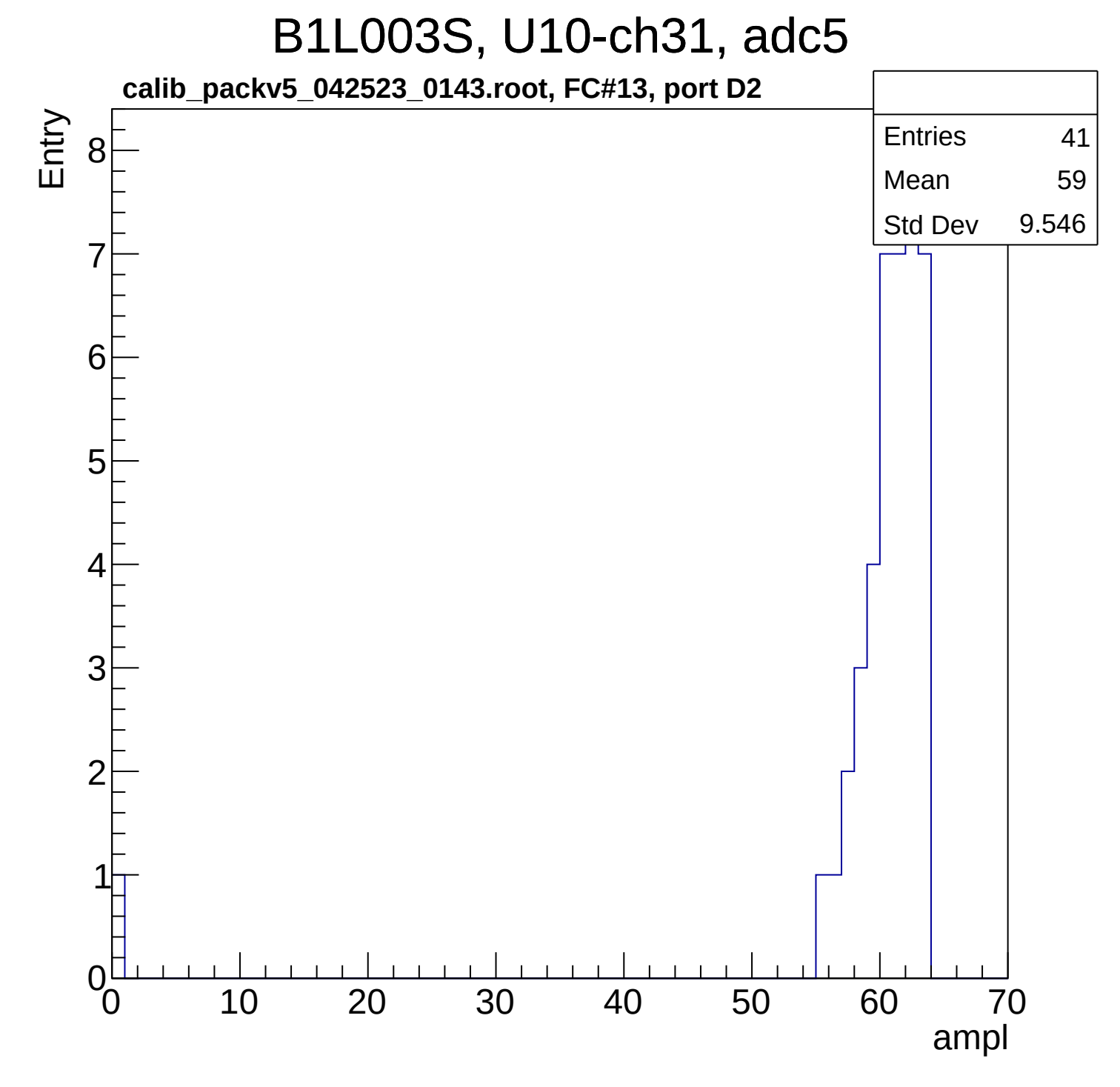
B1L003S, U10-ch31, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59
Std Dev 9.546
Entry
ampl
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1
2
3
4
5
6
7
8
0
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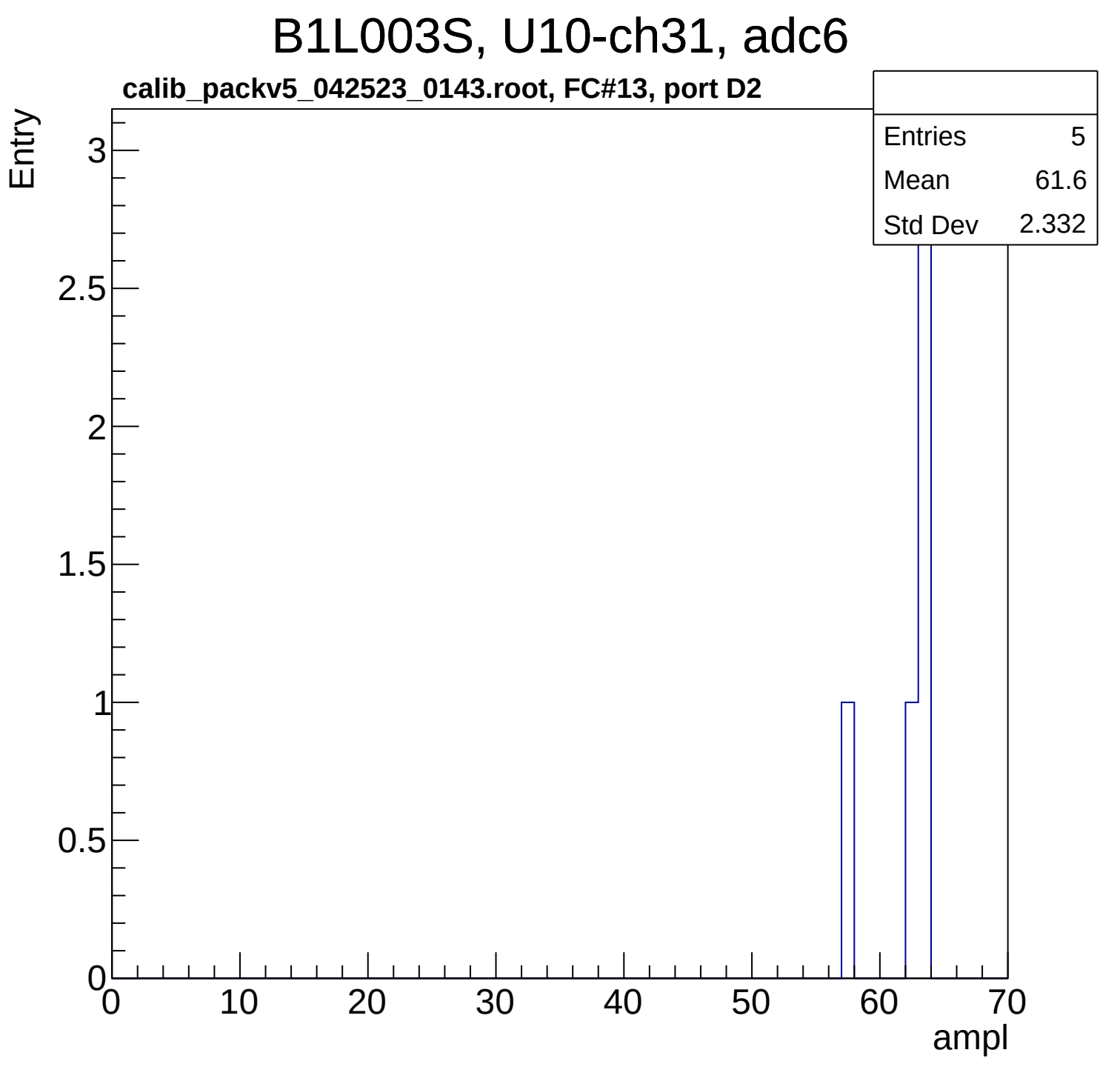
B1L003S, U10-ch31, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.6
Std Dev 2.332
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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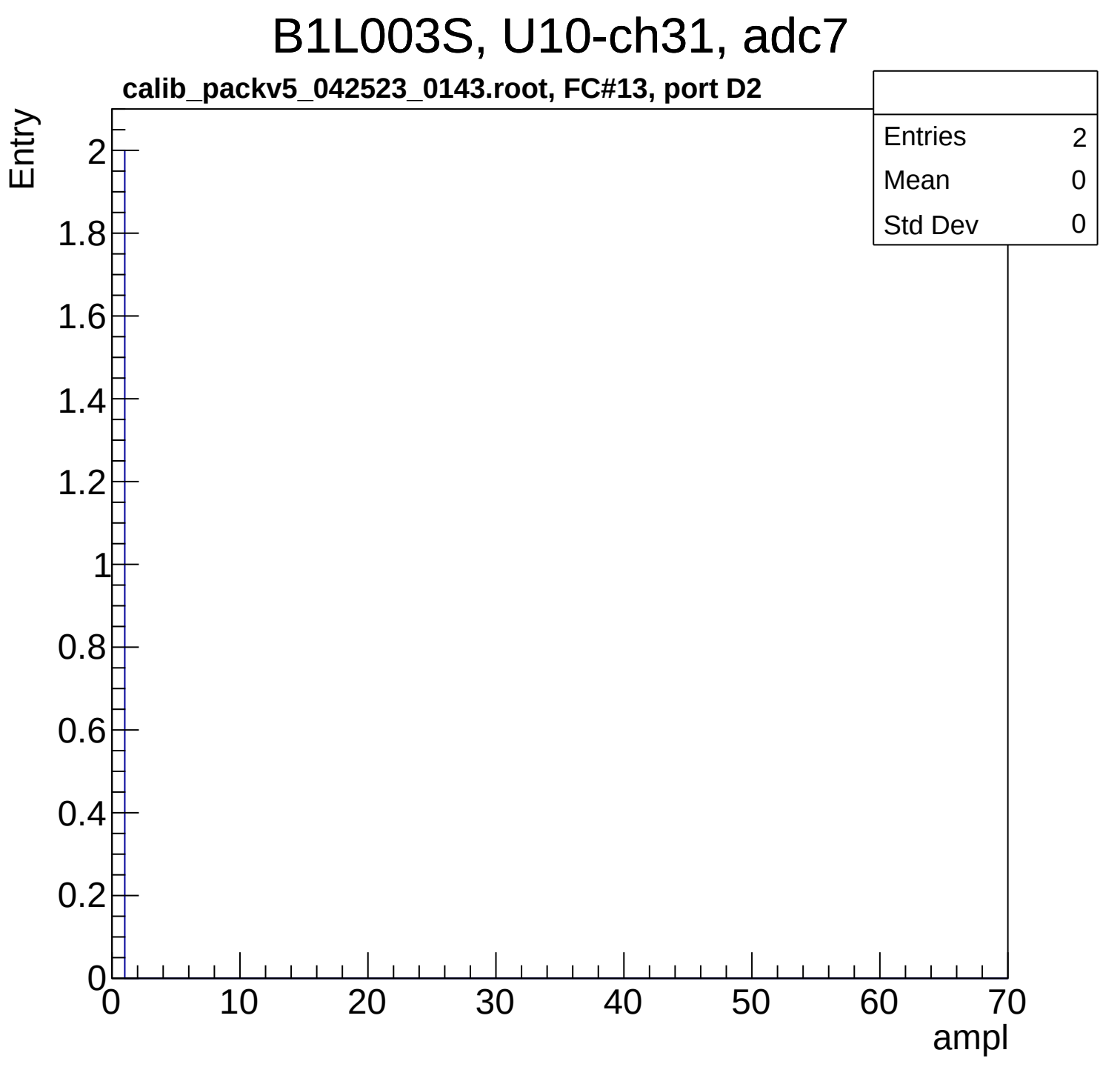
B1L003S, U10-ch31, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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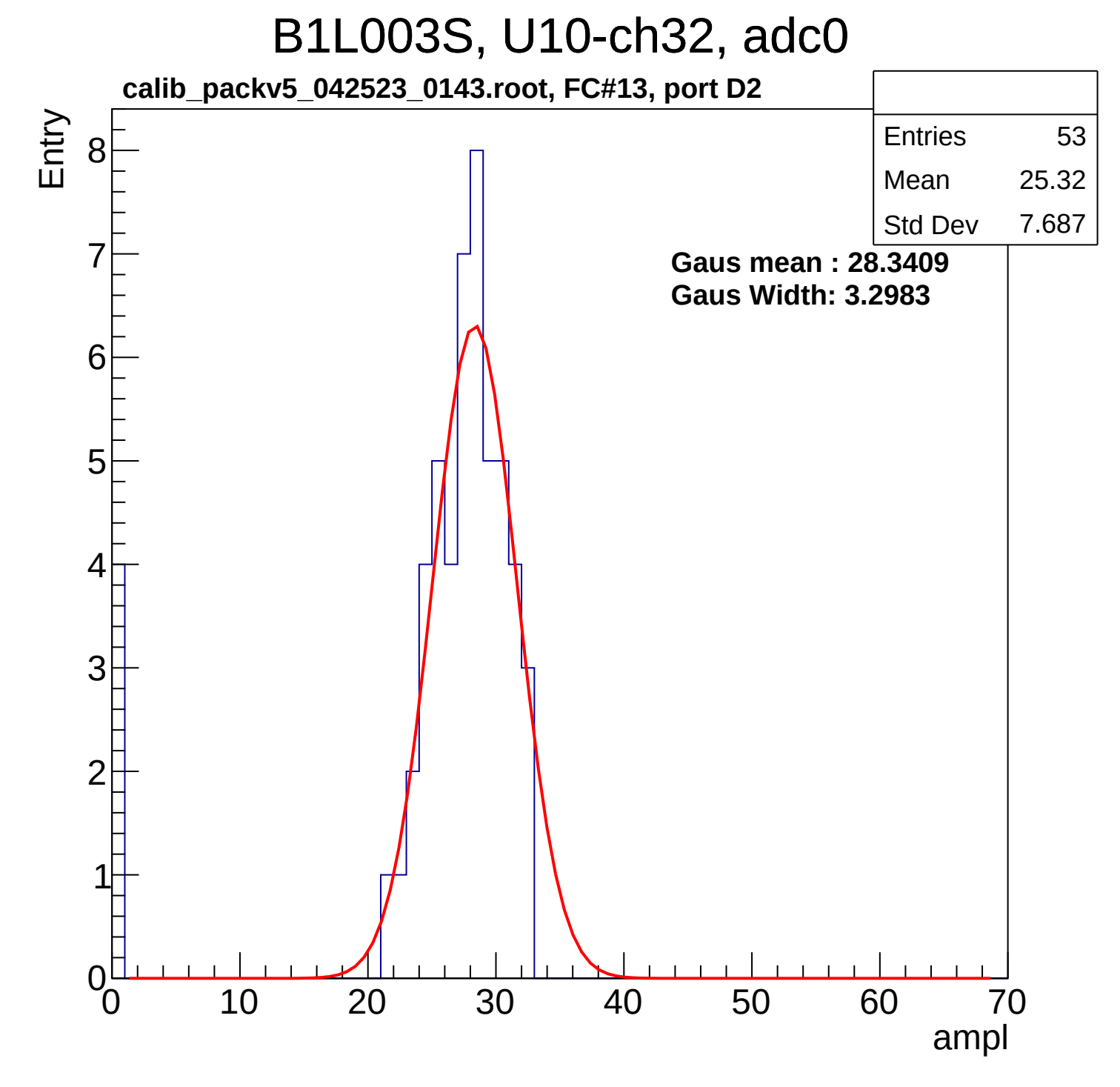
B1L003S, U10-ch32, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 25.32
Std Dev 7.687
Gaus mean : 28.3409
Gaus Width: 3.2983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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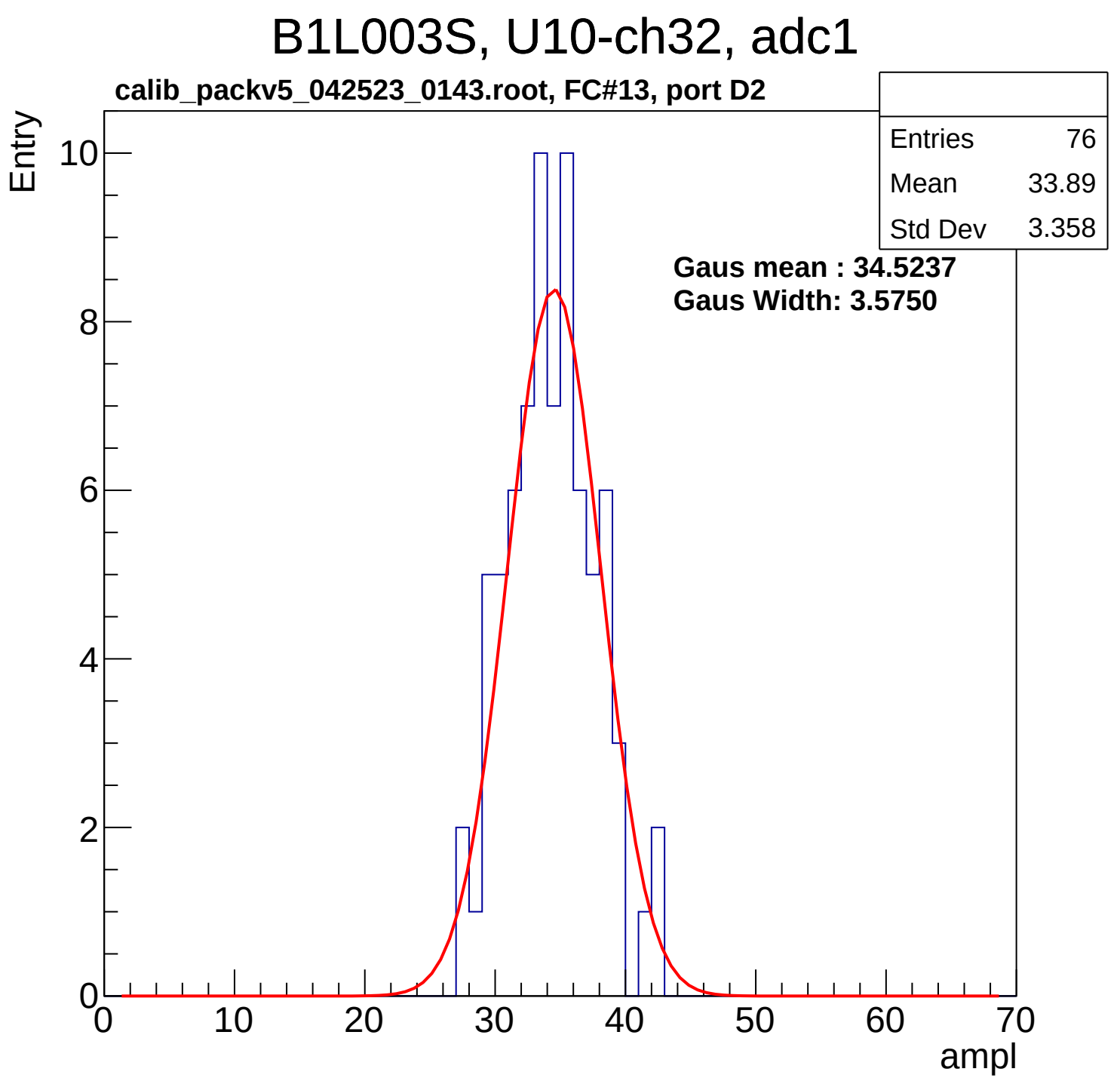
B1L003S, U10-ch32, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 33.89
Std Dev 3.358
Gaus mean : 34.5237
Gaus Width: 3.5750
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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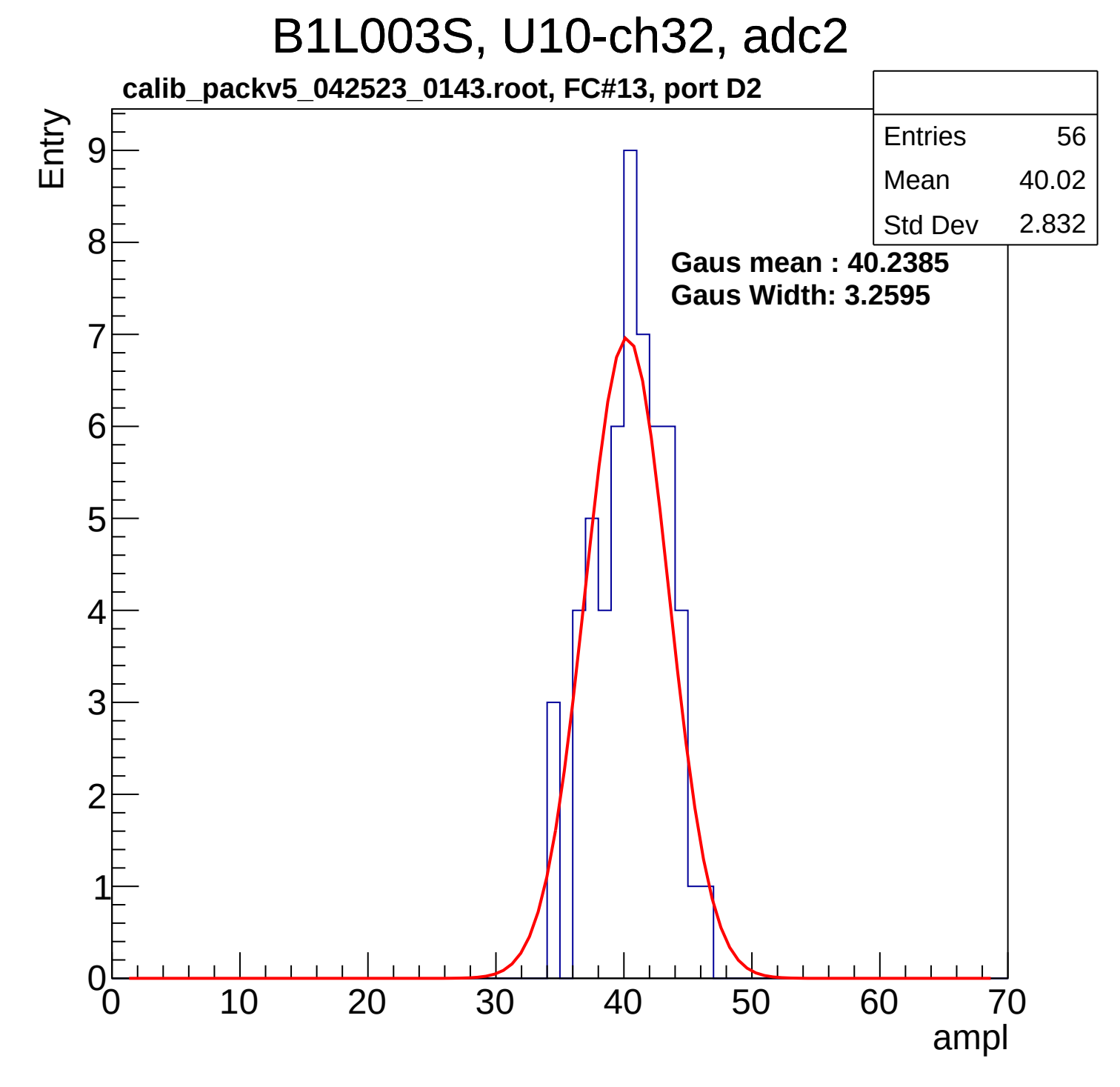
B1L003S, U10-ch32, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 40.02
Std Dev 2.832
Gaus mean : 40.2385
Gaus Width: 3.2595
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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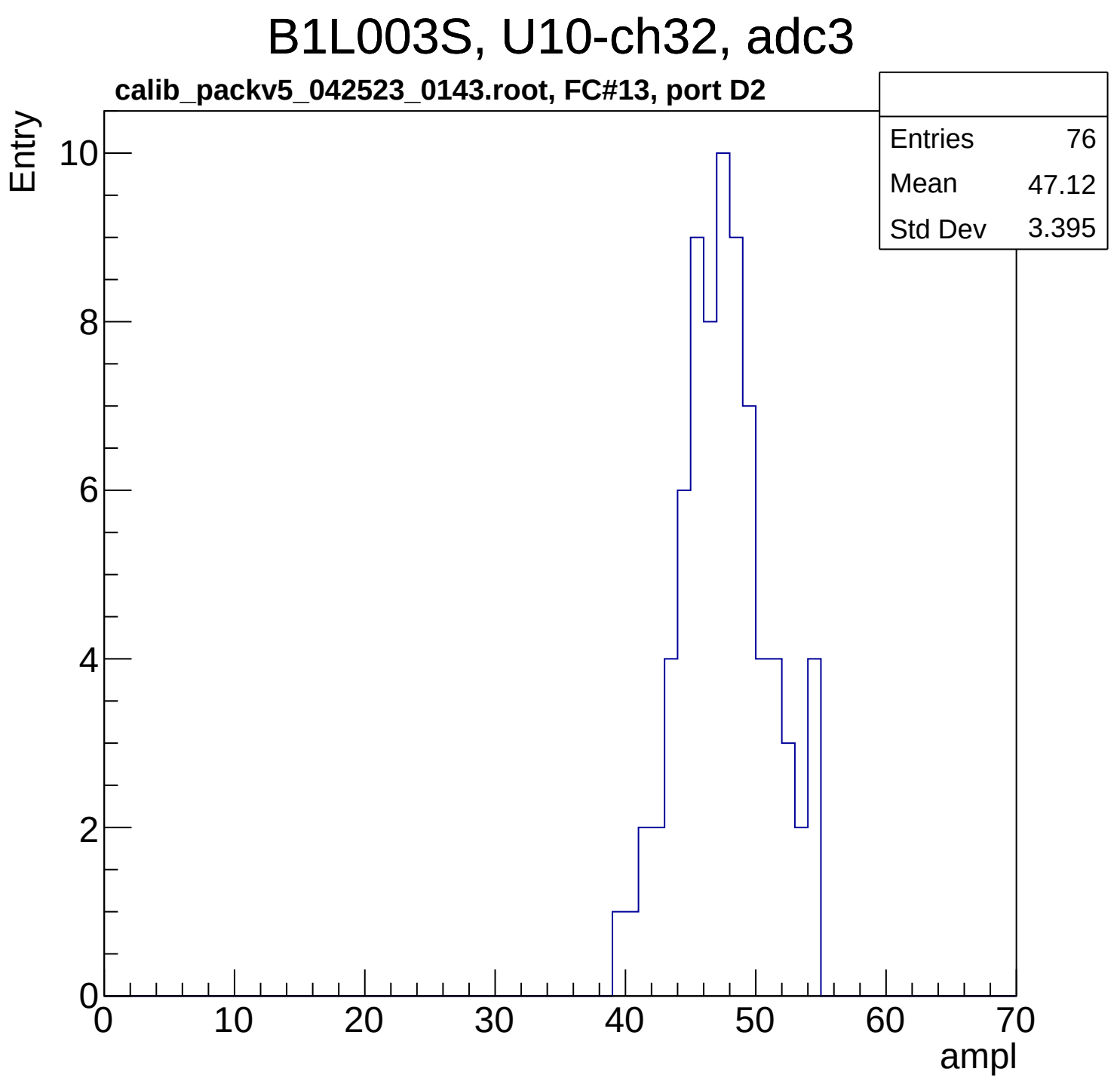
B1L003S, U10-ch32, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 47.12
Std Dev 3.395
Entry
ampl
0
2
4
6
8
10
0
10
20
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60
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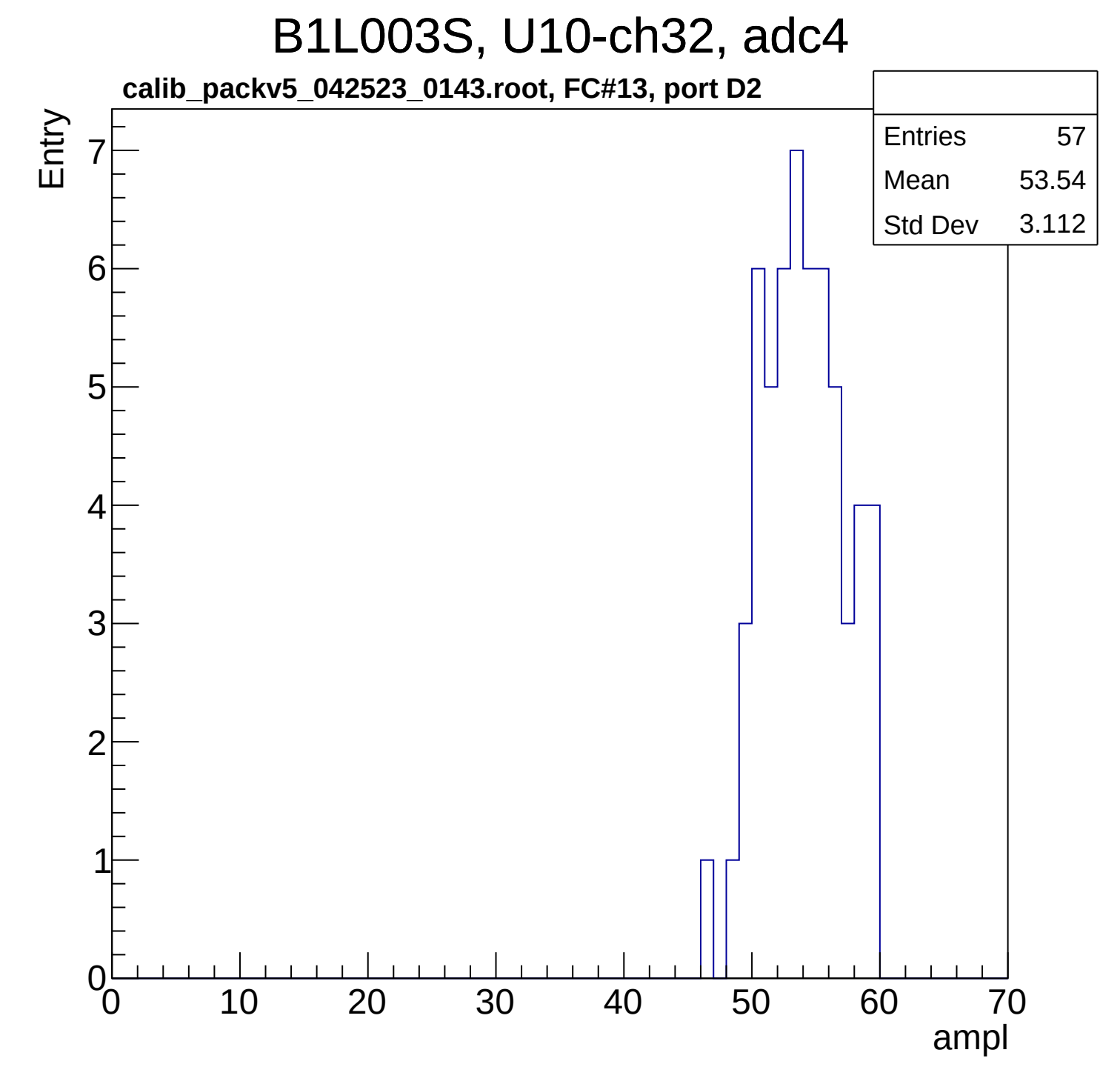
B1L003S, U10-ch32, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 53.54
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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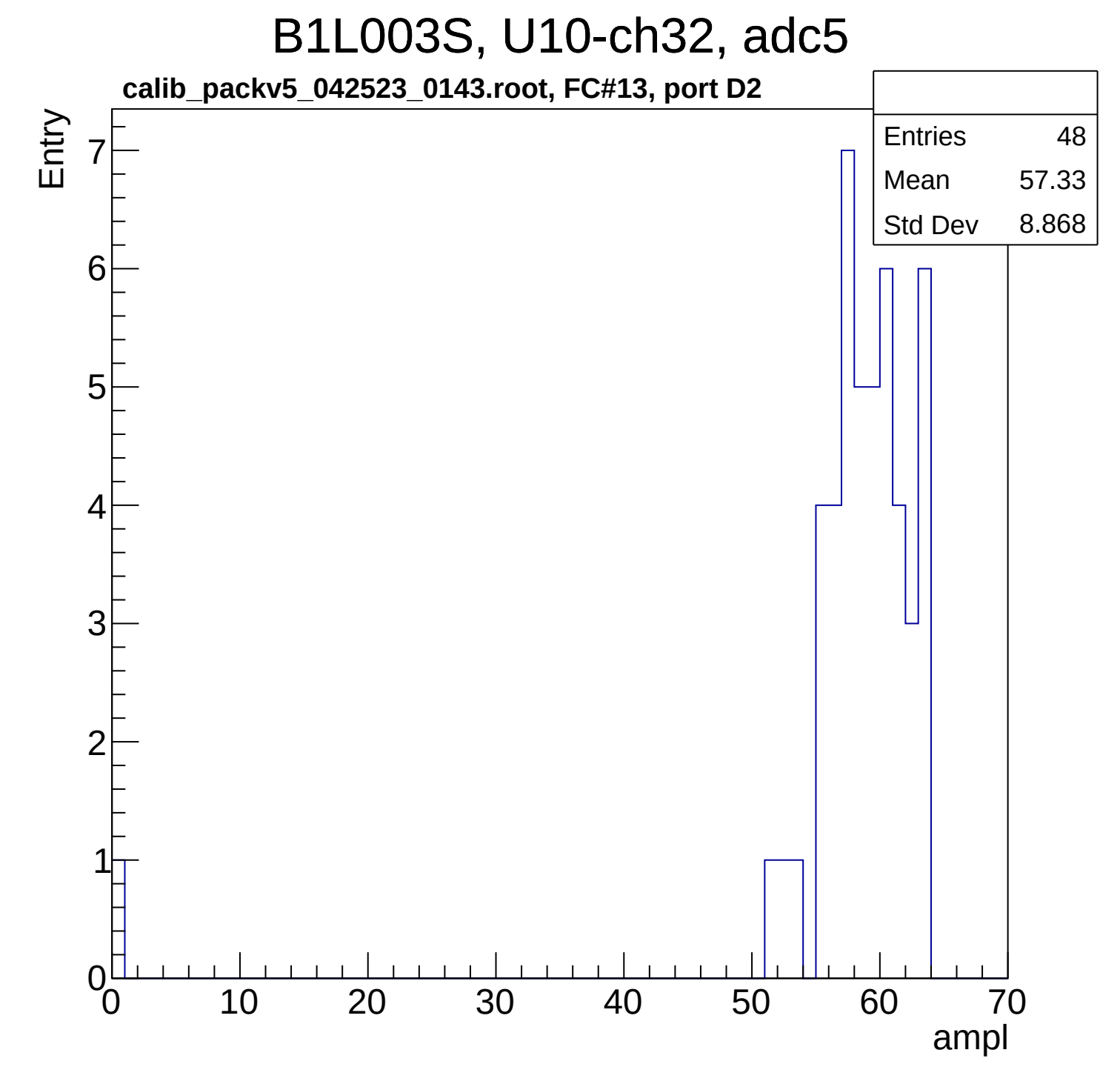
B1L003S, U10-ch32, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 57.33
Std Dev 8.868
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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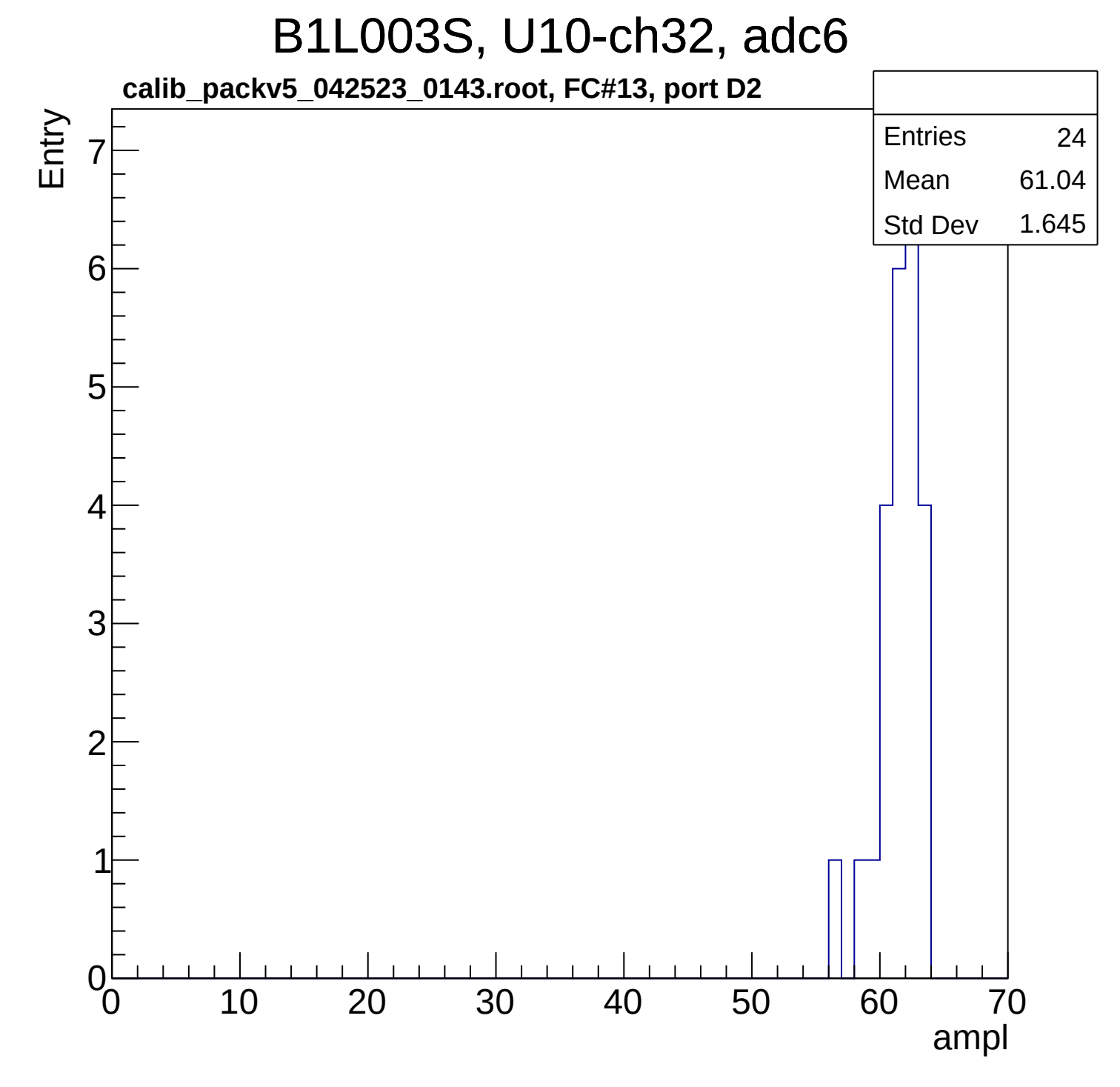
B1L003S, U10-ch32, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 61.04
Std Dev 1.645
Entry
ampl
0
1
2
3
4
5
6
7
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20
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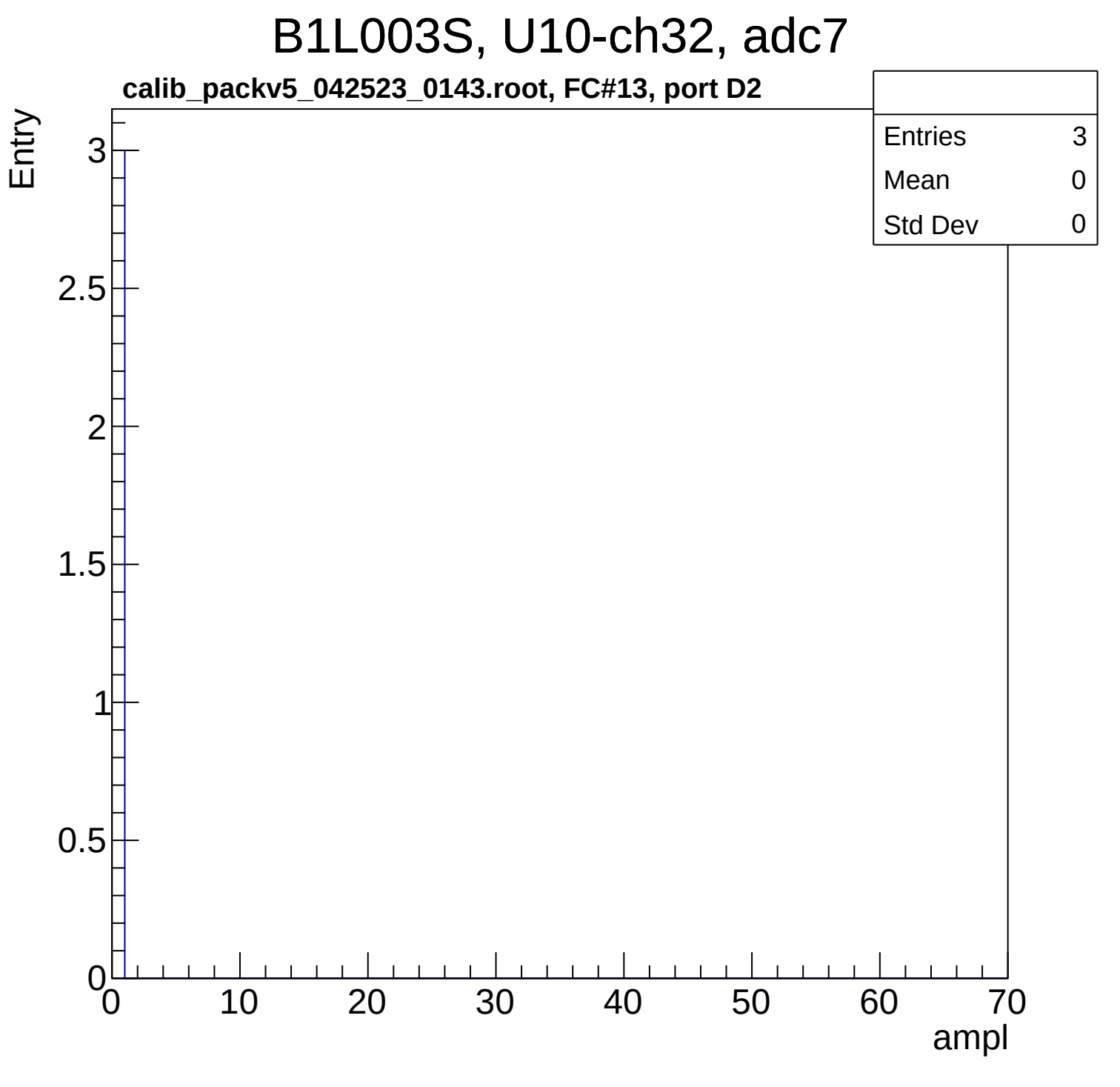
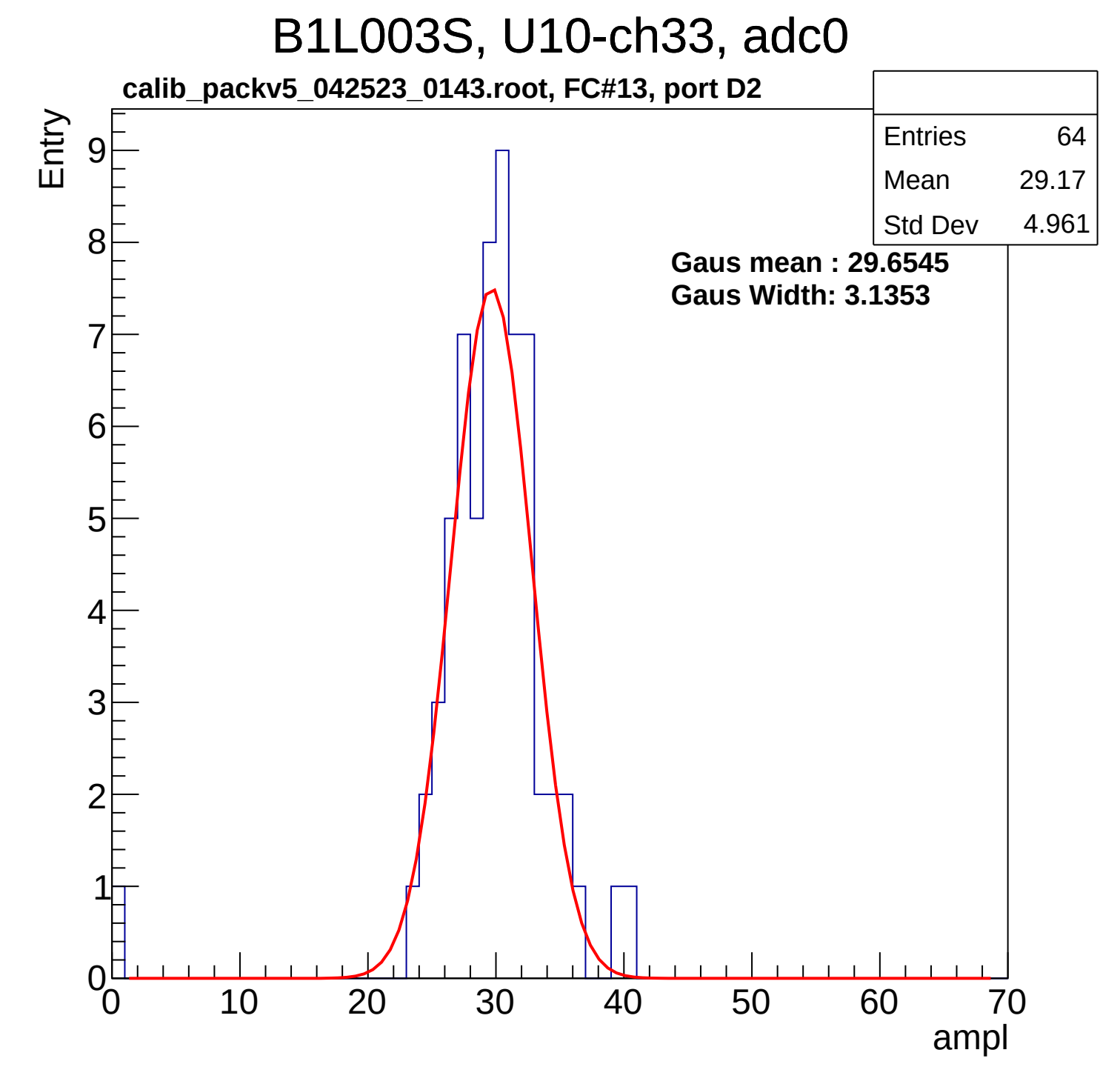
40
50
60
70


B1L003S, U10-ch32, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch33, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.17
Std Dev 4.961
Gaus mean : 29.6545
Gaus Width: 3.1353
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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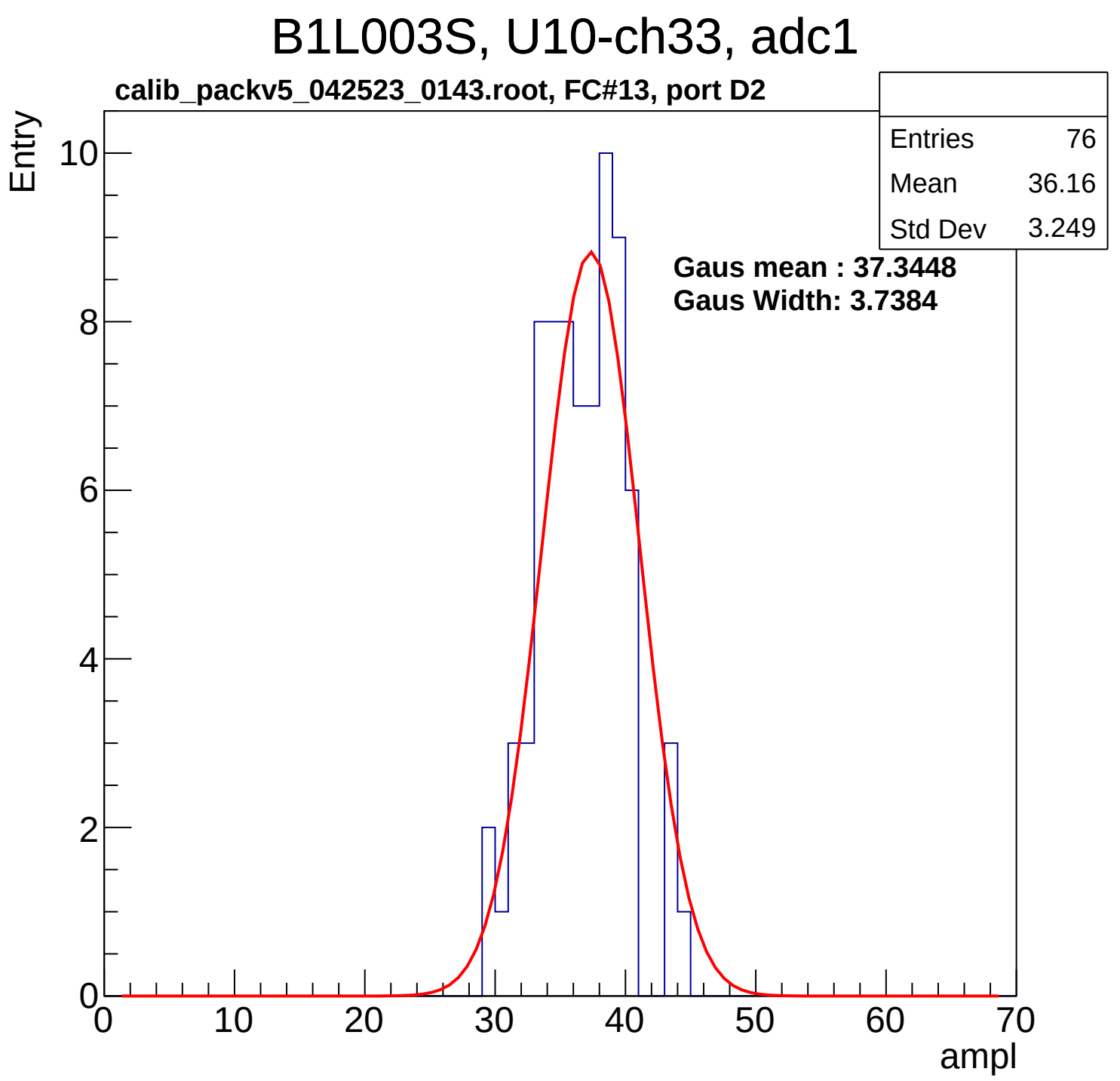
B1L003S, U10-ch33, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.16
Std Dev 3.249
Gaus mean : 37.3448
Gaus Width: 3.7384
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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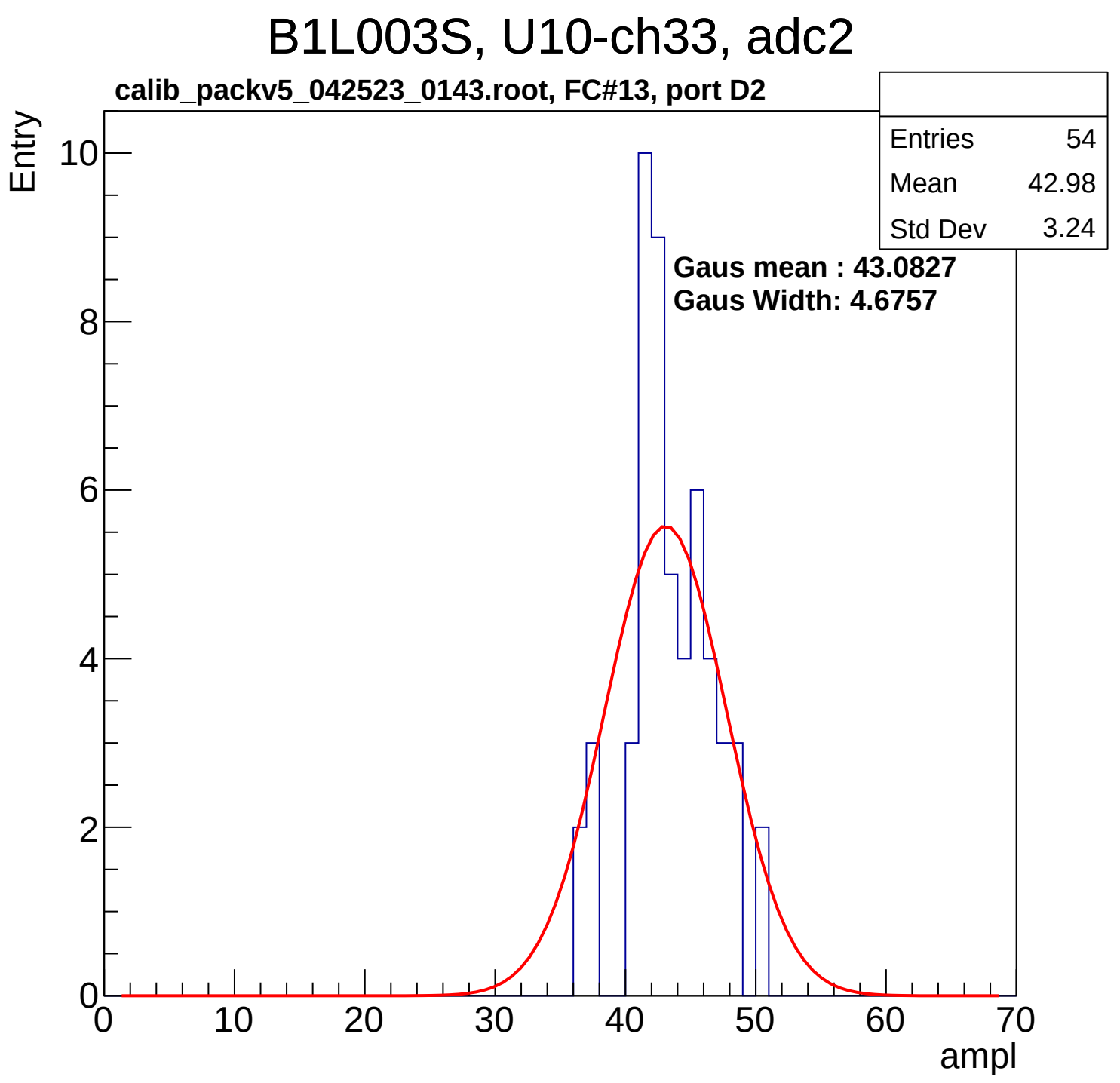
B1L003S, U10-ch33, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.98
Std Dev 3.24
Gaus mean : 43.0827
Gaus Width: 4.6757
Entry
ampl
0
2
4
6
8
10
0
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20
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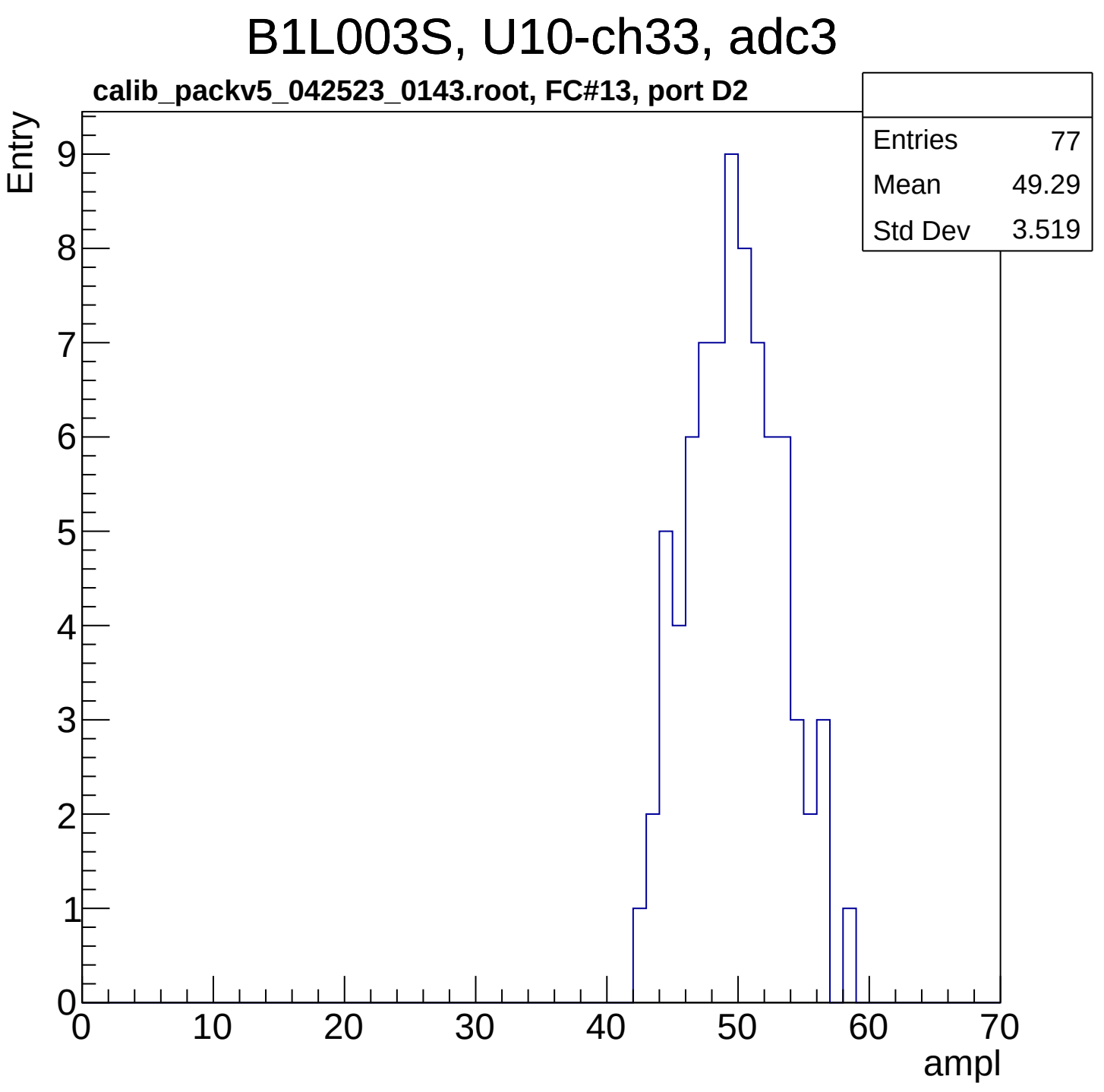
40
50
60
70


B1L003S, U10-ch33, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 49.29
Std Dev 3.519
Entry
ampl
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4
5
6
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8
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0
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60
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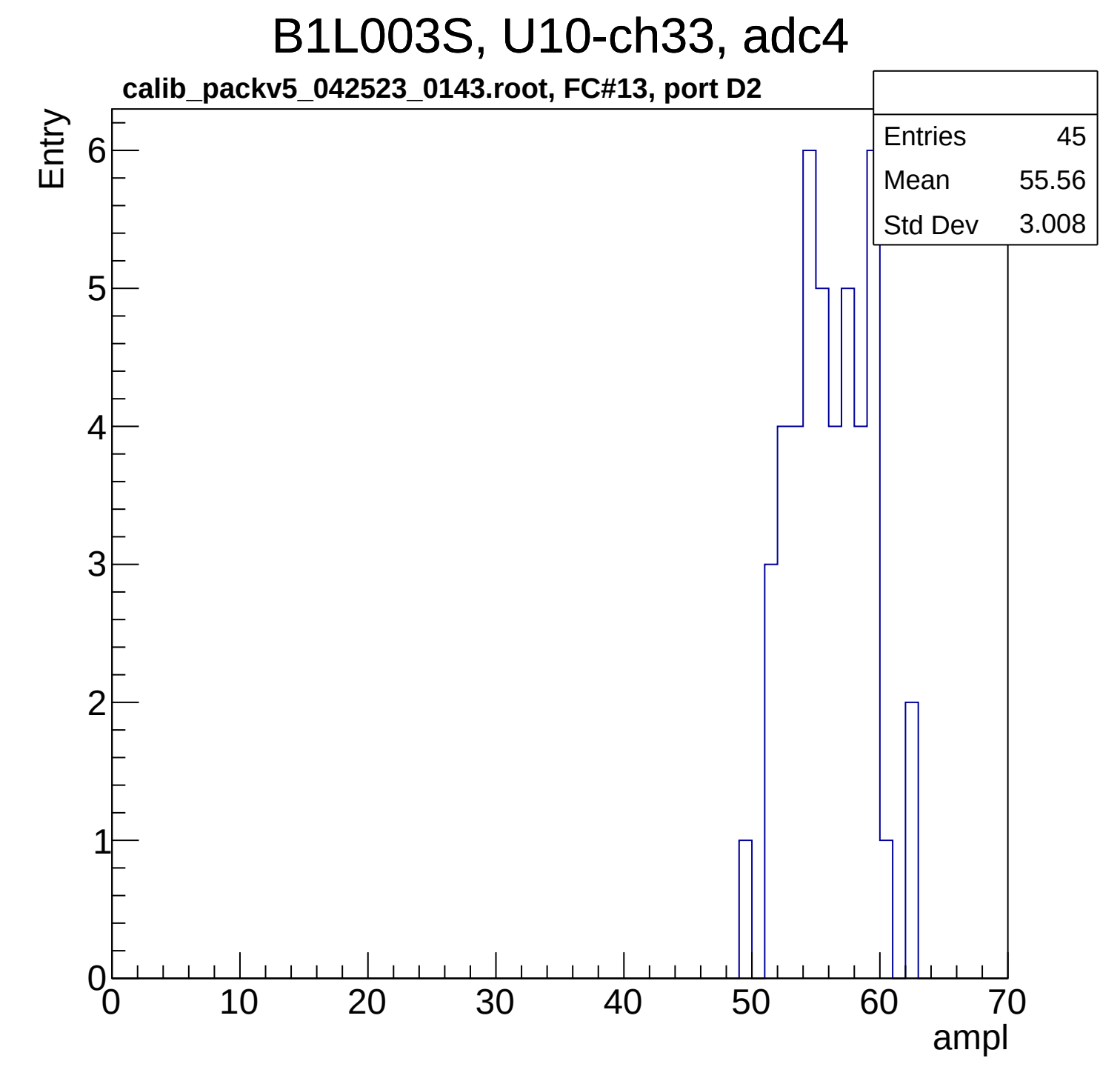
B1L003S, U10-ch33, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 55.56
Std Dev 3.008
Entry
ampl
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3
4
5
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0
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20
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40
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60
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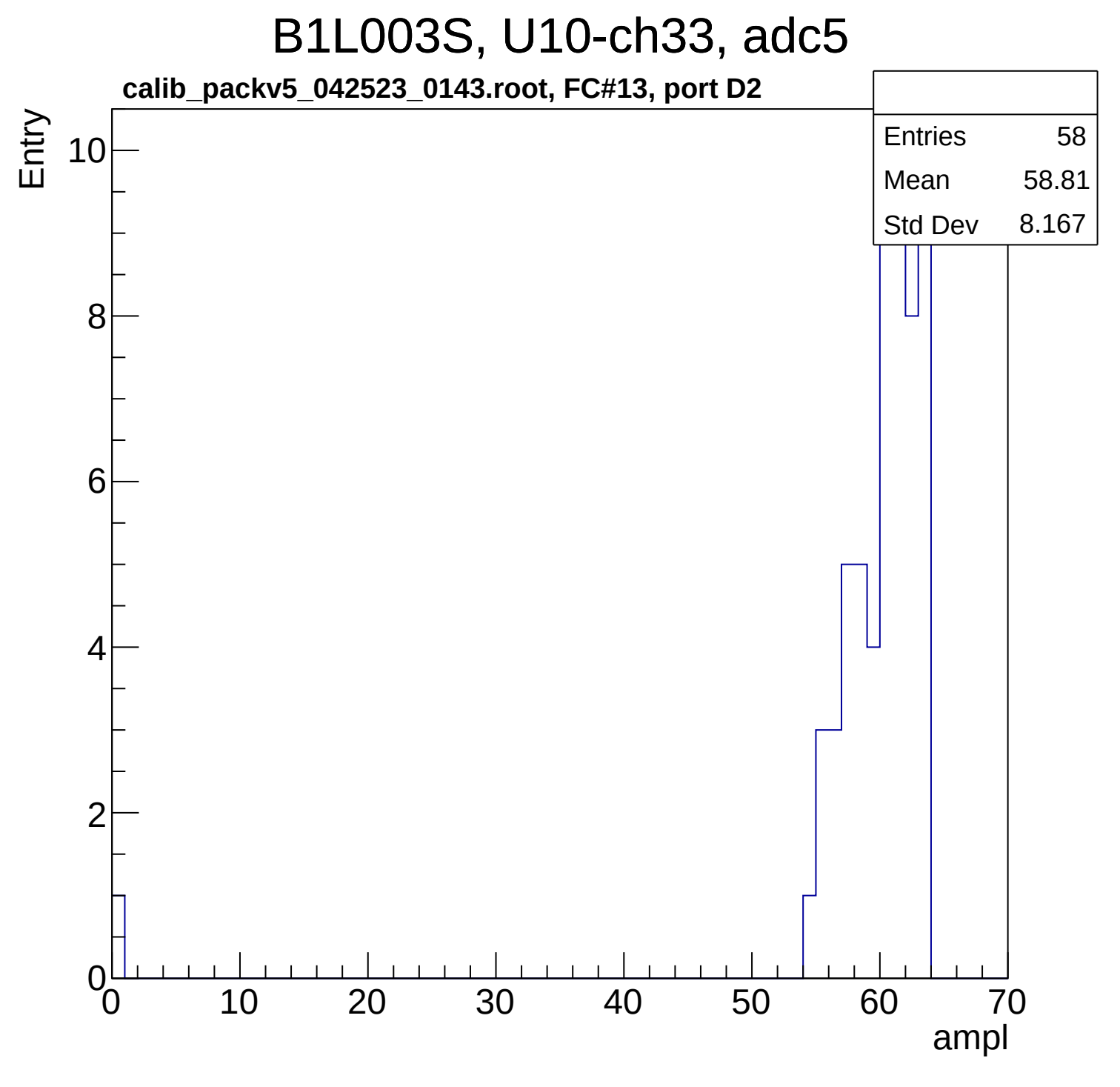
B1L003S, U10-ch33, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 58.81
Std Dev 8.167
Entry
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4
6
8
10
0
10
20
30
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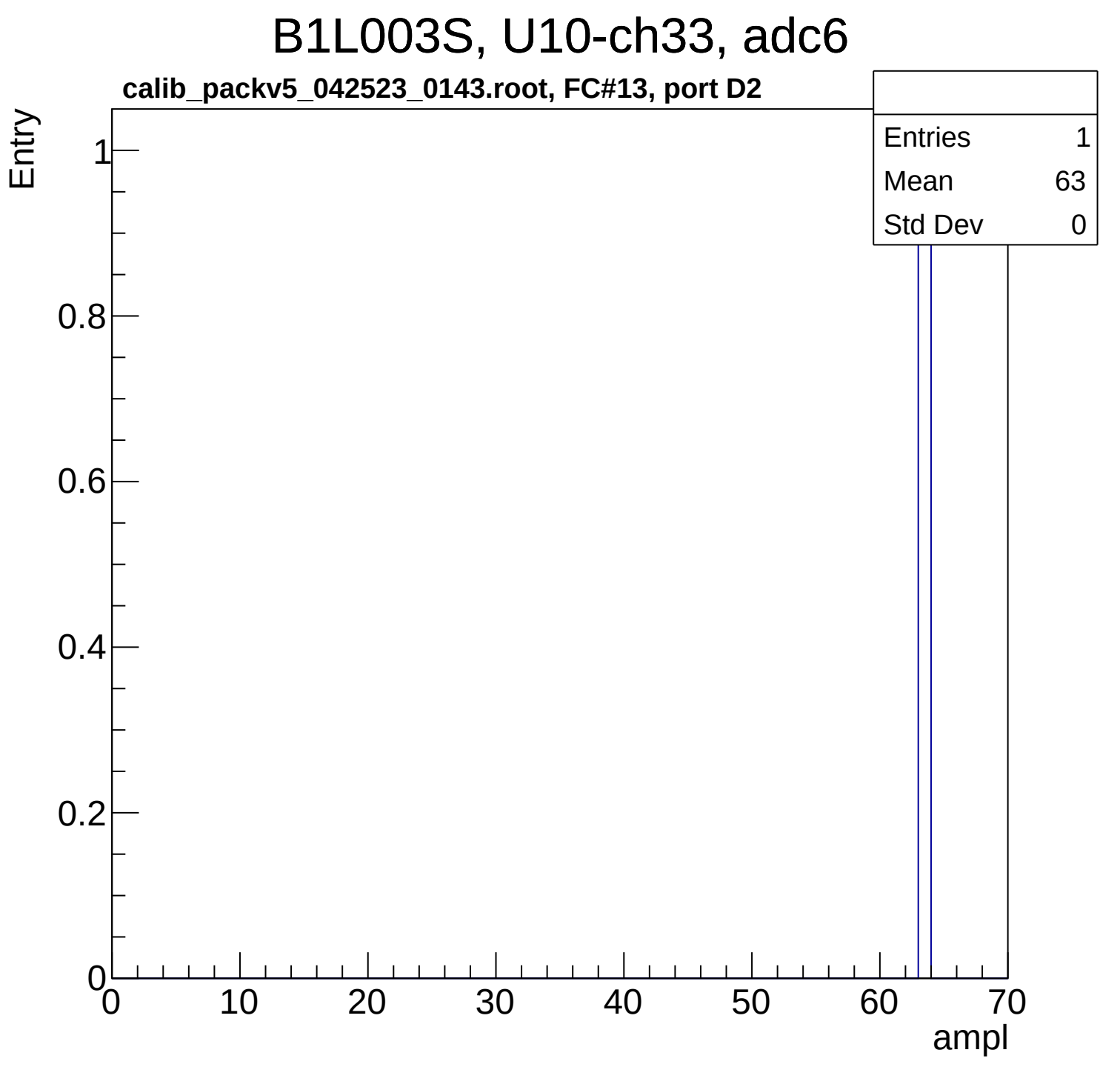
50
60
70
ampl


B1L003S, U10-ch33, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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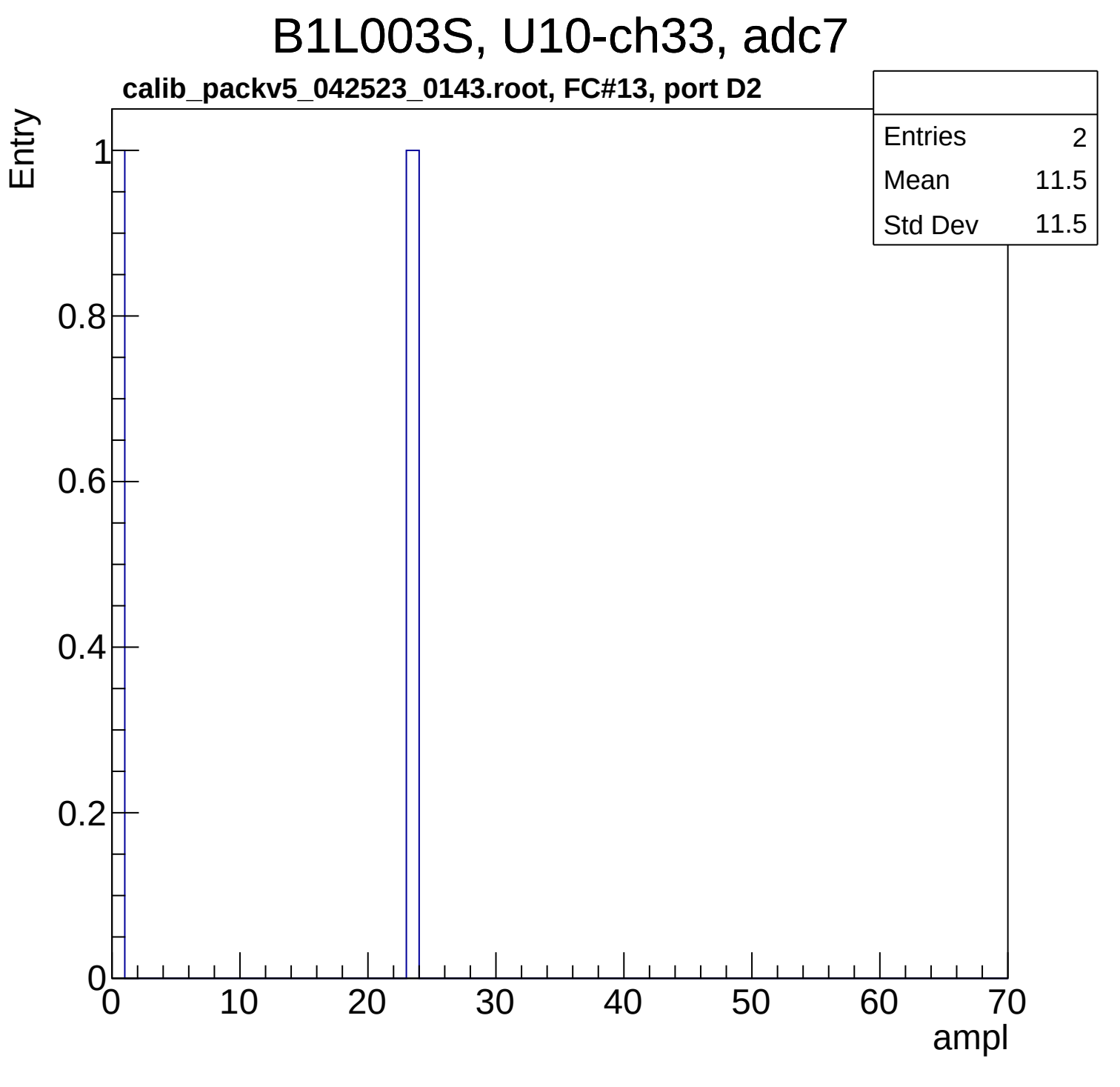
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch33, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
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0
10
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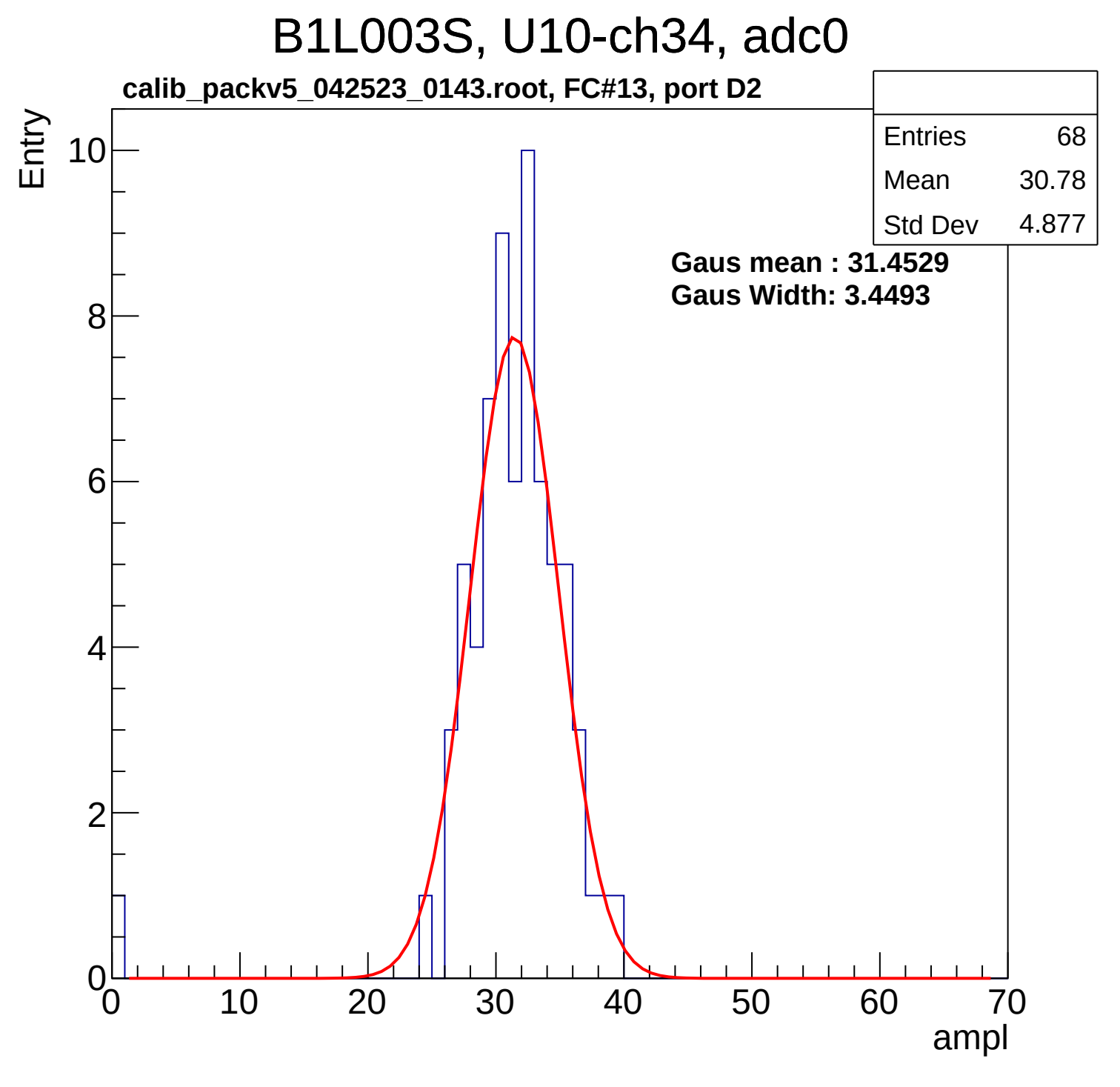
30
40
50
60
70
ampl


B1L003S, U10-ch34, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.78
Std Dev 4.877
Gaus mean : 31.4529
Gaus Width: 3.4493
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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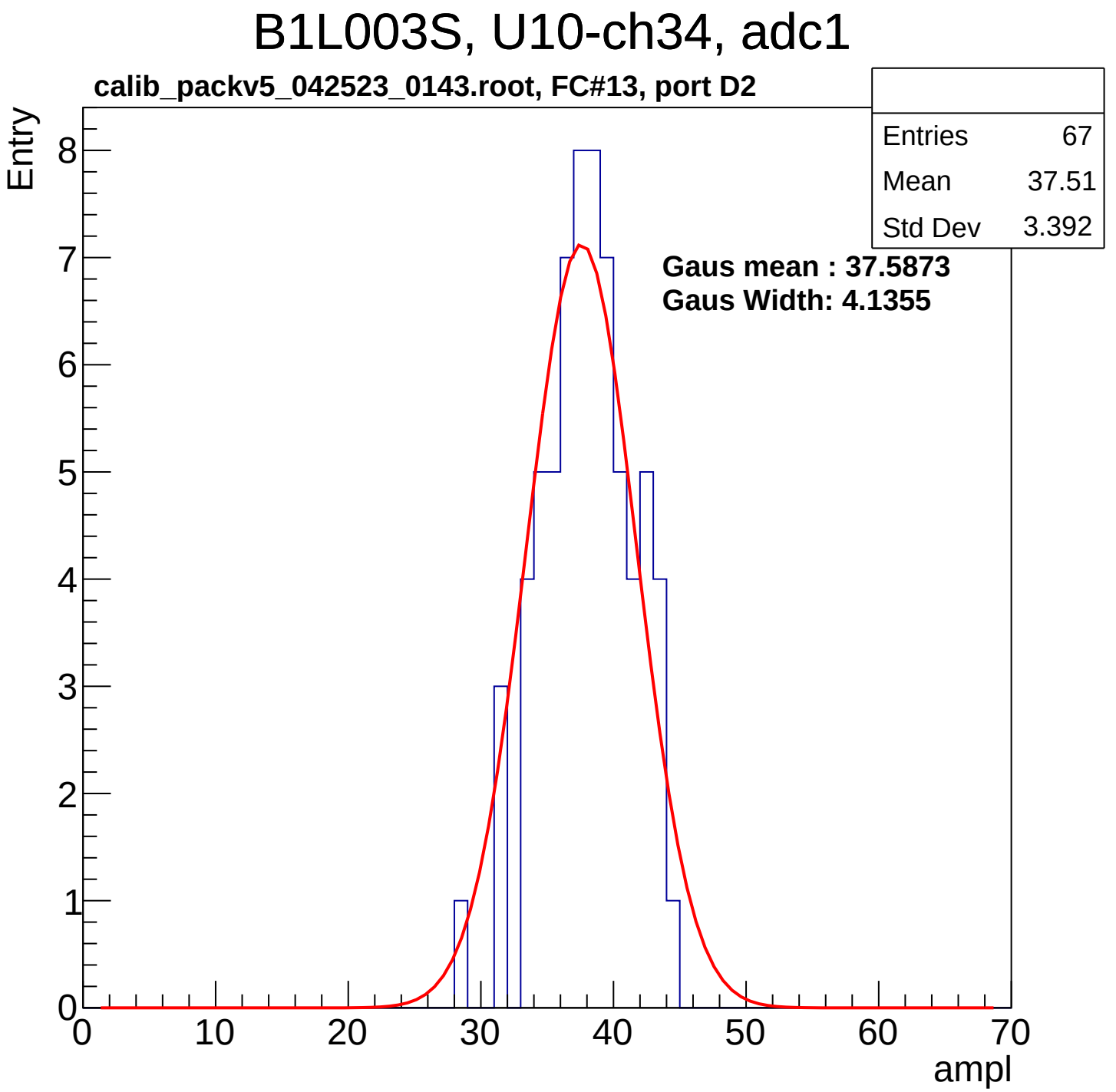
B1L003S, U10-ch34, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.51
Std Dev 3.392
Gaus mean : 37.5873
Gaus Width: 4.1355
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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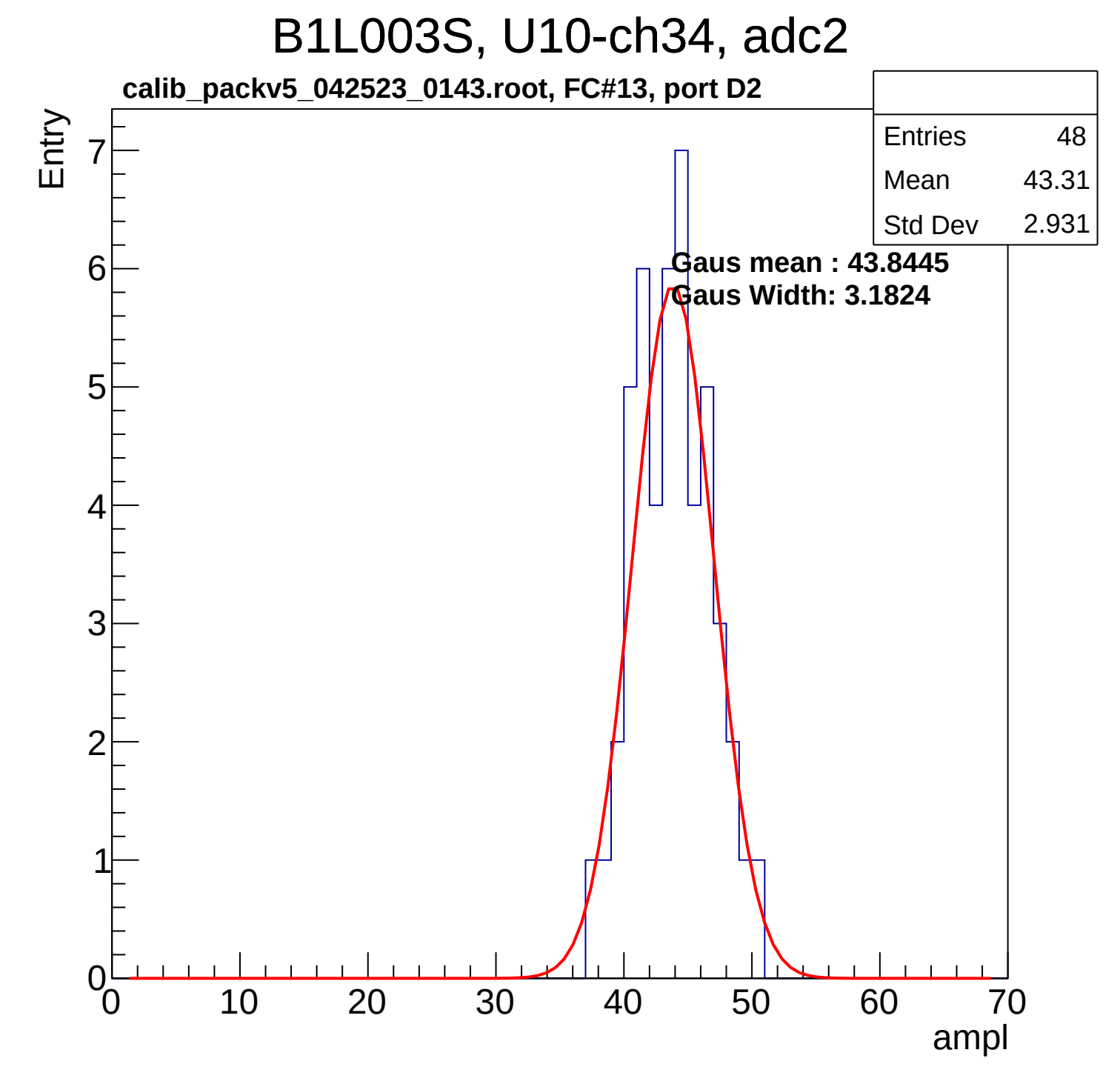
B1L003S, U10-ch34, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 43.31
Std Dev 2.931
Gaus mean : 43.8445
Gaus Width: 3.1824
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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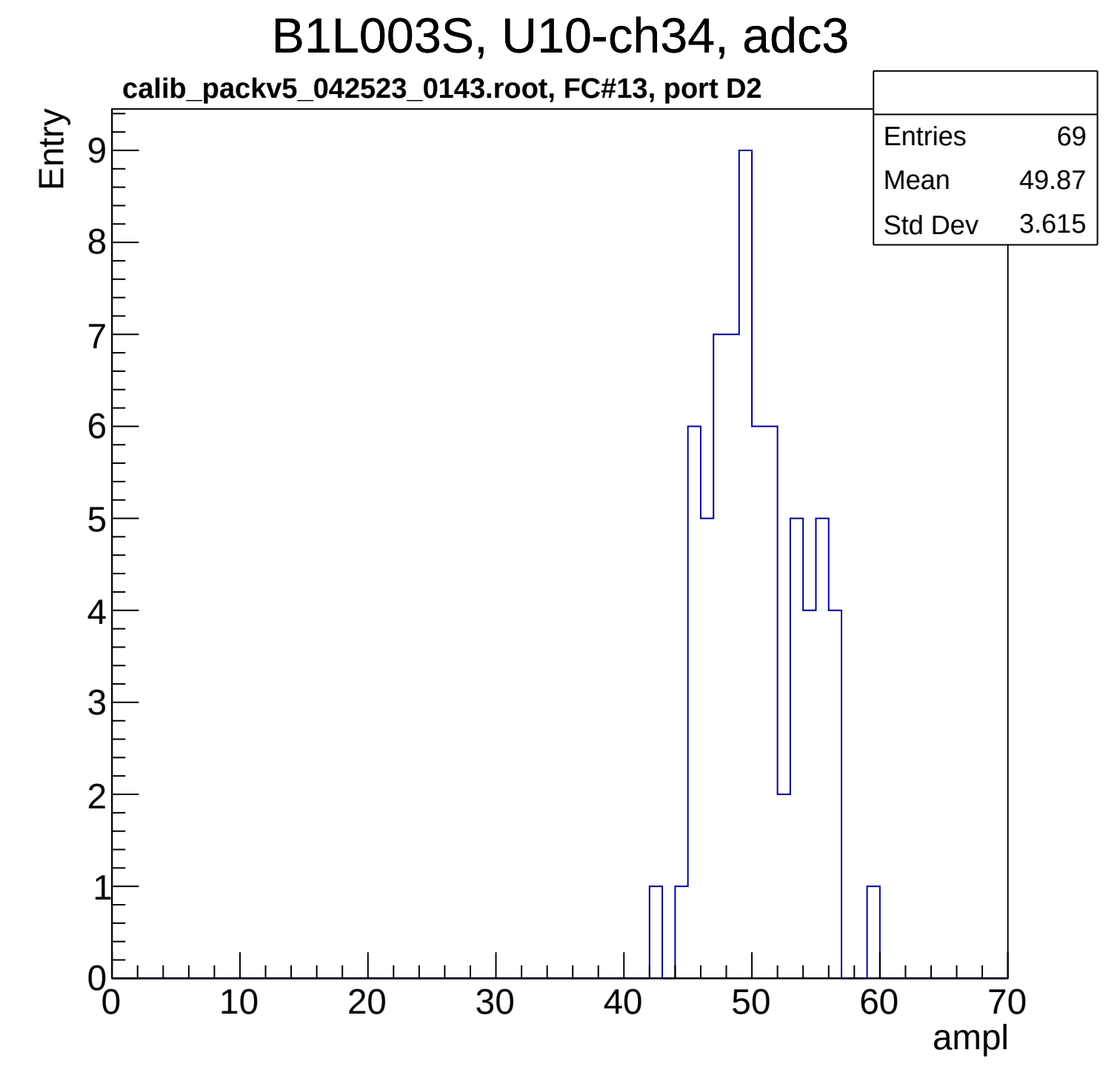
B1L003S, U10-ch34, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 49.87
Std Dev 3.615
Entry
ampl
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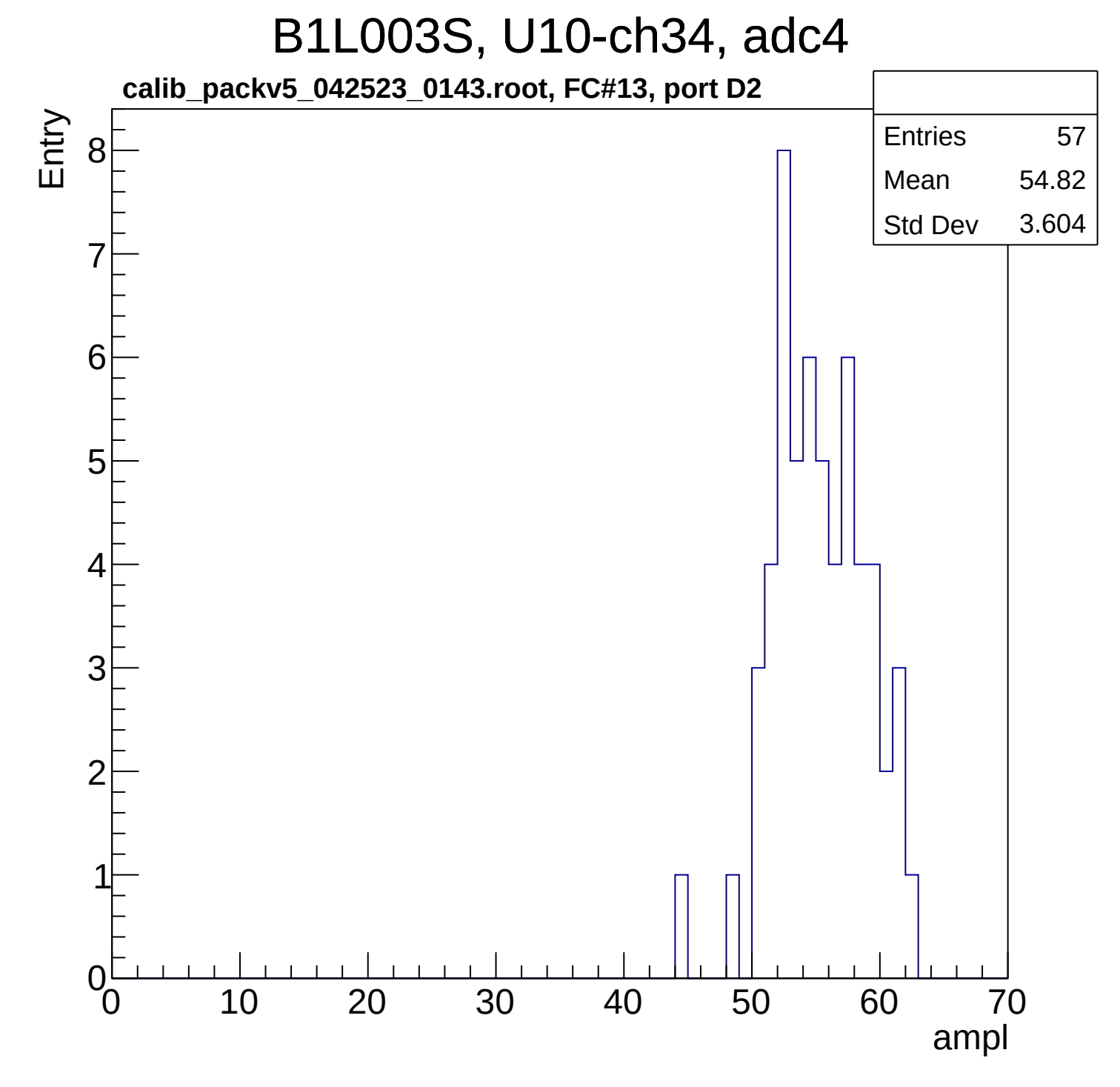
B1L003S, U10-ch34, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.82
Std Dev 3.604
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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60
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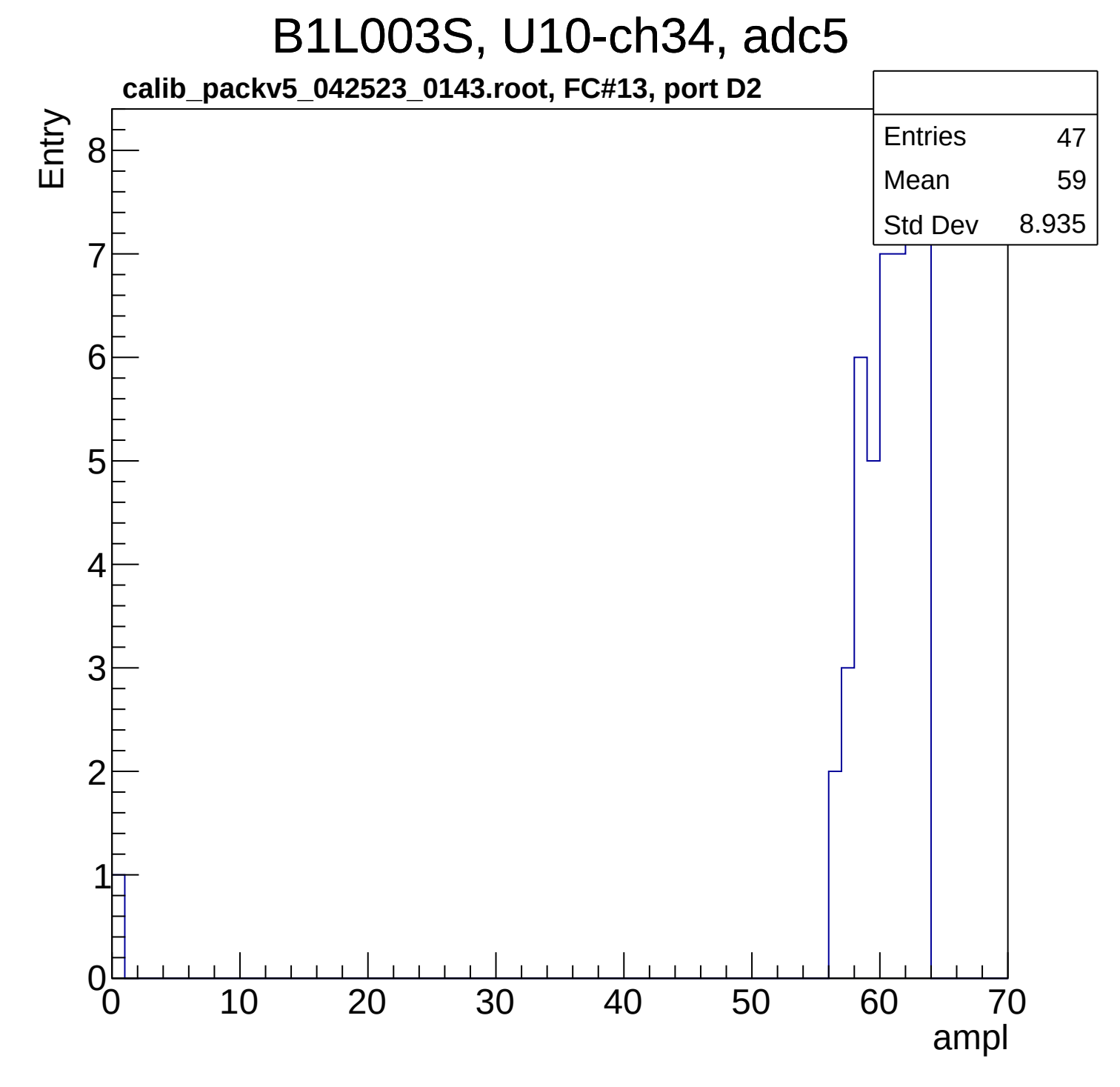
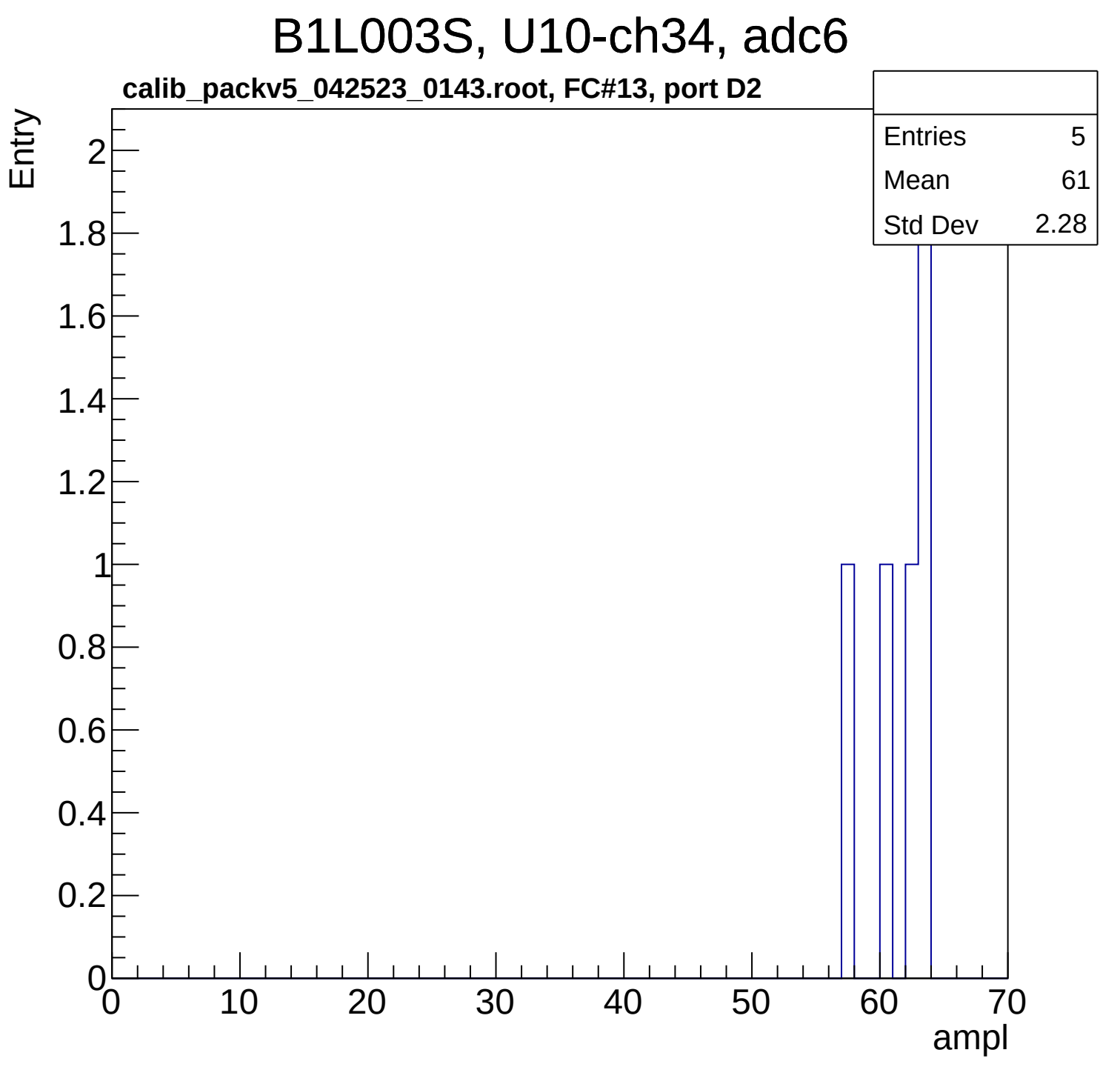
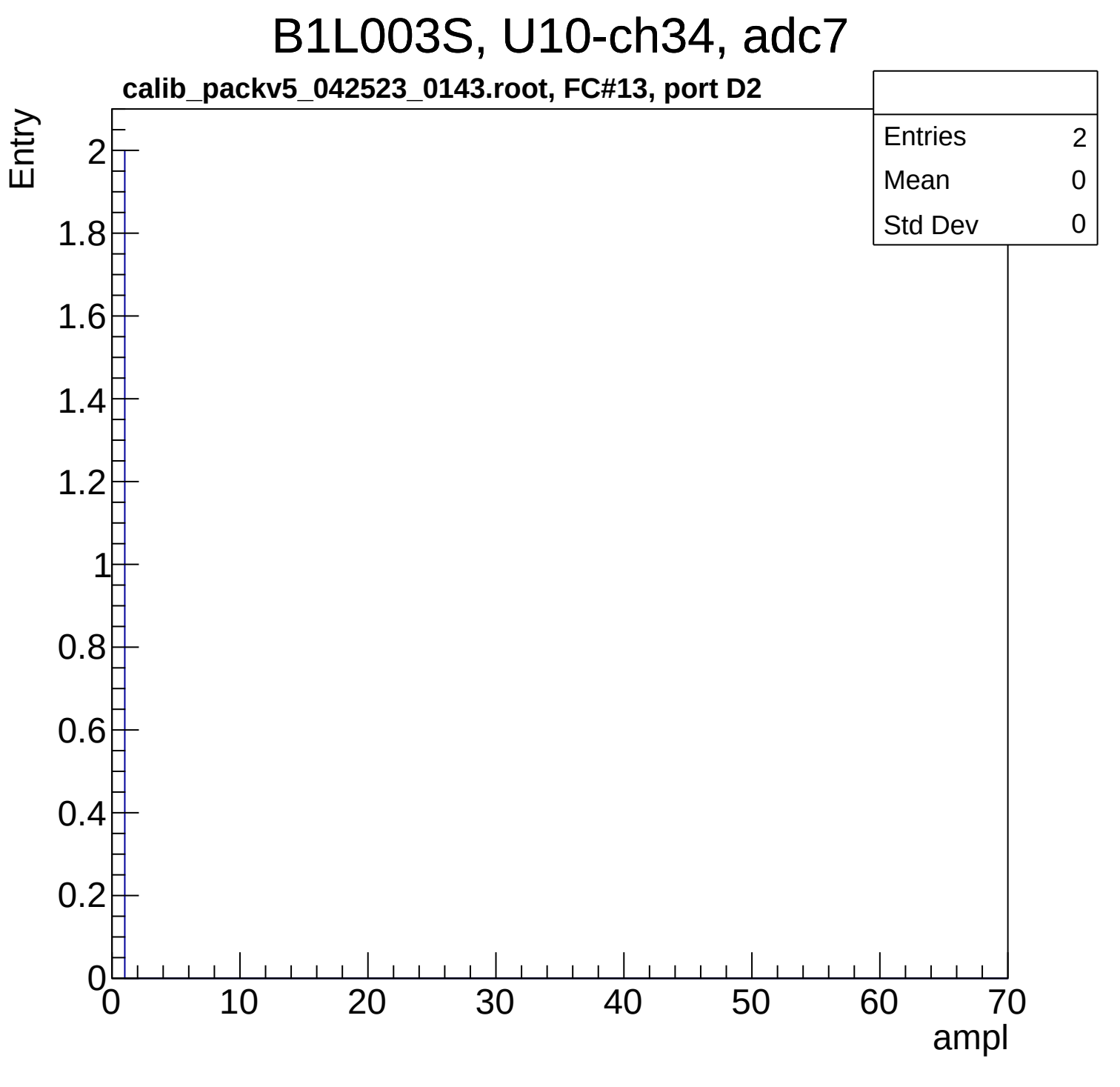
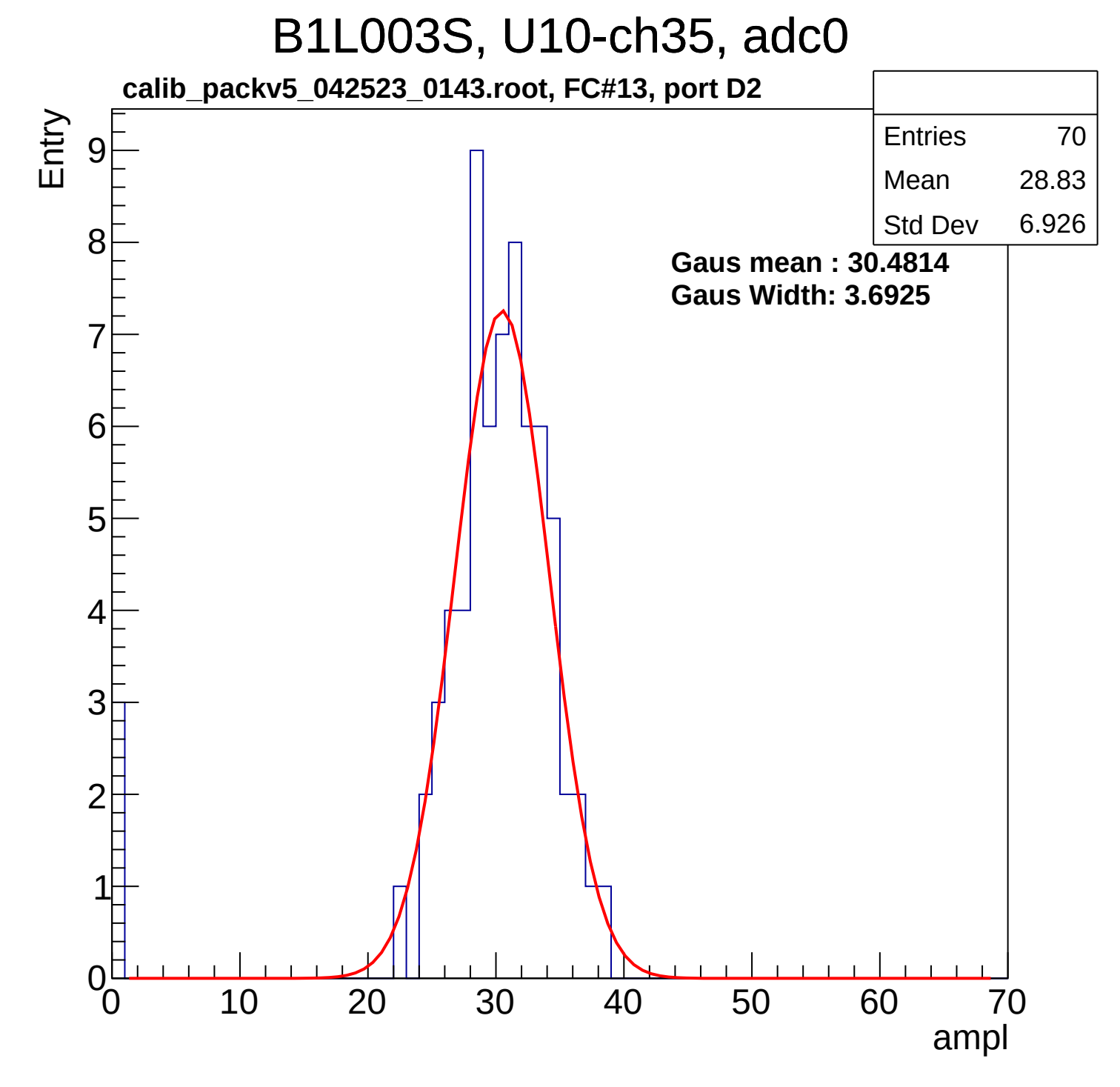
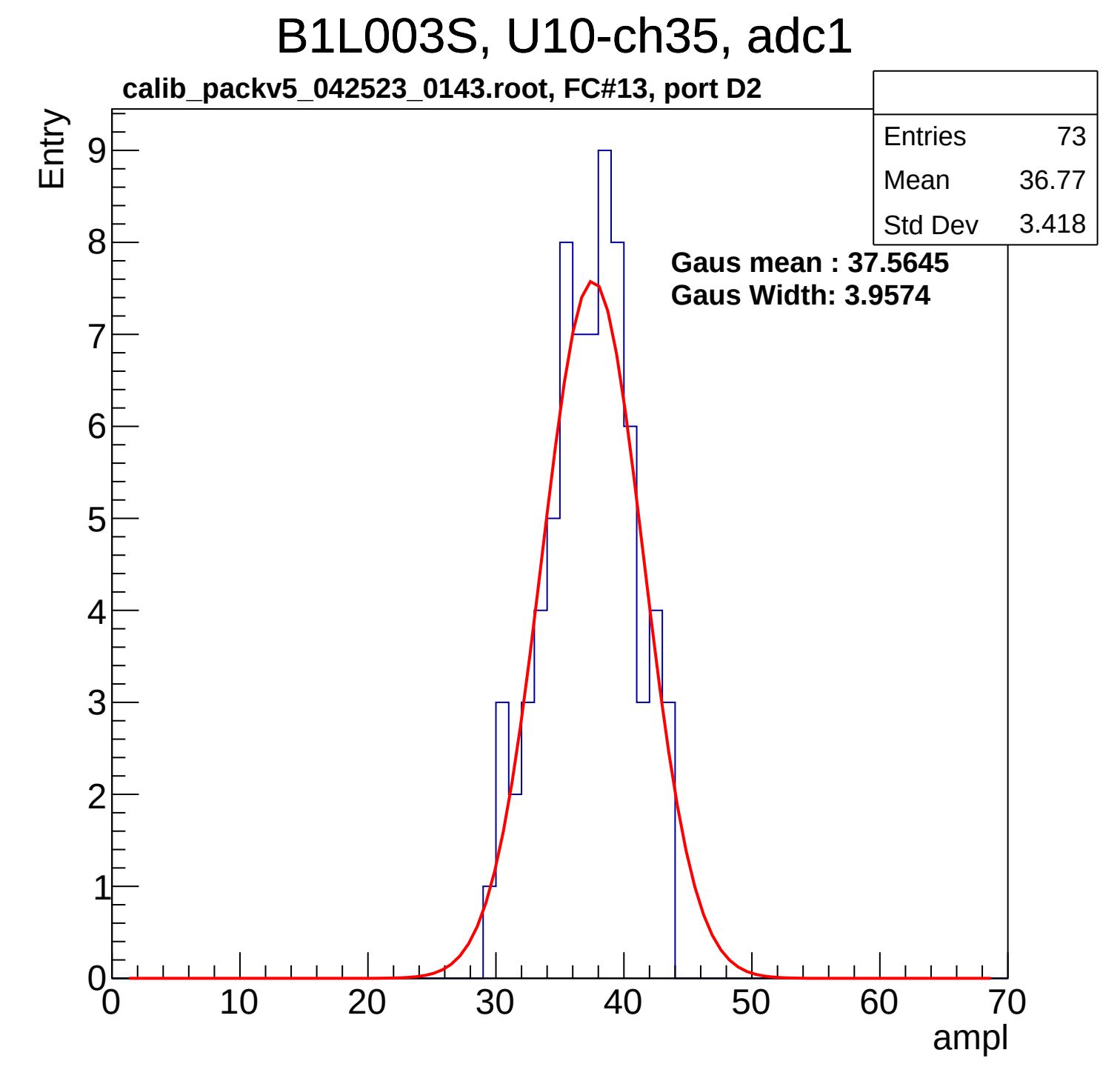
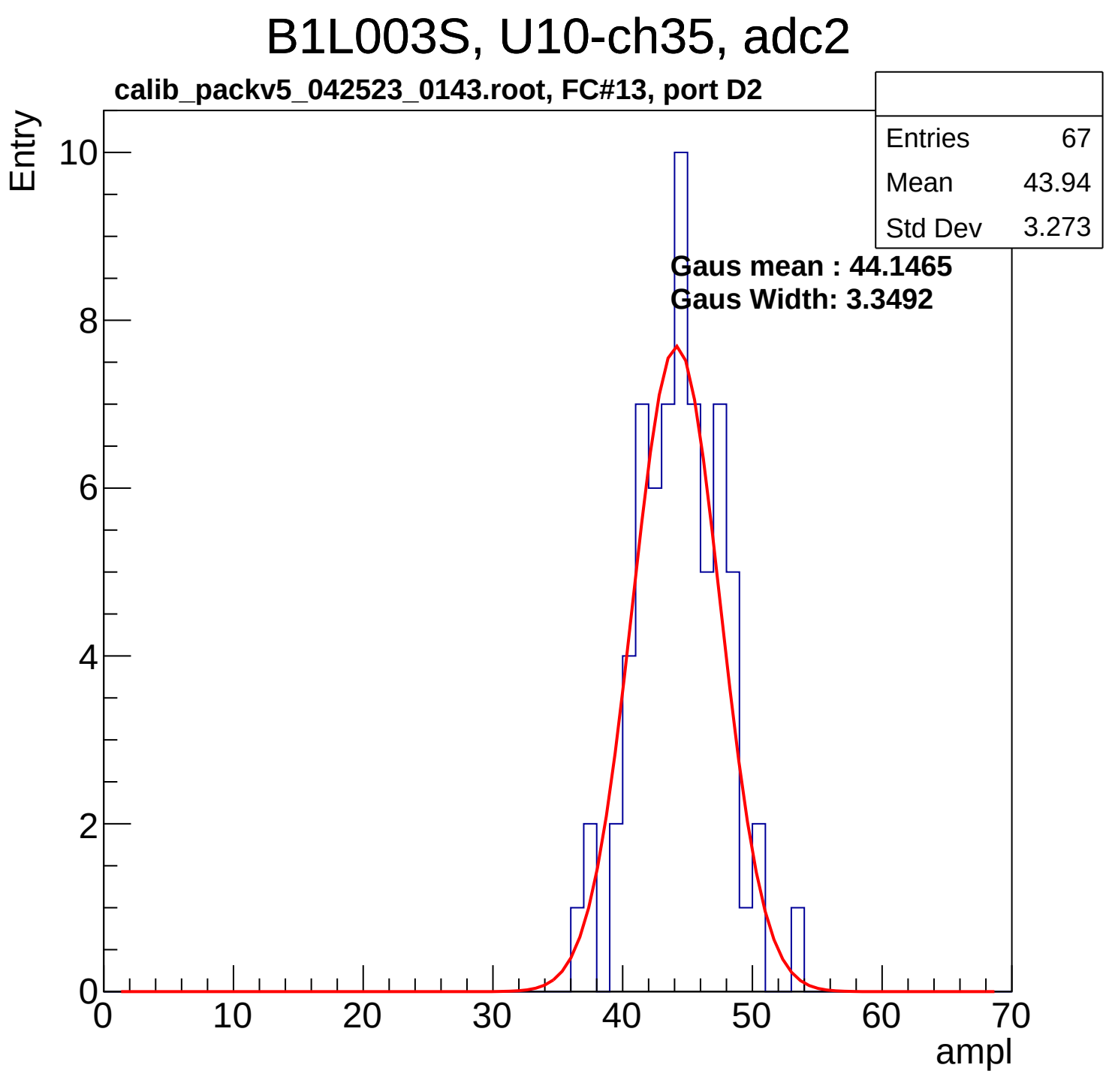

B1L003S, U10-ch34, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59
Std Dev 8.935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

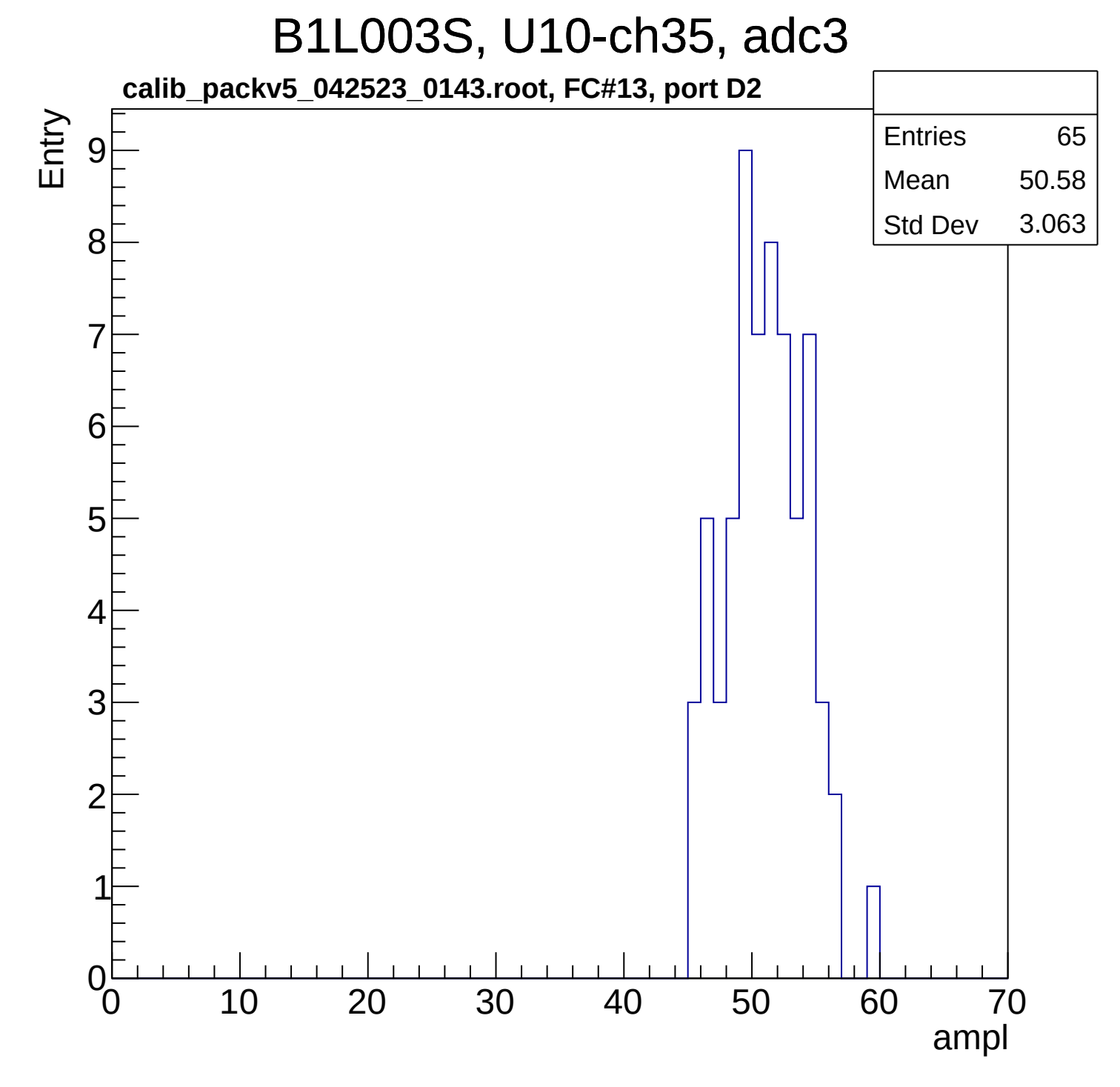

B1L003S, U10-ch34, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61
Std Dev 2.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

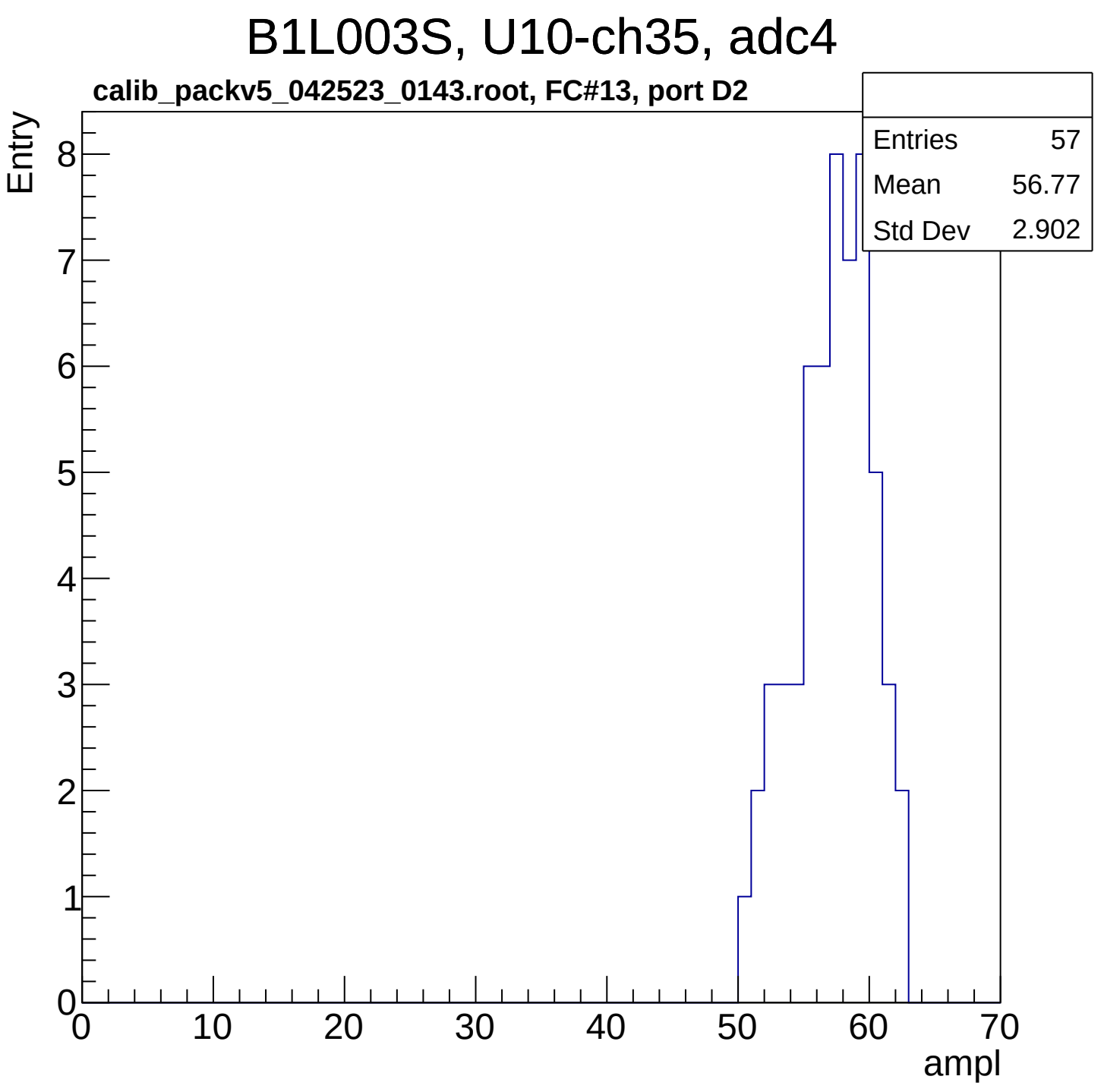

B1L003S, U10-ch34, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

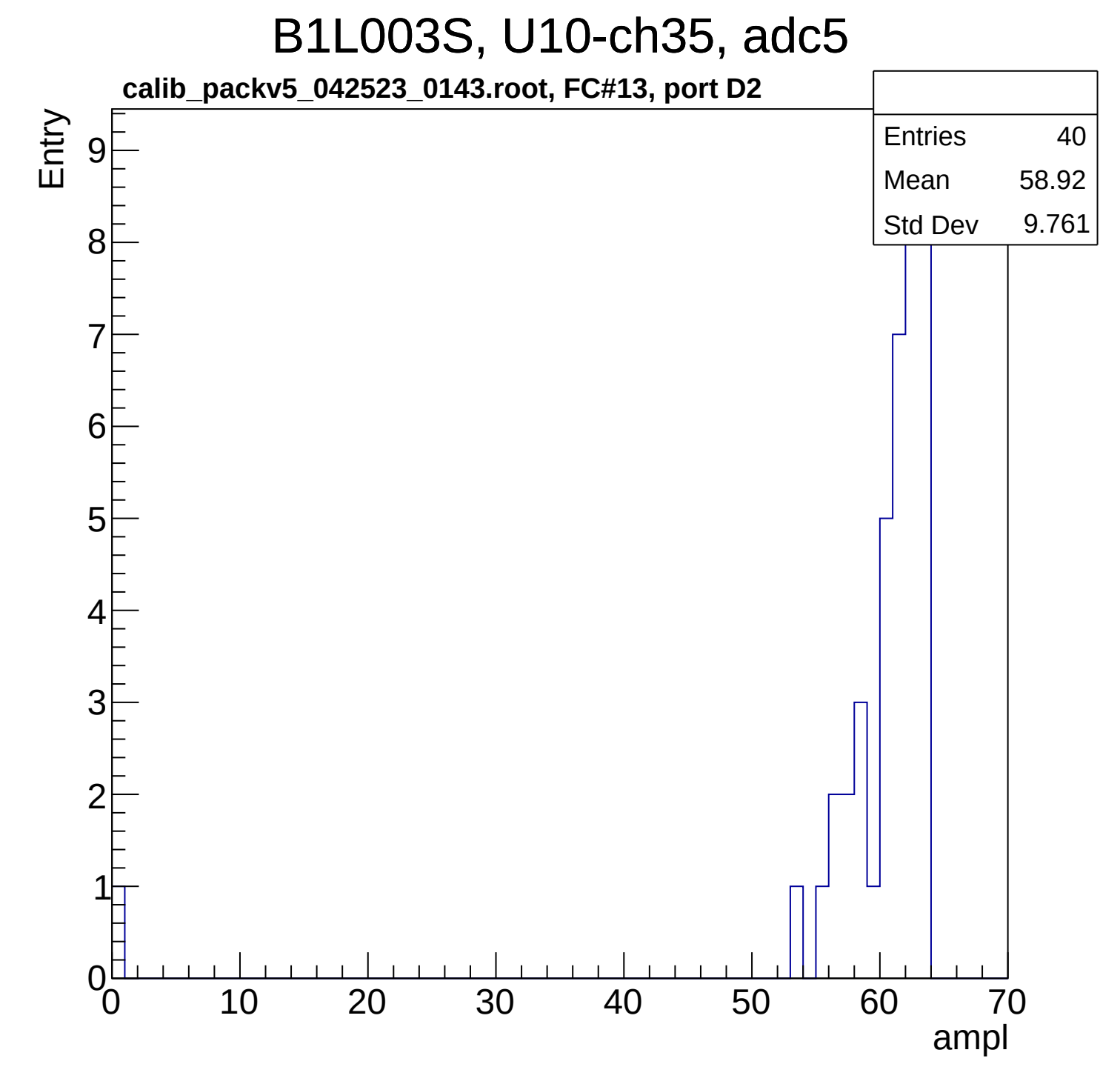

B1L003S, U10-ch35, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 28.83
Std Dev 6.926
Gaus mean : 30.4814
Gaus Width: 3.6925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

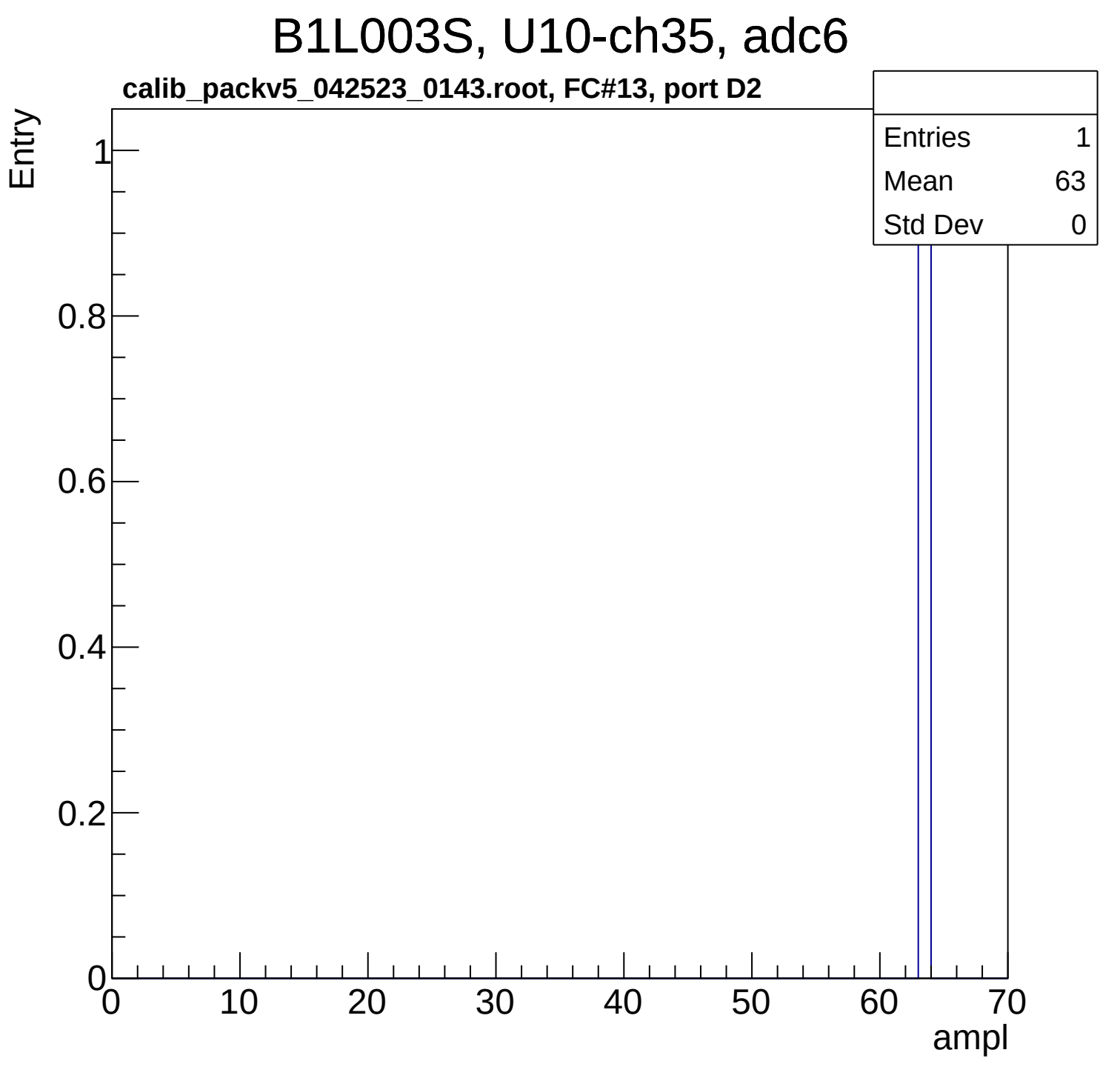

B1L003S, U10-ch35, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.77
Std Dev 3.418
Gaus mean : 37.5645
Gaus Width: 3.9574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

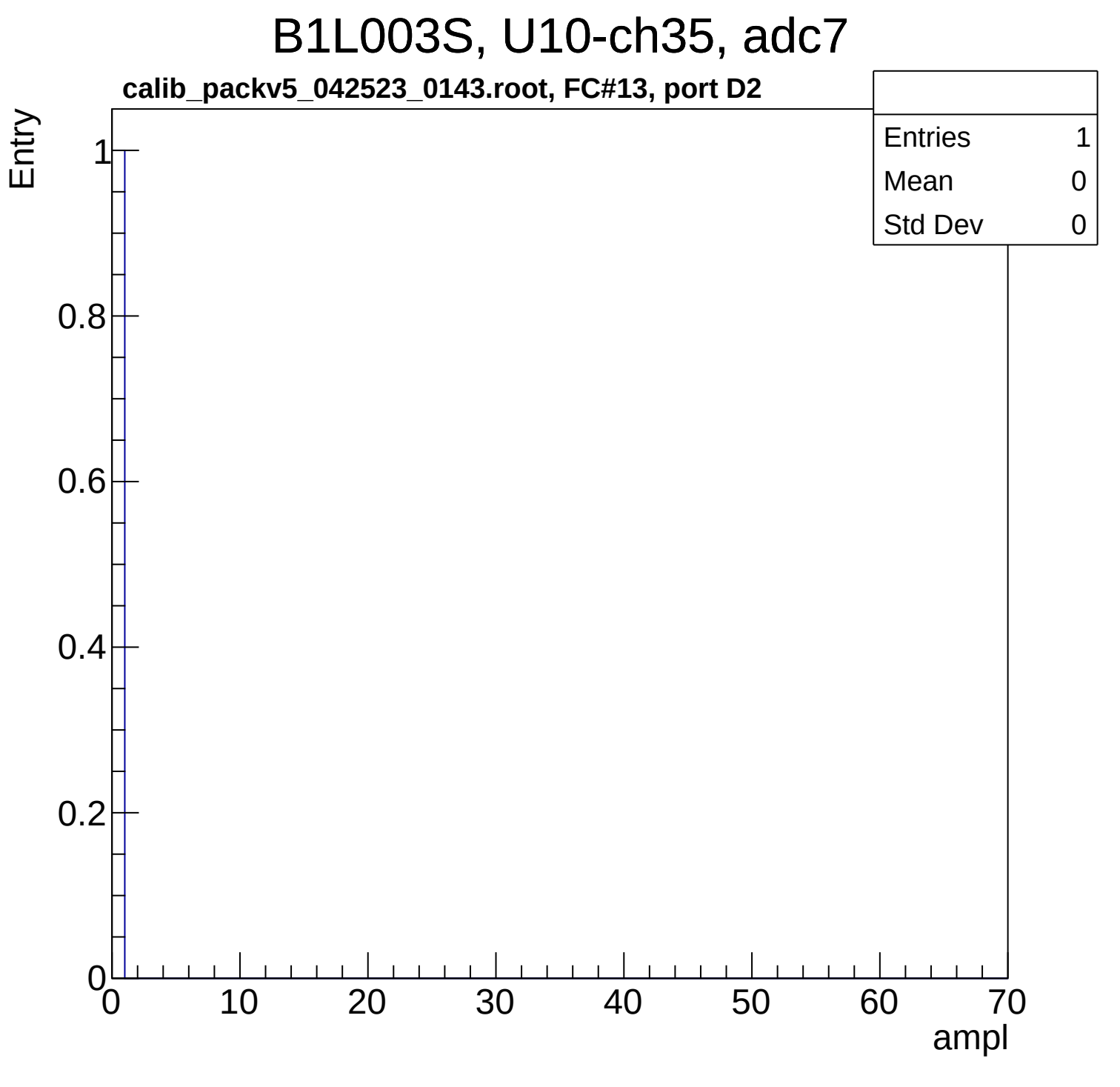

B1L003S, U10-ch35, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.94
Std Dev 3.273
Gaus mean : 44.1465
Gaus Width: 3.3492
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

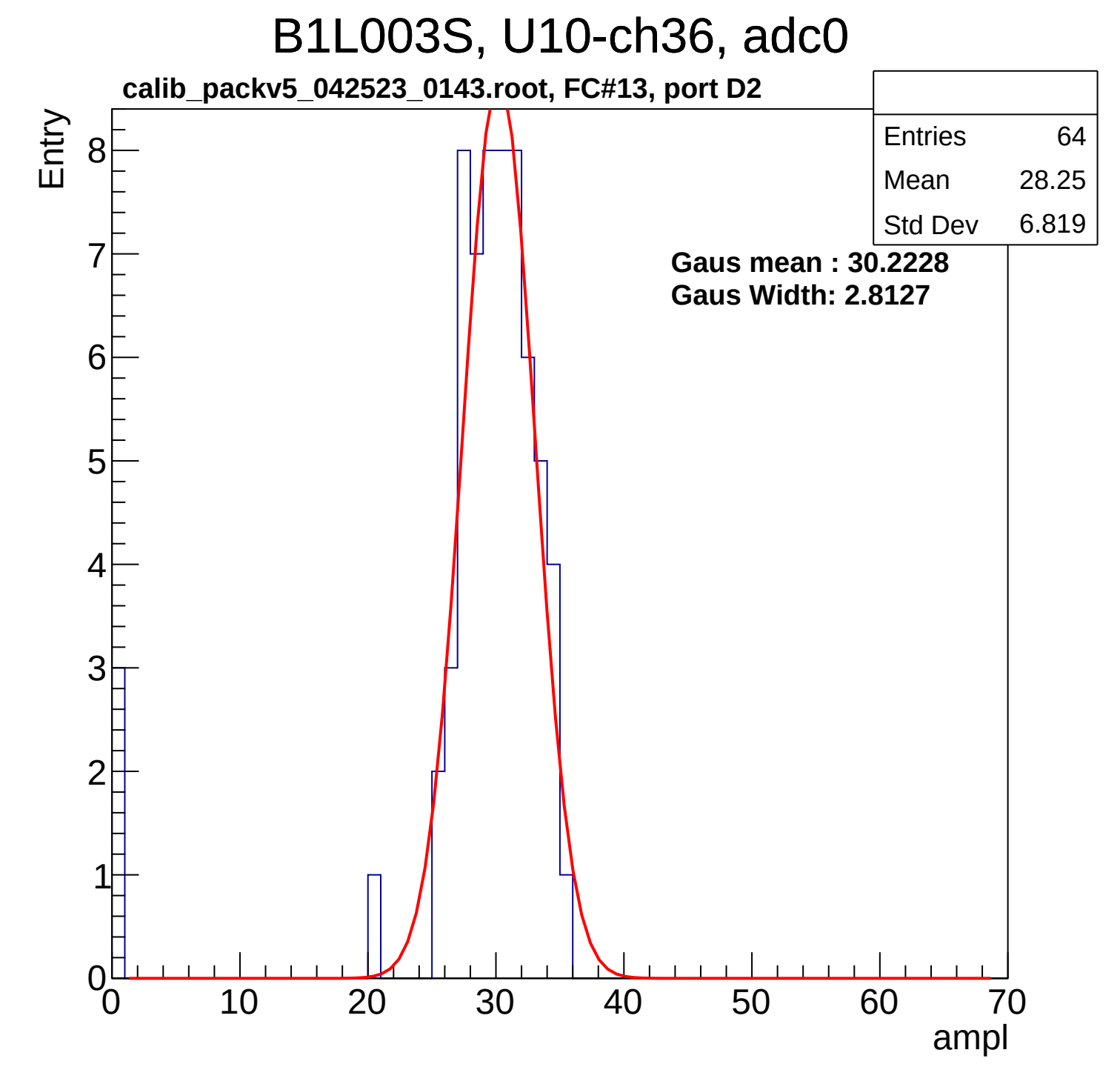

B1L003S, U10-ch35, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 50.58
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

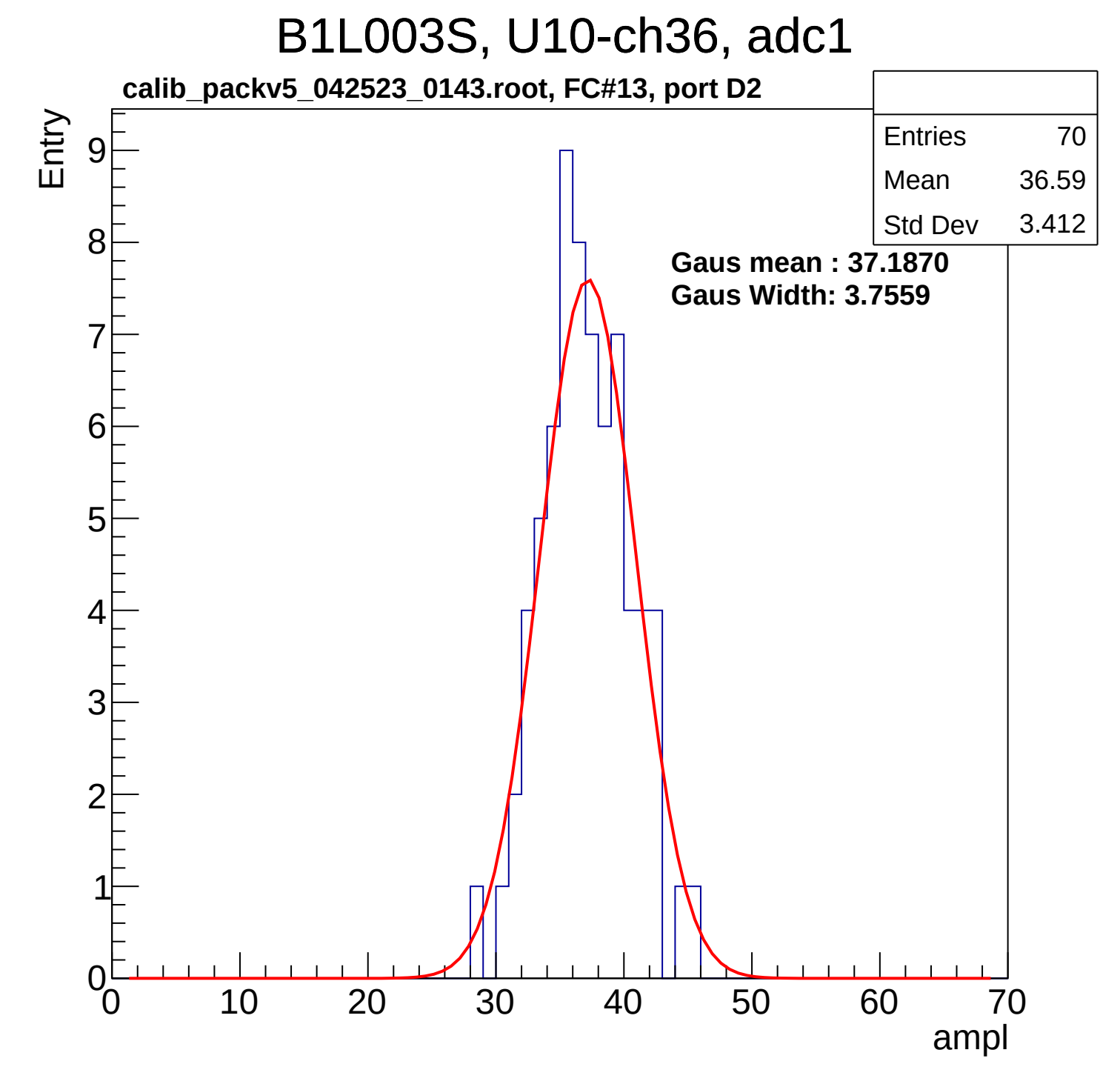

B1L003S, U10-ch35, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 56.77
Std Dev 2.902
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

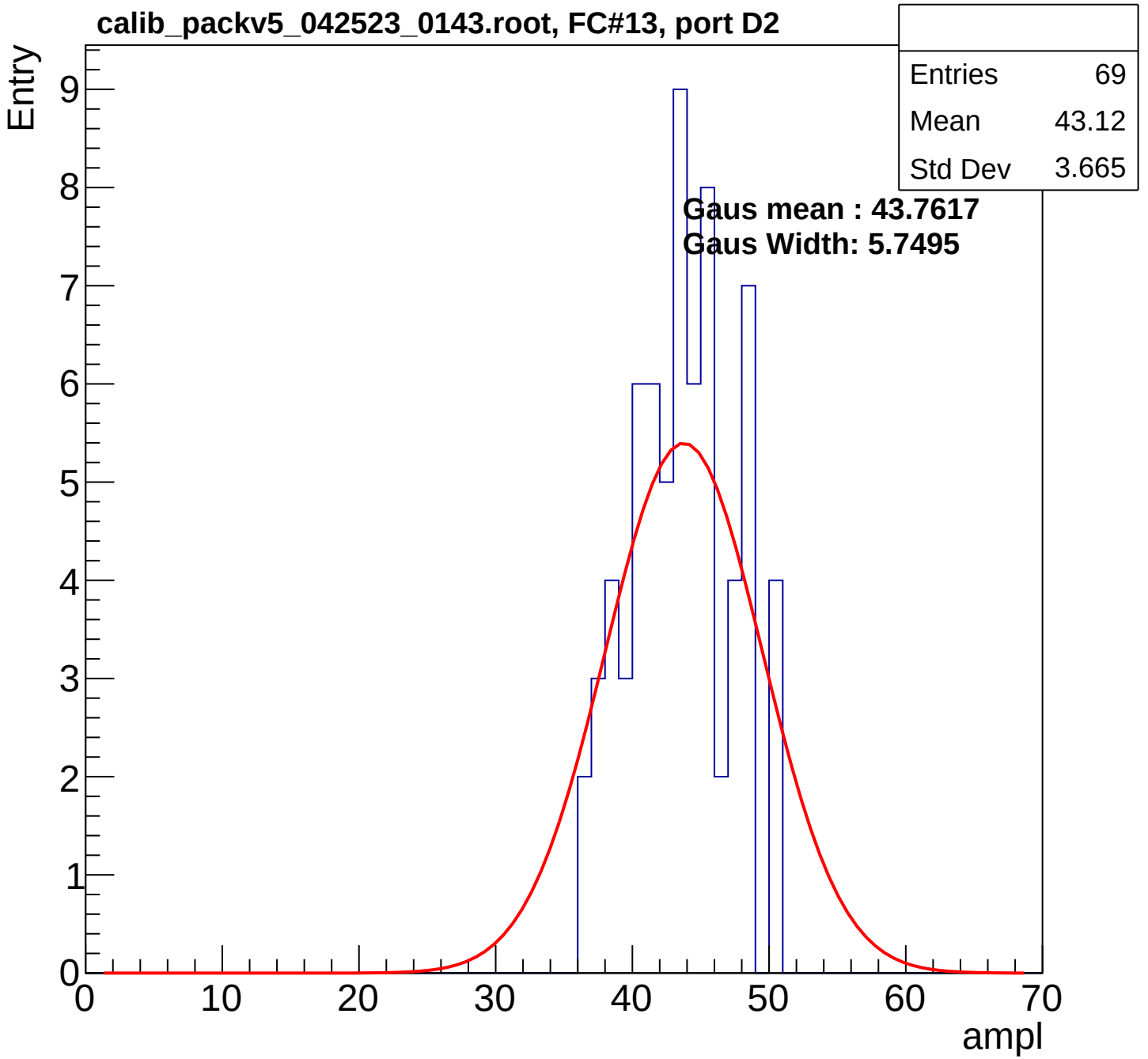

B1L003S, U10-ch35, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.92
Std Dev 9.761
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

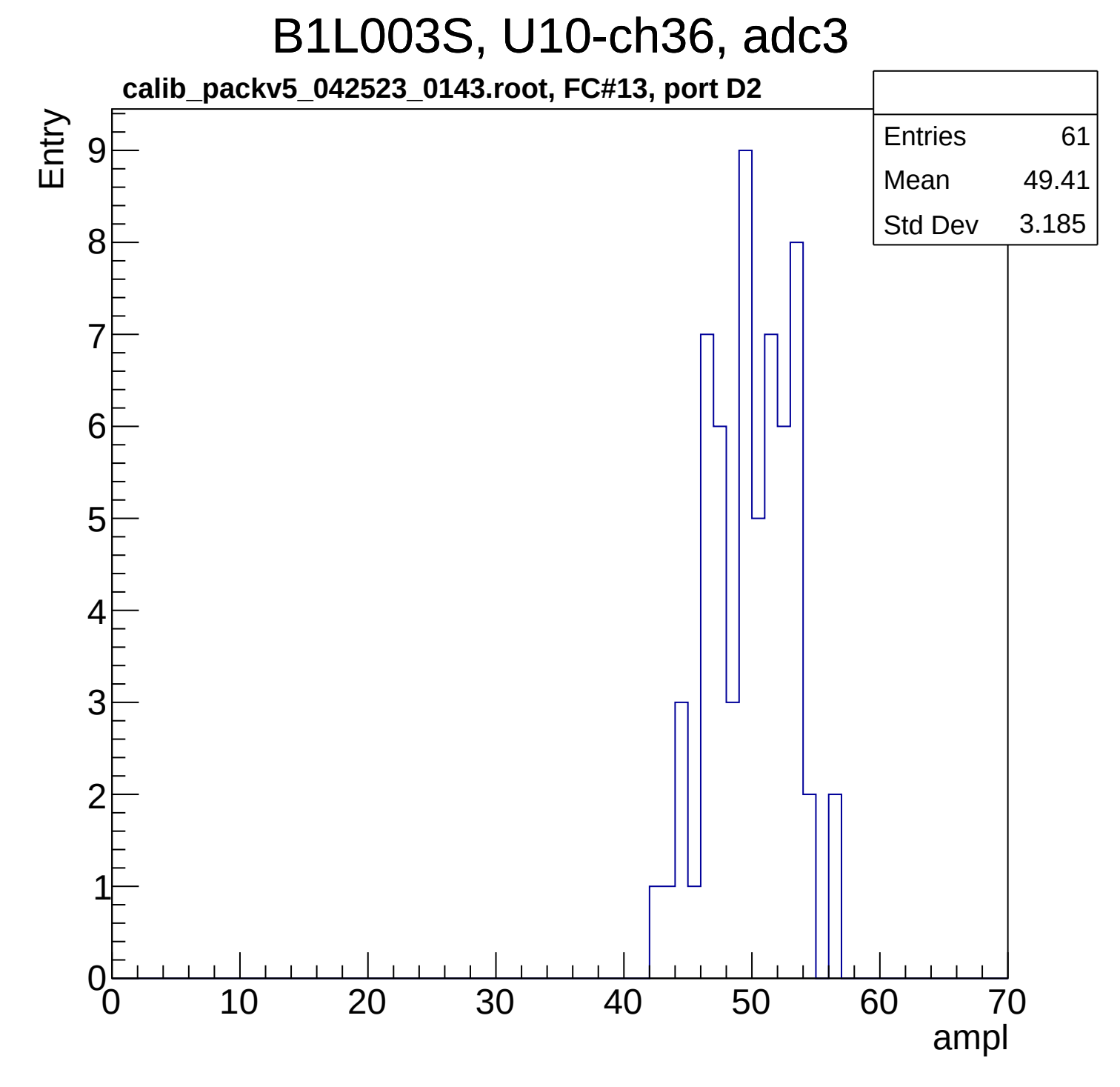

B1L003S, U10-ch35, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

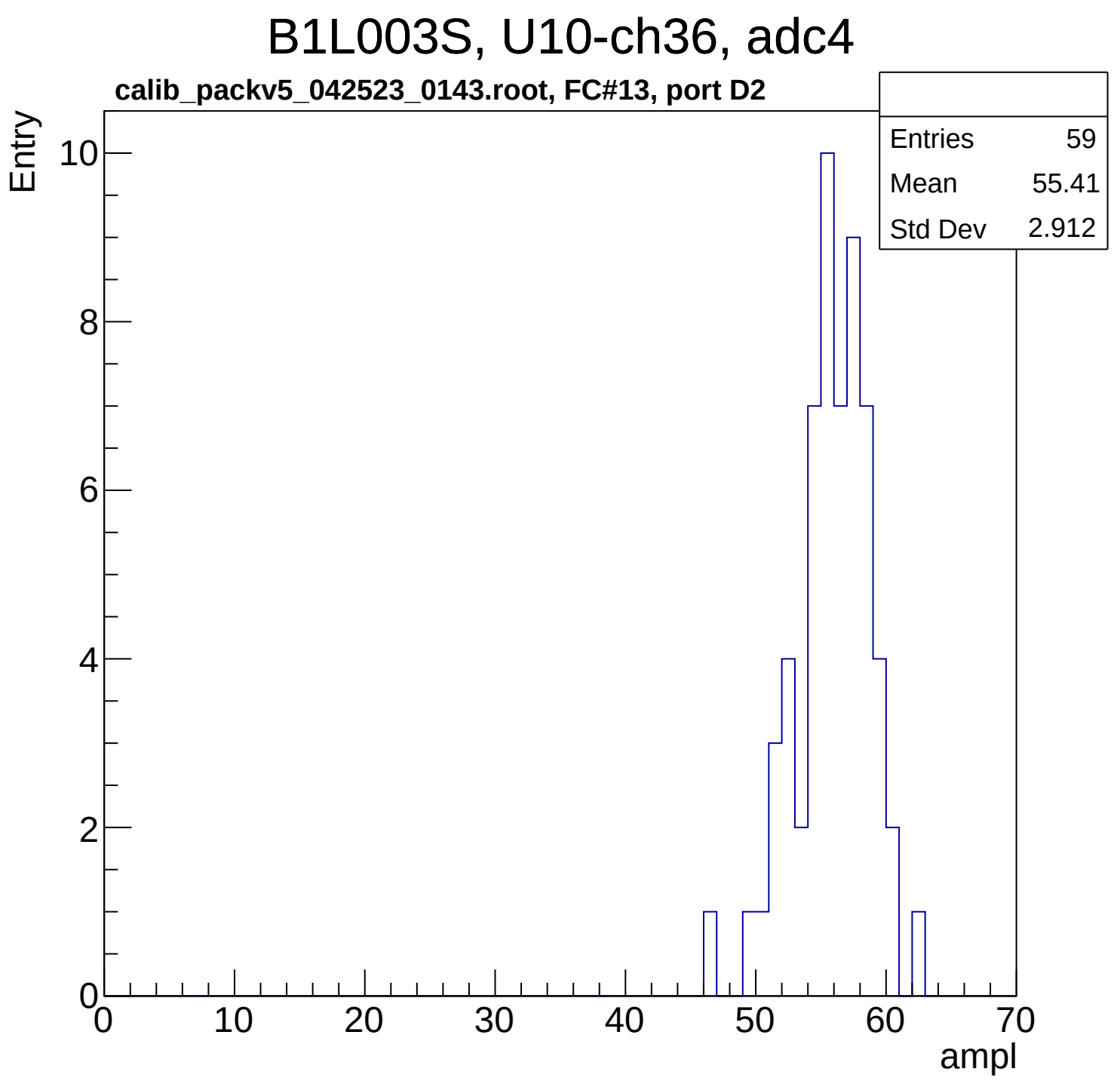

B1L003S, U10-ch35, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

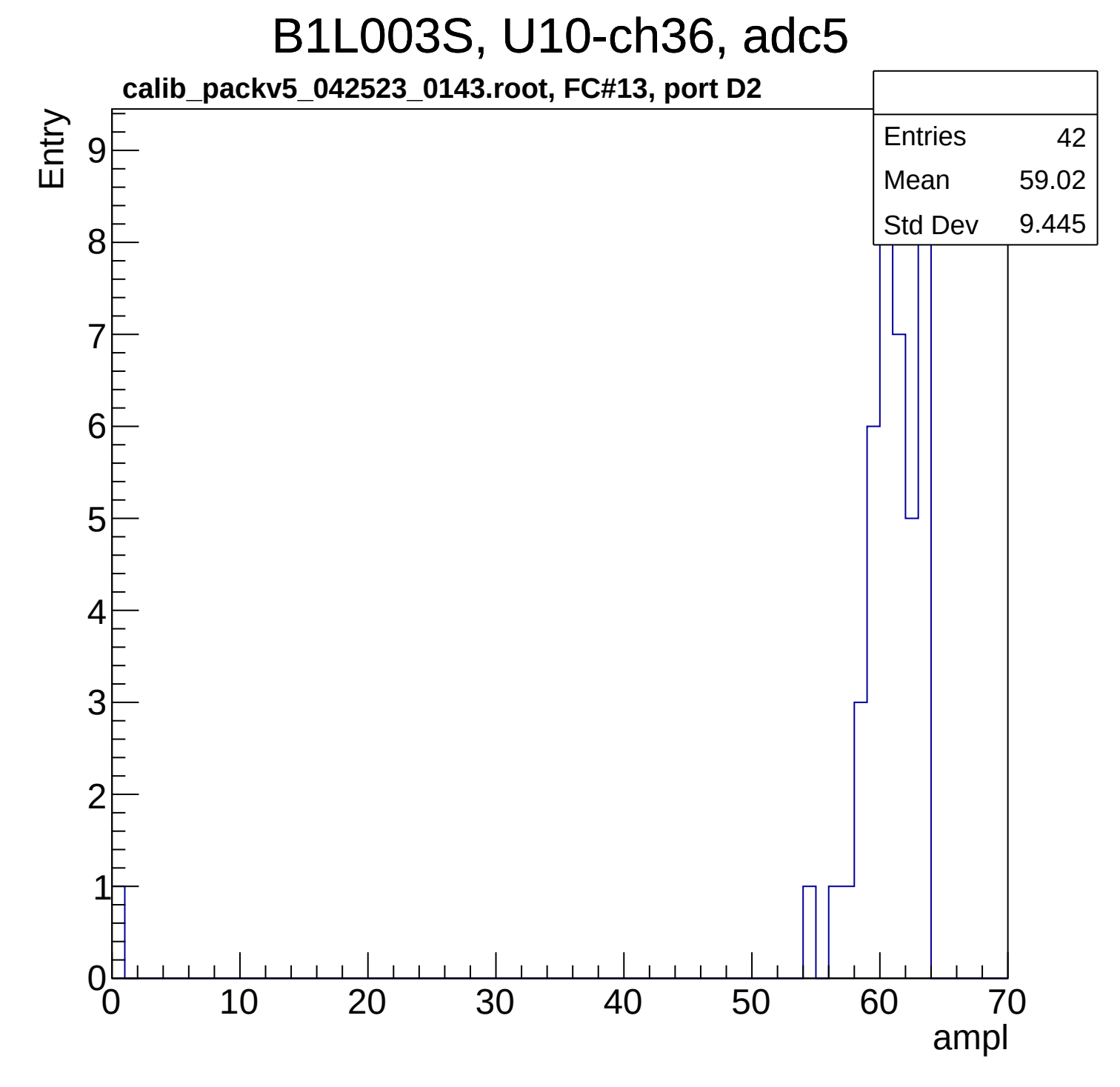

B1L003S, U10-ch36, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 28.25
Std Dev 6.819
Gaus mean : 30.2228
Gaus Width: 2.8127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

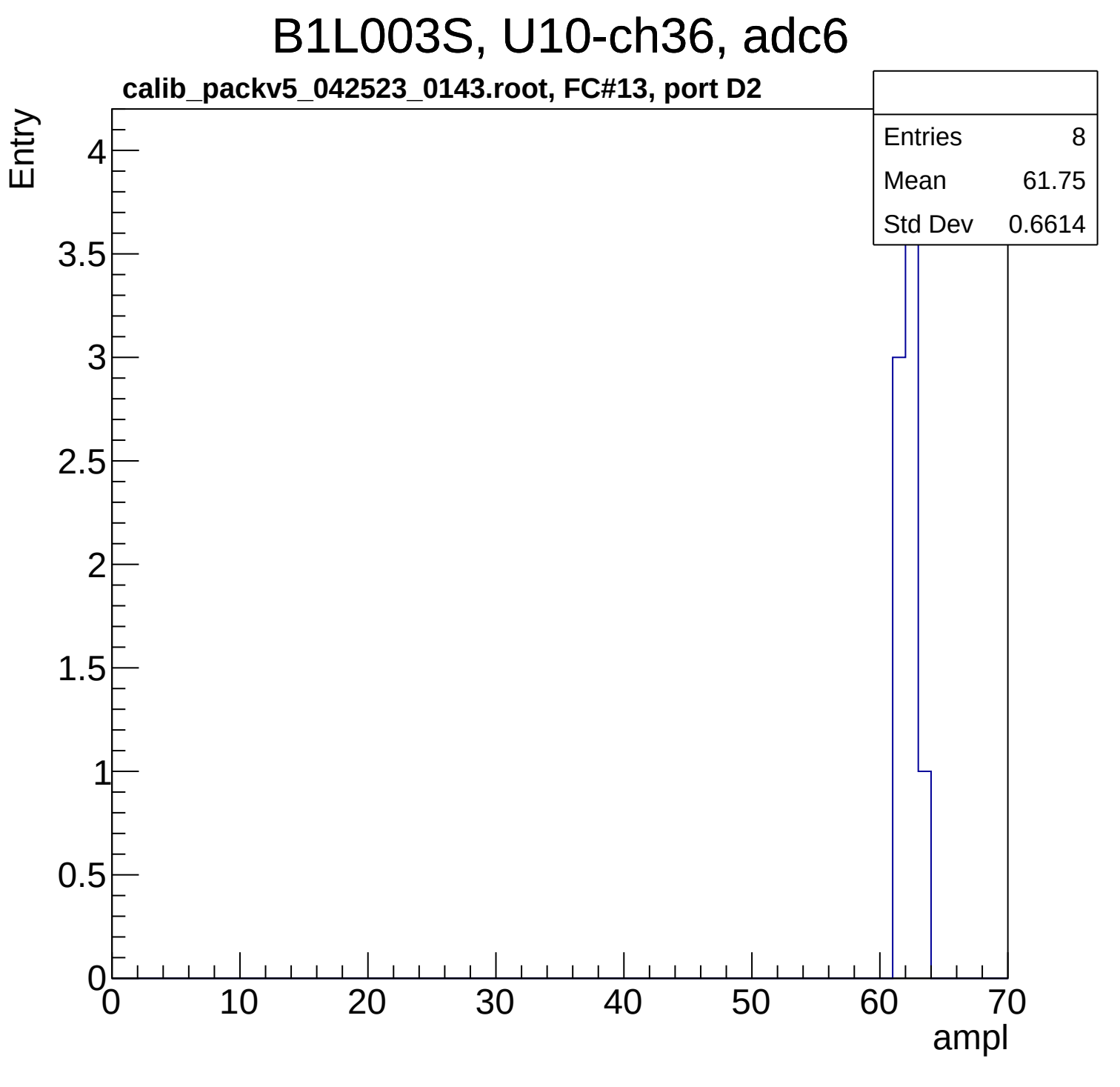

B1L003S, U10-ch36, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36.59
Std Dev 3.412
Gaus mean : 37.1870
Gaus Width: 3.7559
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

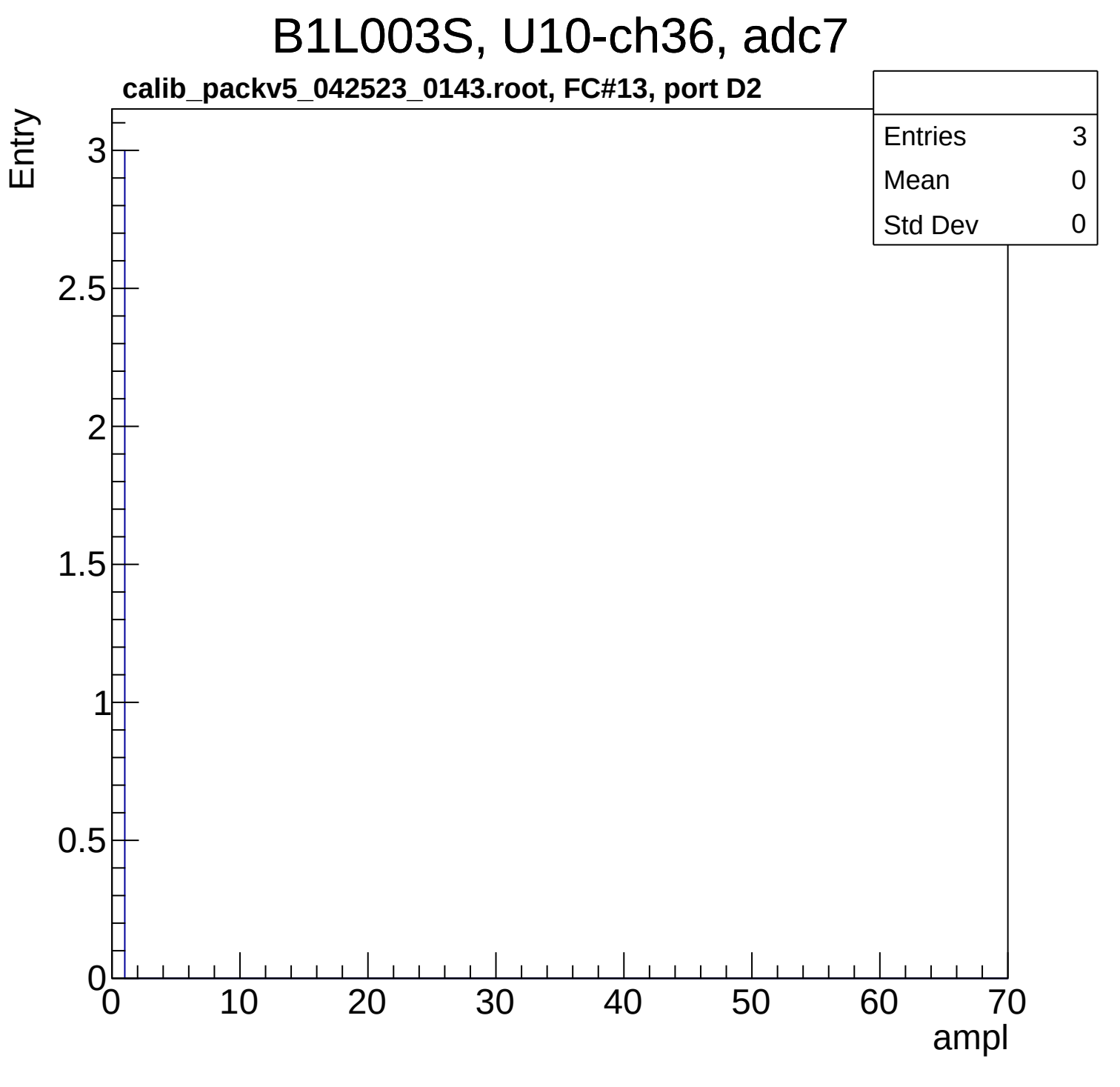

B1L003S, U10-ch36, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 43.12
Std Dev 3.665
Gaus mean : 43.7617
Gaus Width: 5.7495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

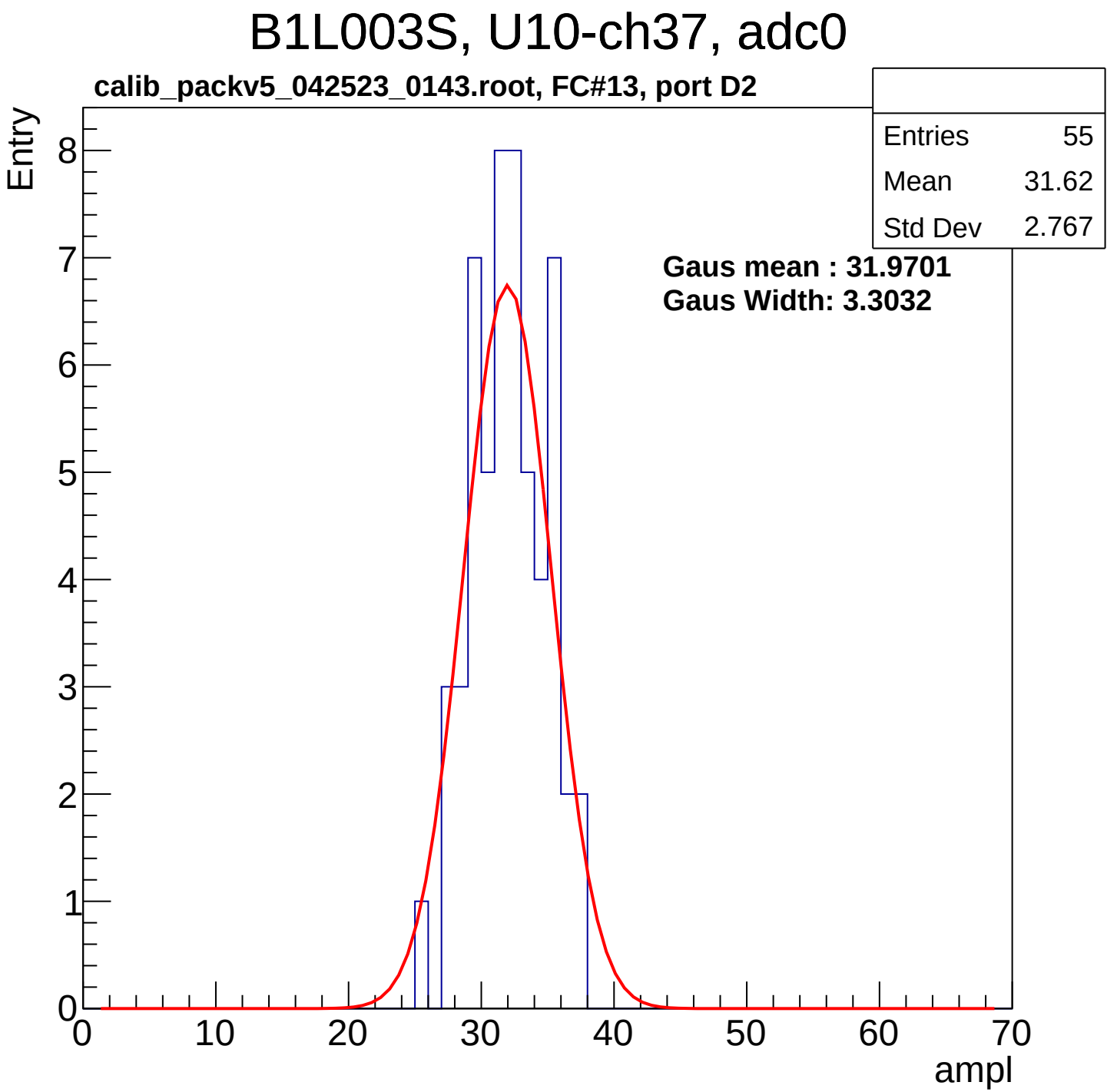

B1L003S, U10-ch36, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.41
Std Dev 3.185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

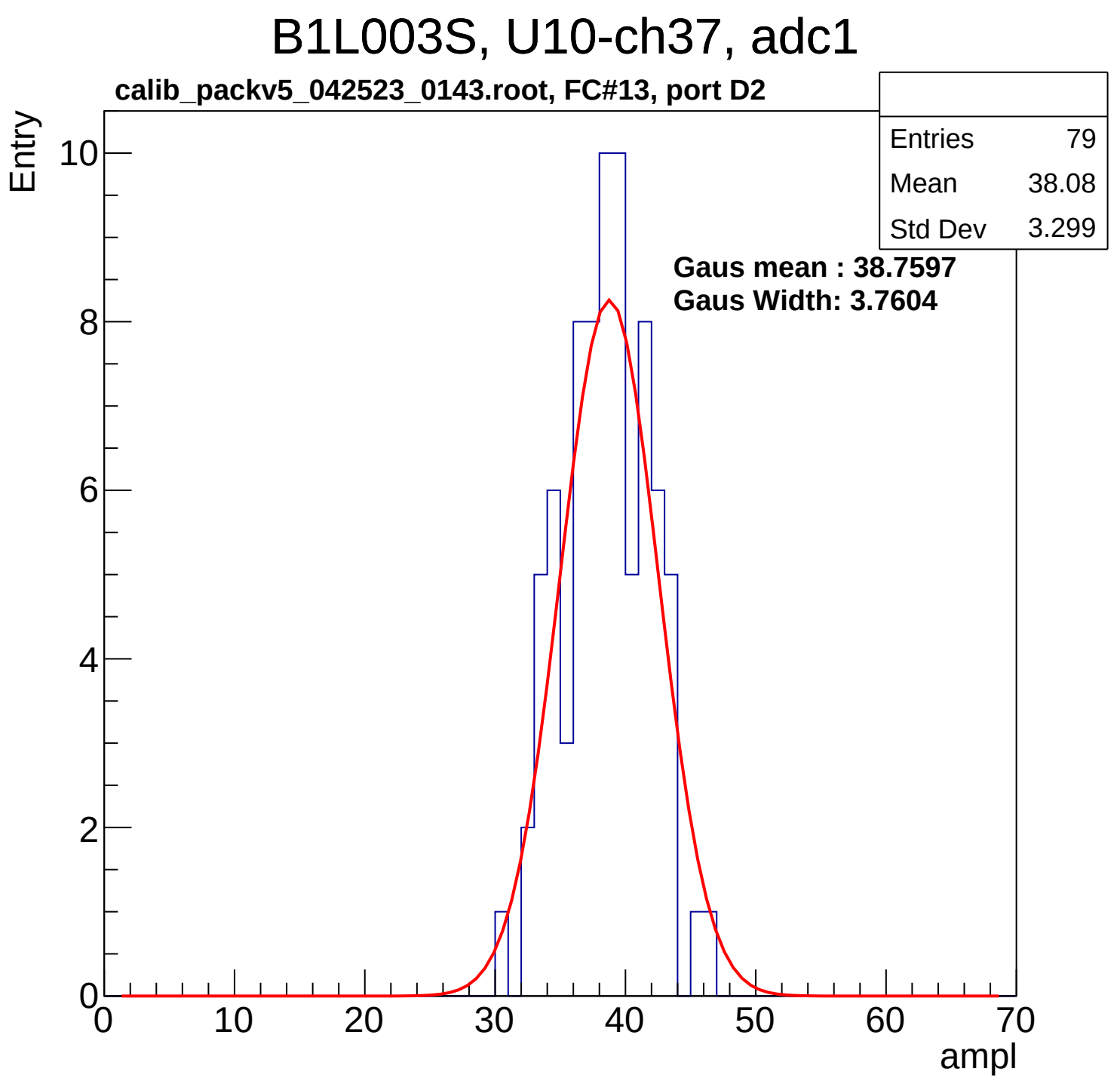

B1L003S, U10-ch36, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.41
Std Dev 2.912
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

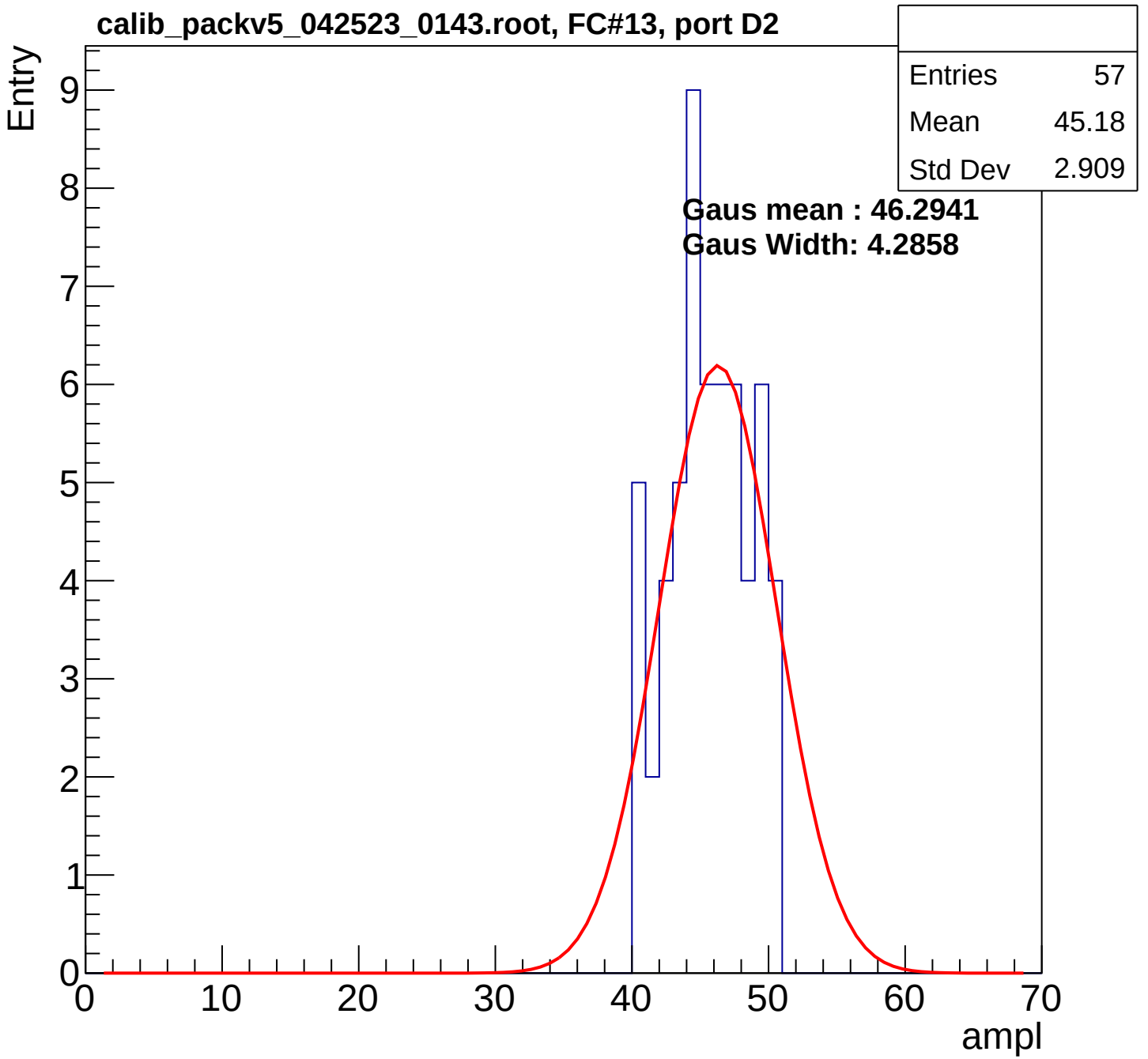

B1L003S, U10-ch36, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 59.02
Std Dev 9.445
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

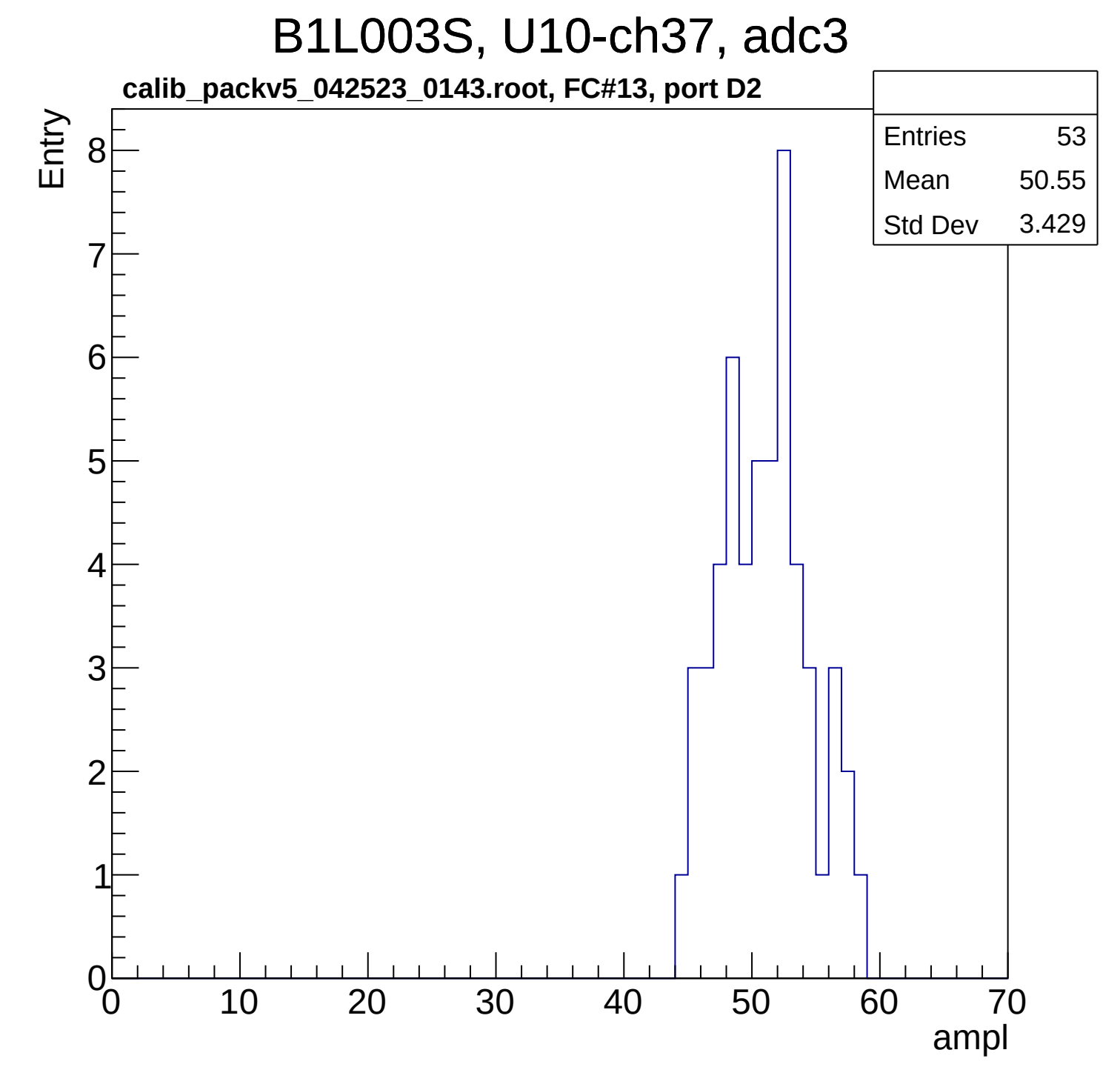

B1L003S, U10-ch36, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.75
Std Dev 0.6614
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

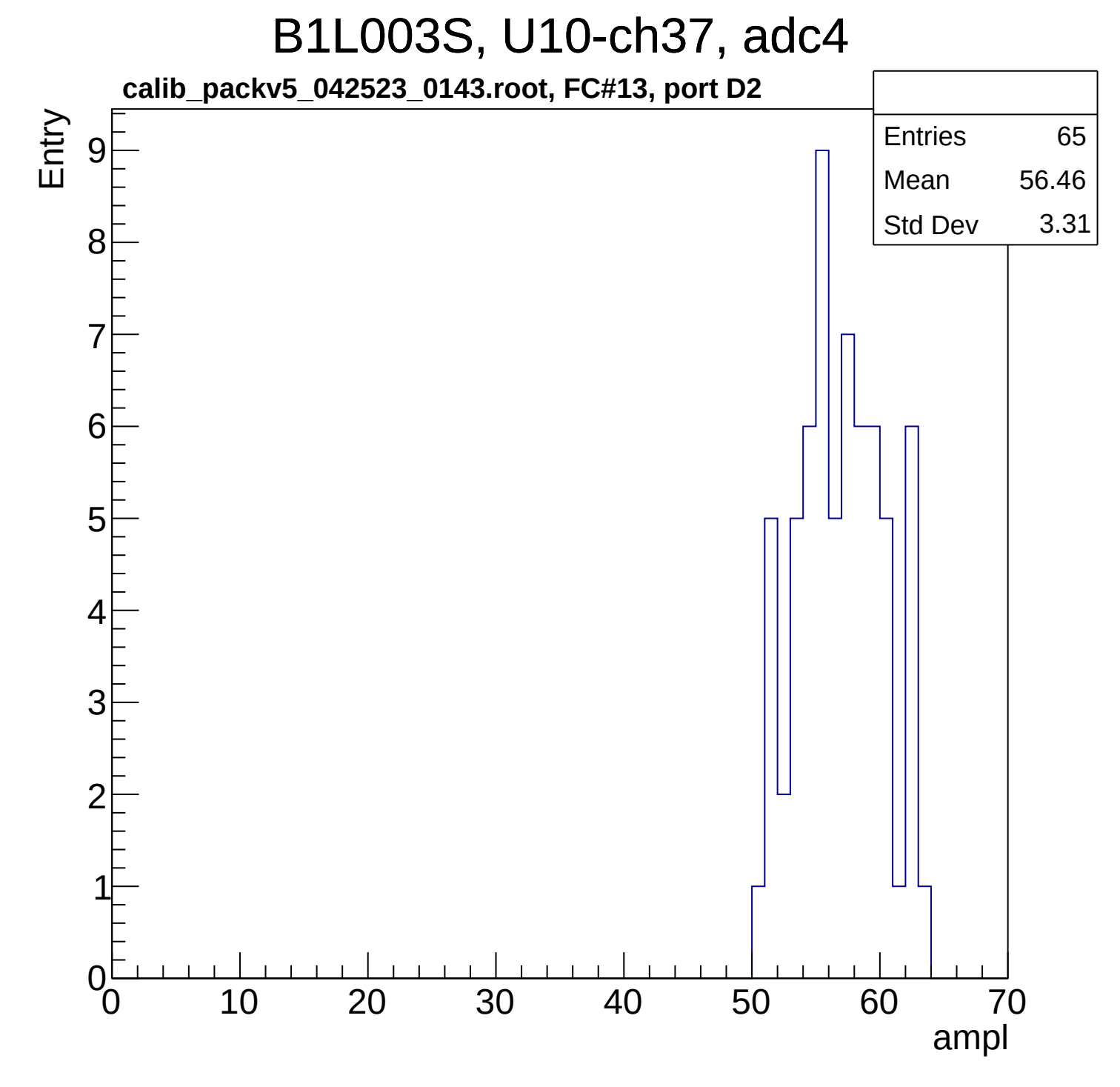

B1L003S, U10-ch36, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

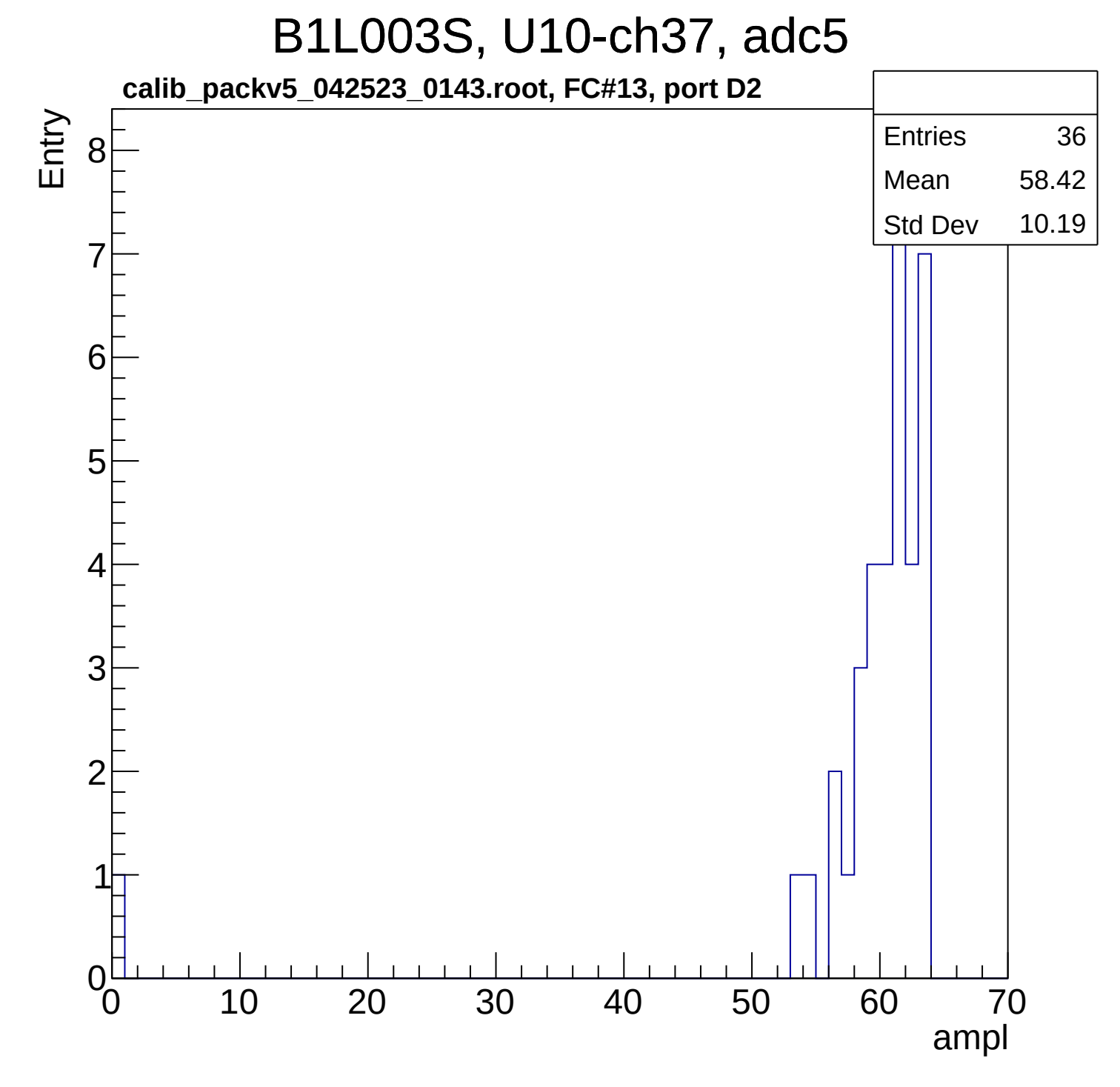

B1L003S, U10-ch37, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 31.62
Std Dev 2.767
Gaus mean : 31.9701
Gaus Width: 3.3032
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

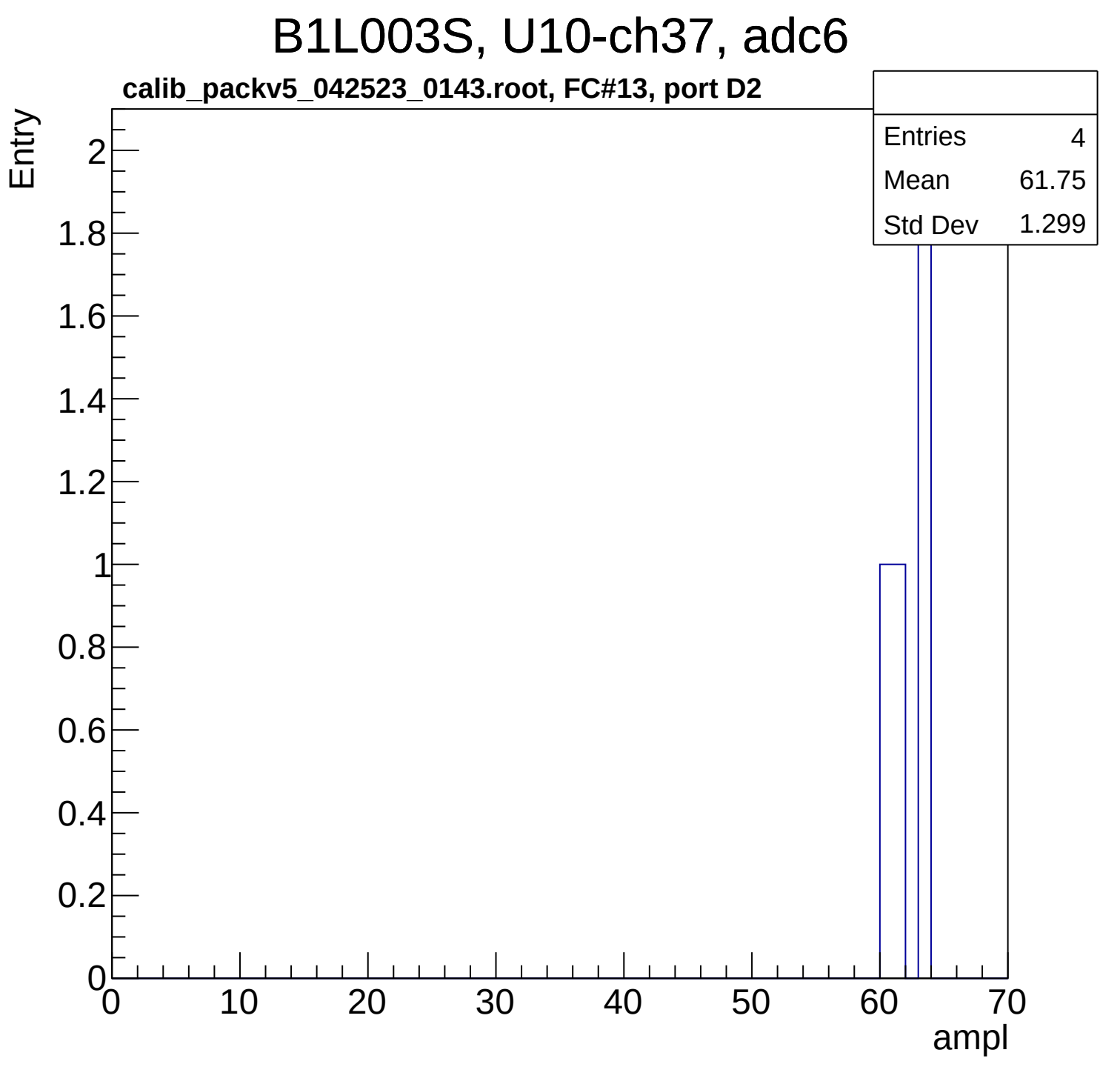

B1L003S, U10-ch37, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 38.08
Std Dev 3.299
Gaus mean : 38.7597
Gaus Width: 3.7604
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

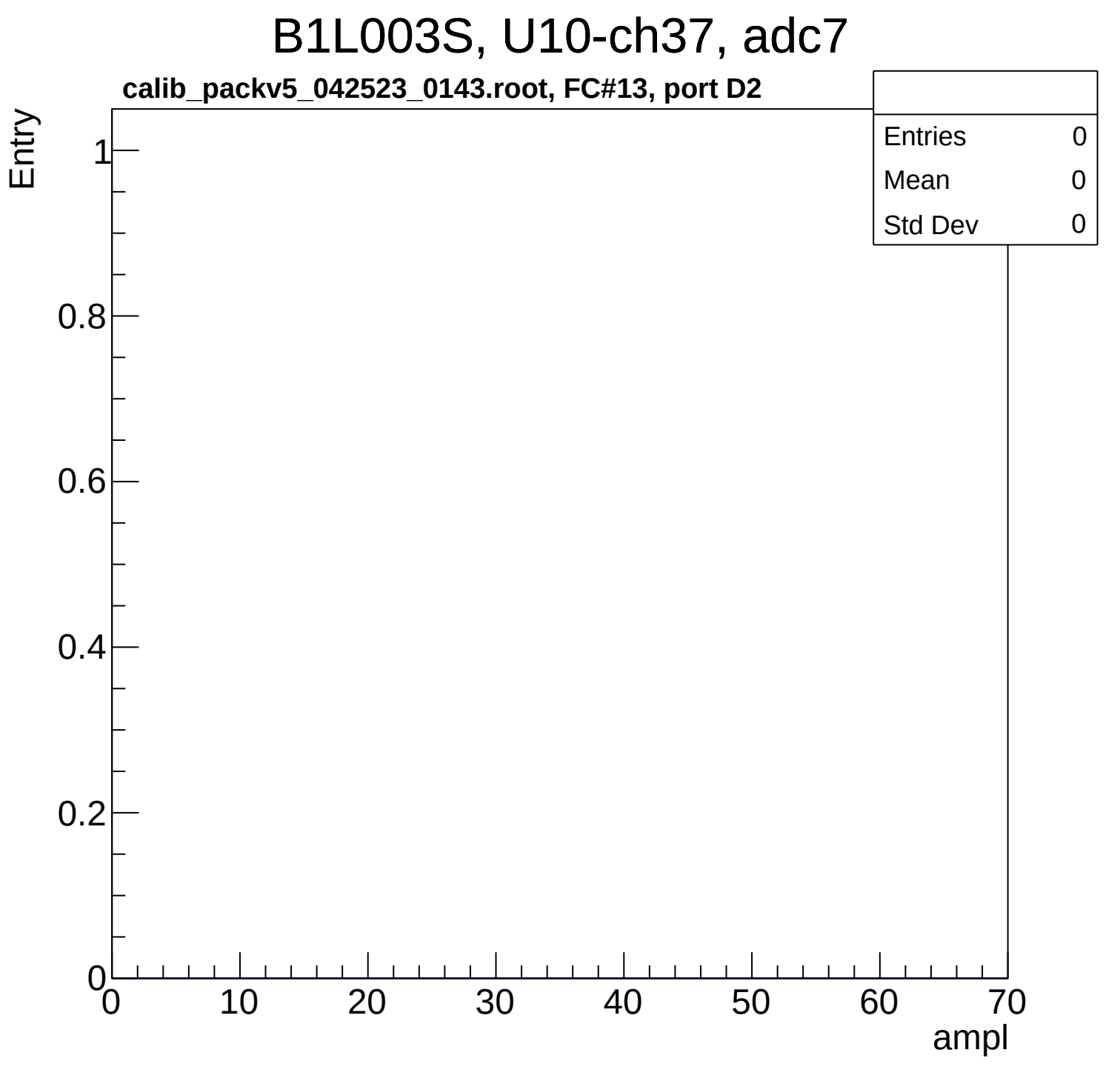

B1L003S, U10-ch37, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 45.18
Std Dev 2.909
Gaus mean : 46.2941
Gaus Width: 4.2858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

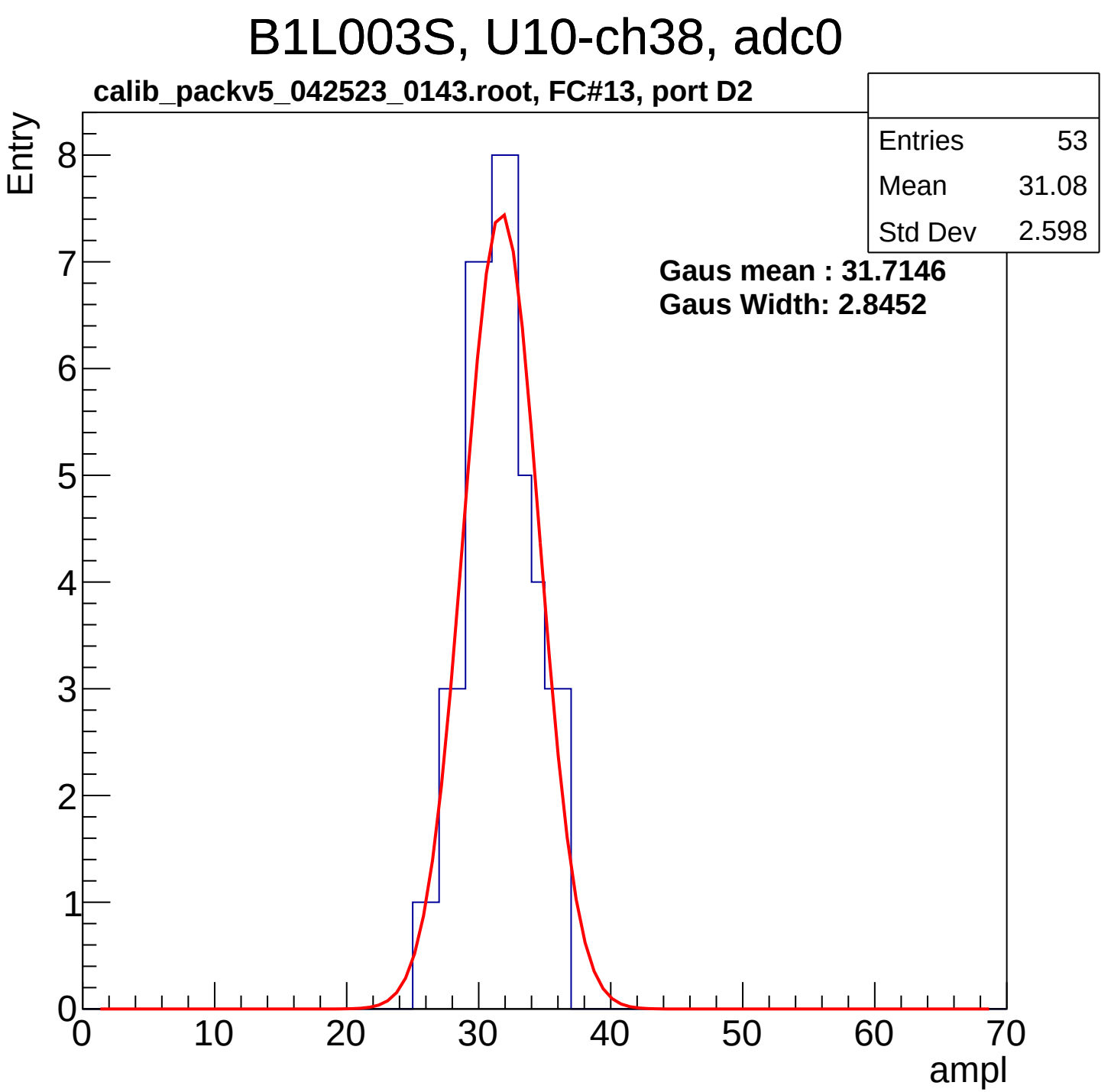

B1L003S, U10-ch37, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 50.55
Std Dev 3.429
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

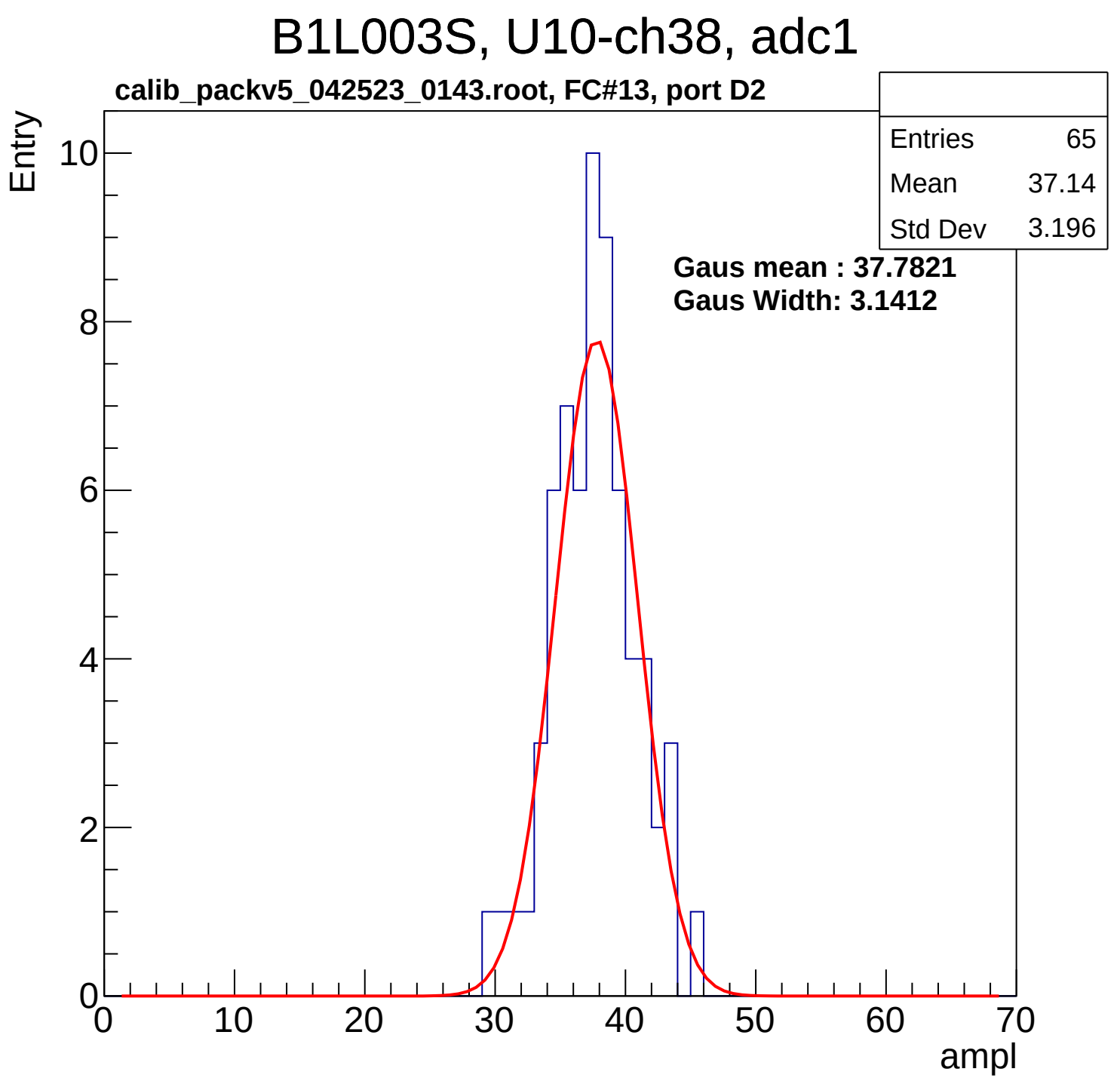

B1L003S, U10-ch37, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 56.46
Std Dev 3.31
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

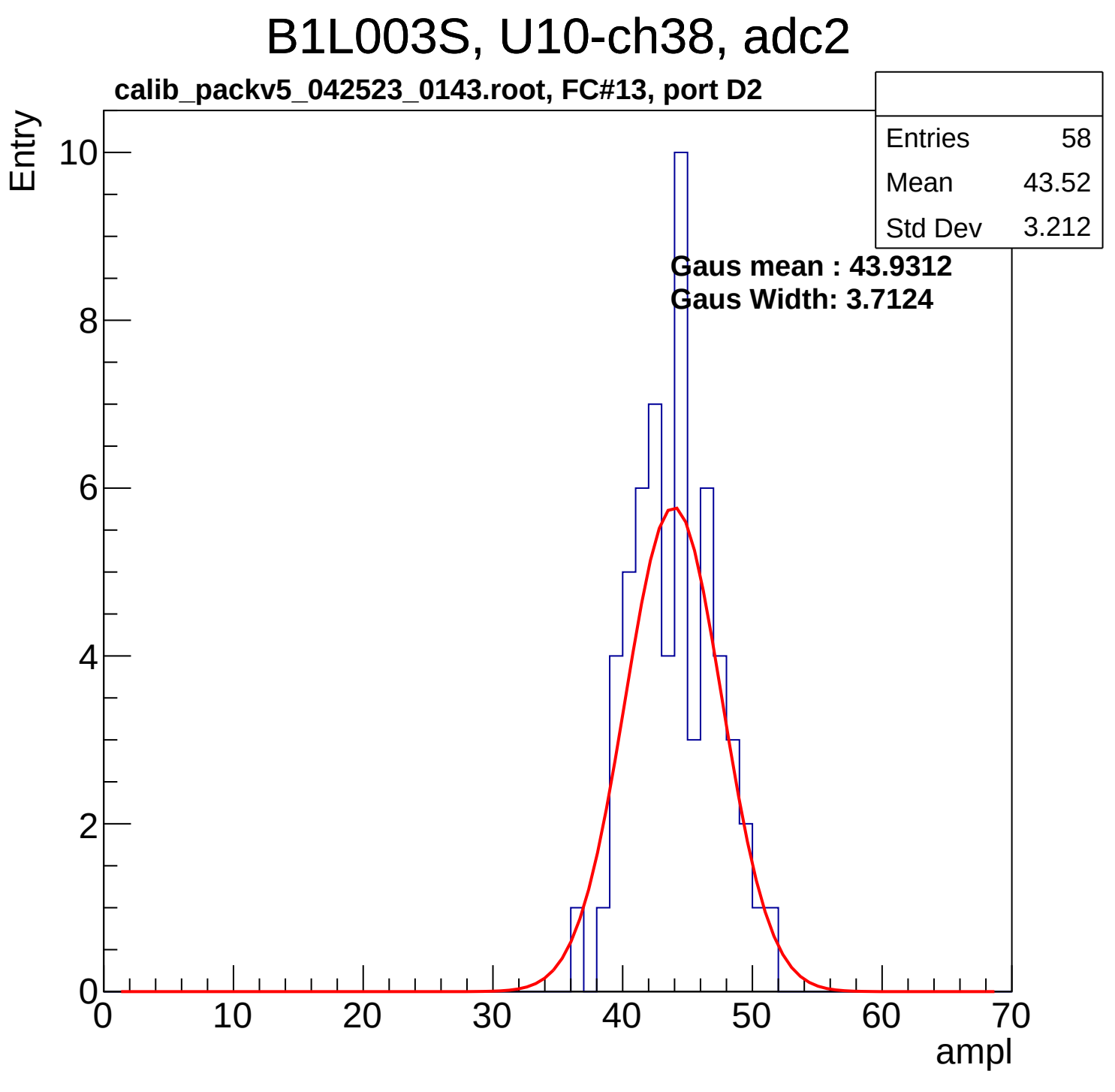

B1L003S, U10-ch37, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.42
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch37, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch37, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch38, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 31.08
Std Dev 2.598
Gaus mean : 31.7146
Gaus Width: 2.8452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch38, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 37.14
Std Dev 3.196
Gaus mean : 37.7821
Gaus Width: 3.1412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch38, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 43.52
Std Dev 3.212
Gaus mean : 43.9312
Gaus Width: 3.7124
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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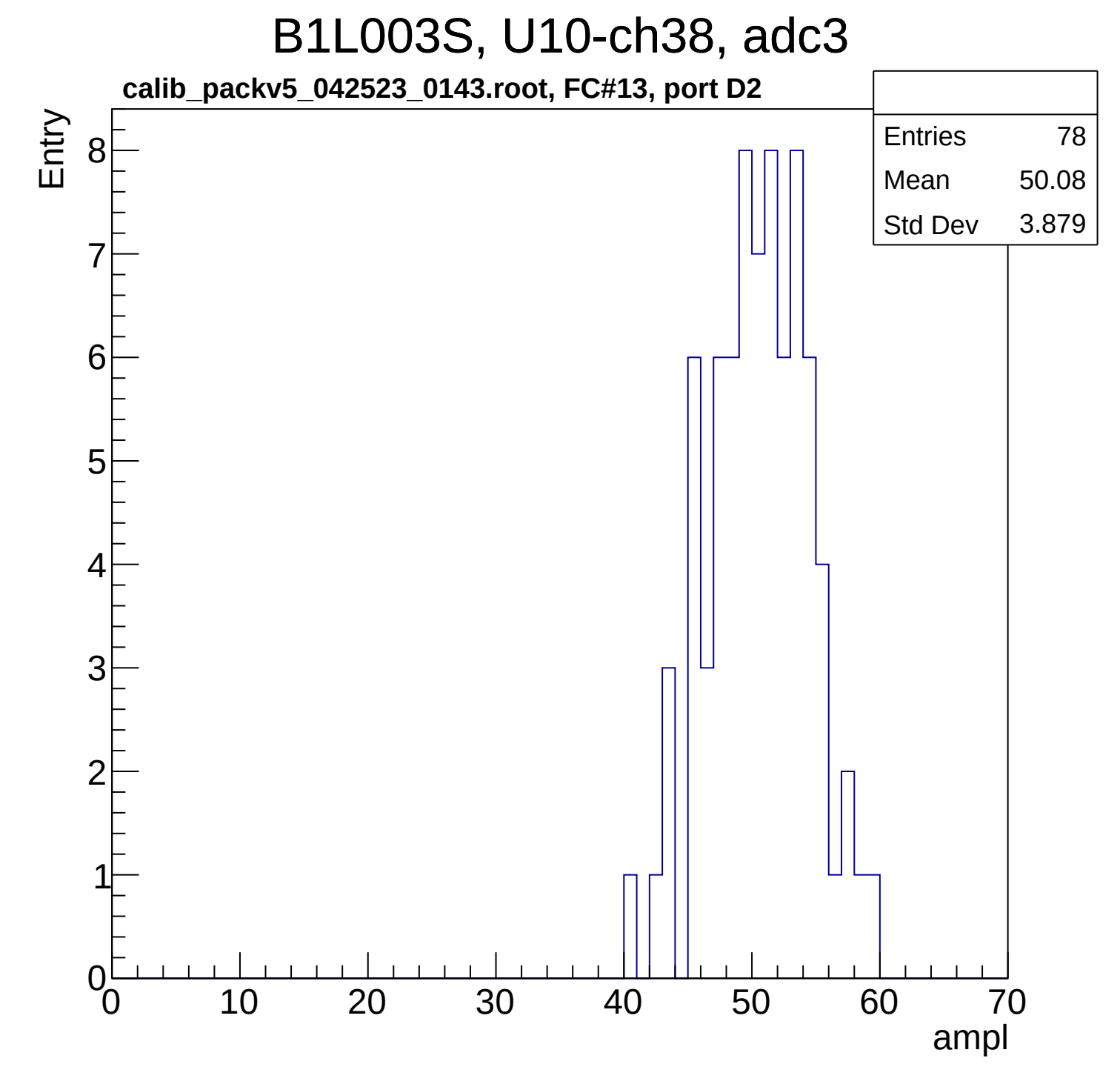
B1L003S, U10-ch38, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 50.08
Std Dev 3.879
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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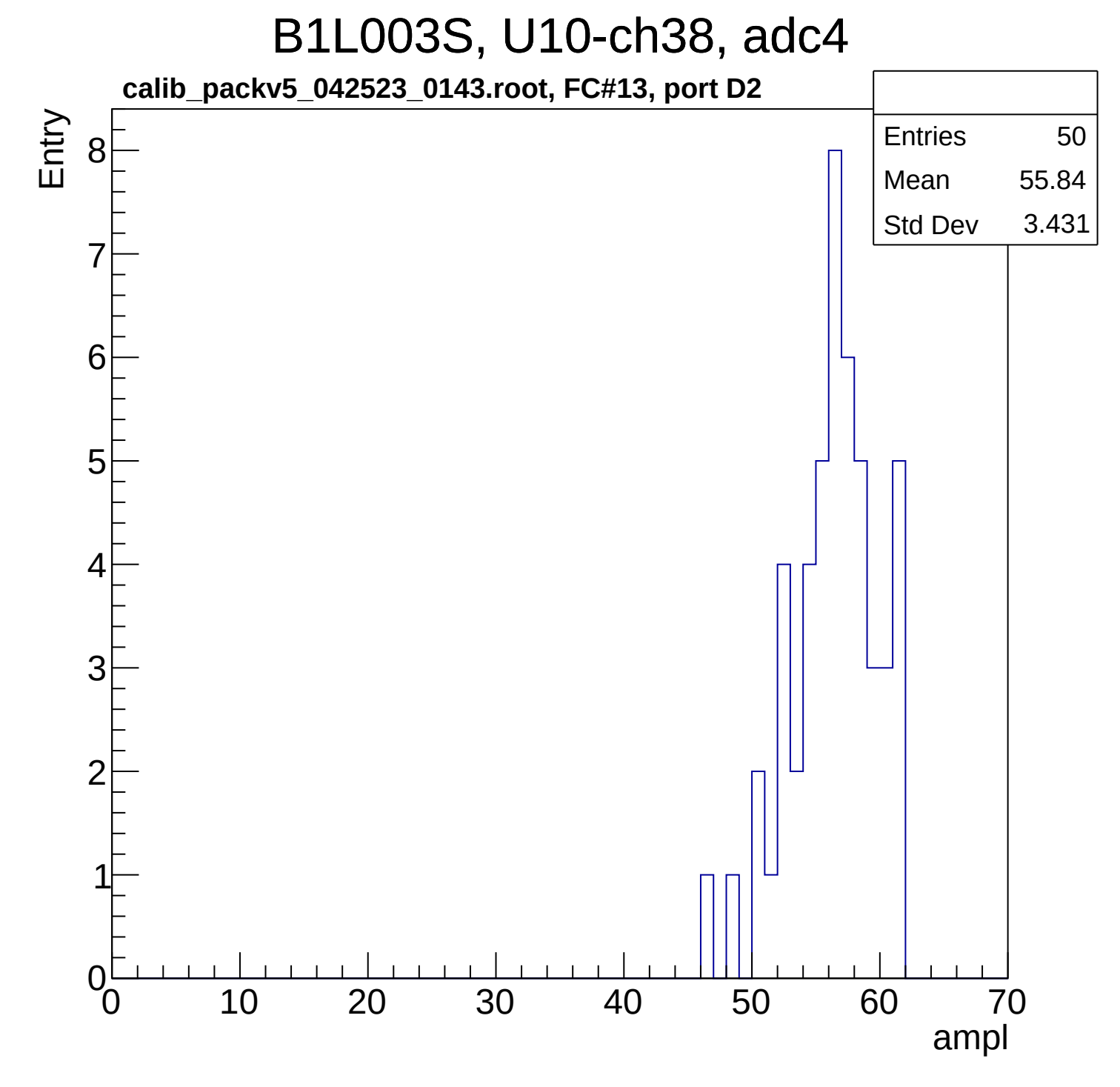
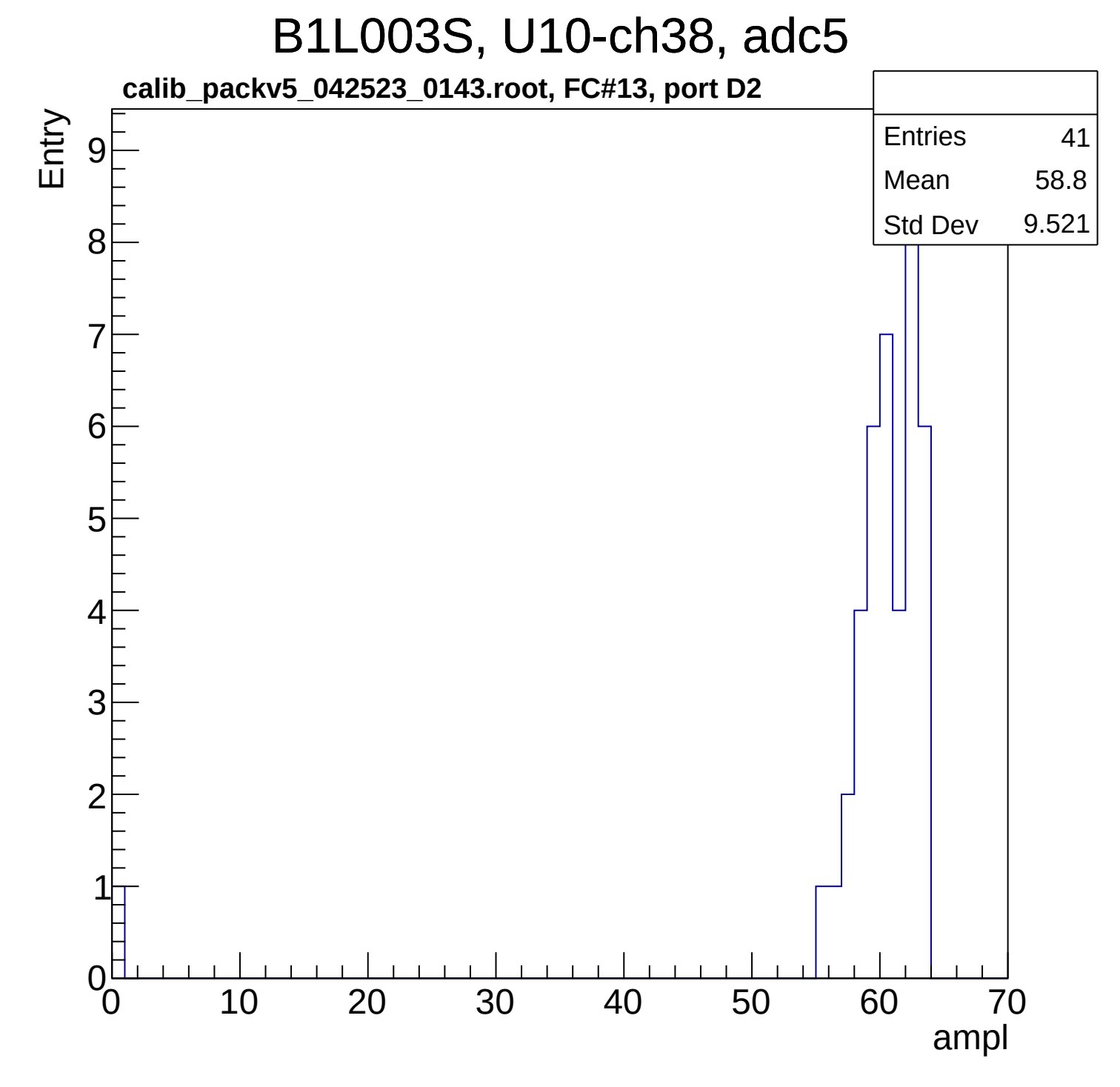
50
60
70


B1L003S, U10-ch38, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 55.84
Std Dev 3.431
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch38, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.8
Std Dev 9.521
Entry
ampl
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1
2
3
4
5
6
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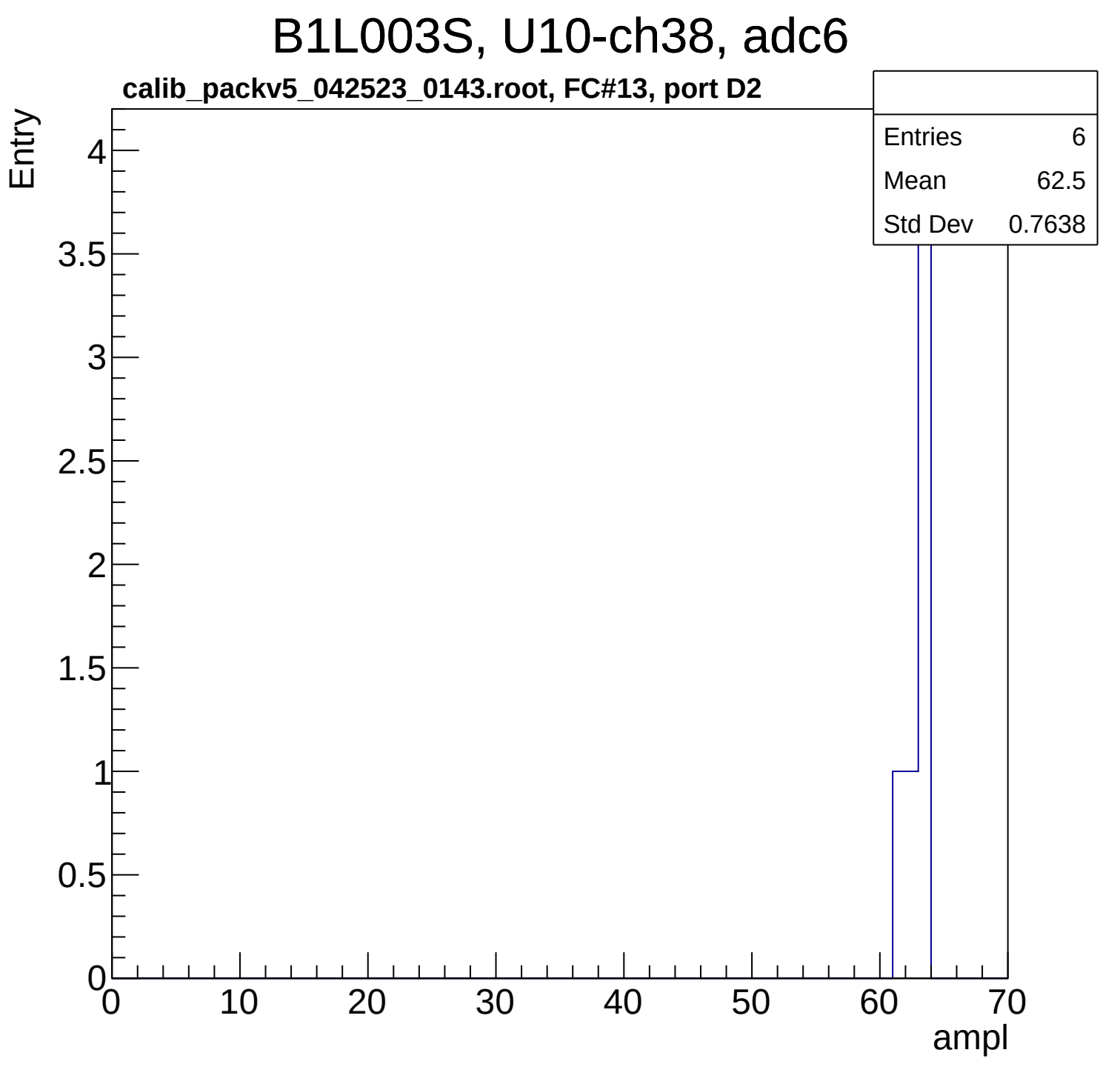
B1L003S, U10-ch38, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
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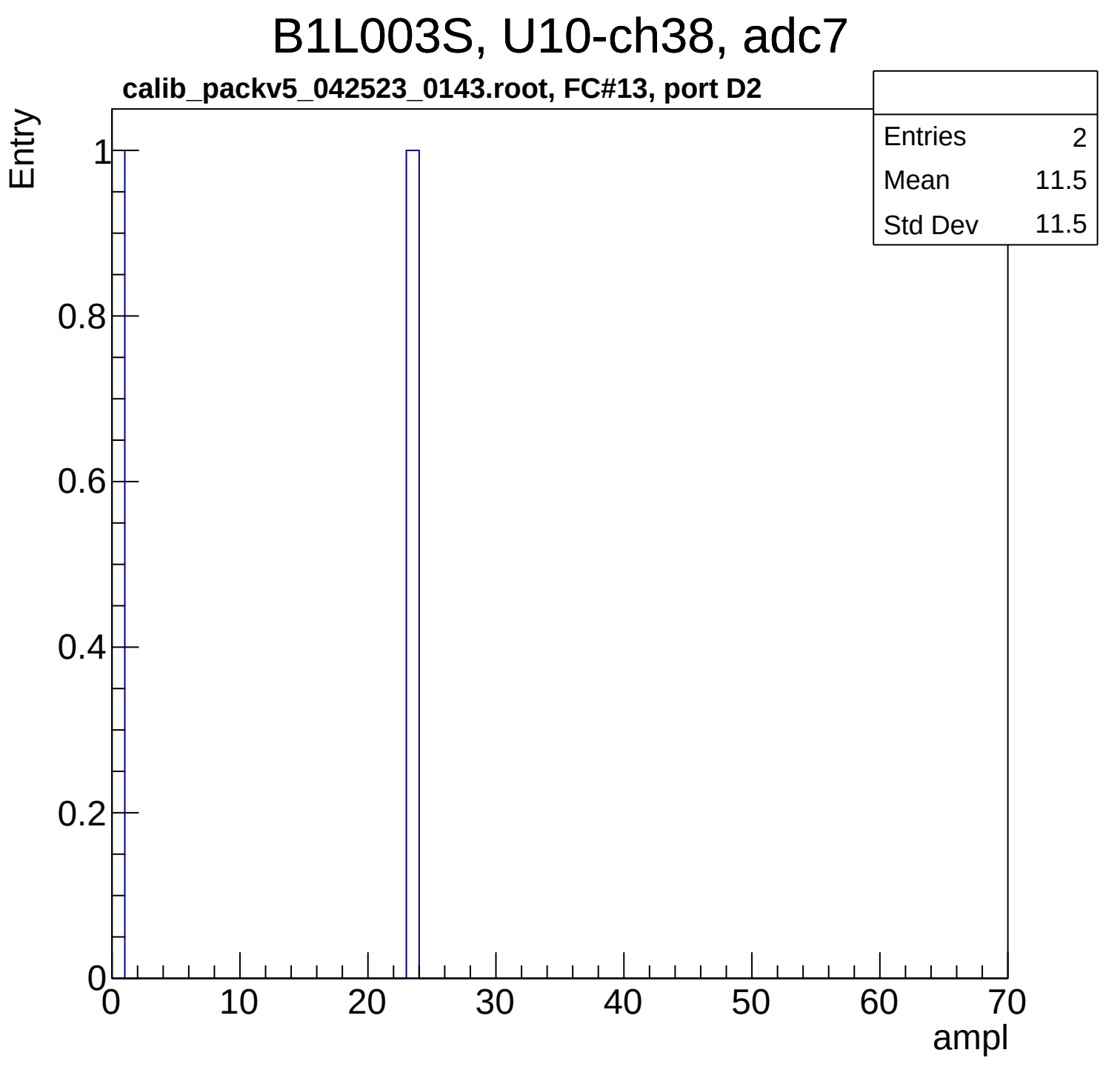
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch38, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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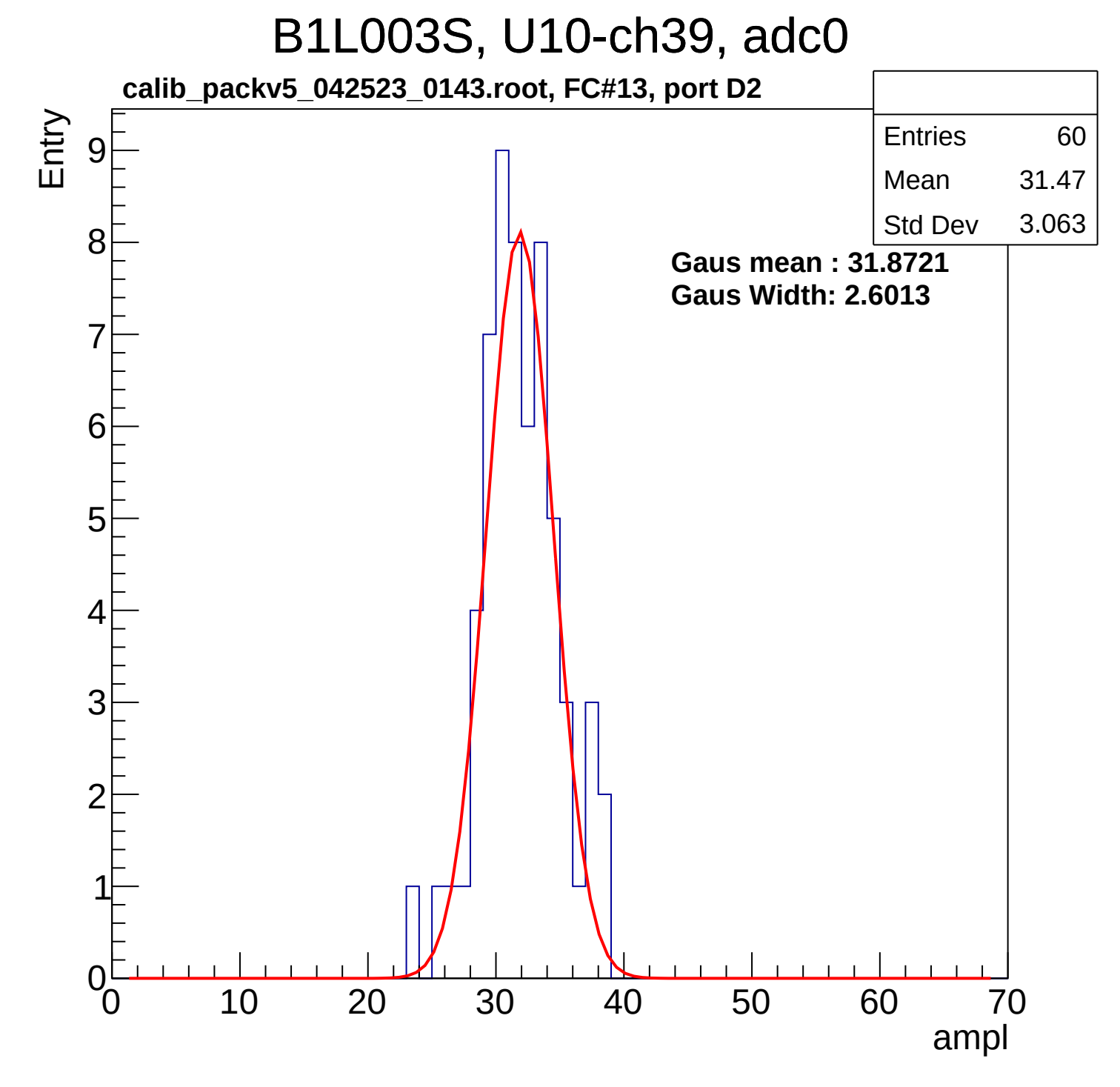
40
50
60
70
ampl


B1L003S, U10-ch39, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 31.47
Std Dev 3.063
Gaus mean : 31.8721
Gaus Width: 2.6013
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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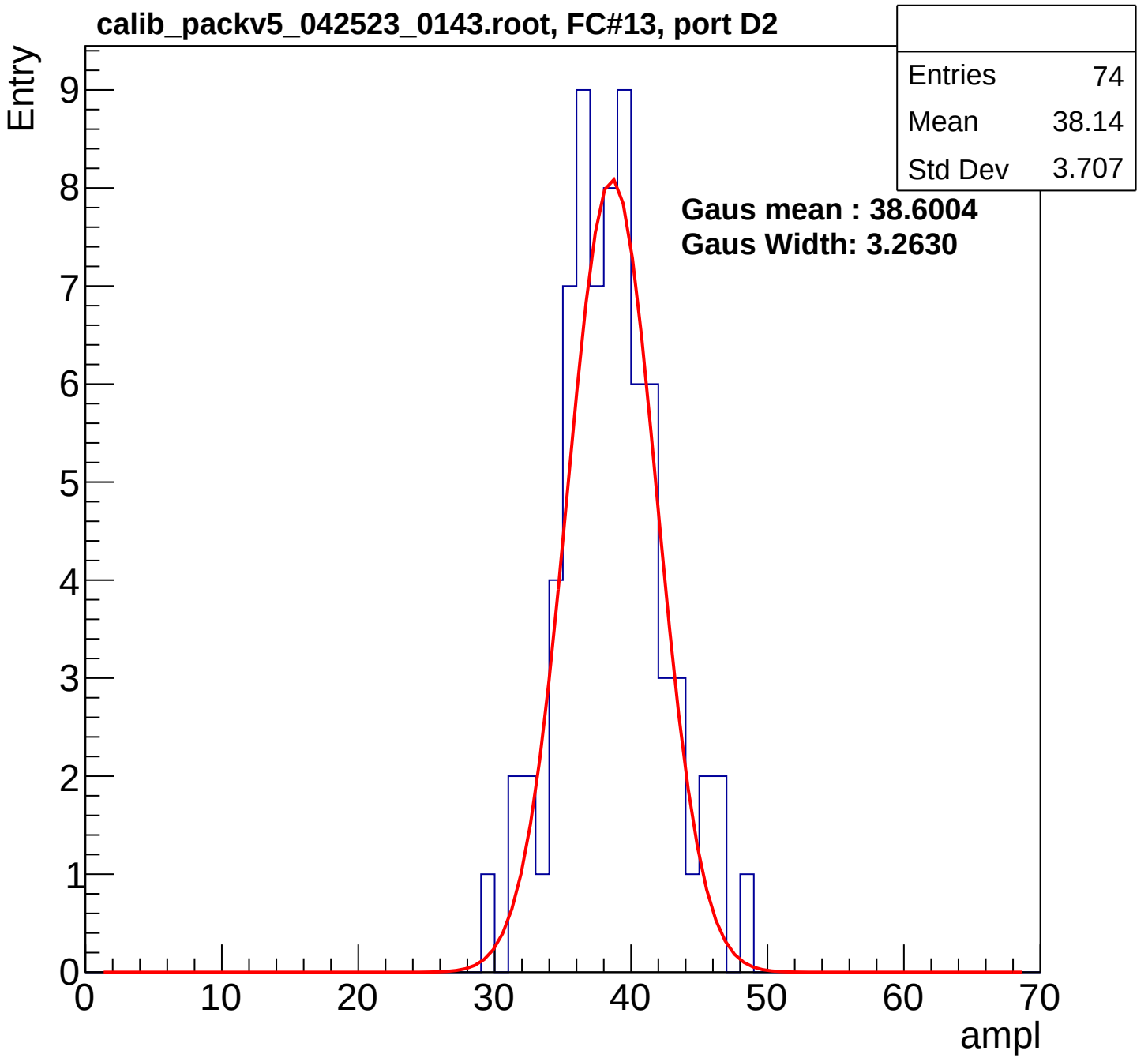
30
40
50
60
70


B1L003S, U10-ch39, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 38.14
Std Dev 3.707
Gaus mean : 38.6004
Gaus Width: 3.2630
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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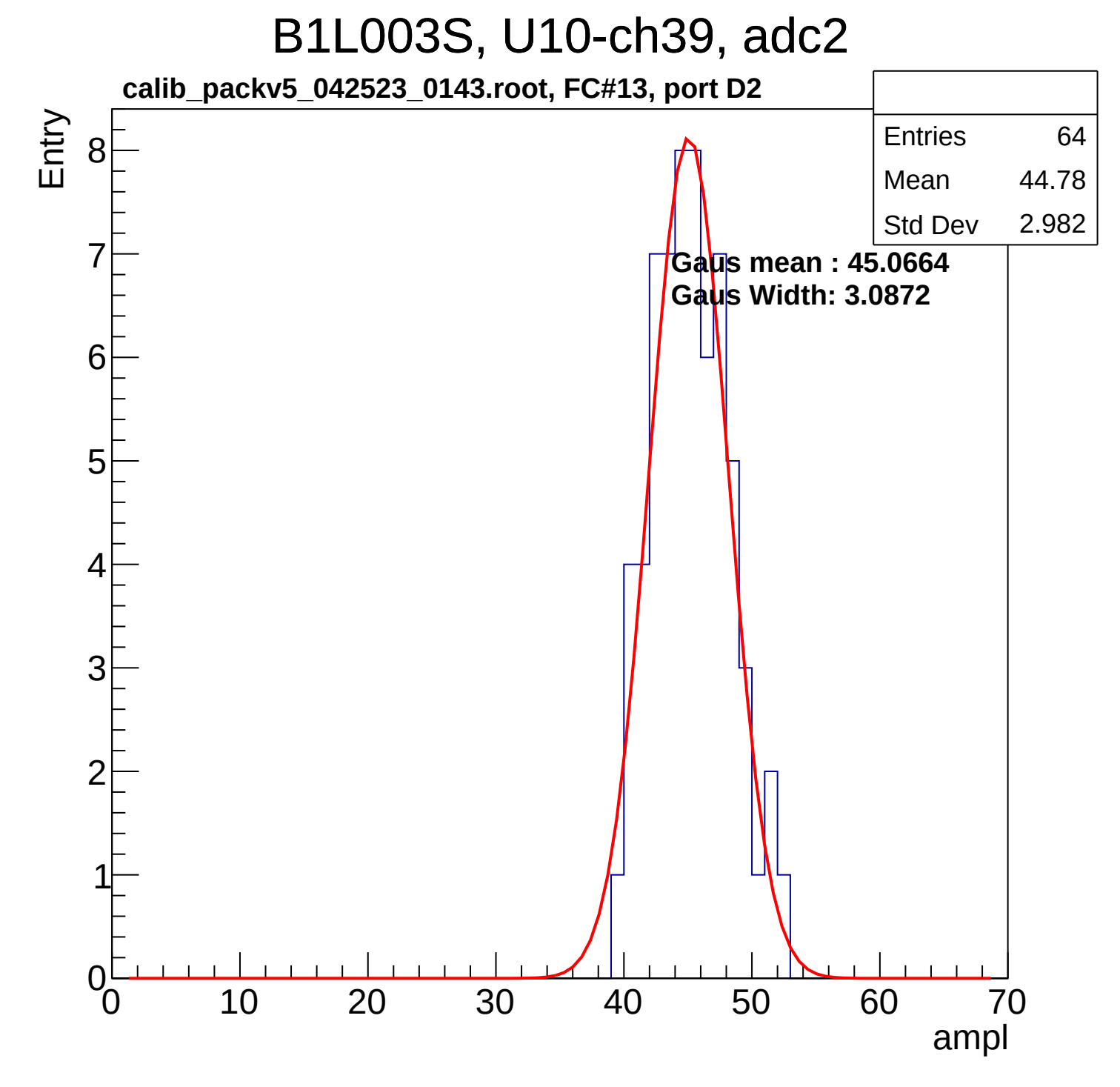
B1L003S, U10-ch39, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 44.78
Std Dev 2.982
Gaus mean : 45.0664
Gaus Width: 3.0872
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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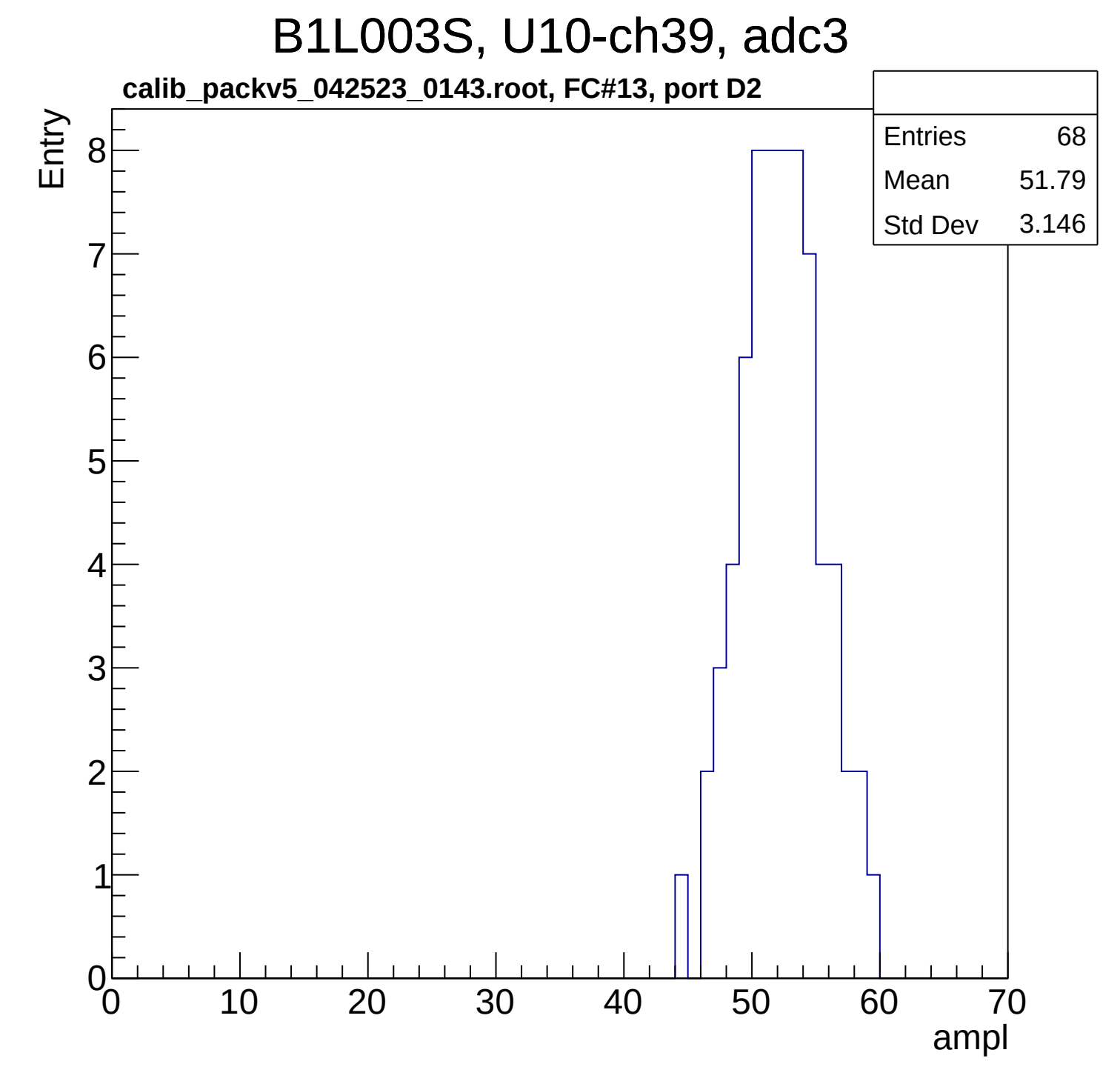
B1L003S, U10-ch39, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 51.79
Std Dev 3.146
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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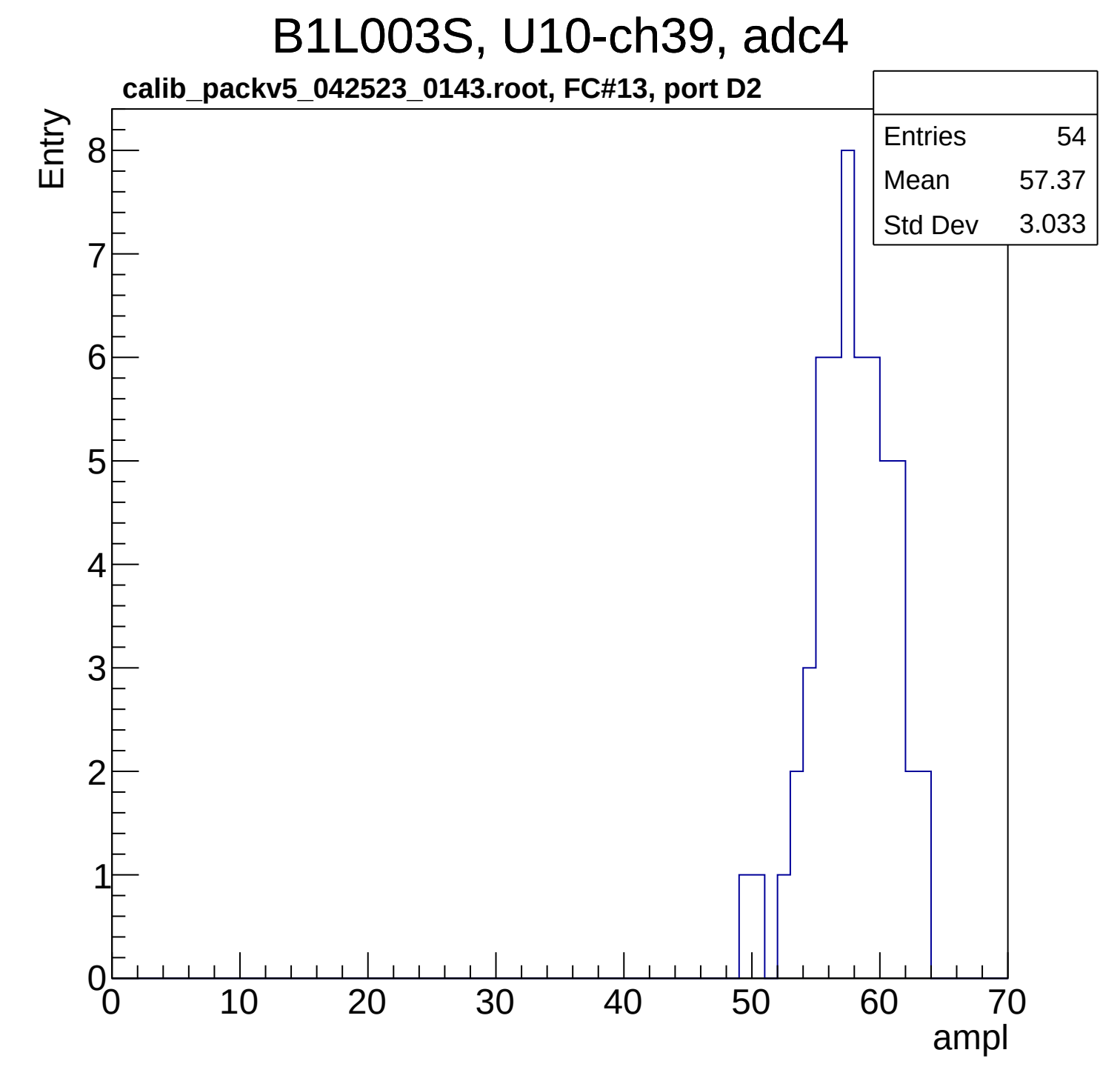
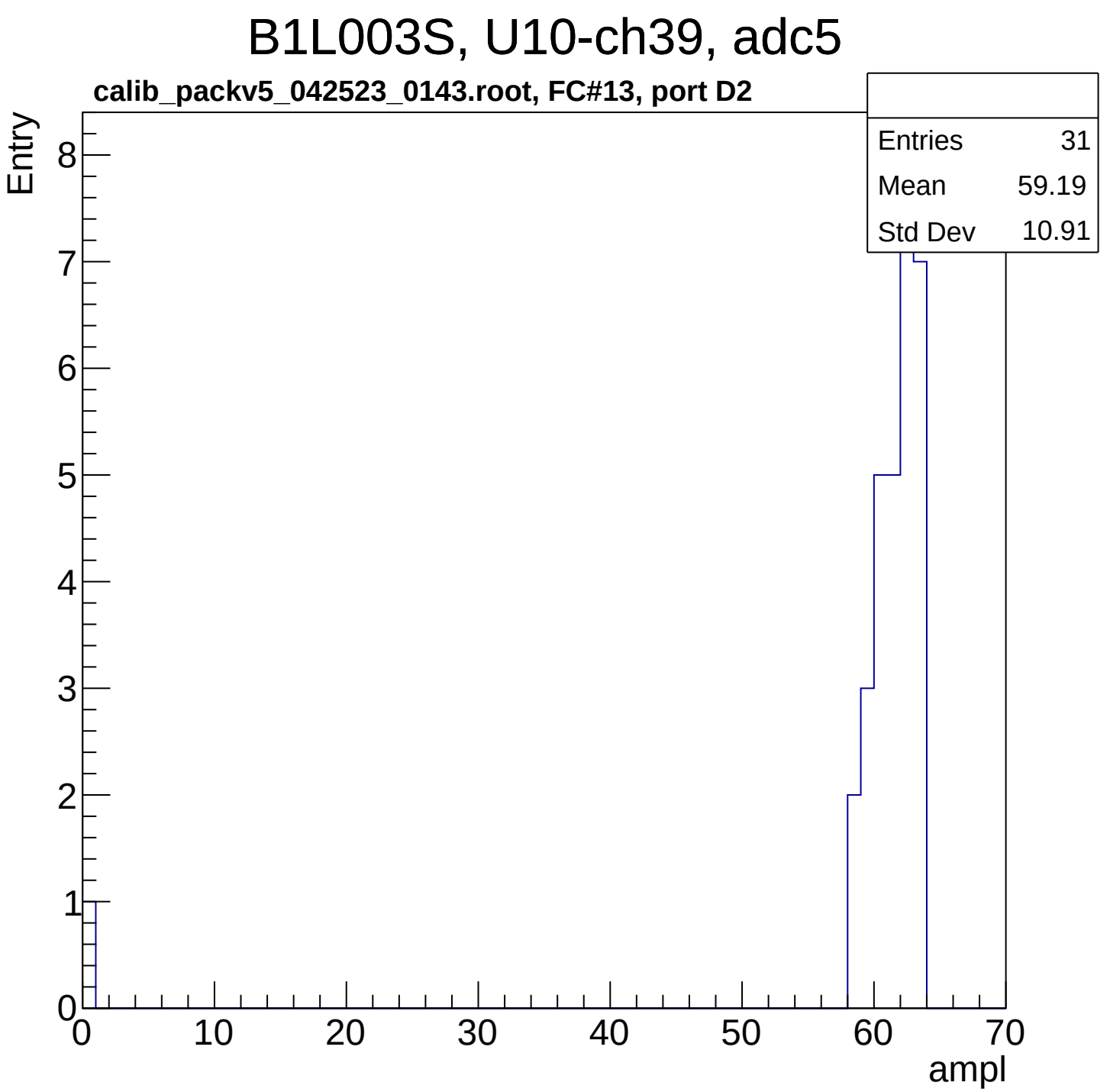
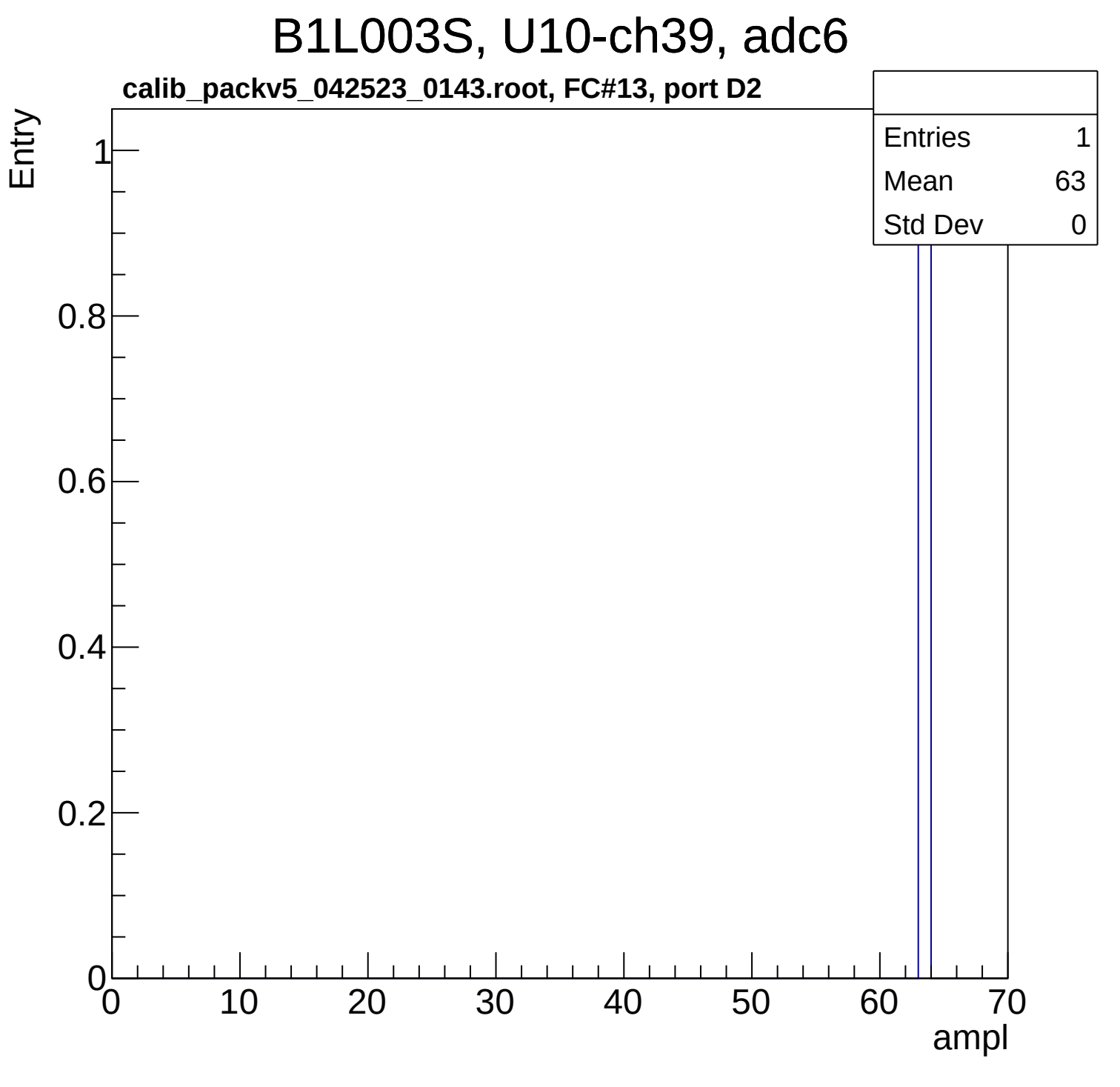
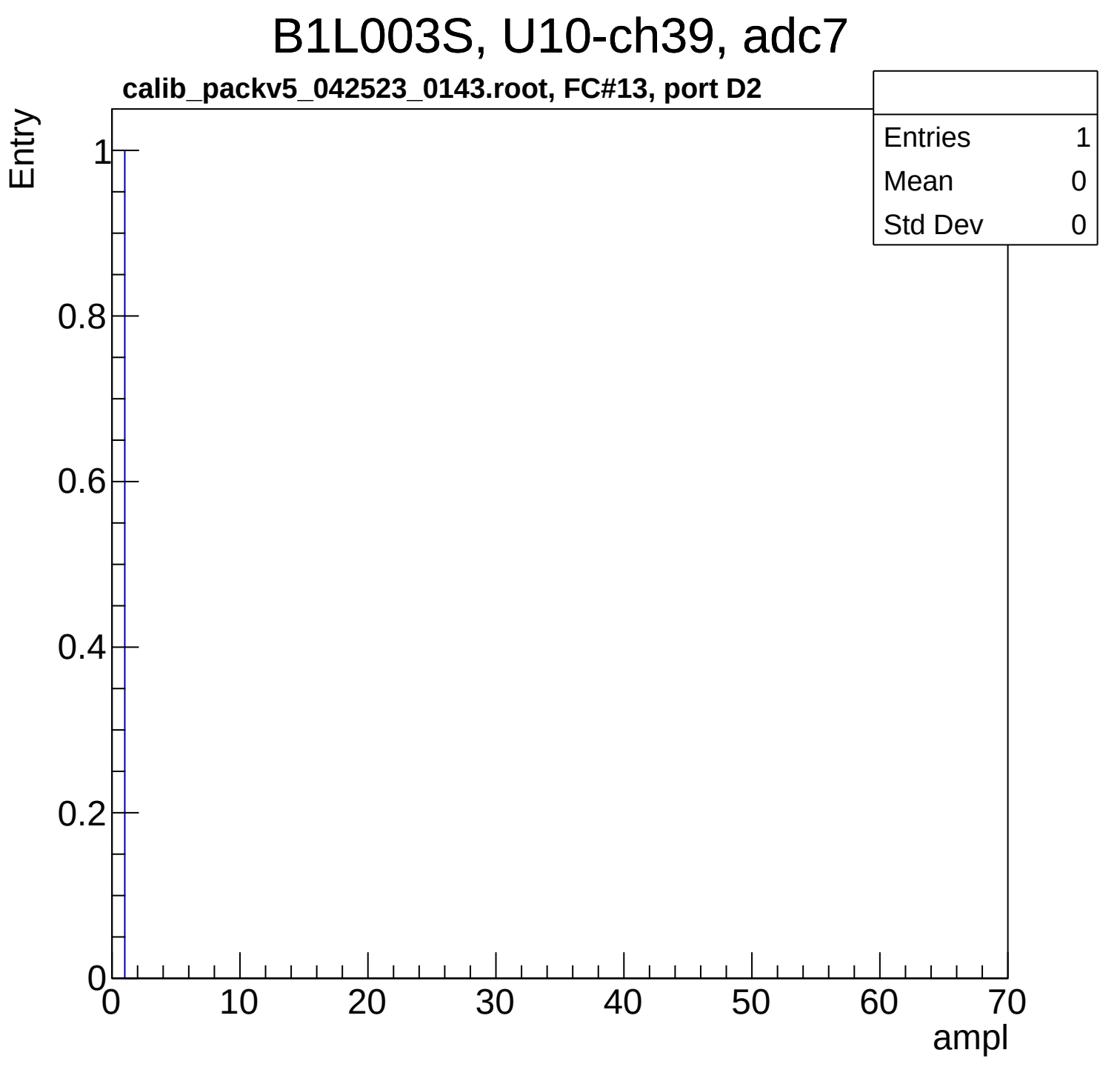
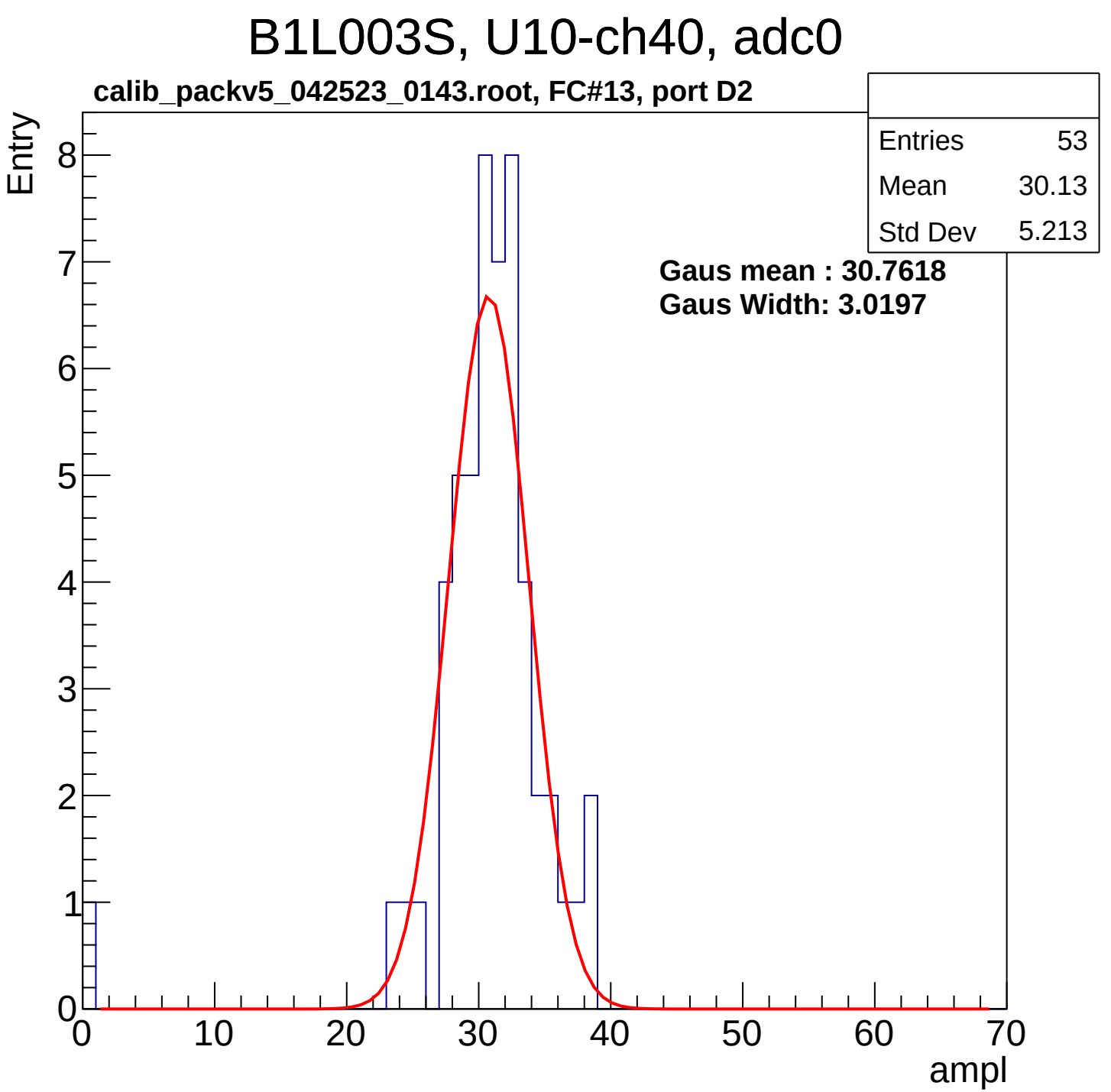
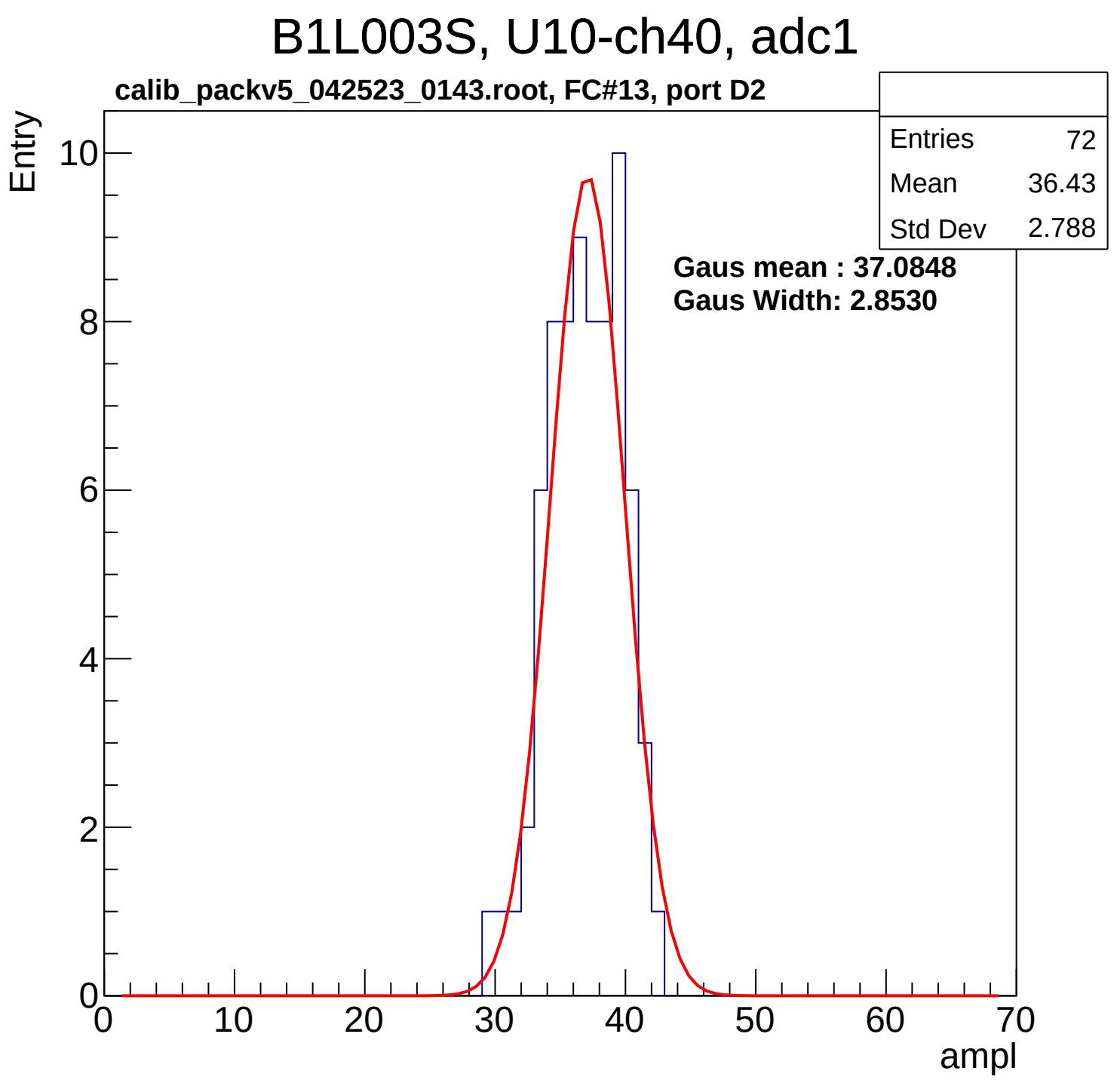

B1L003S, U10-ch39, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 57.37
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

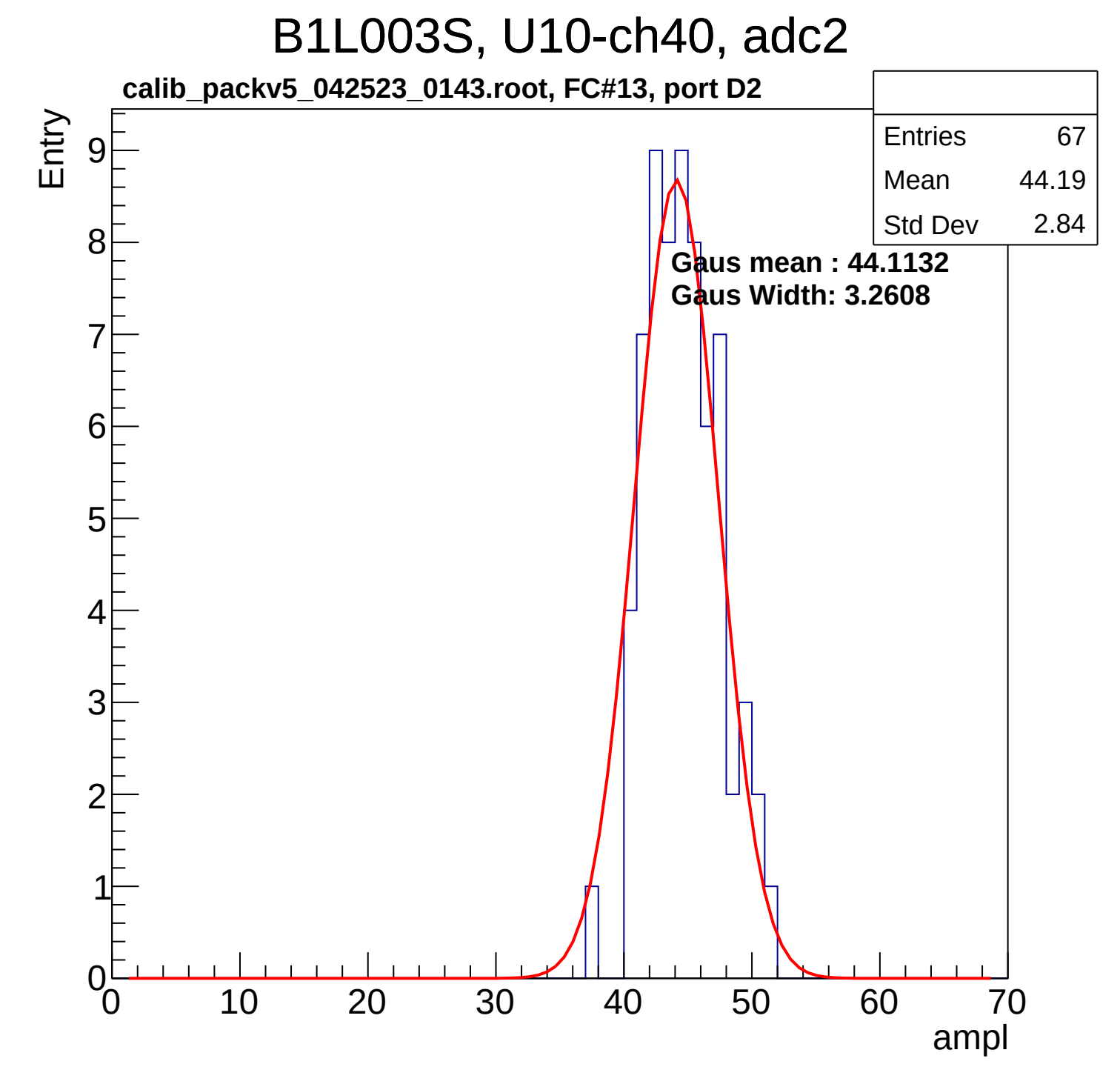

B1L003S, U10-ch39, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 31
Mean 59.19
Std Dev 10.91
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch39, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch39, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch40, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 30.13
Std Dev 5.213
Gaus mean : 30.7618
Gaus Width: 3.0197
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch40, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.43
Std Dev 2.788
Gaus mean : 37.0848
Gaus Width: 2.8530
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch40, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 44.19
Std Dev 2.84
Gaus mean : 44.1132
Gaus Width: 3.2608
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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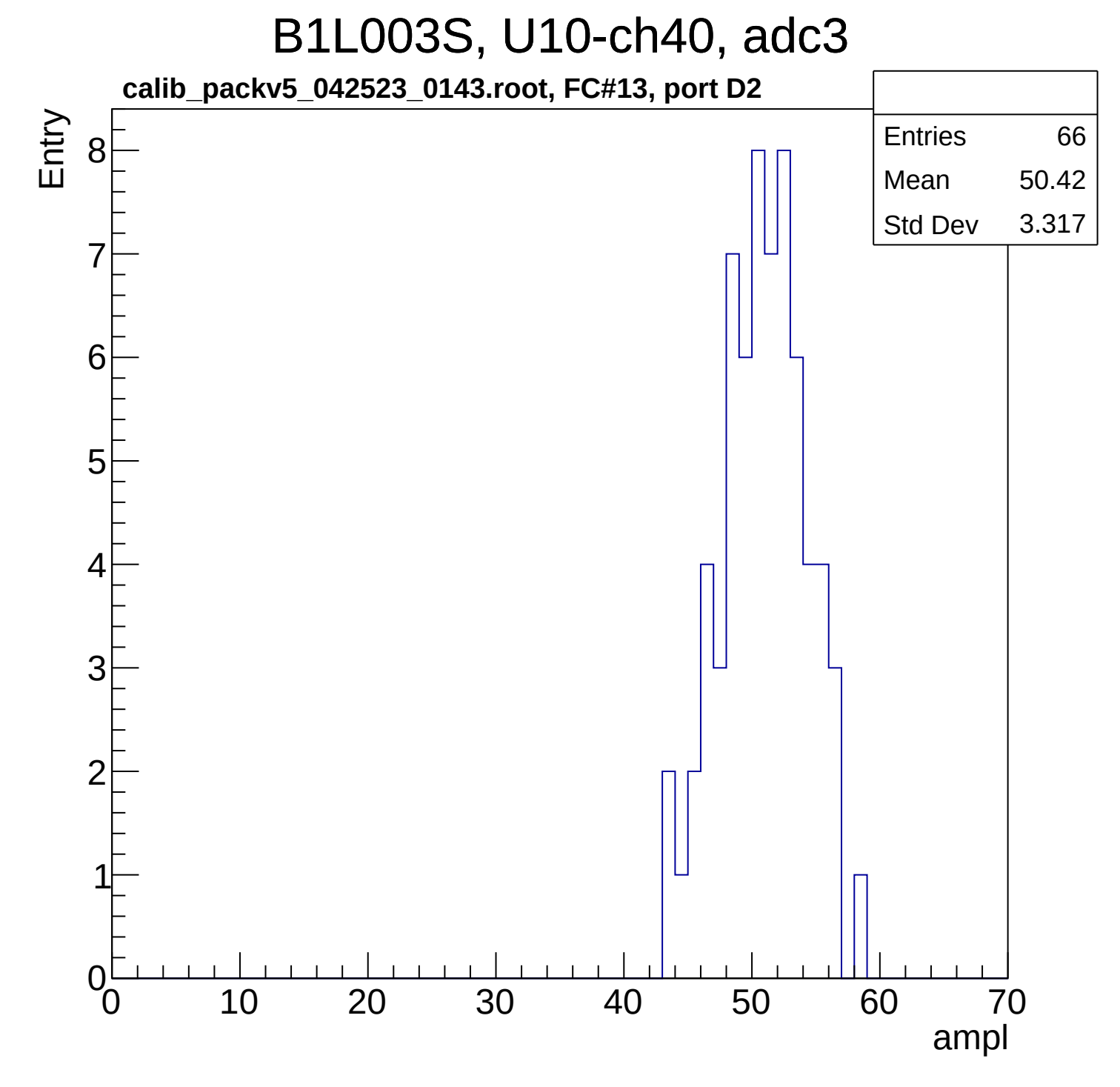
B1L003S, U10-ch40, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 50.42
Std Dev 3.317
Entry
ampl
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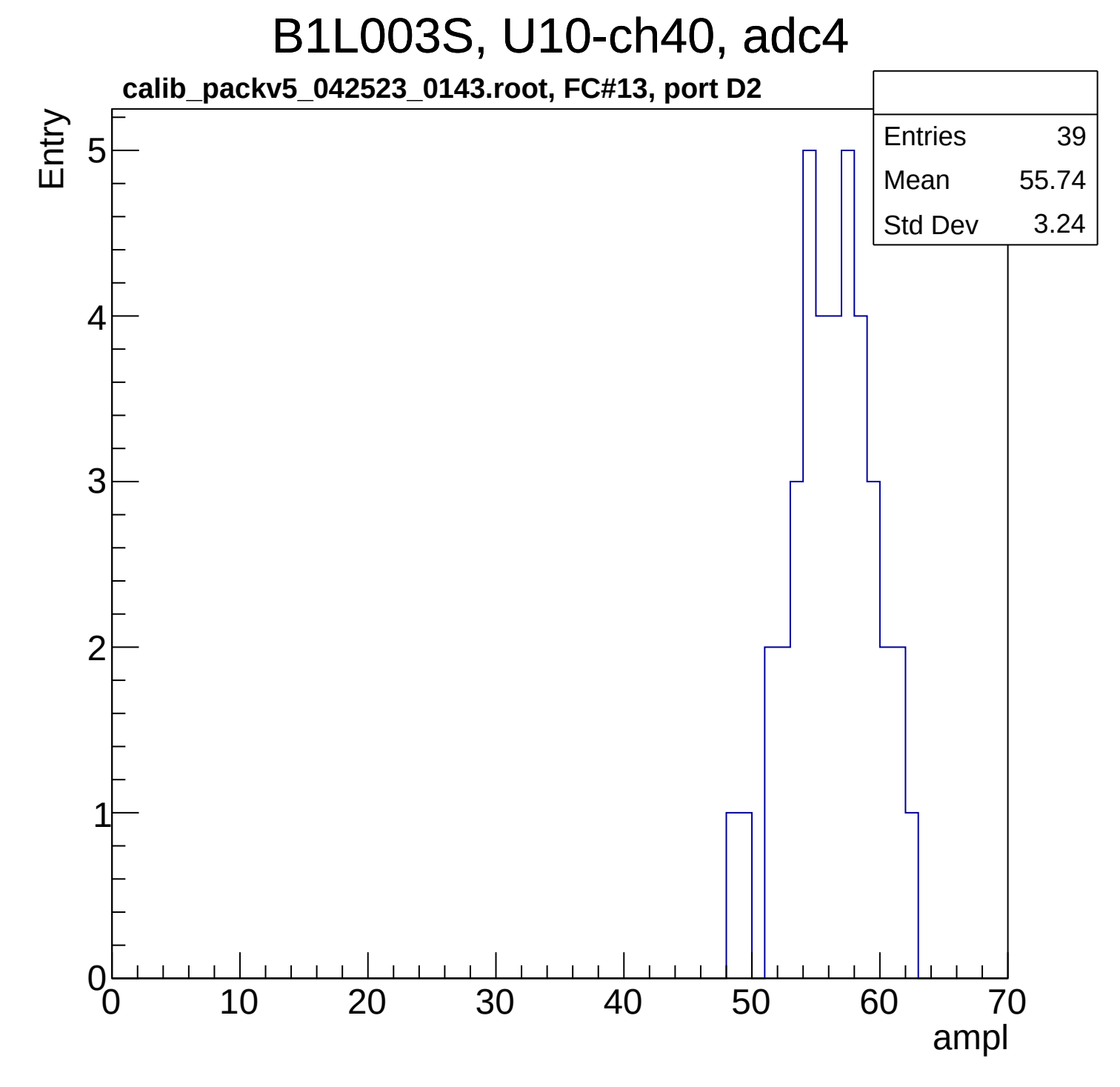
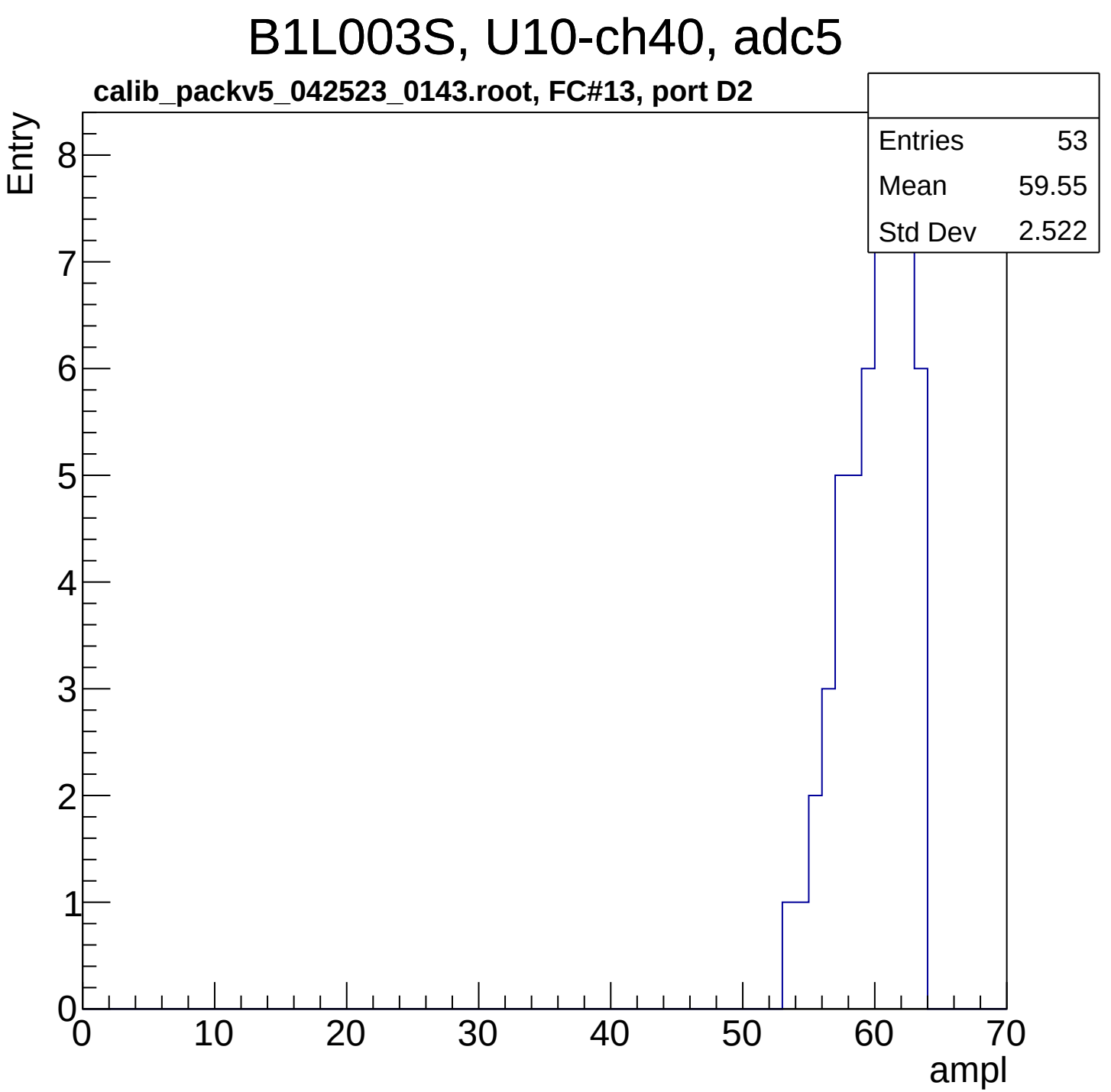
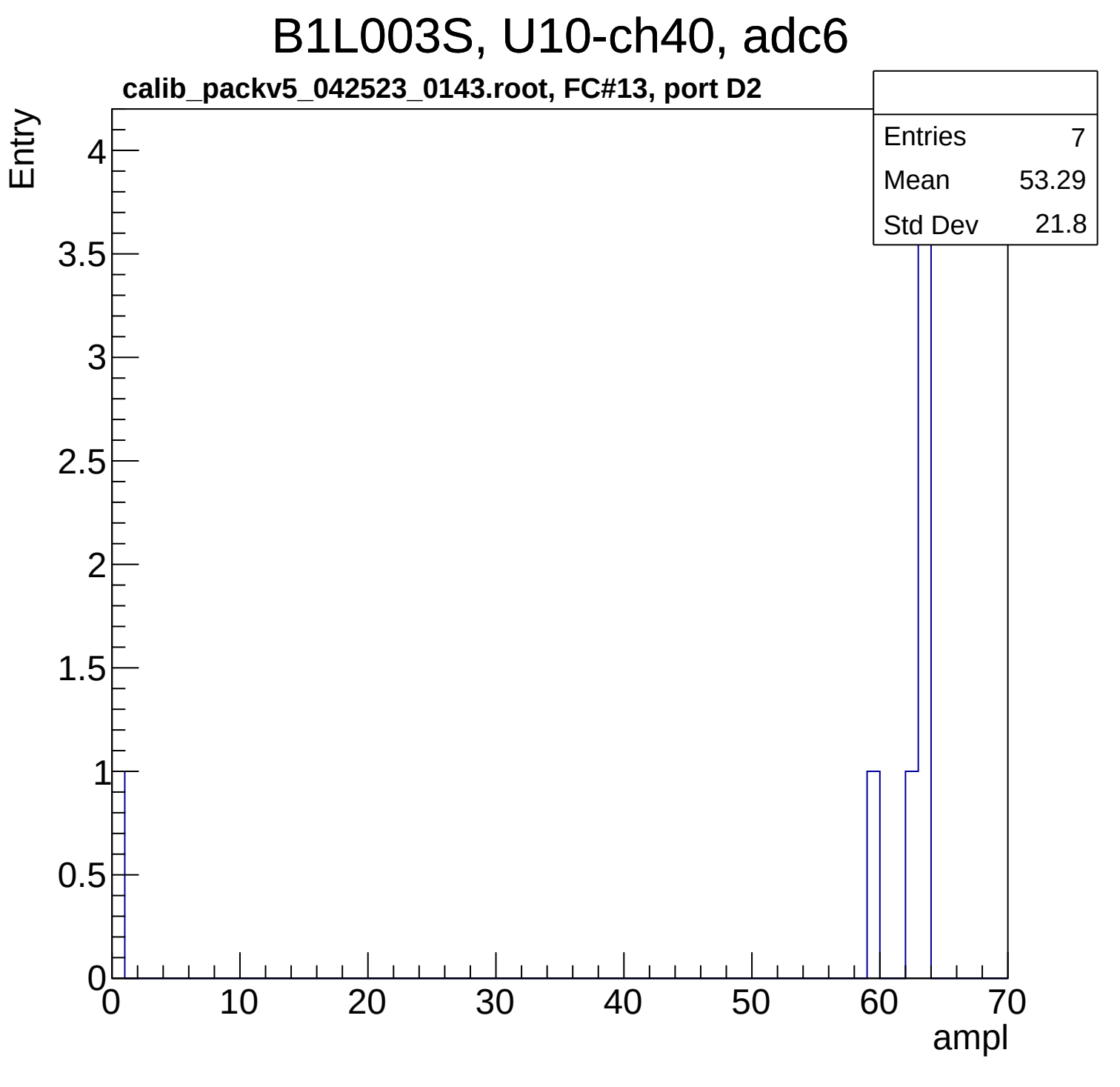
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch40, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 55.74
Std Dev 3.24
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch40, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.55
Std Dev 2.522
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch40, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.29
Std Dev 21.8
Entry
ampl
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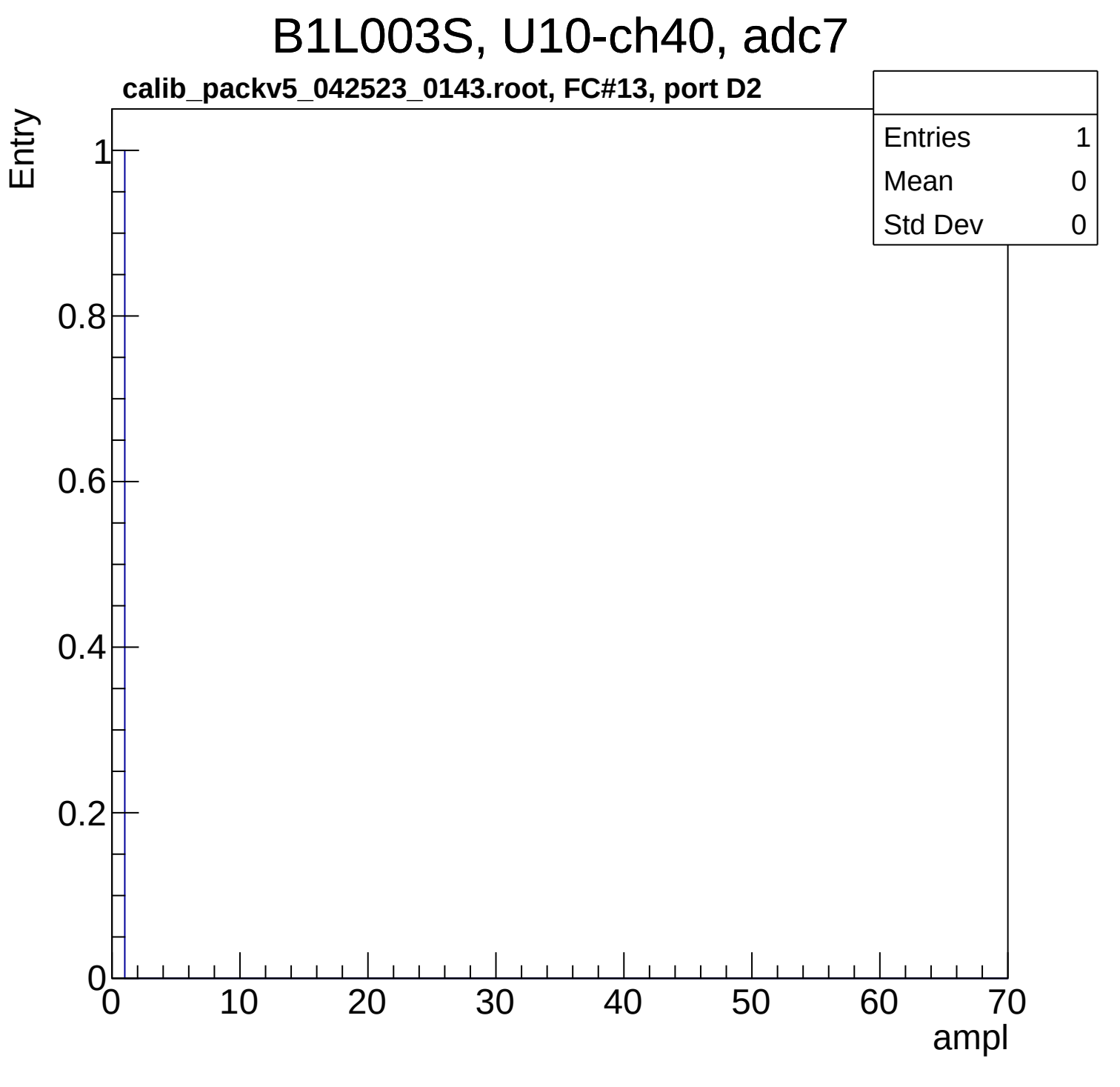
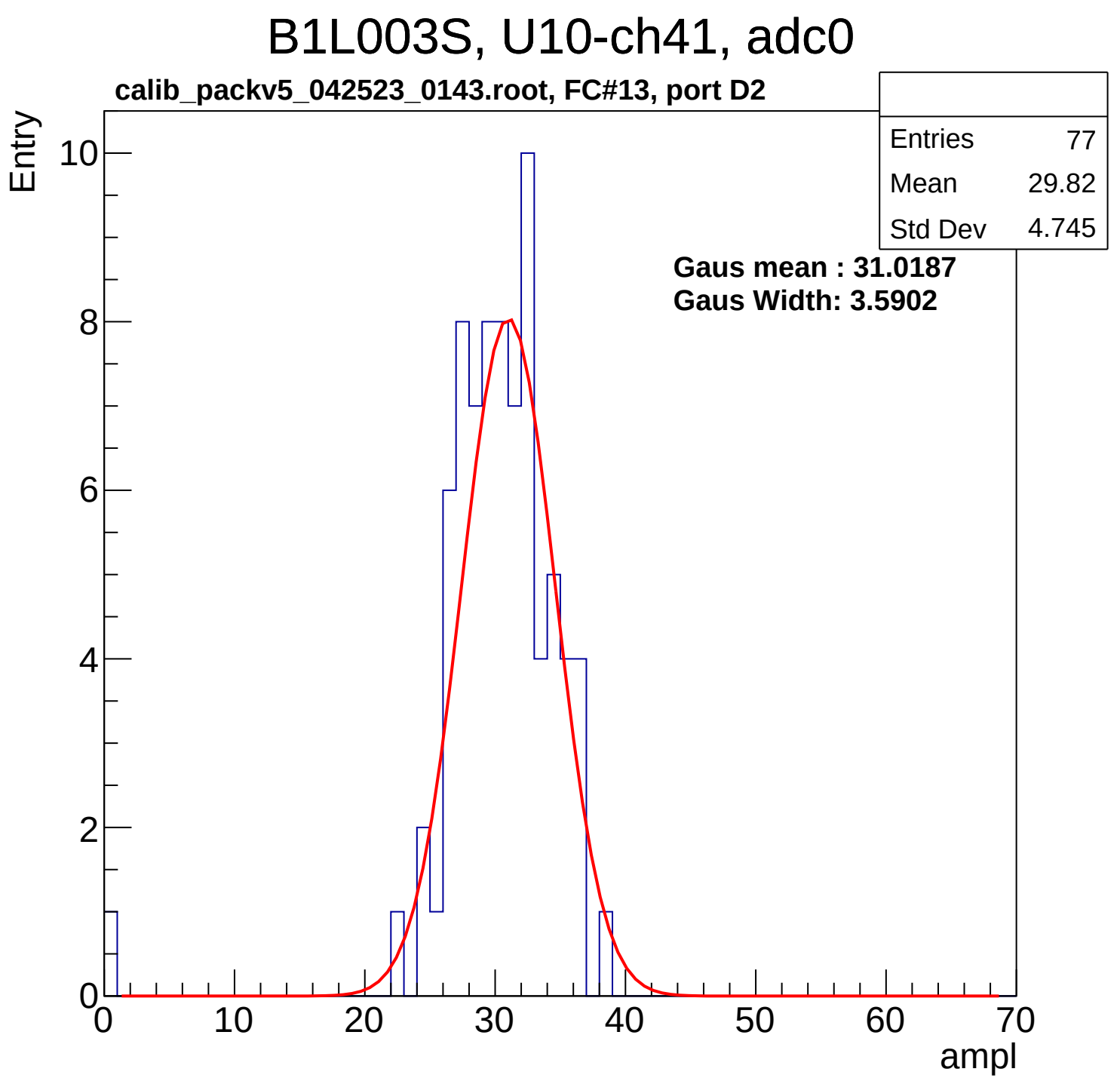
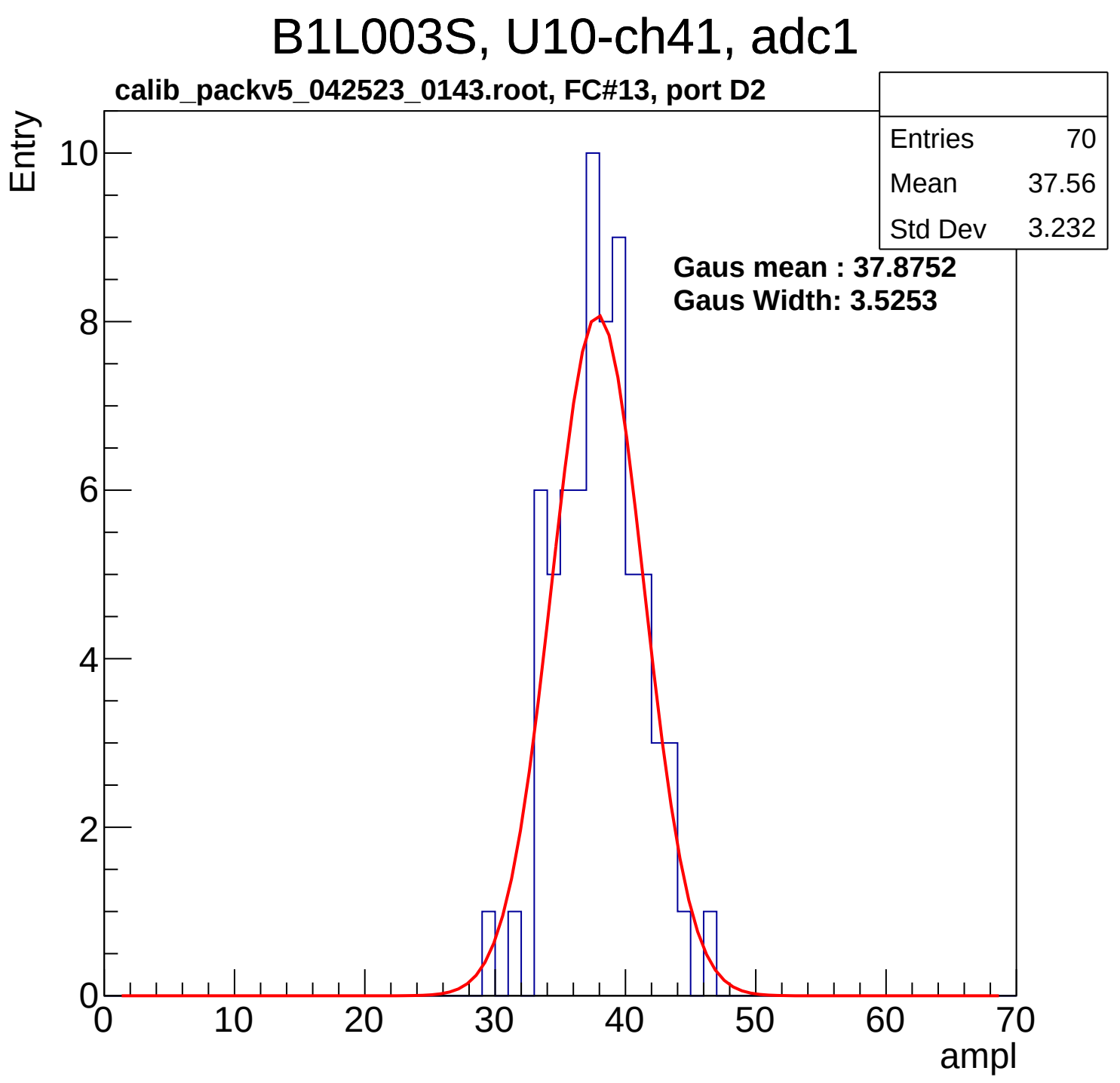
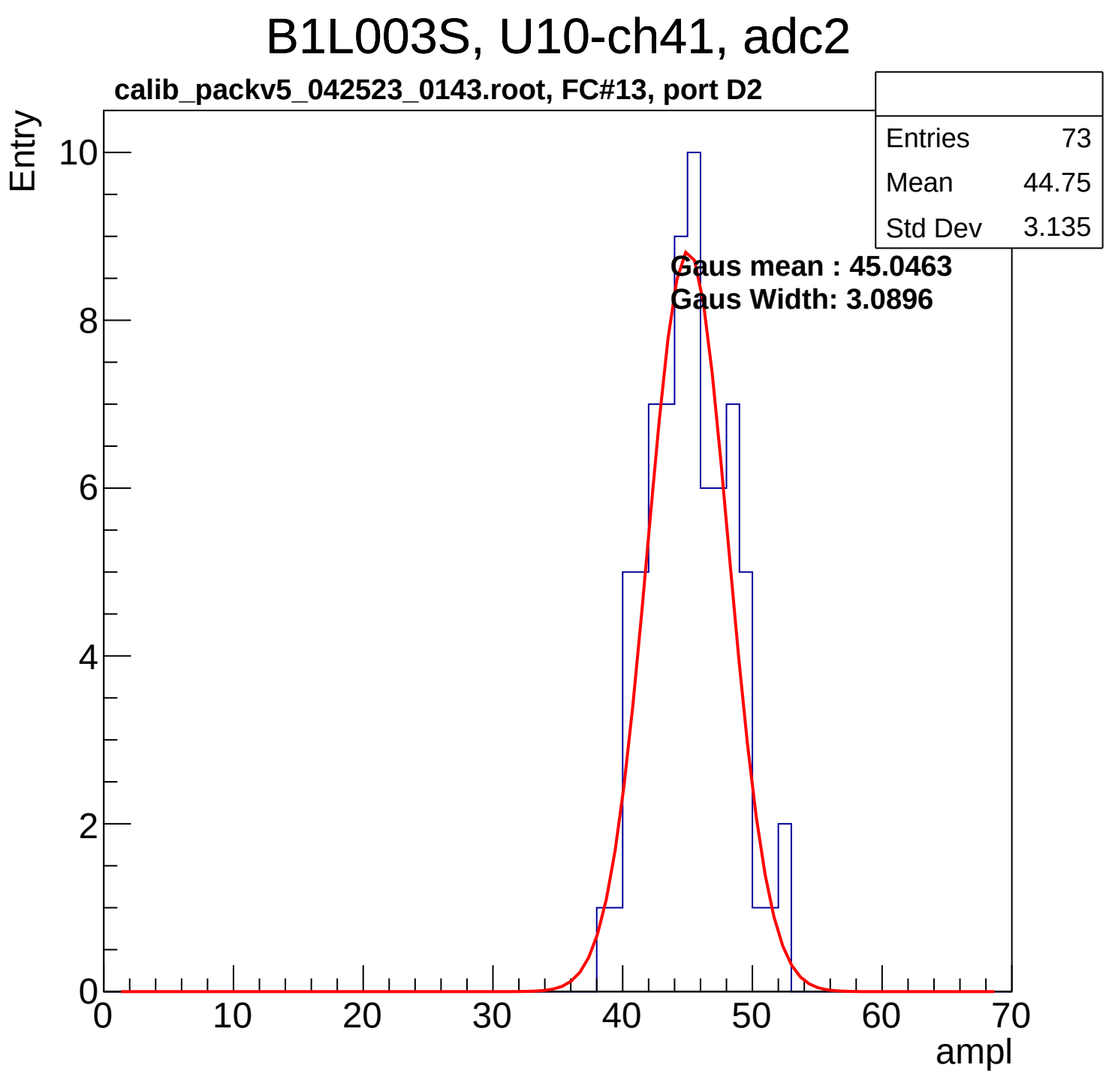
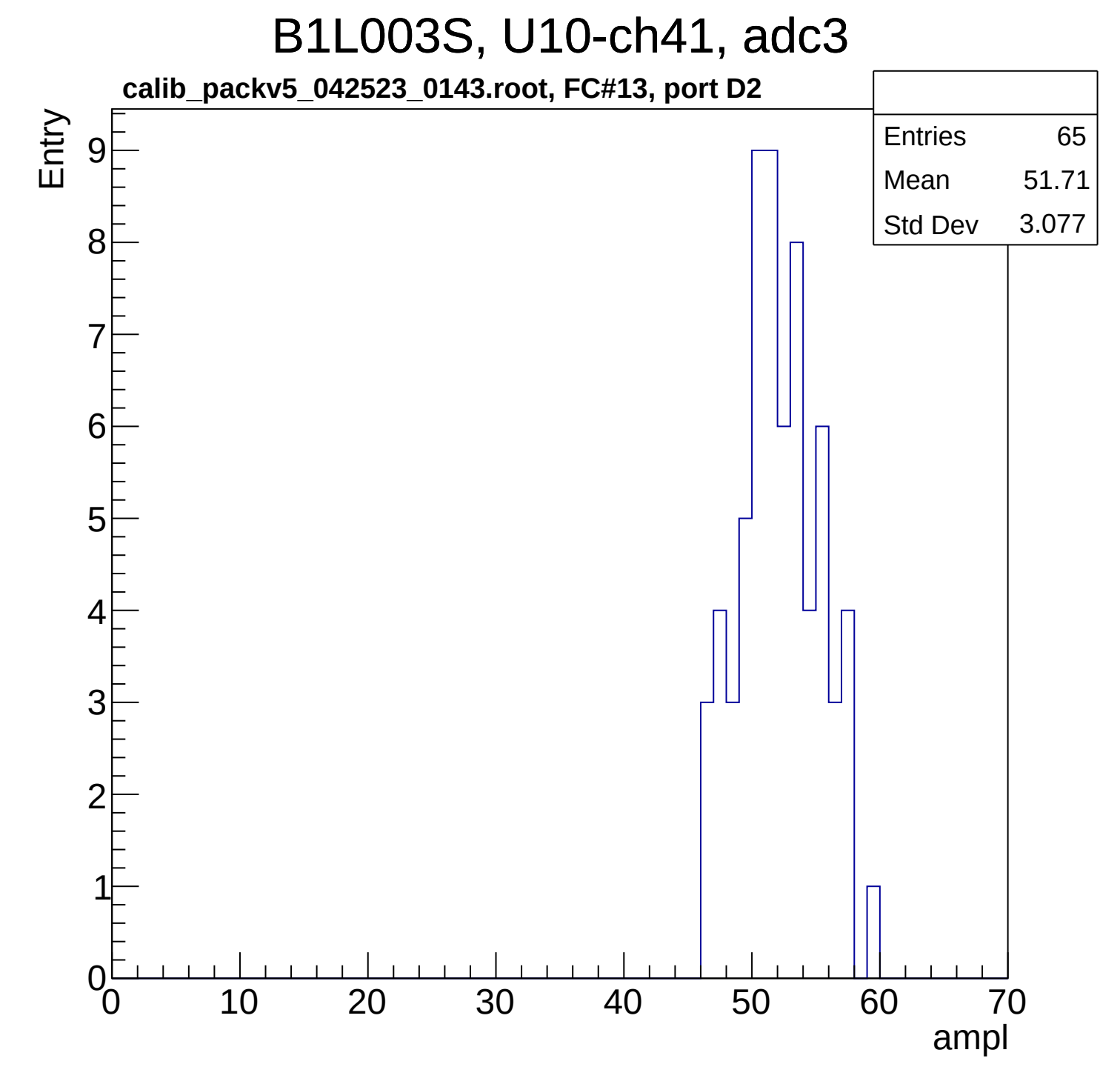
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch40, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch41, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 29.82
Std Dev 4.745
Gaus mean : 31.0187
Gaus Width: 3.5902
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch41, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 37.56
Std Dev 3.232
Gaus mean : 37.8752
Gaus Width: 3.5253
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch41, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 44.75
Std Dev 3.135
Gaus mean : 45.0463
Gaus Width: 3.0896
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch41, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 51.71
Std Dev 3.077
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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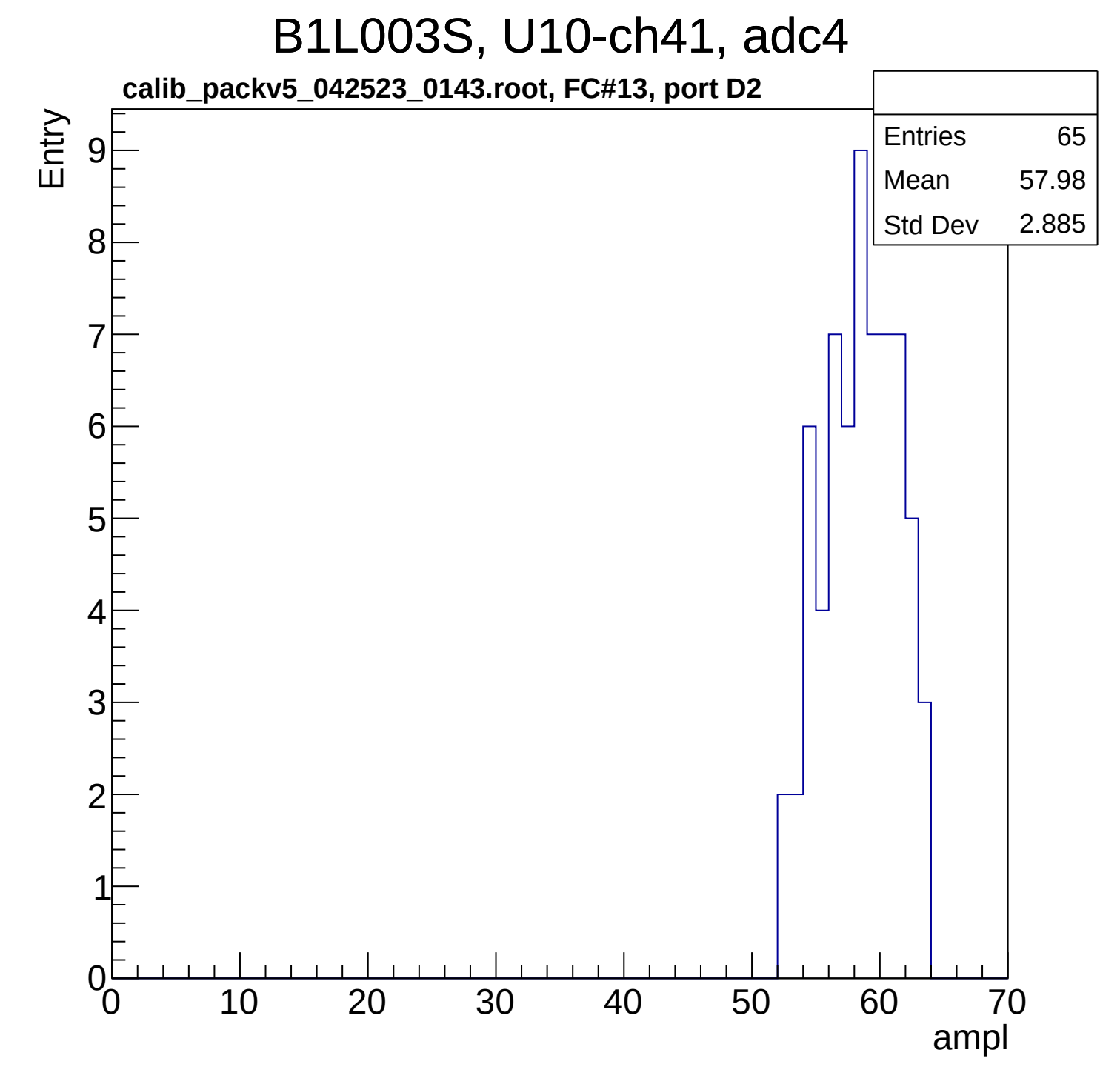
B1L003S, U10-ch41, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 57.98
Std Dev 2.885
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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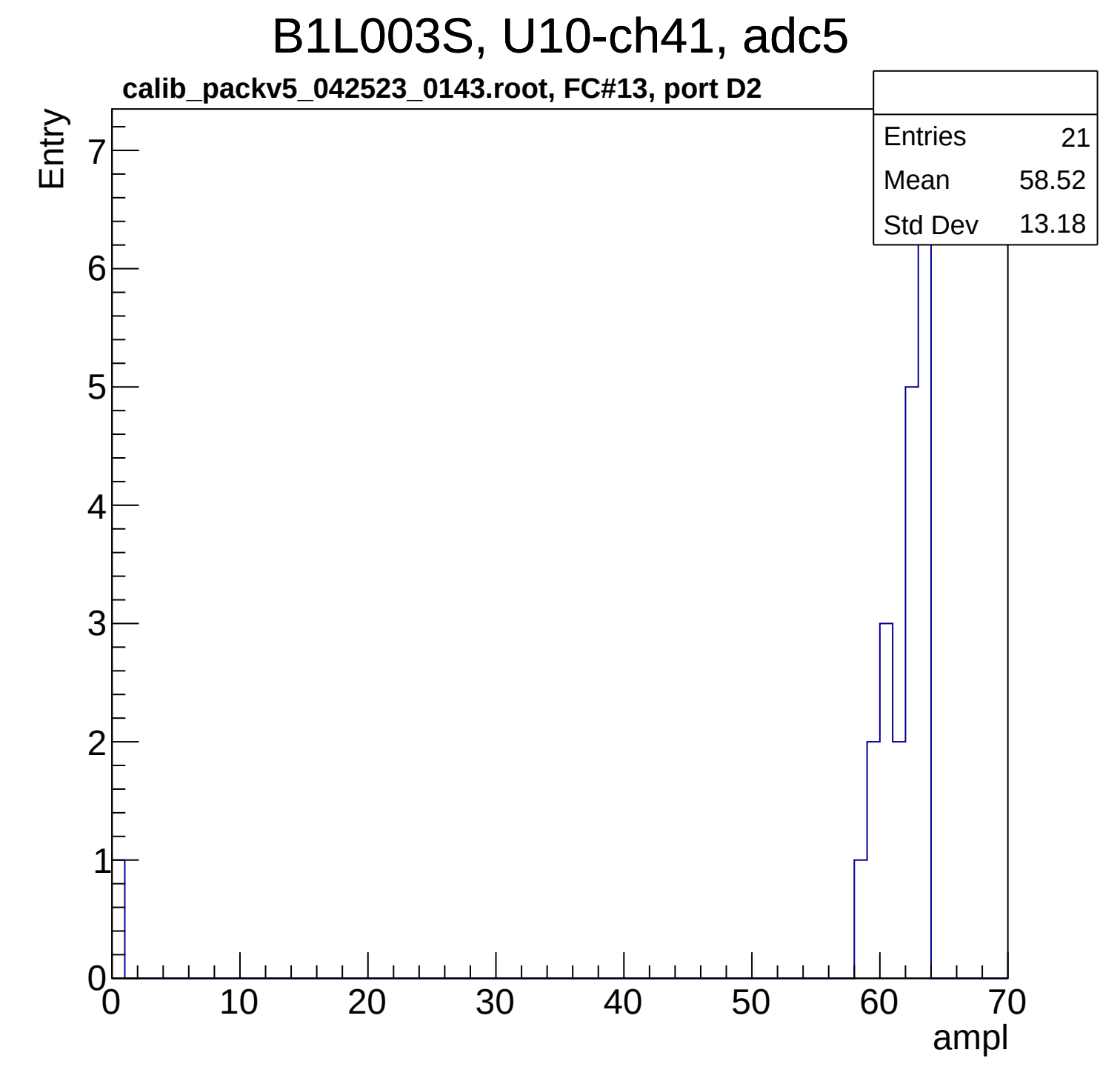
B1L003S, U10-ch41, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 58.52
Std Dev 13.18
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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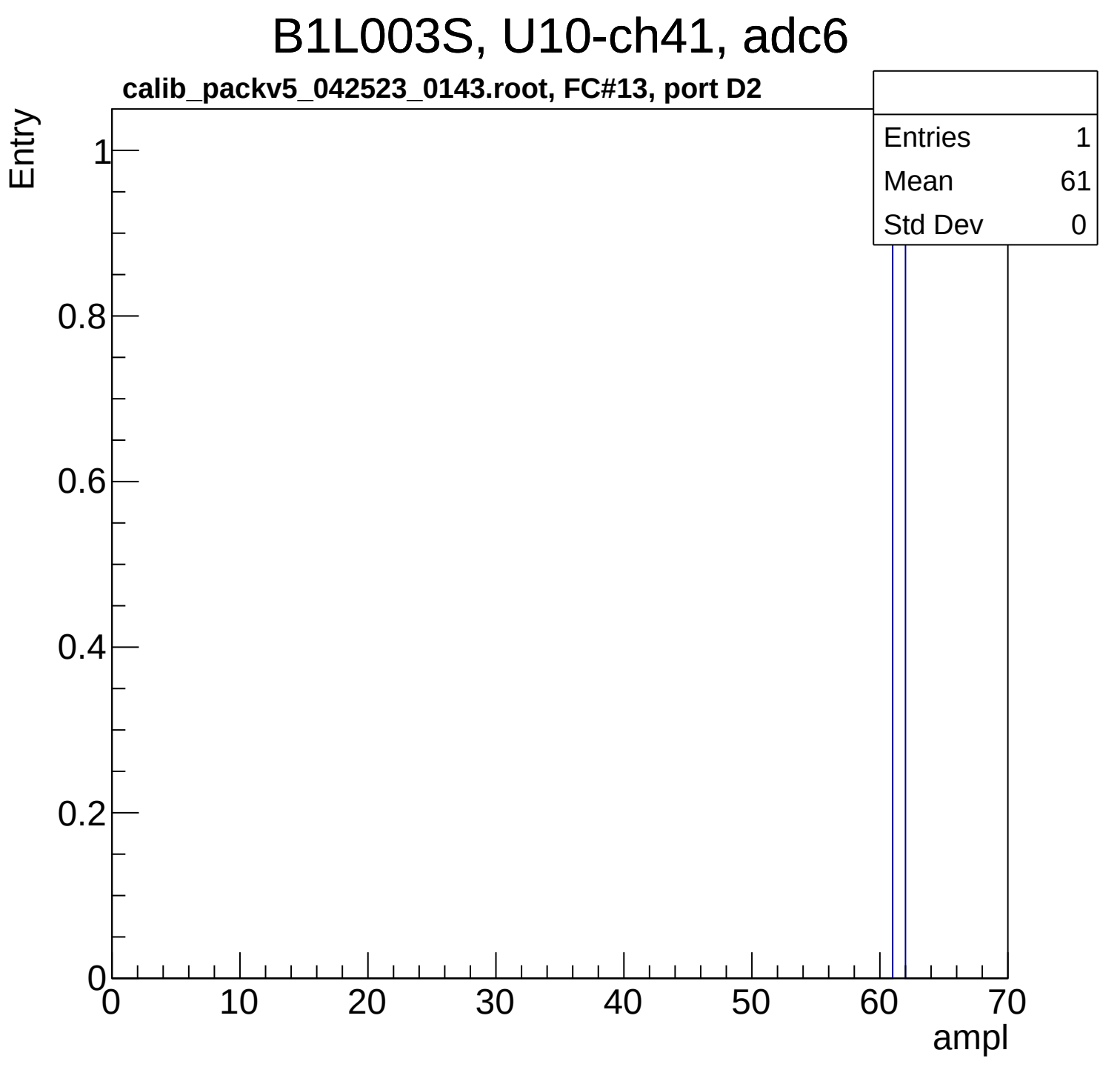
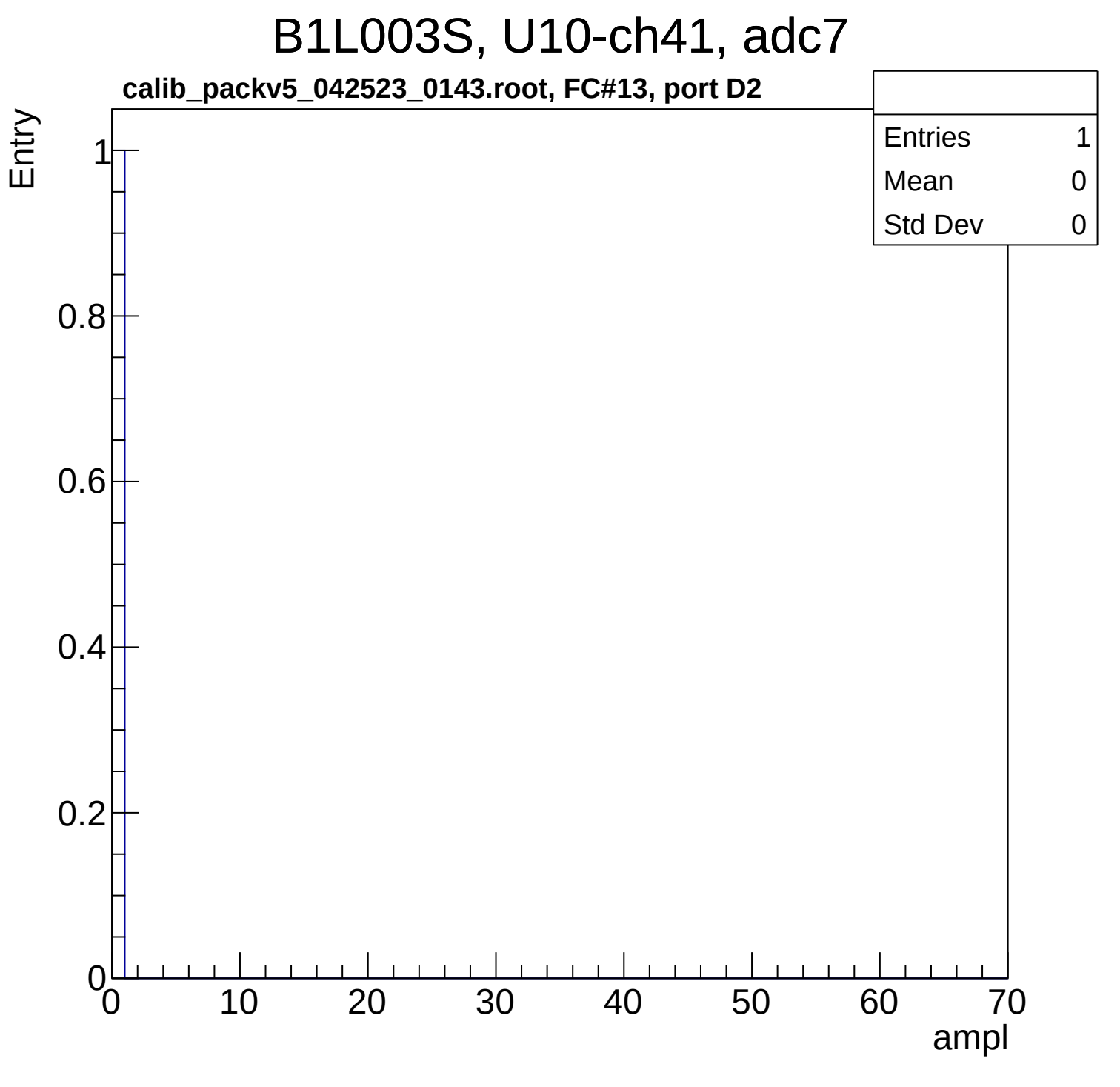

B1L003S, U10-ch41, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch41, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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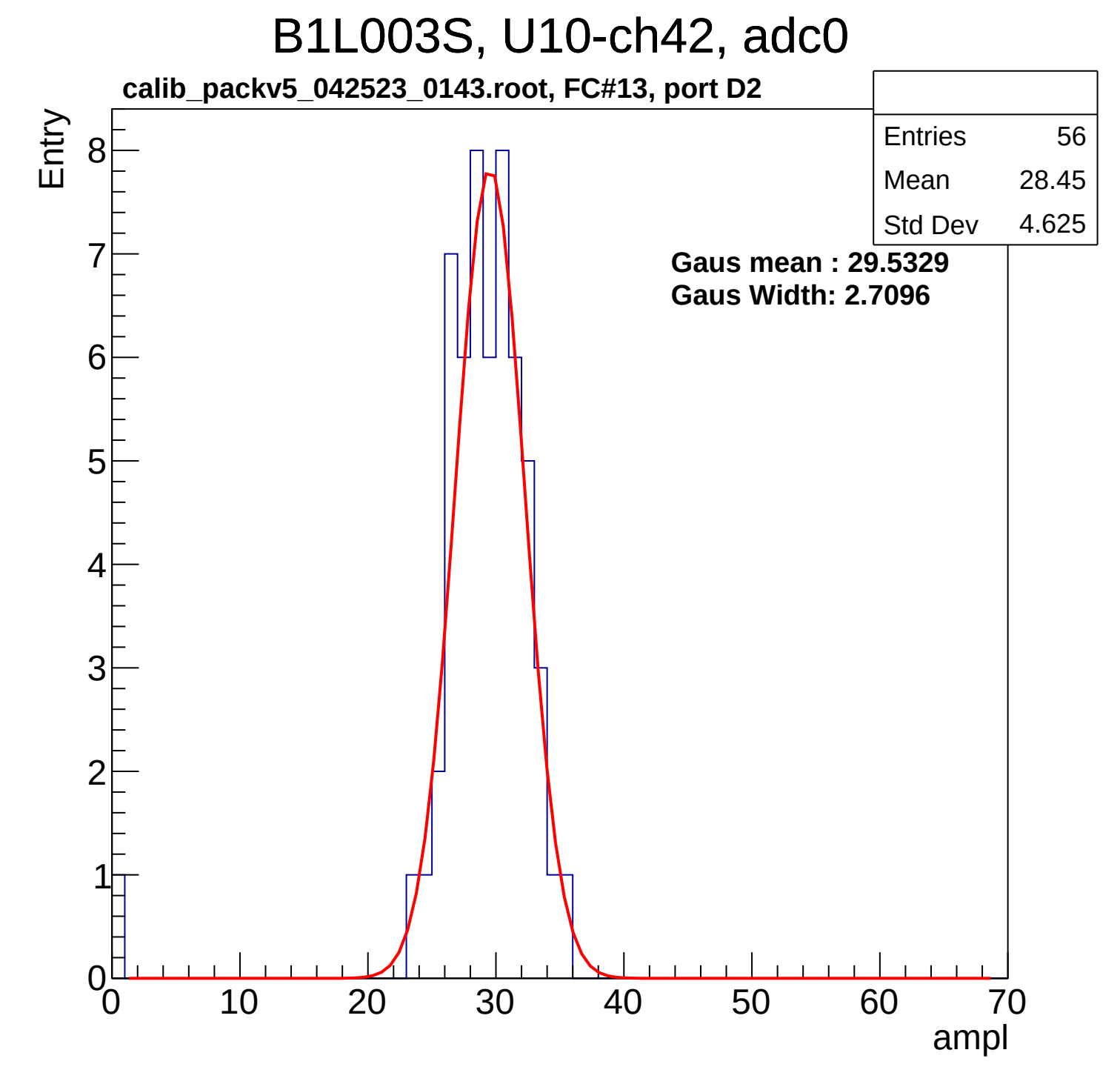
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch42, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 28.45
Std Dev 4.625
Gaus mean : 29.5329
Gaus Width: 2.7096
Entry
ampl
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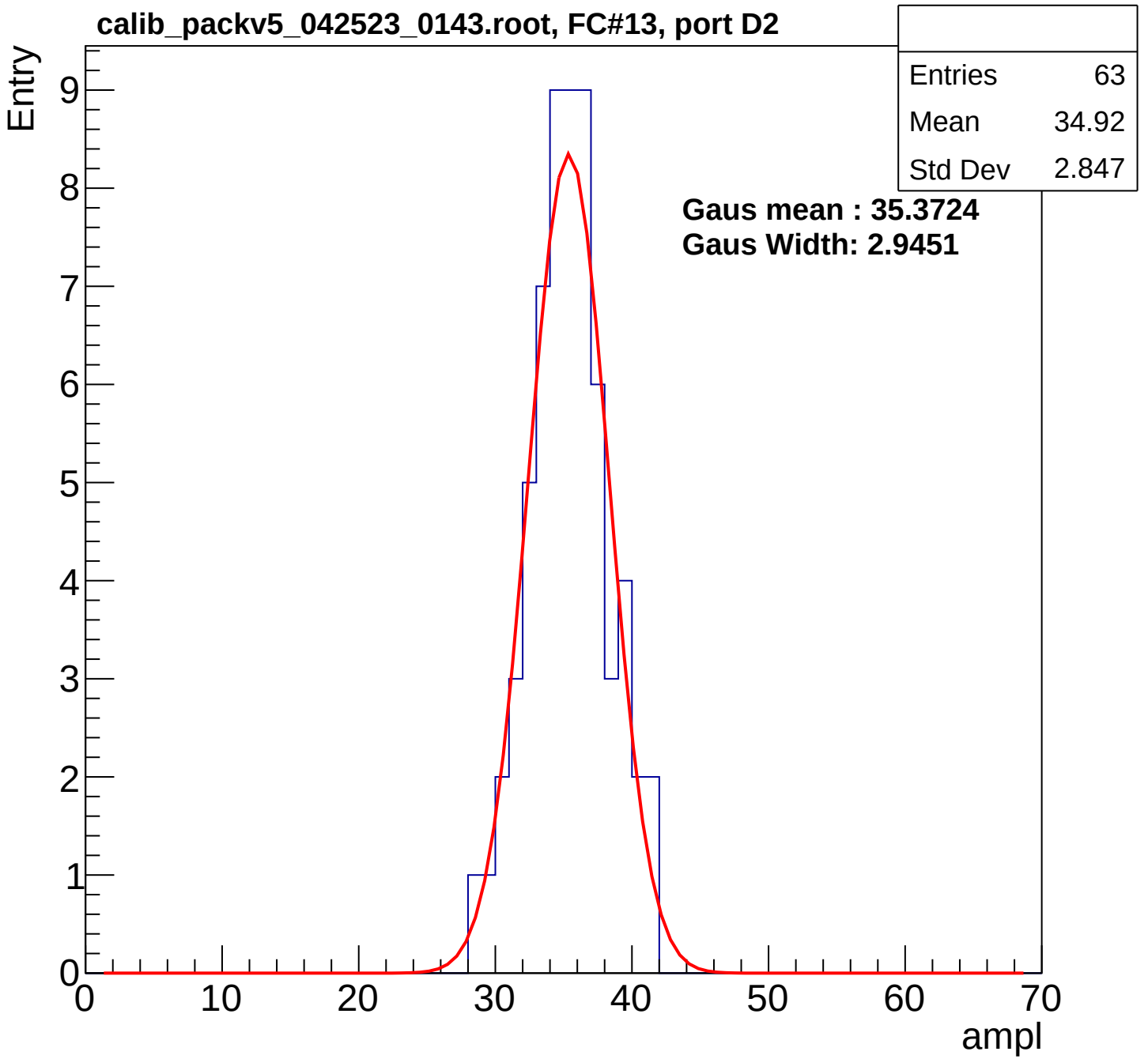
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch42, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 34.92
Std Dev 2.847
Gaus mean : 35.3724
Gaus Width: 2.9451
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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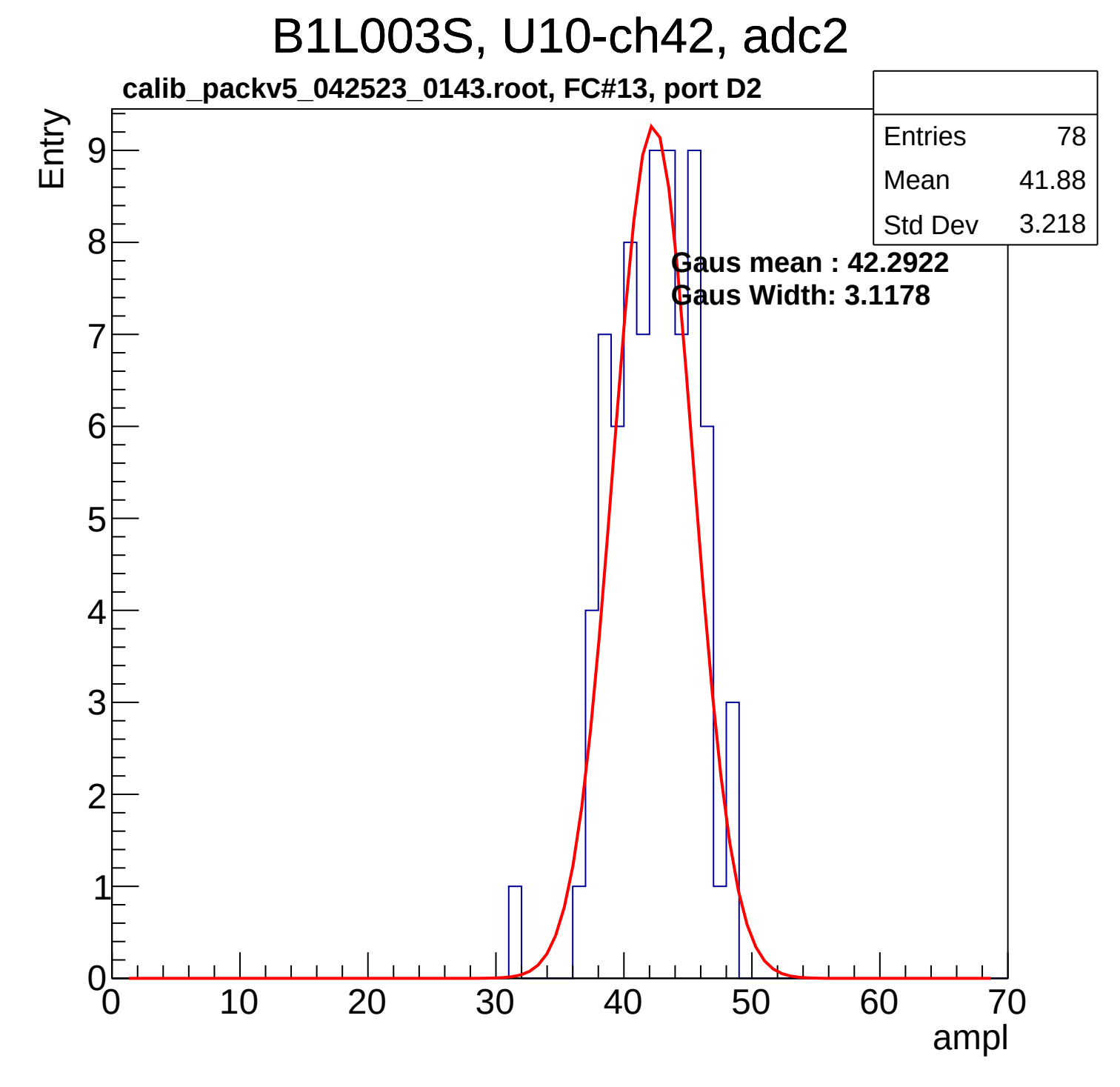
B1L003S, U10-ch42, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 41.88
Std Dev 3.218
Gaus mean : 42.2922
Gaus Width: 3.1178
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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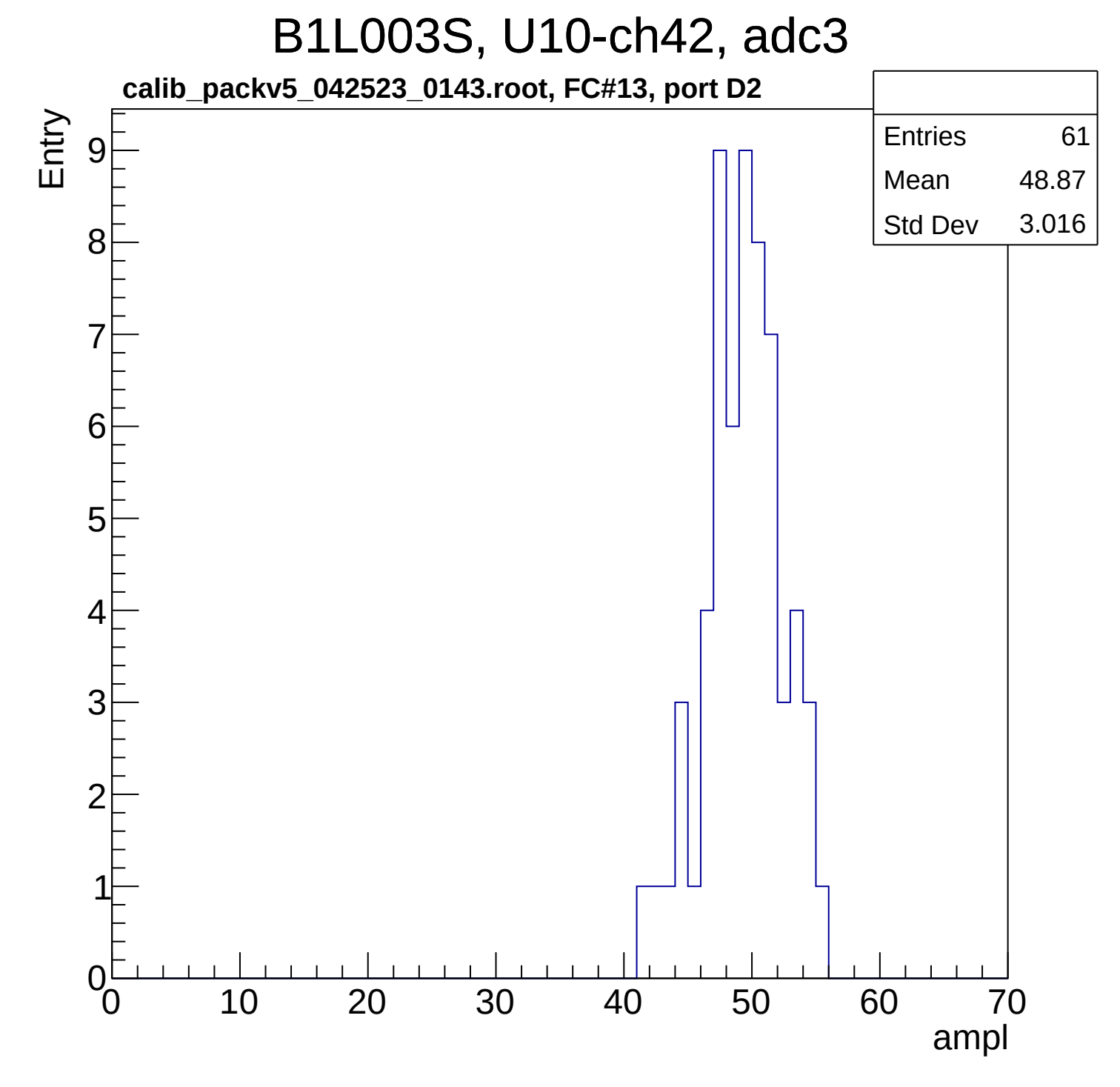
B1L003S, U10-ch42, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.87
Std Dev 3.016
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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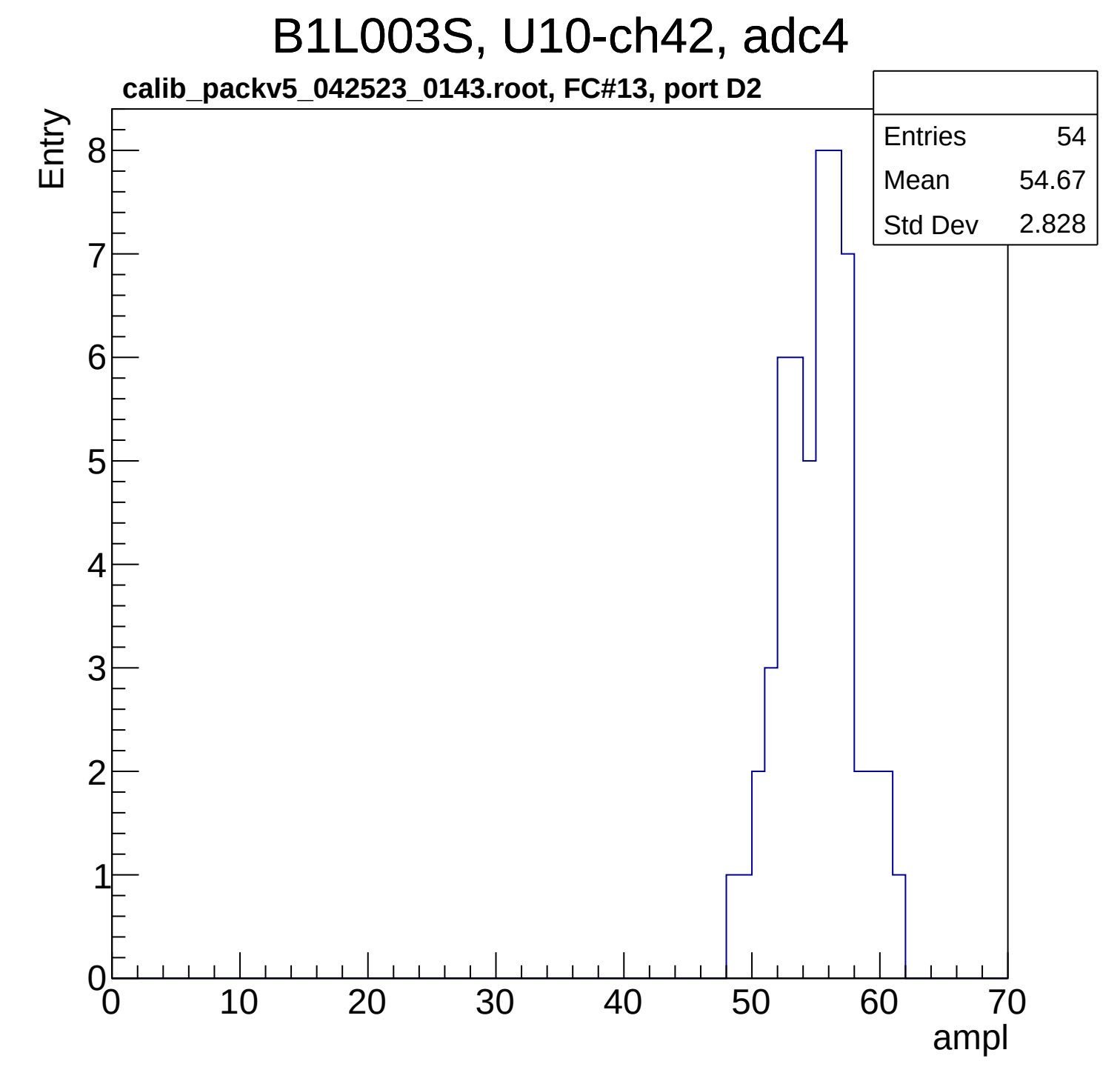
40
50
60
70


B1L003S, U10-ch42, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.67
Std Dev 2.828
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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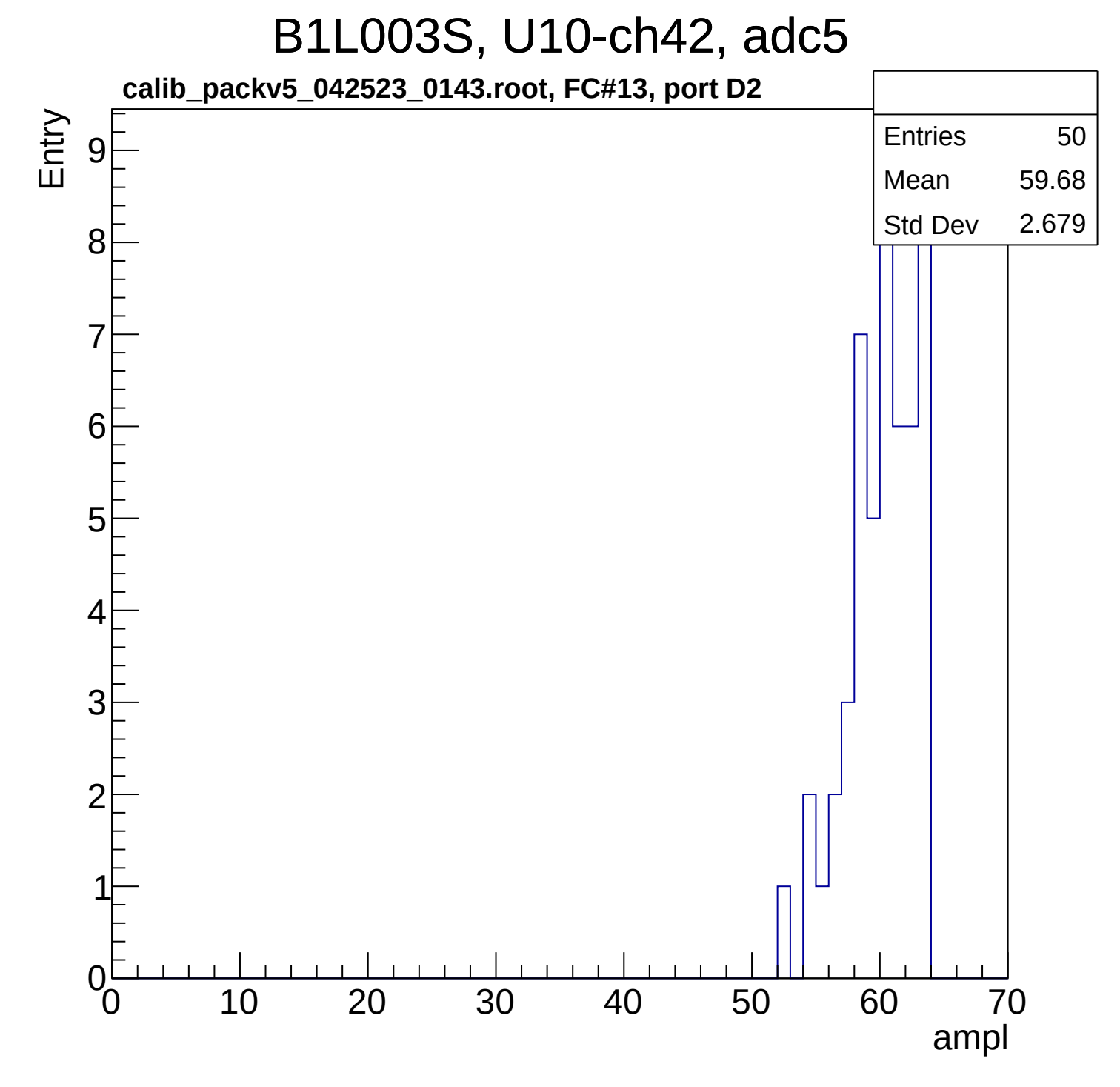
B1L003S, U10-ch42, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.68
Std Dev 2.679
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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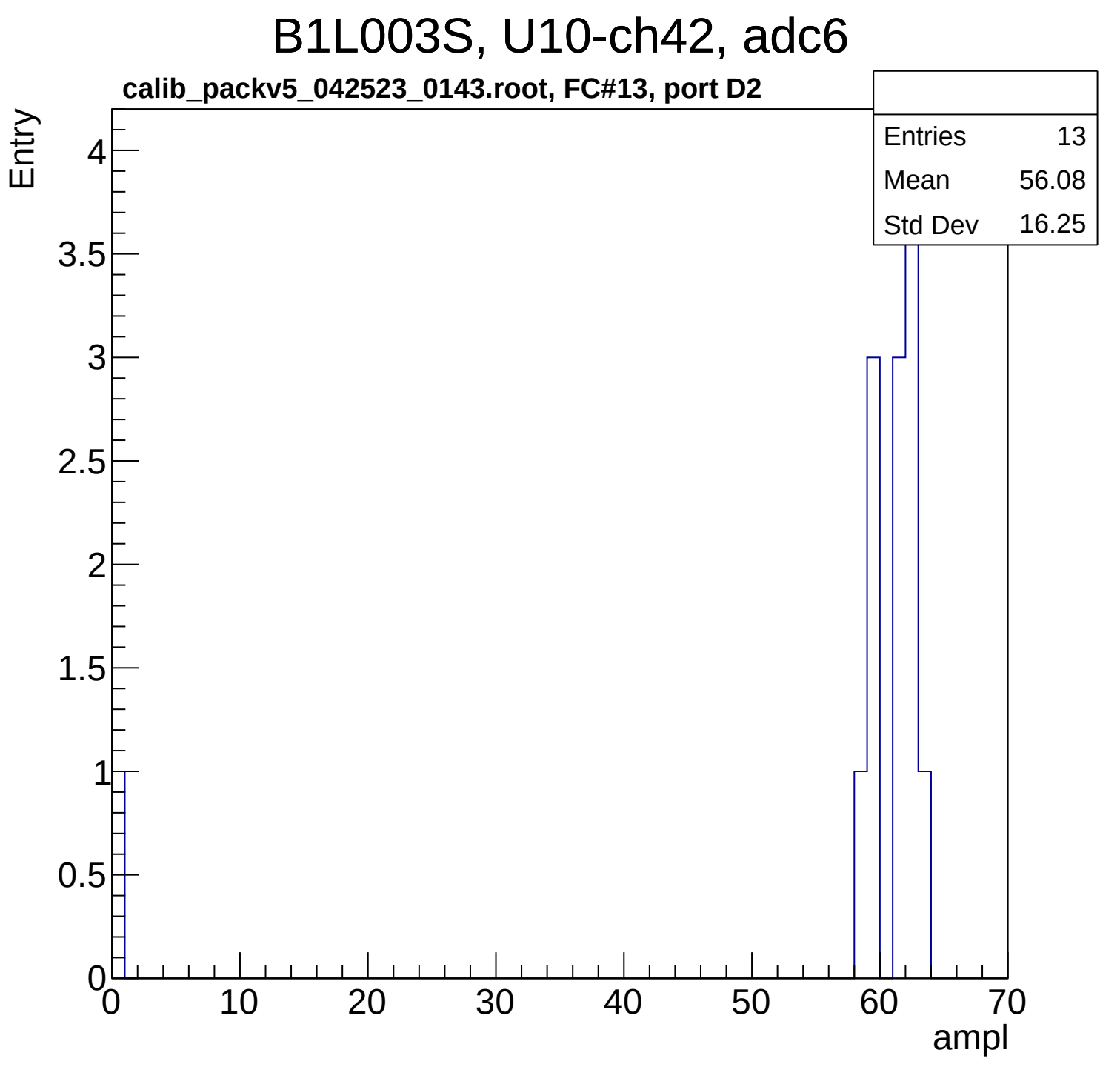
B1L003S, U10-ch42, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.08
Std Dev 16.25
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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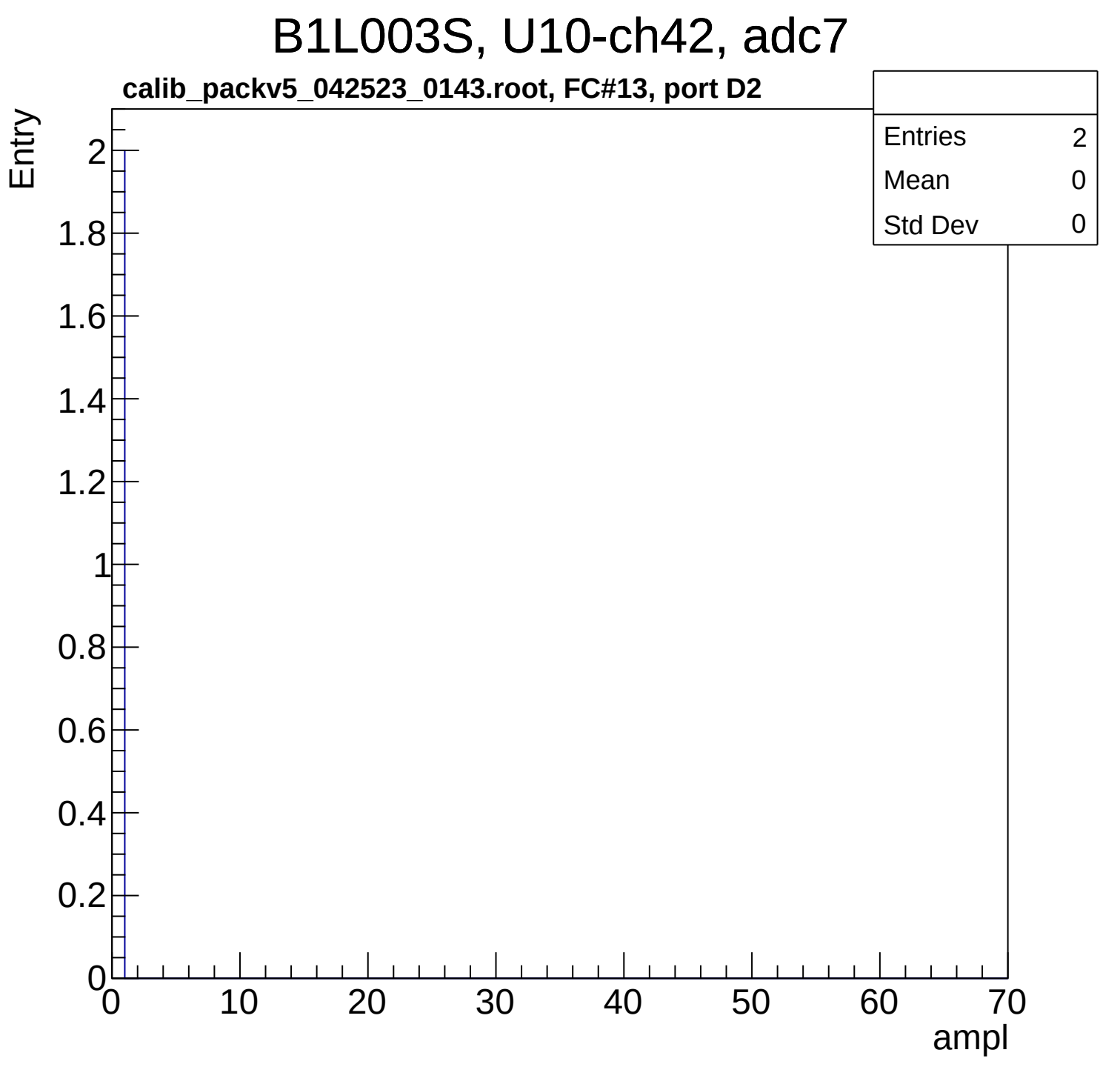
B1L003S, U10-ch42, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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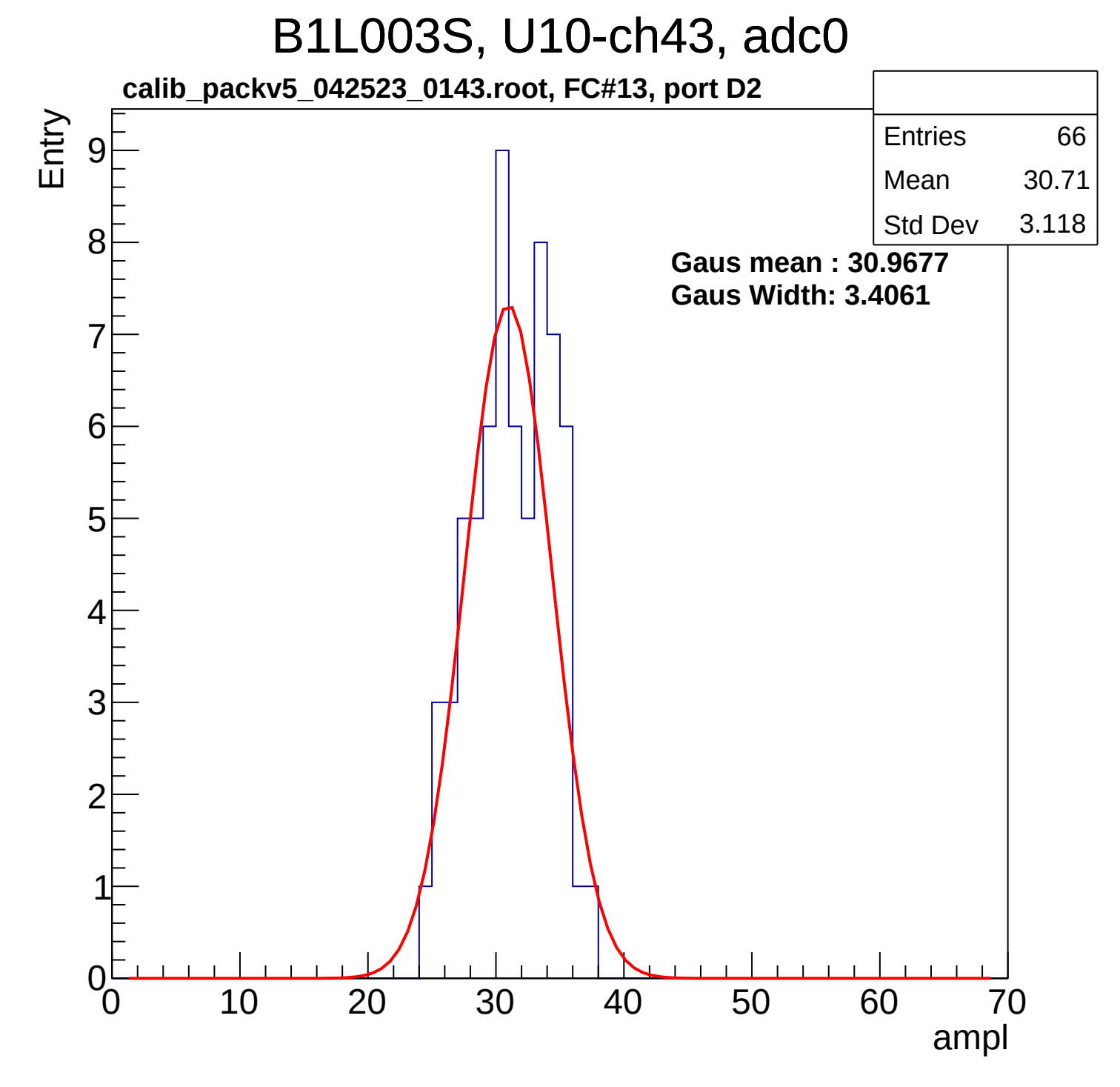
B1L003S, U10-ch43, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.71
Std Dev 3.118
Gaus mean : 30.9677
Gaus Width: 3.4061
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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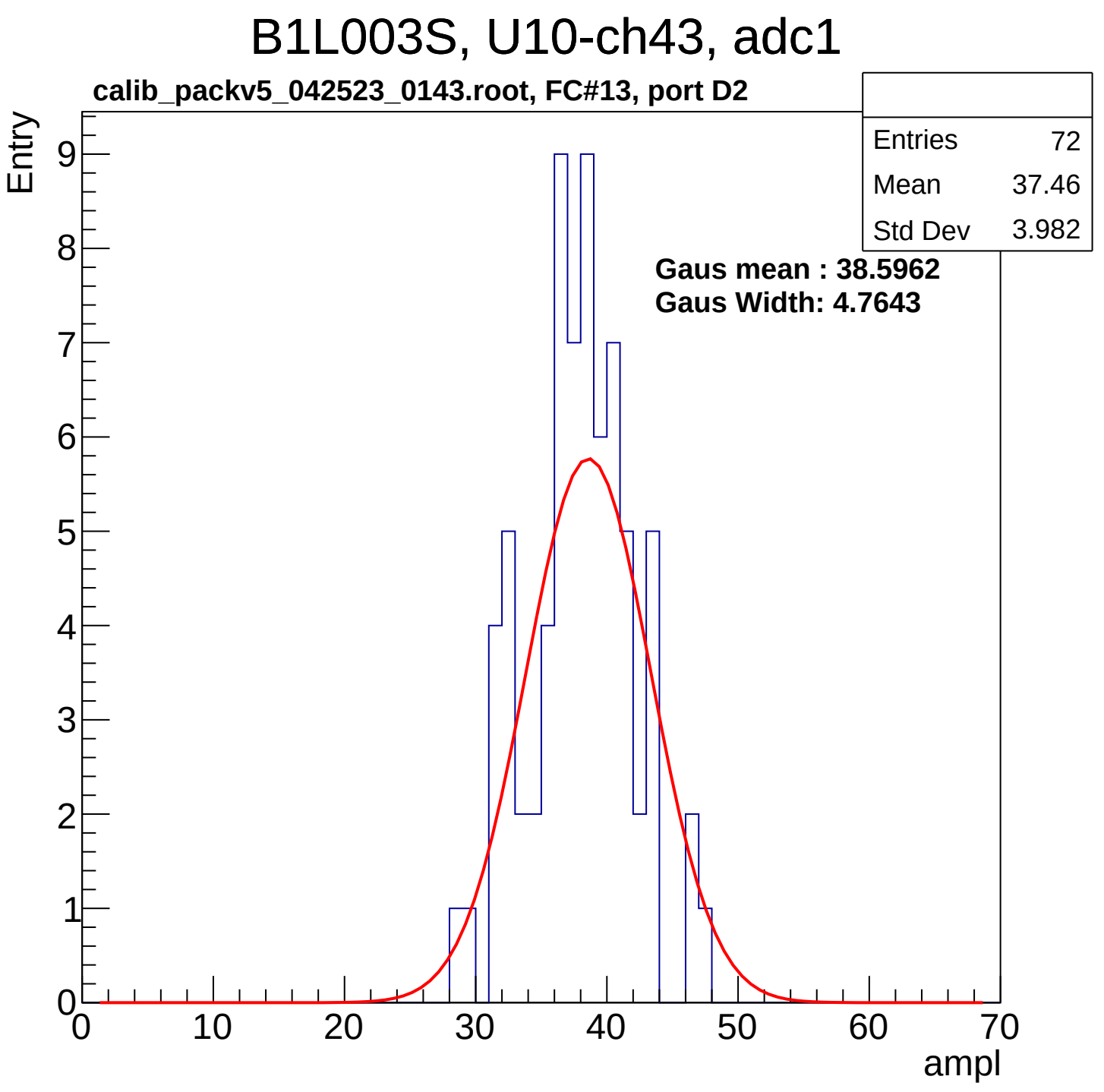
B1L003S, U10-ch43, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 37.46
Std Dev 3.982
Gaus mean : 38.5962
Gaus Width: 4.7643
Entry
ampl
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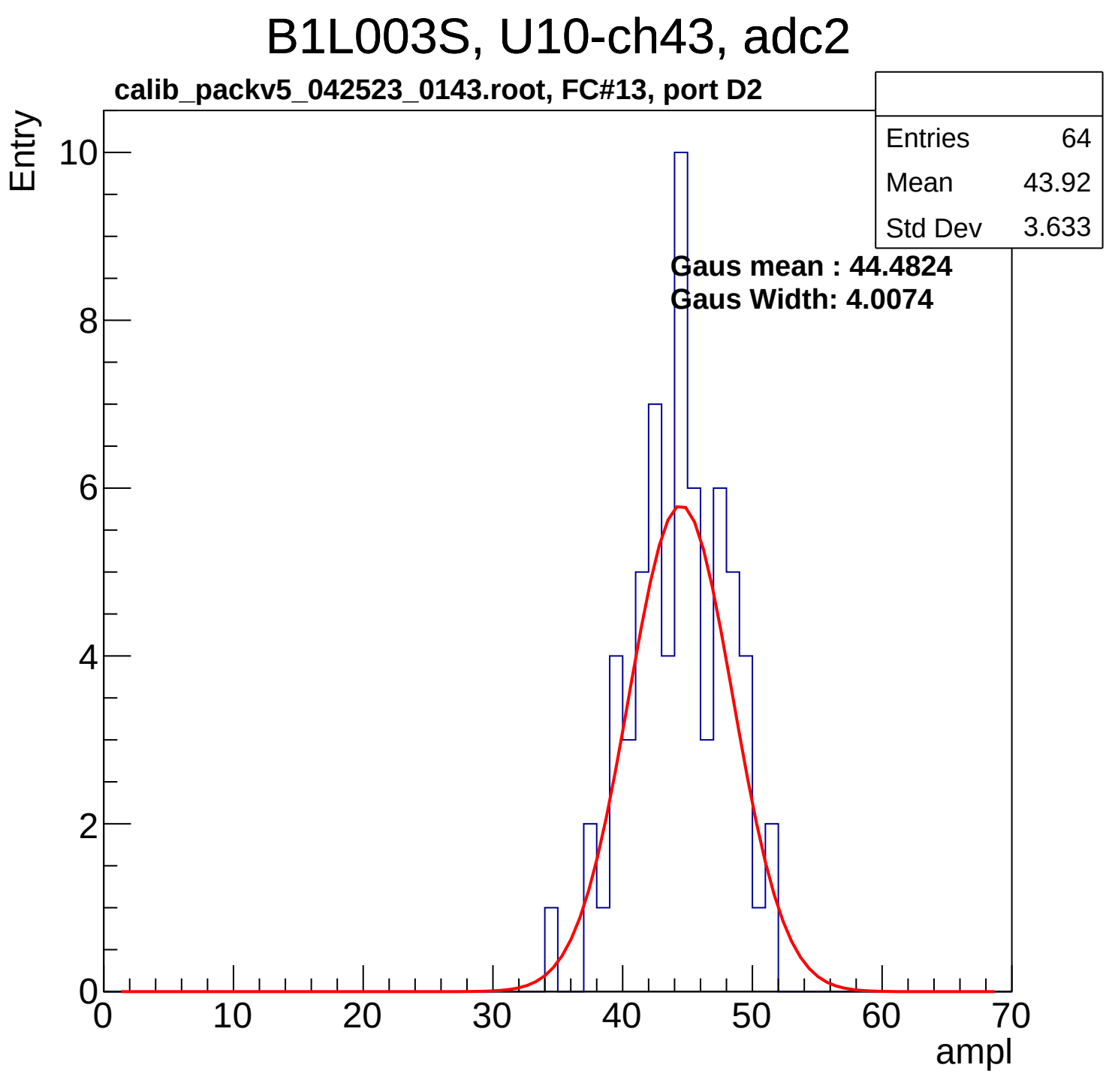
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch43, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.92
Std Dev 3.633
Gaus mean : 44.4824
Gaus Width: 4.0074
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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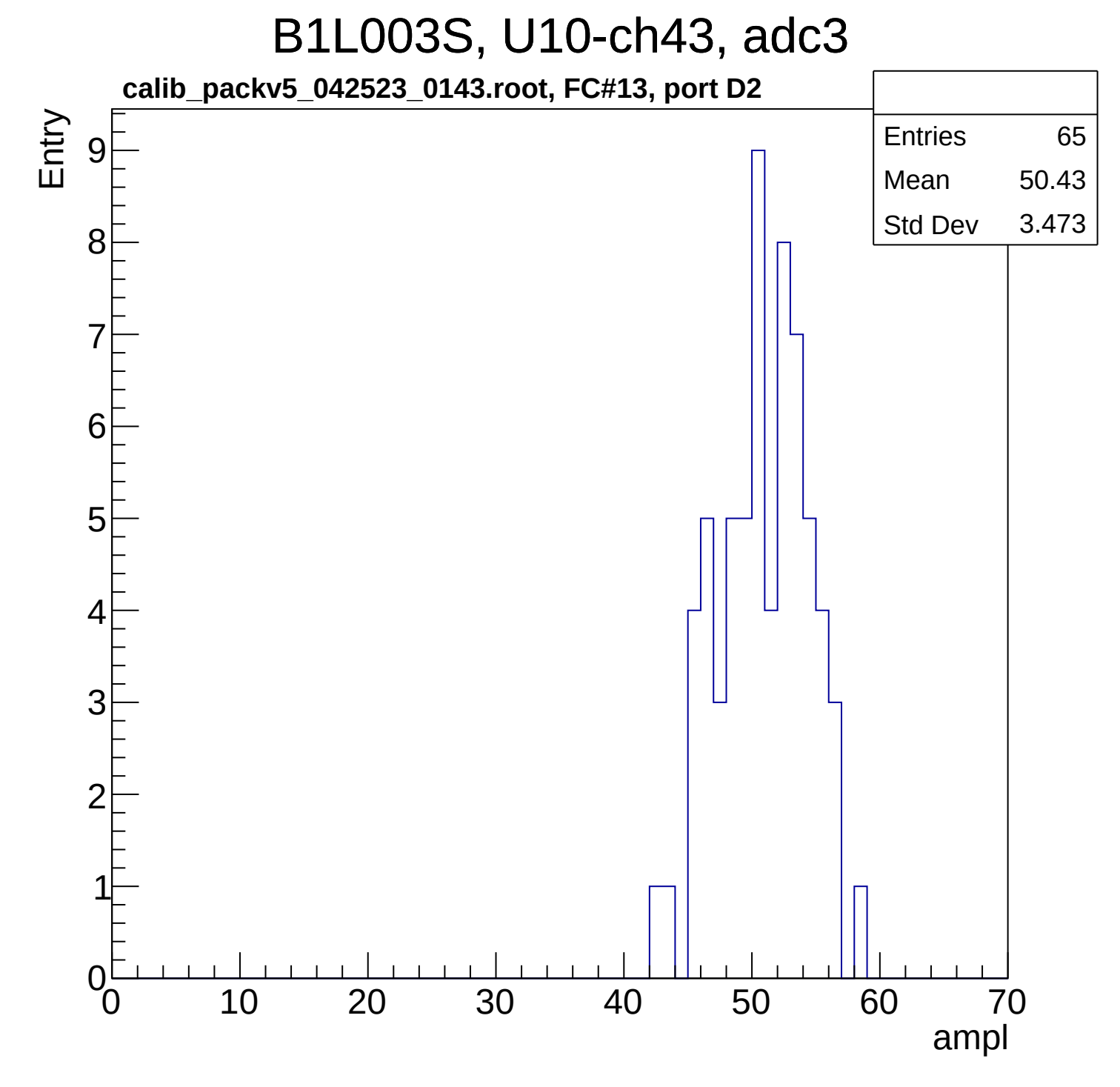
B1L003S, U10-ch43, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 50.43
Std Dev 3.473
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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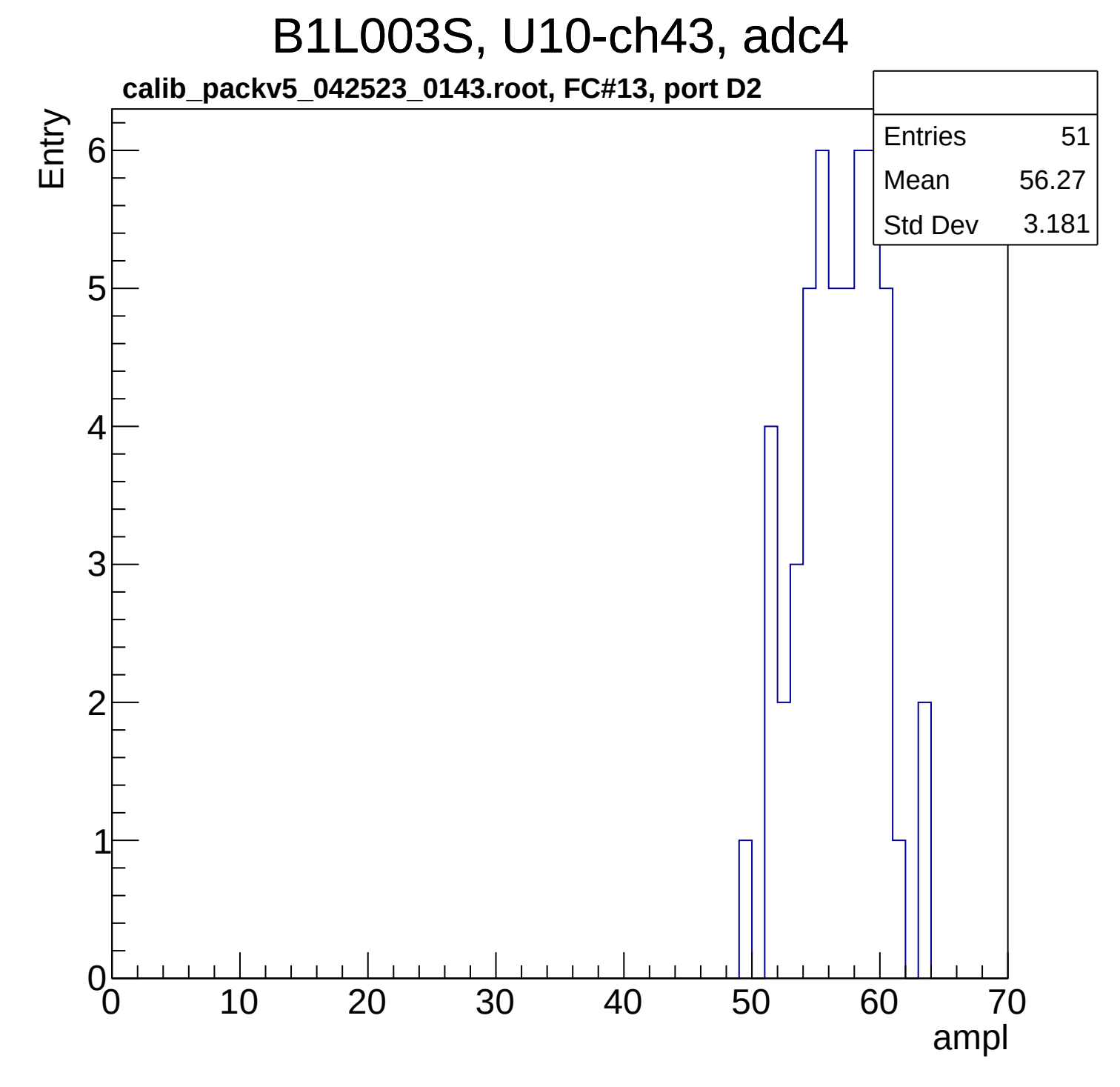
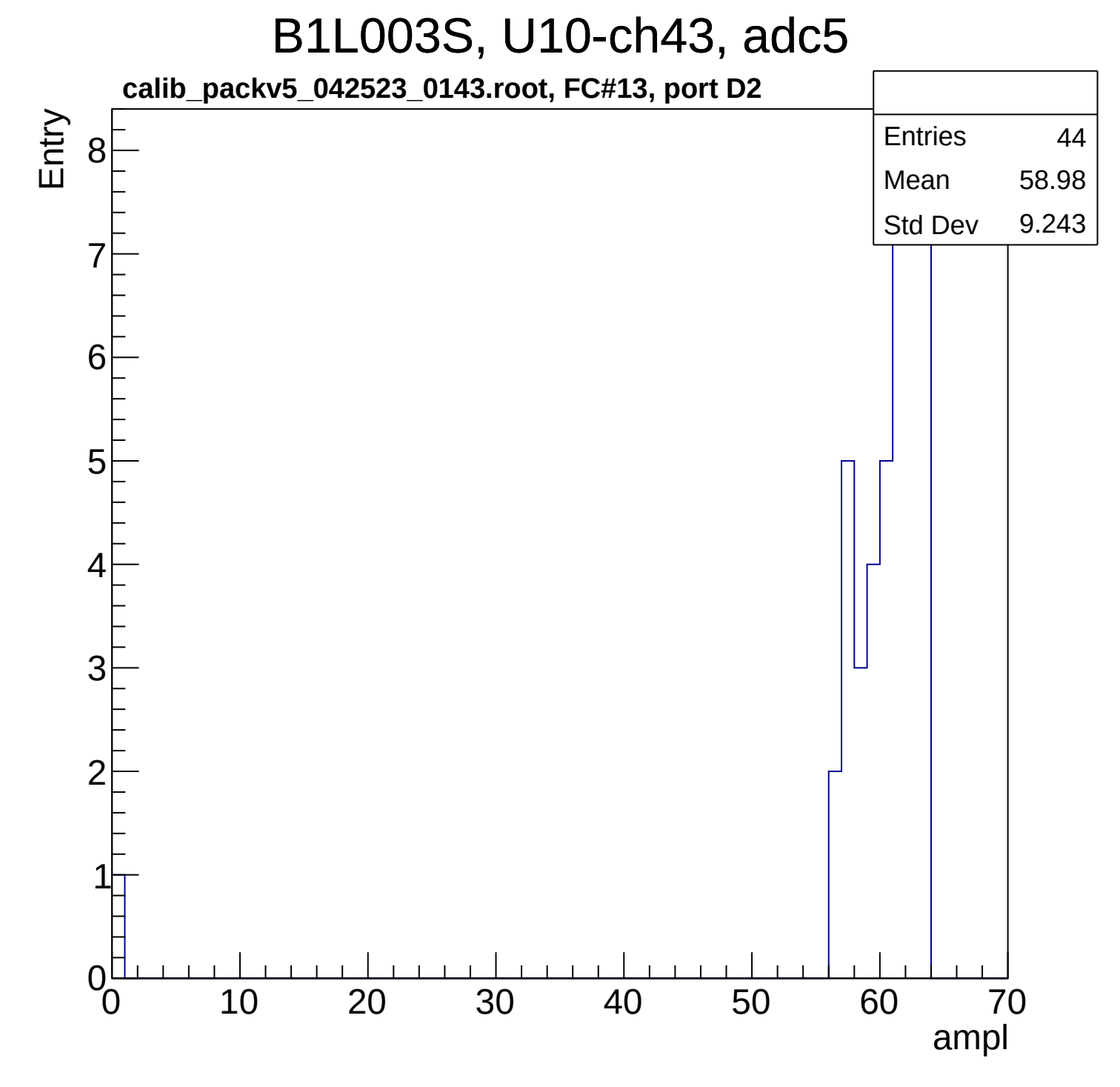

B1L003S, U10-ch43, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 56.27
Std Dev 3.181
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U10-ch43, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.98
Std Dev 9.243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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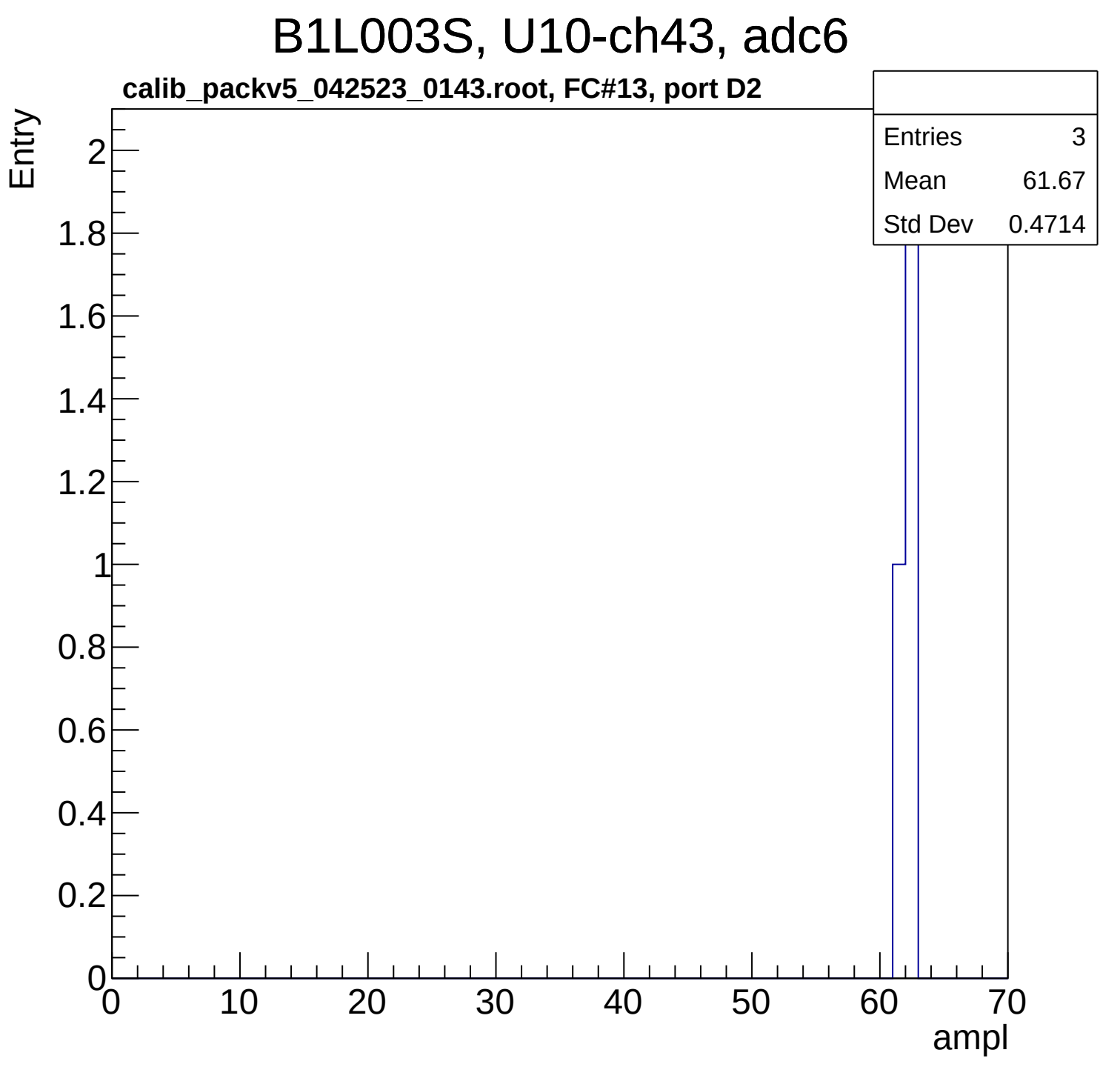
B1L003S, U10-ch43, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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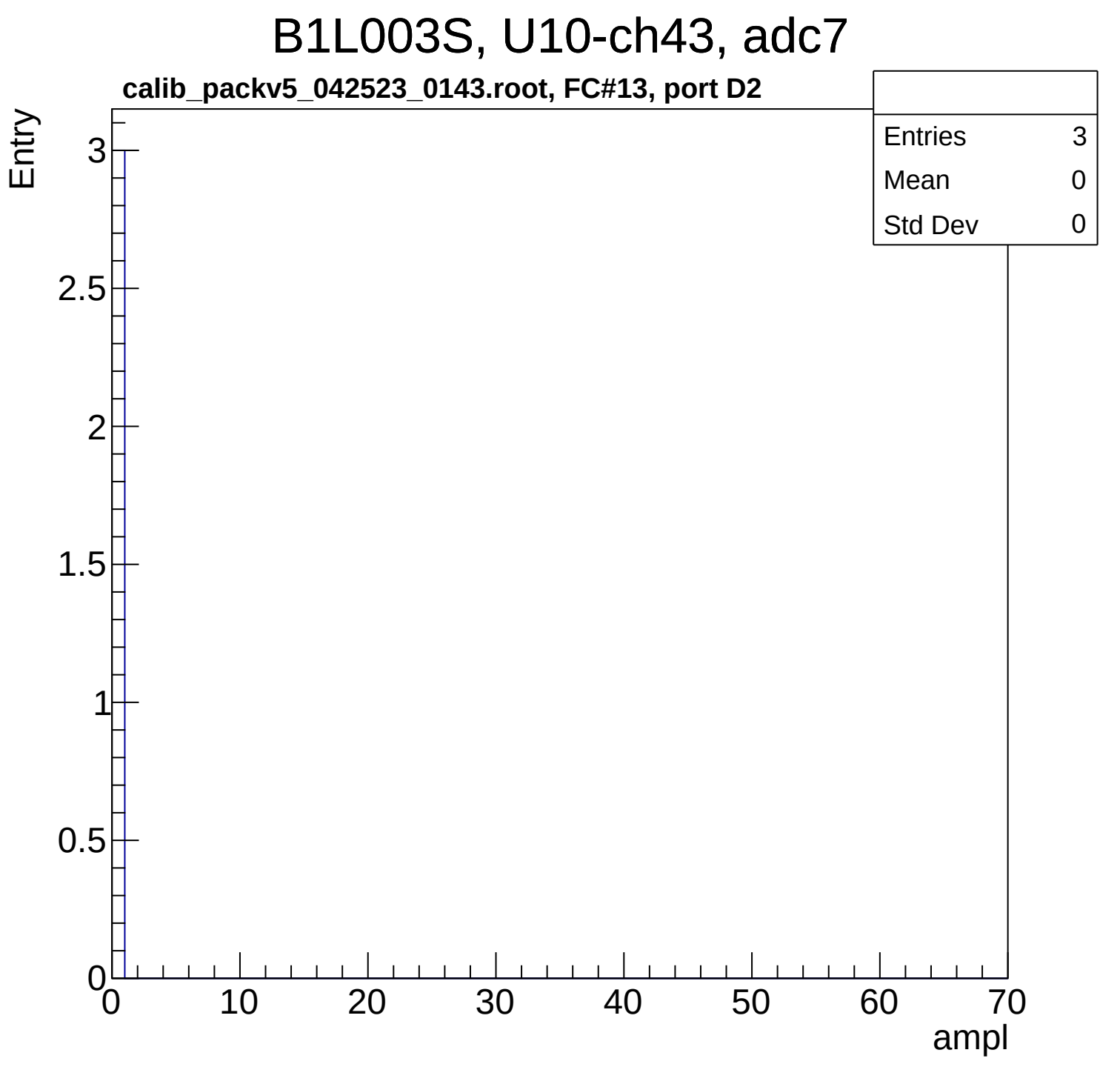
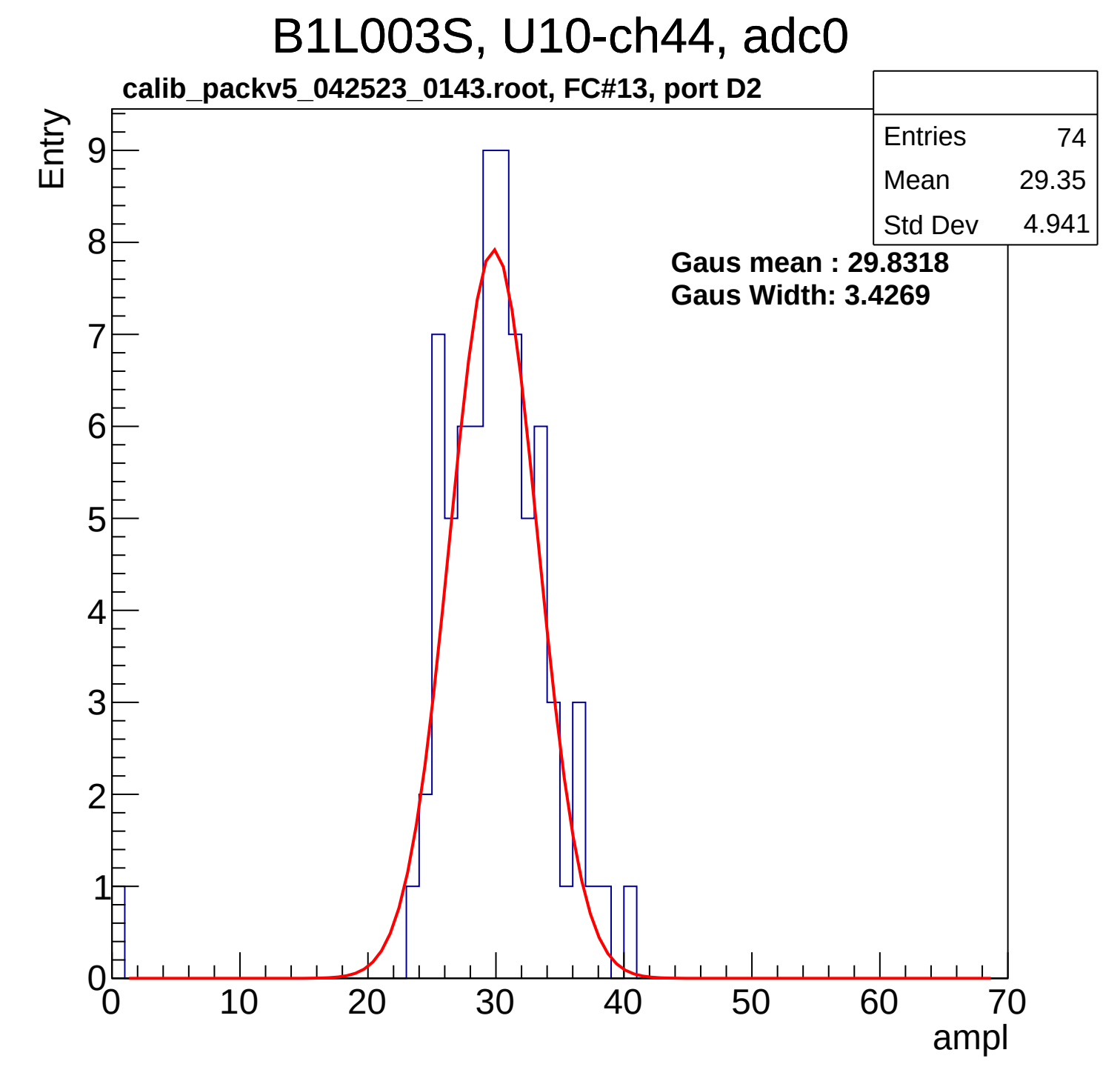
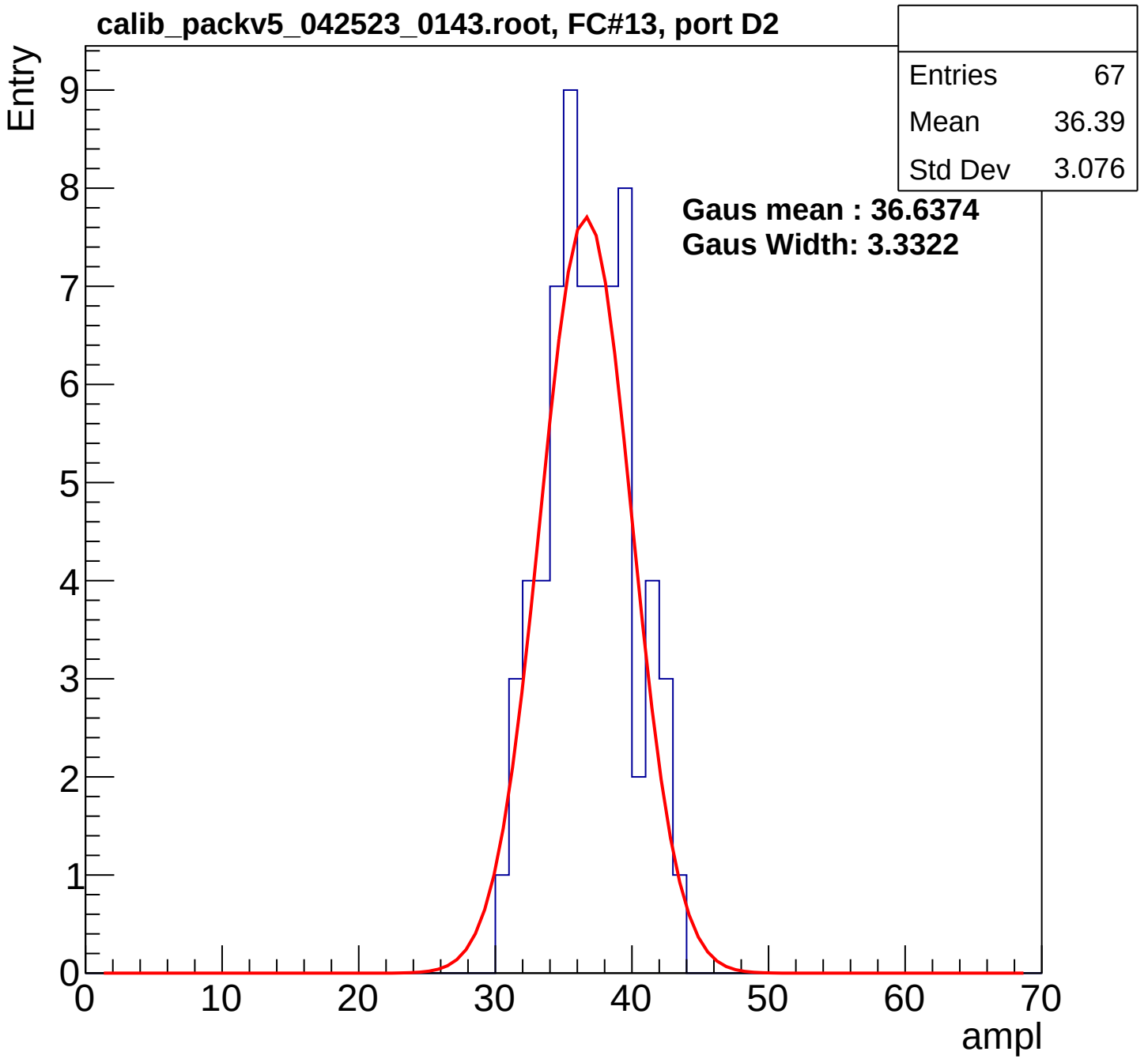

B1L003S, U10-ch43, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch44, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.35
Std Dev 4.941
Gaus mean : 29.8318
Gaus Width: 3.4269
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch44, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.39
Std Dev 3.076
Gaus mean : 36.6374
Gaus Width: 3.3322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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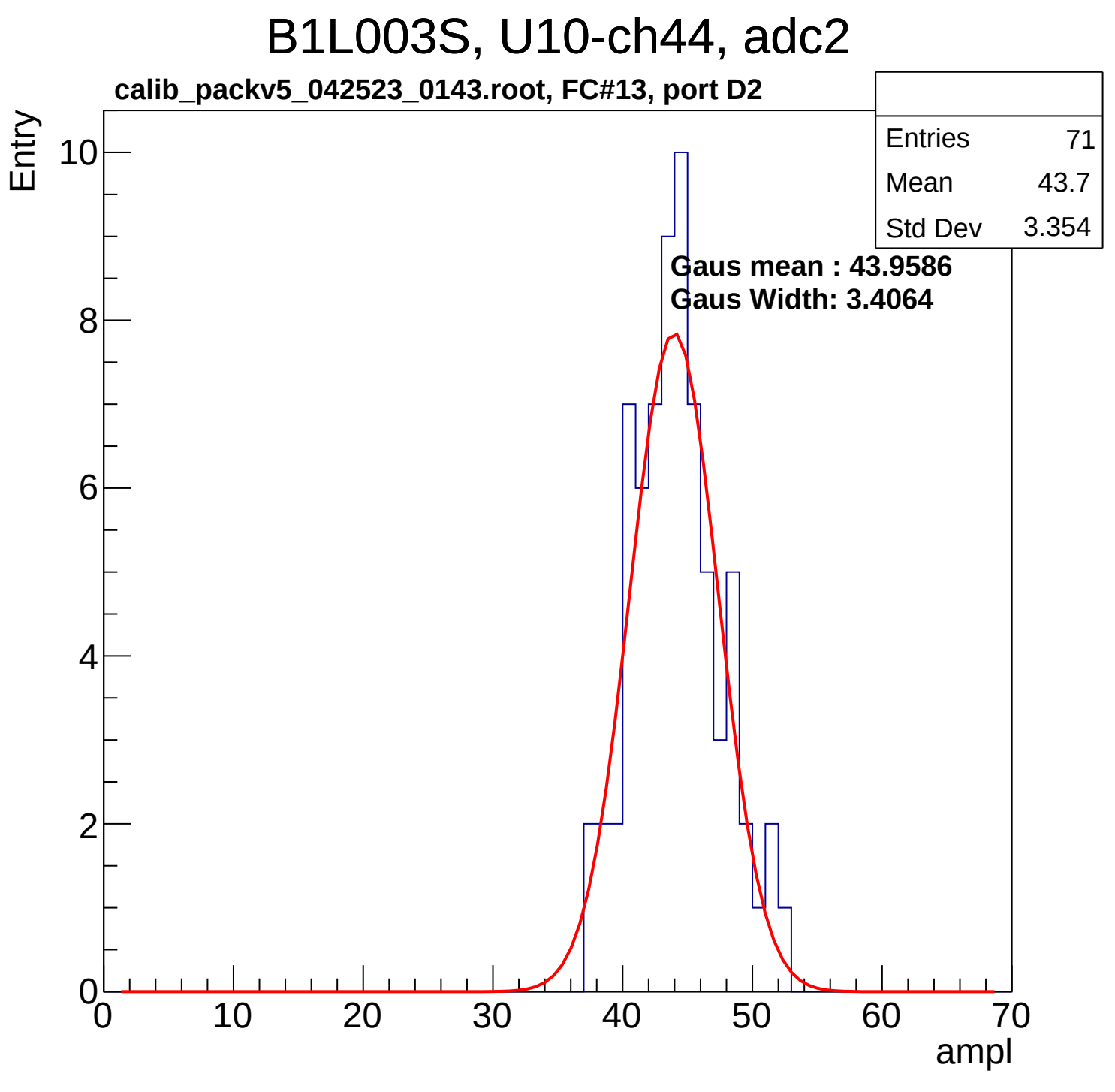
B1L003S, U10-ch44, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 43.7
Std Dev 3.354
Gaus mean : 43.9586
Gaus Width: 3.4064
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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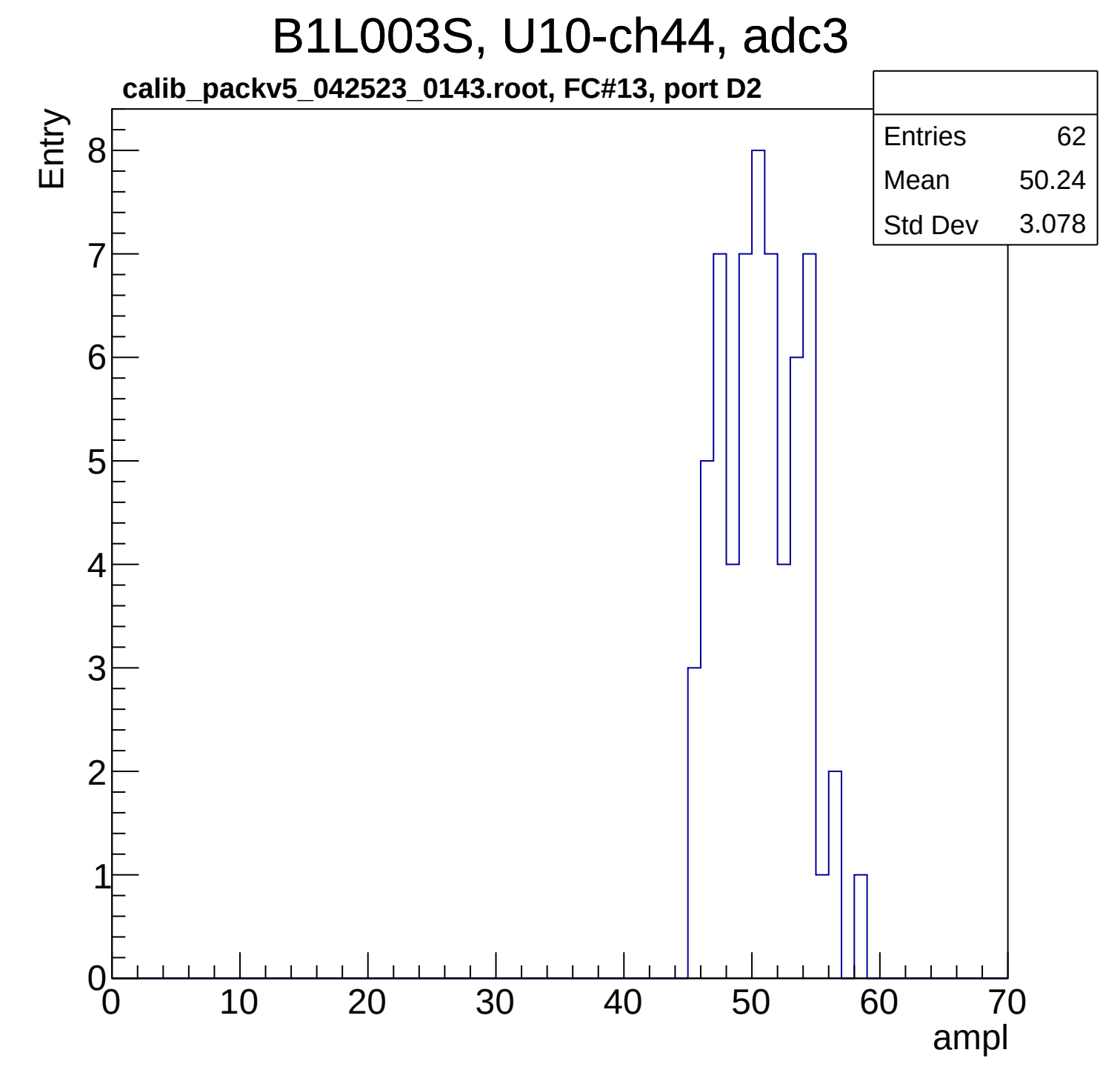
B1L003S, U10-ch44, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 50.24
Std Dev 3.078
Entry
ampl
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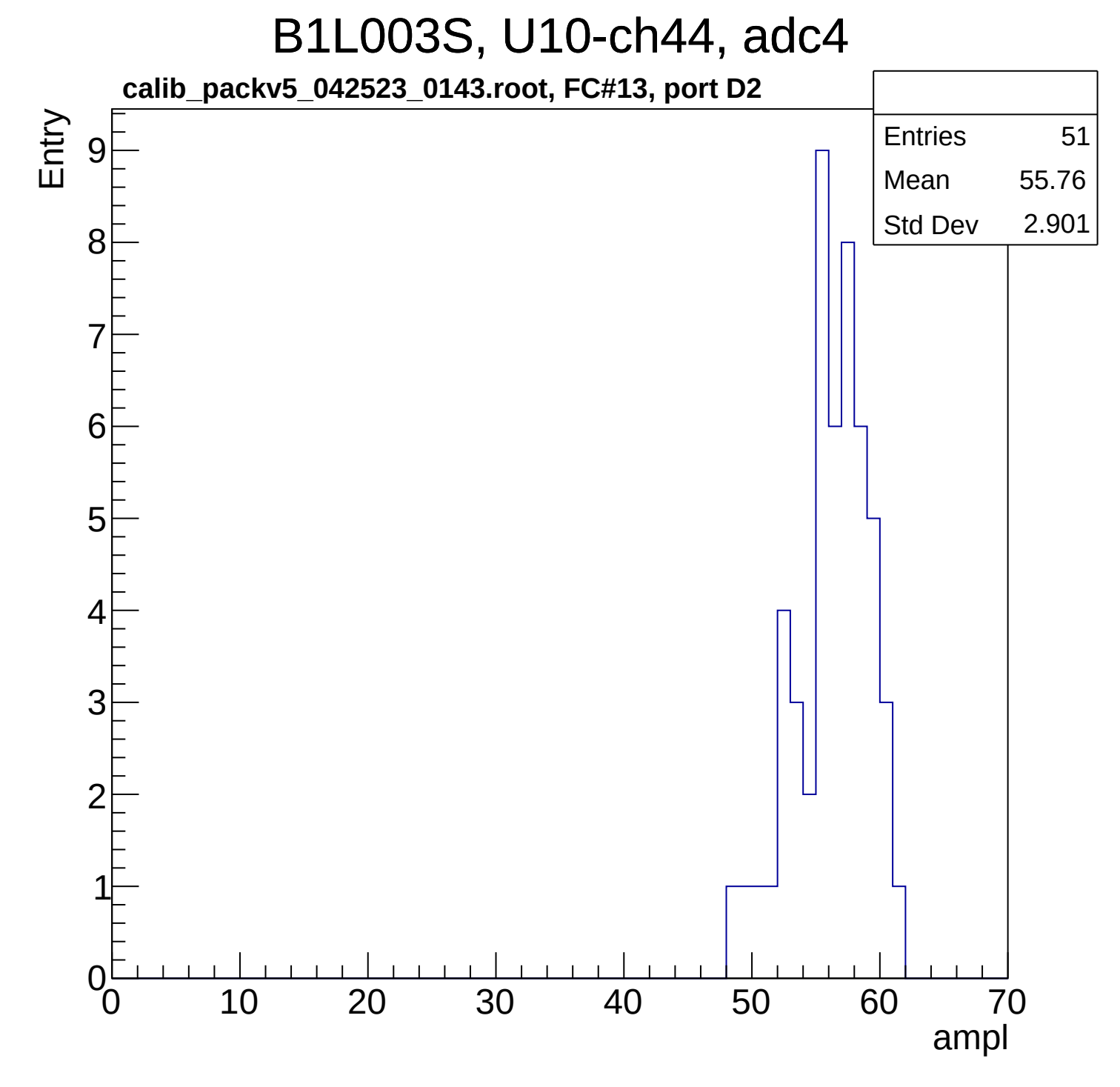
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch44, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.76
Std Dev 2.901
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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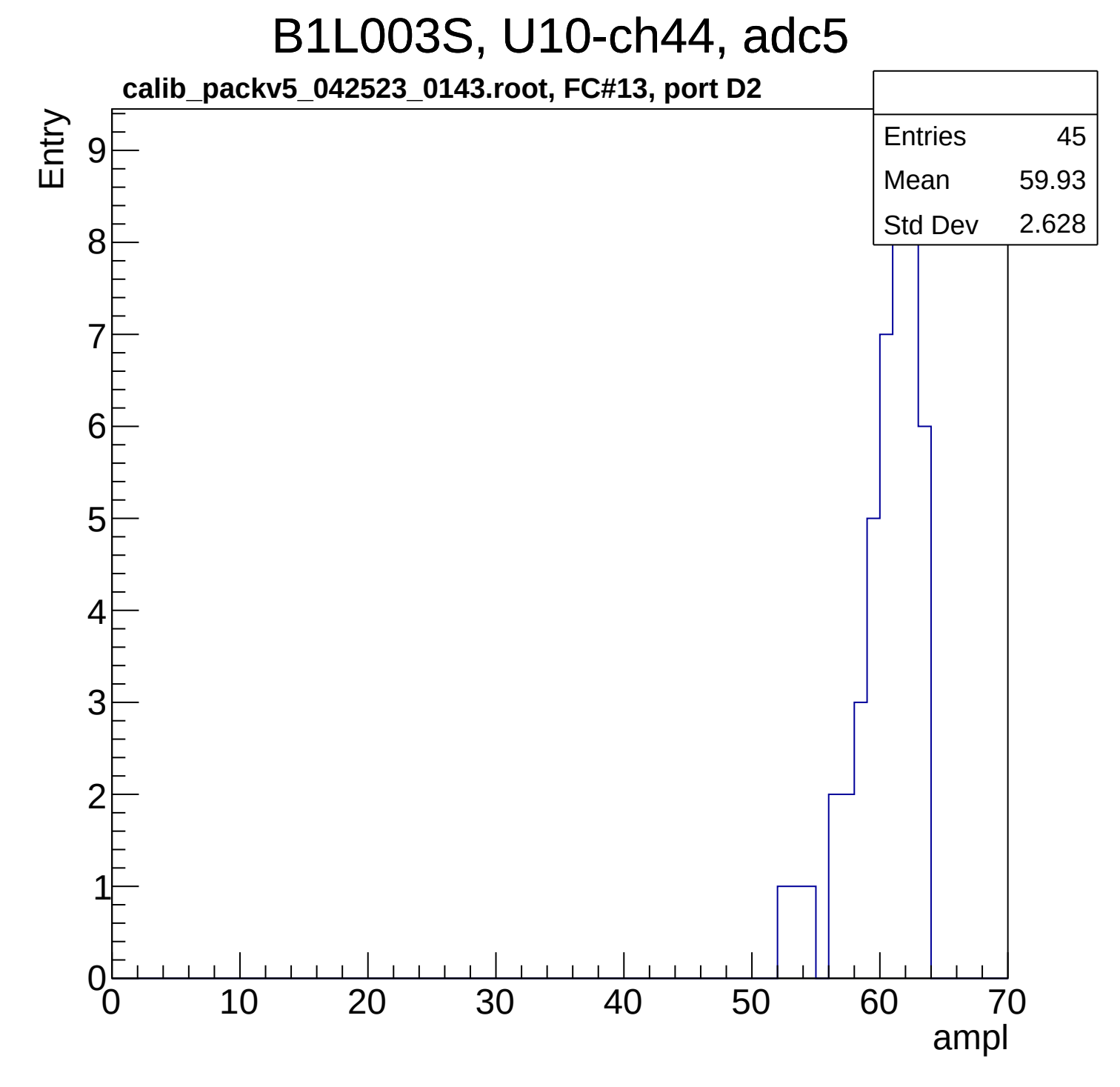
B1L003S, U10-ch44, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 59.93
Std Dev 2.628
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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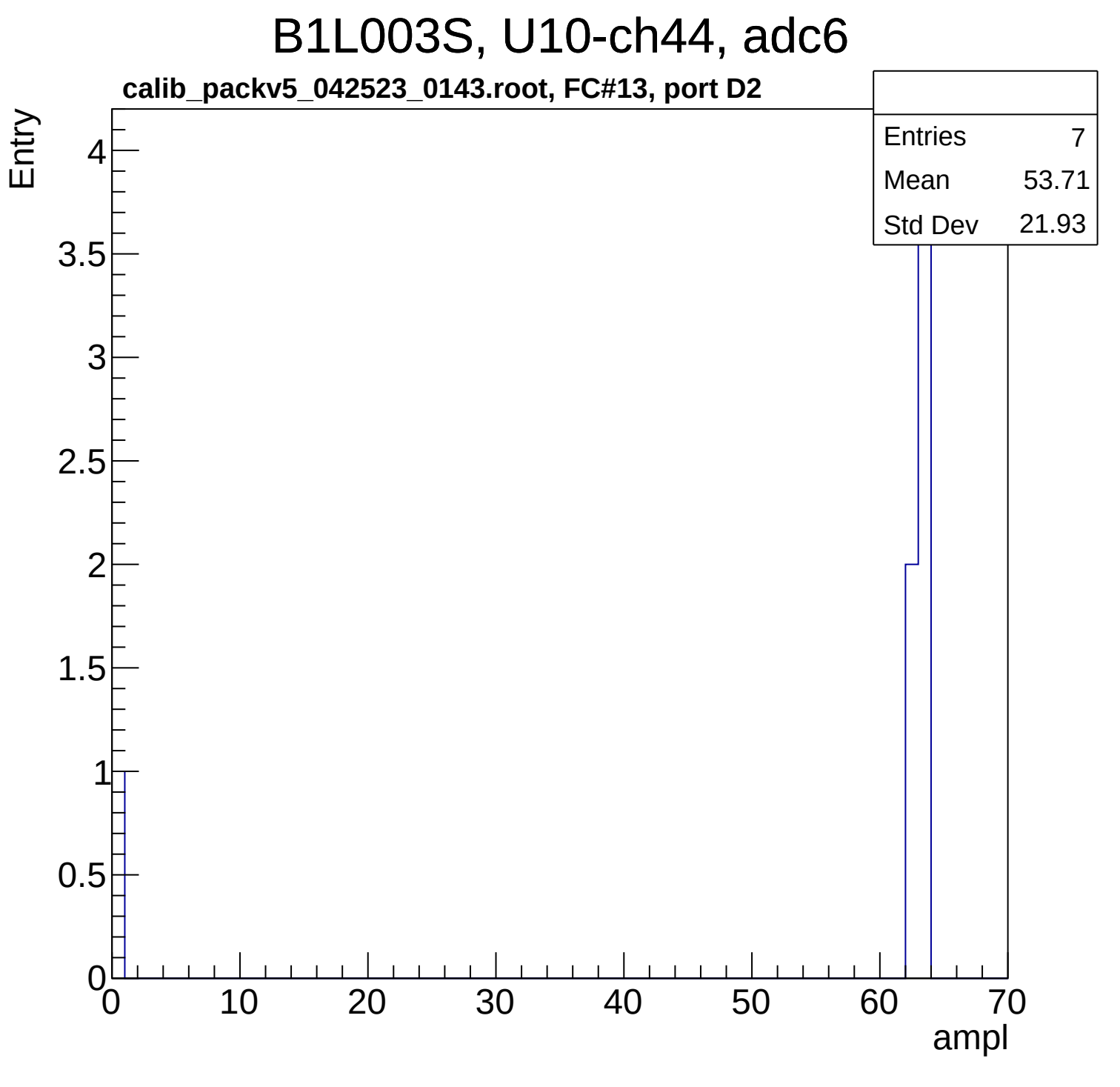
B1L003S, U10-ch44, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.71
Std Dev 21.93
Entry
ampl
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1
1.5
2
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3
3.5
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0
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60
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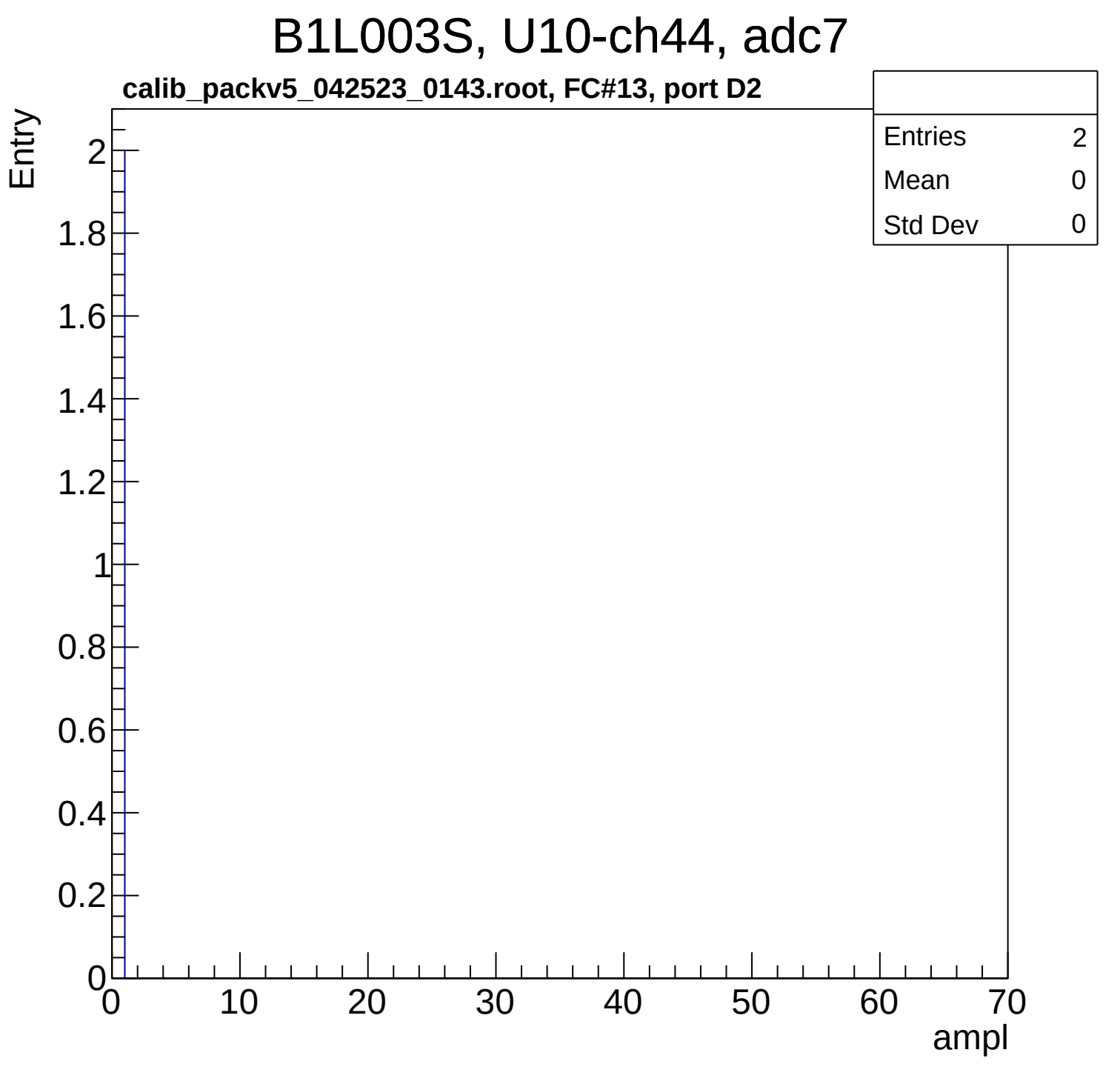
B1L003S, U10-ch44, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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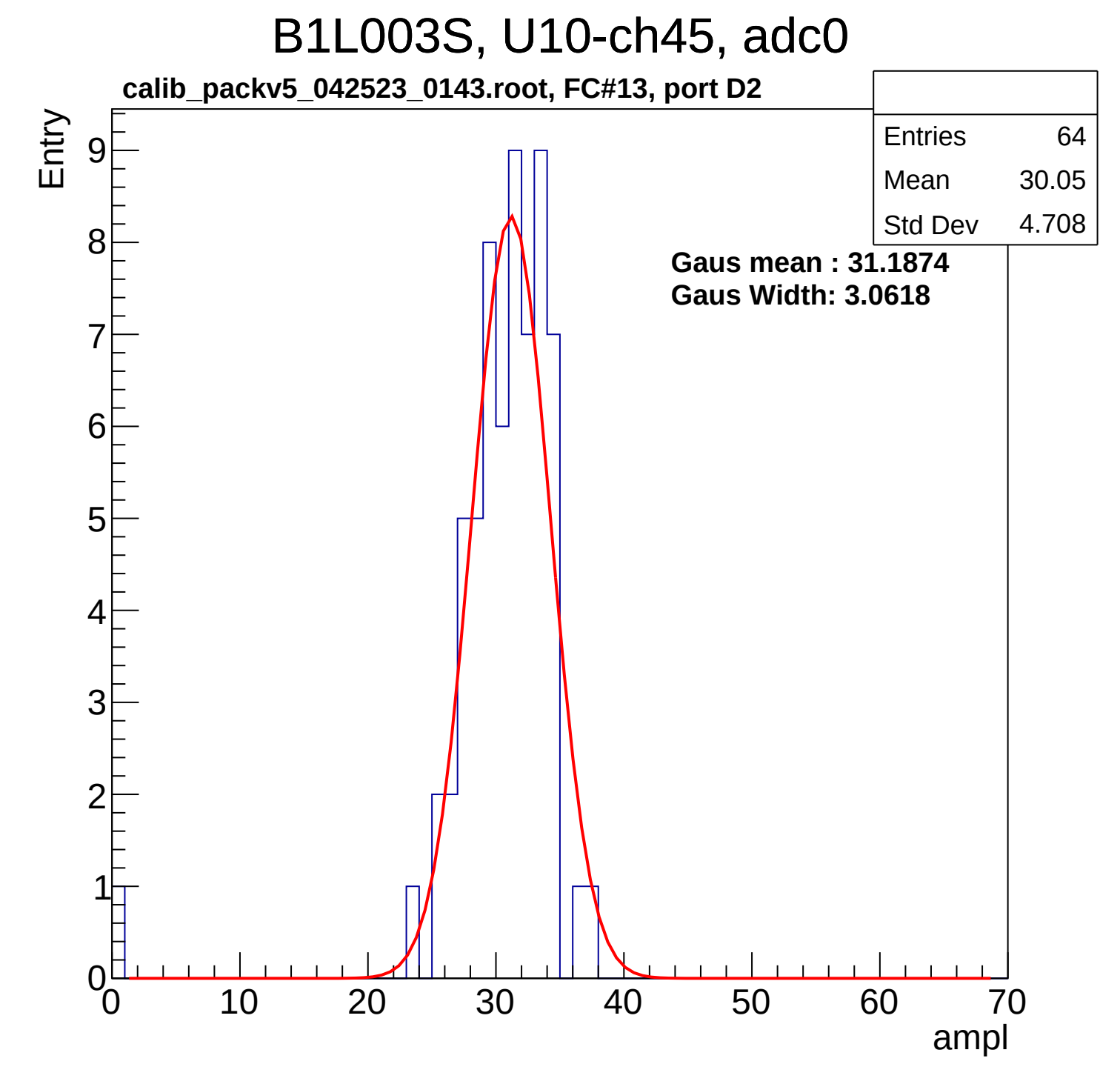
B1L003S, U10-ch45, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 30.05
Std Dev 4.708
Gaus mean : 31.1874
Gaus Width: 3.0618
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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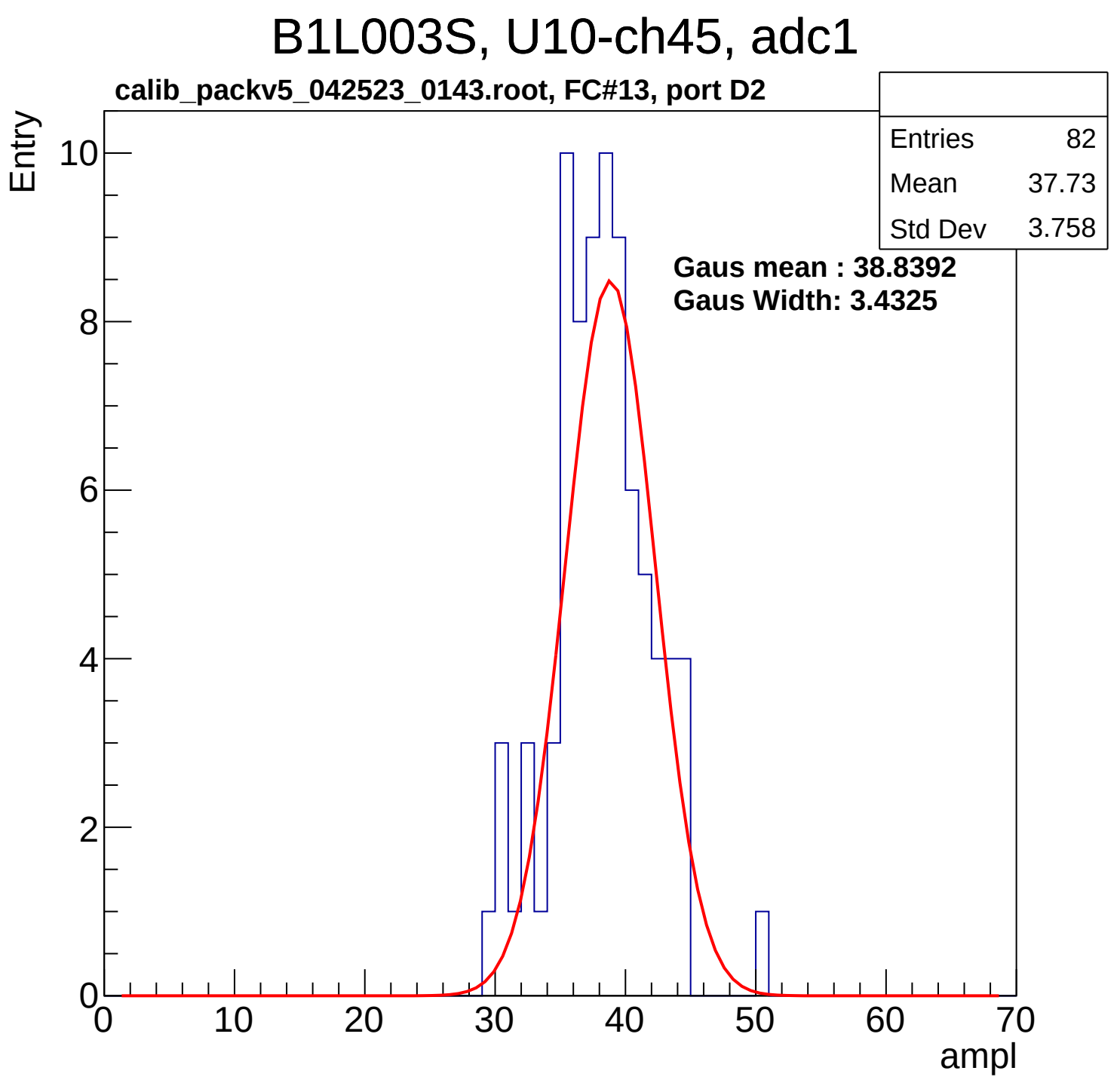
B1L003S, U10-ch45, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 37.73
Std Dev 3.758
Gaus mean : 38.8392
Gaus Width: 3.4325
Entry
ampl
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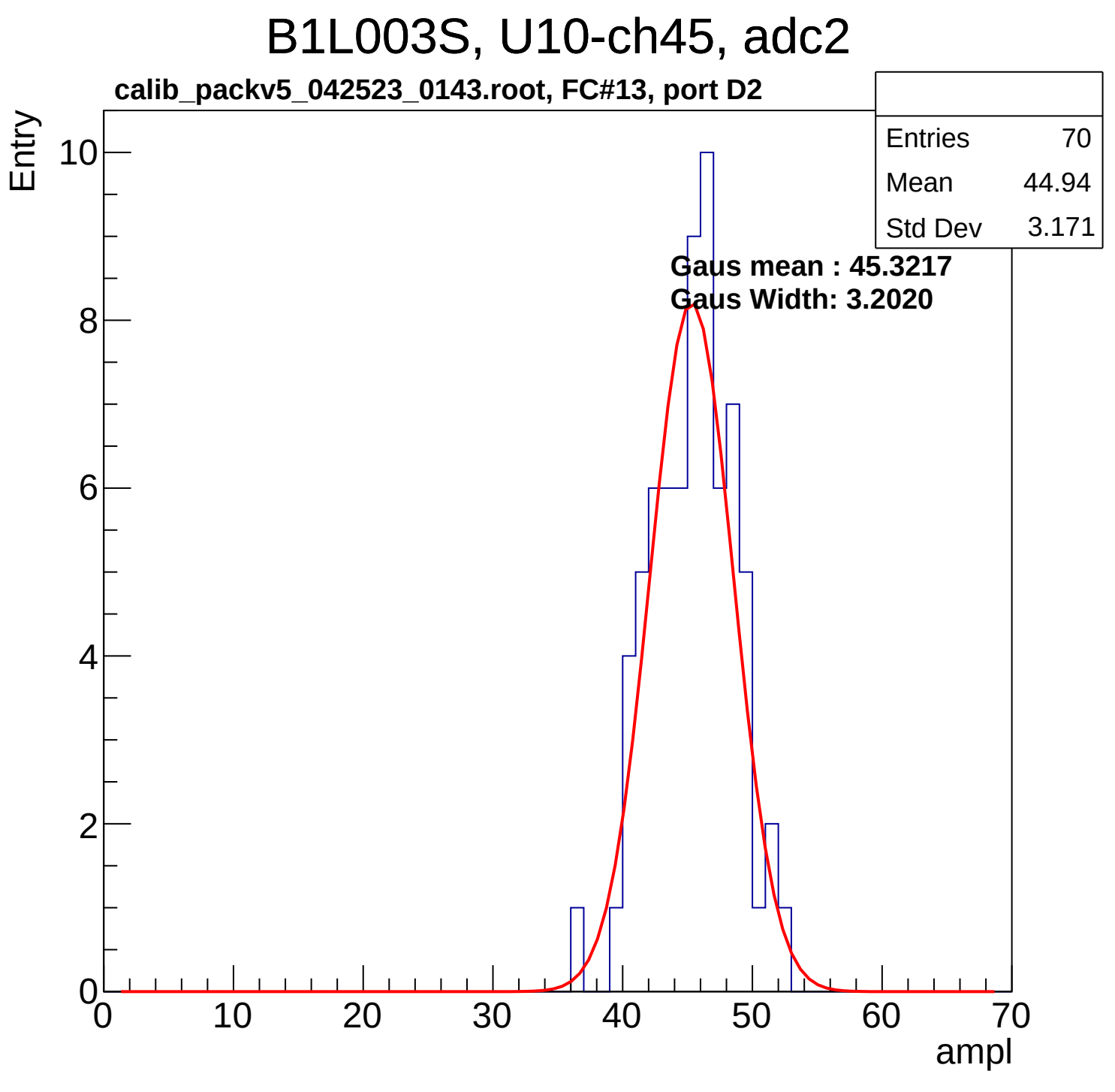
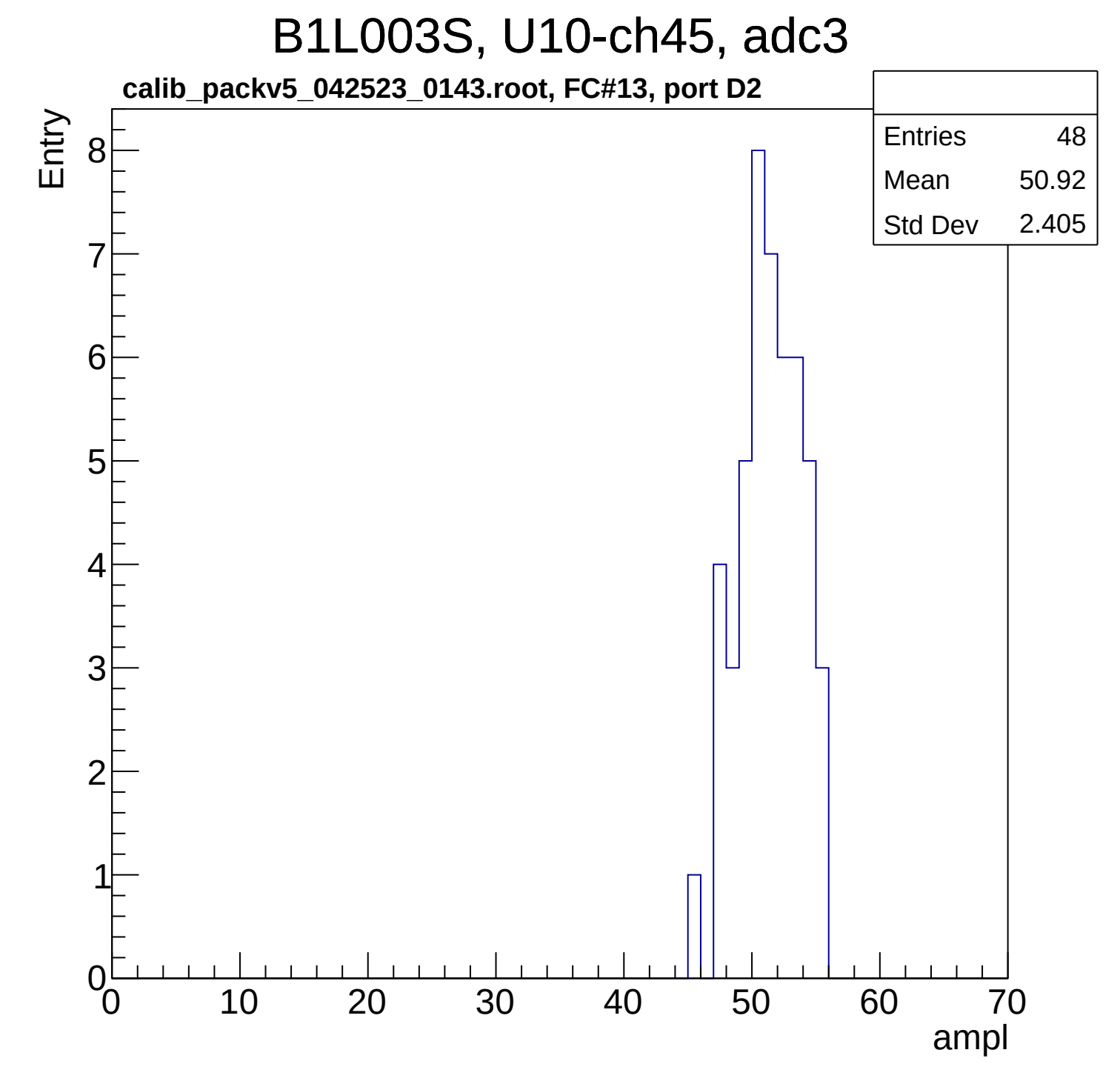
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch45, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 44.94
Std Dev 3.171
Gaus mean : 45.3217
Gaus Width: 3.2020
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch45, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 50.92
Std Dev 2.405
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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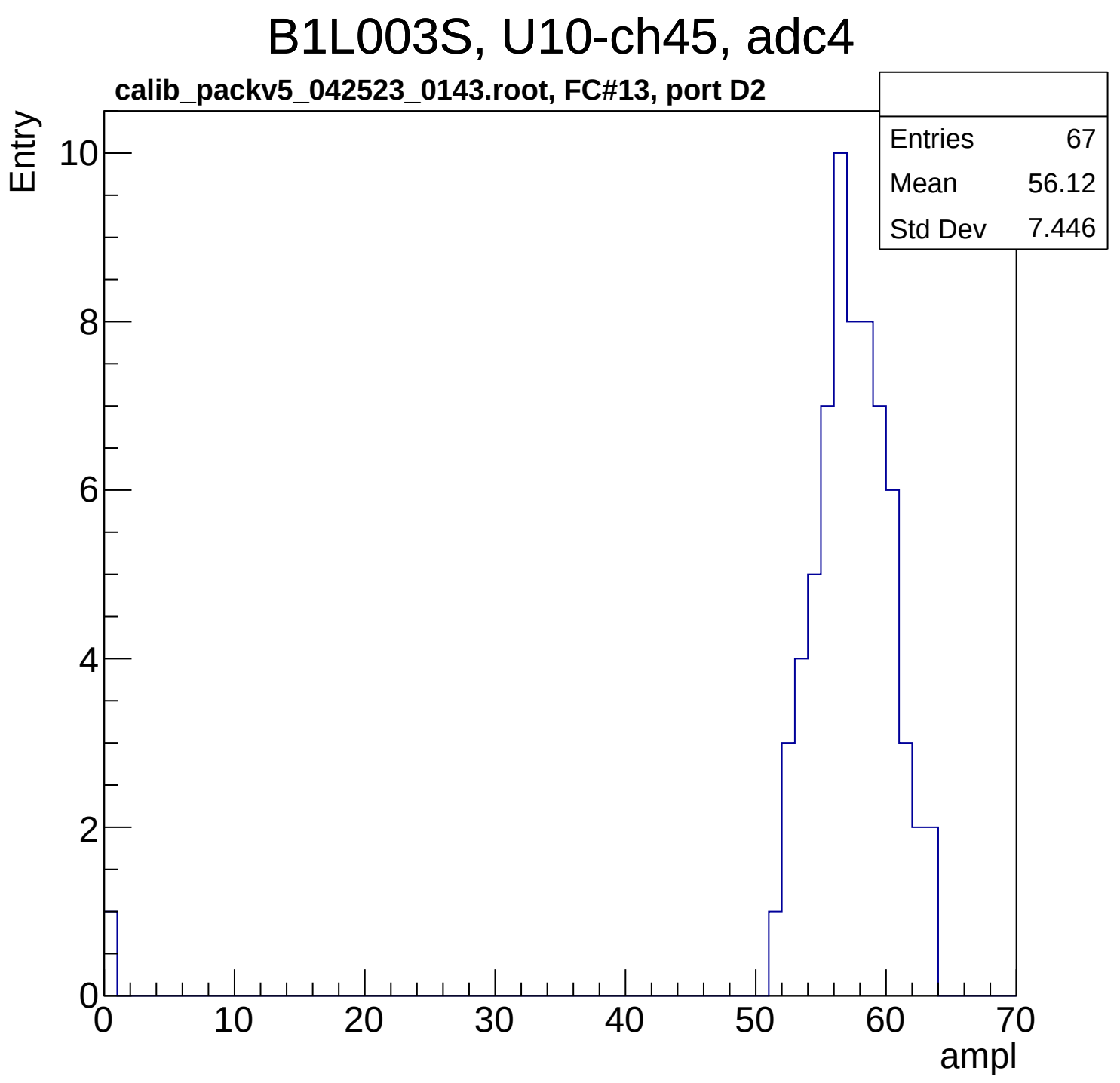
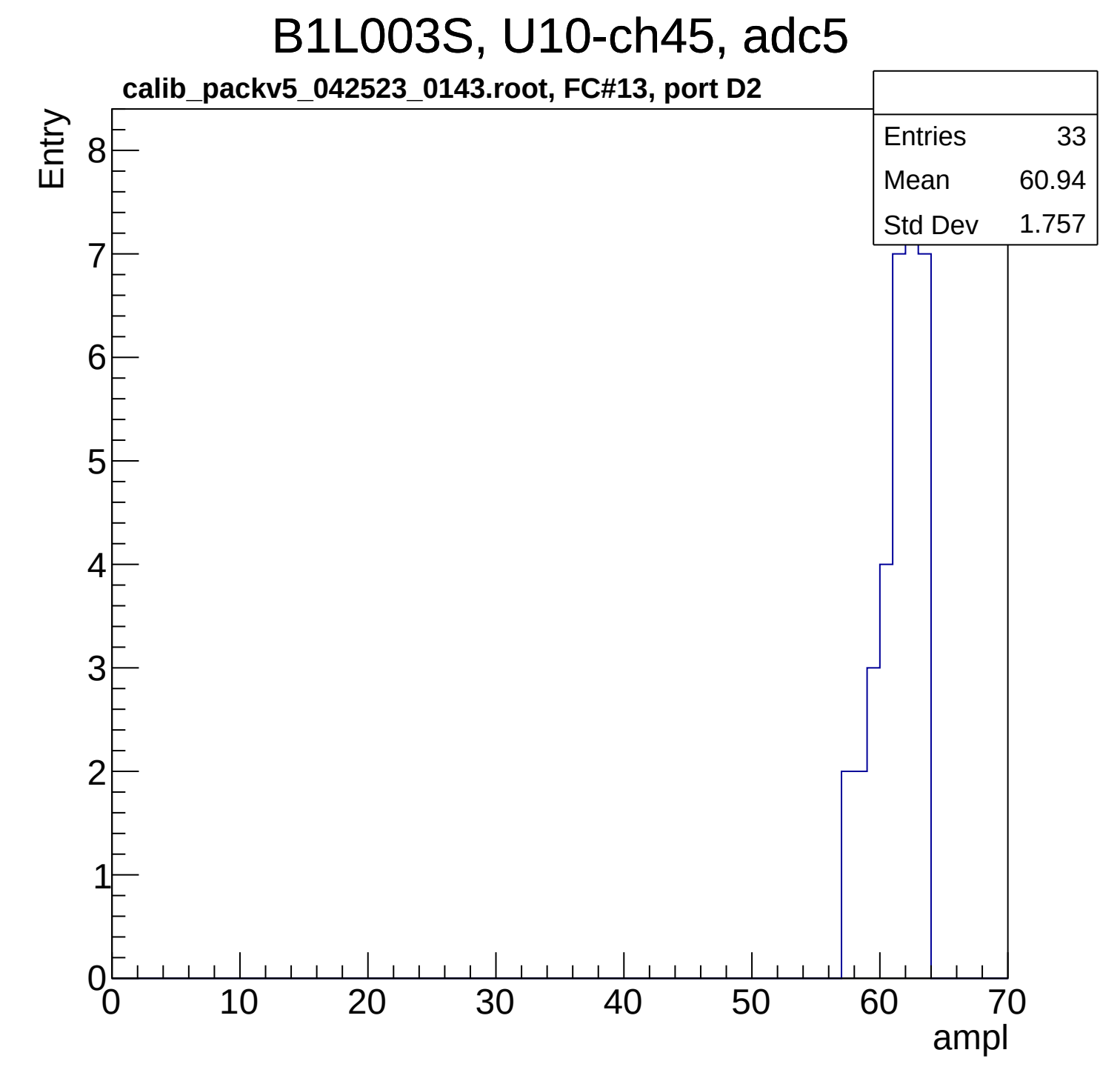
60
70


B1L003S, U10-ch45, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 56.12
Std Dev 7.446
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch45, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 60.94
Std Dev 1.757
Entry
ampl
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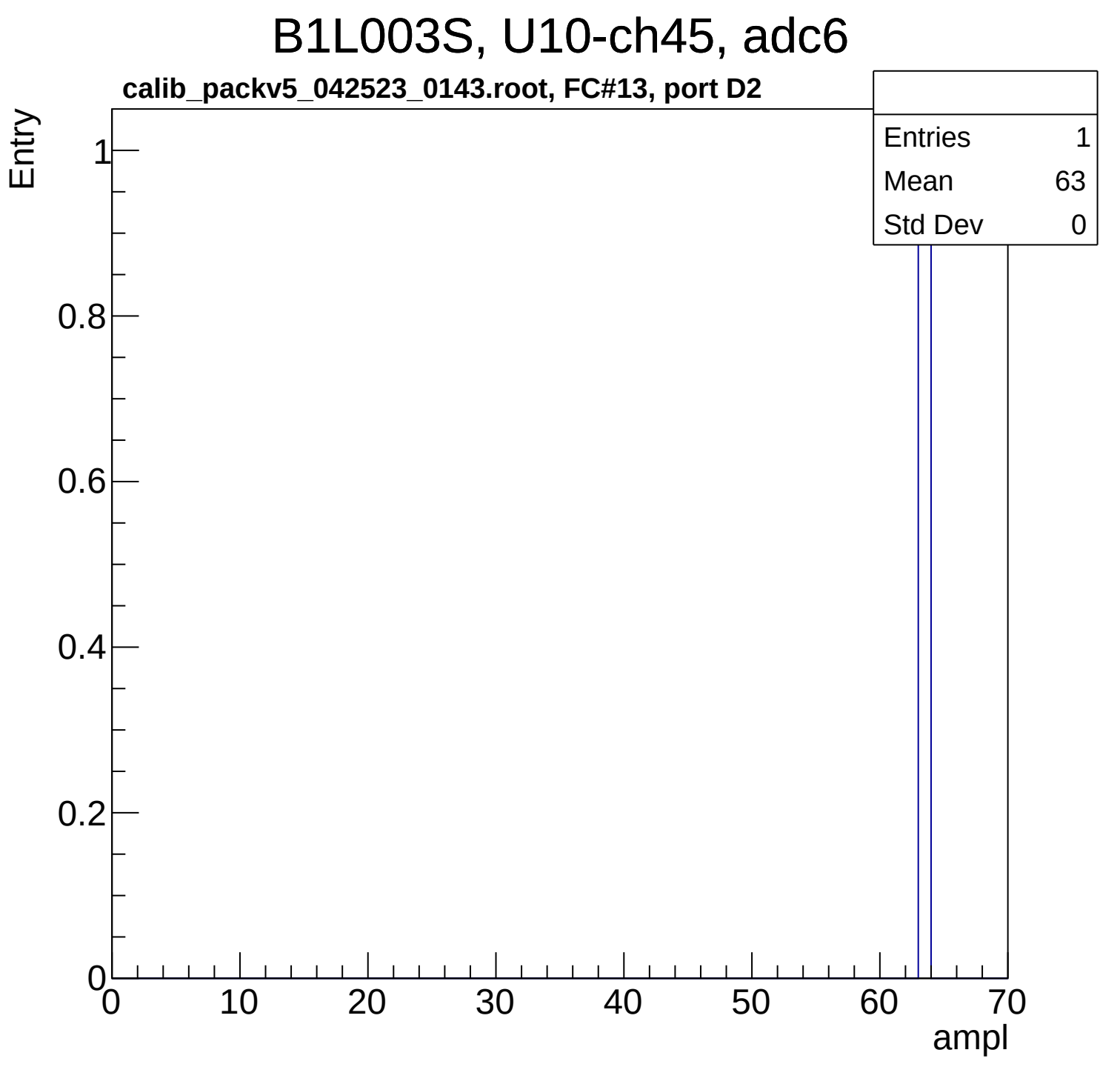
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch45, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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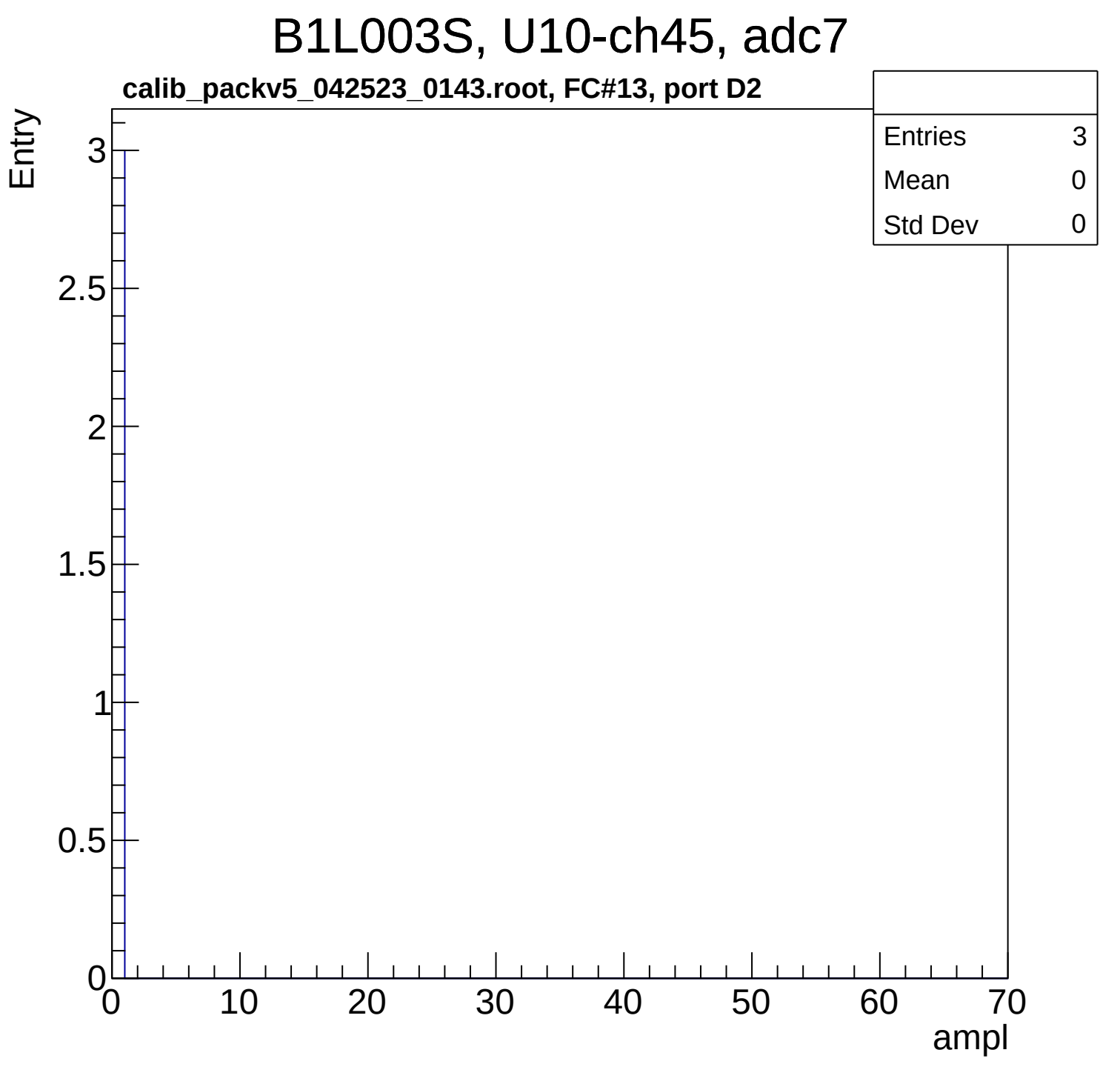
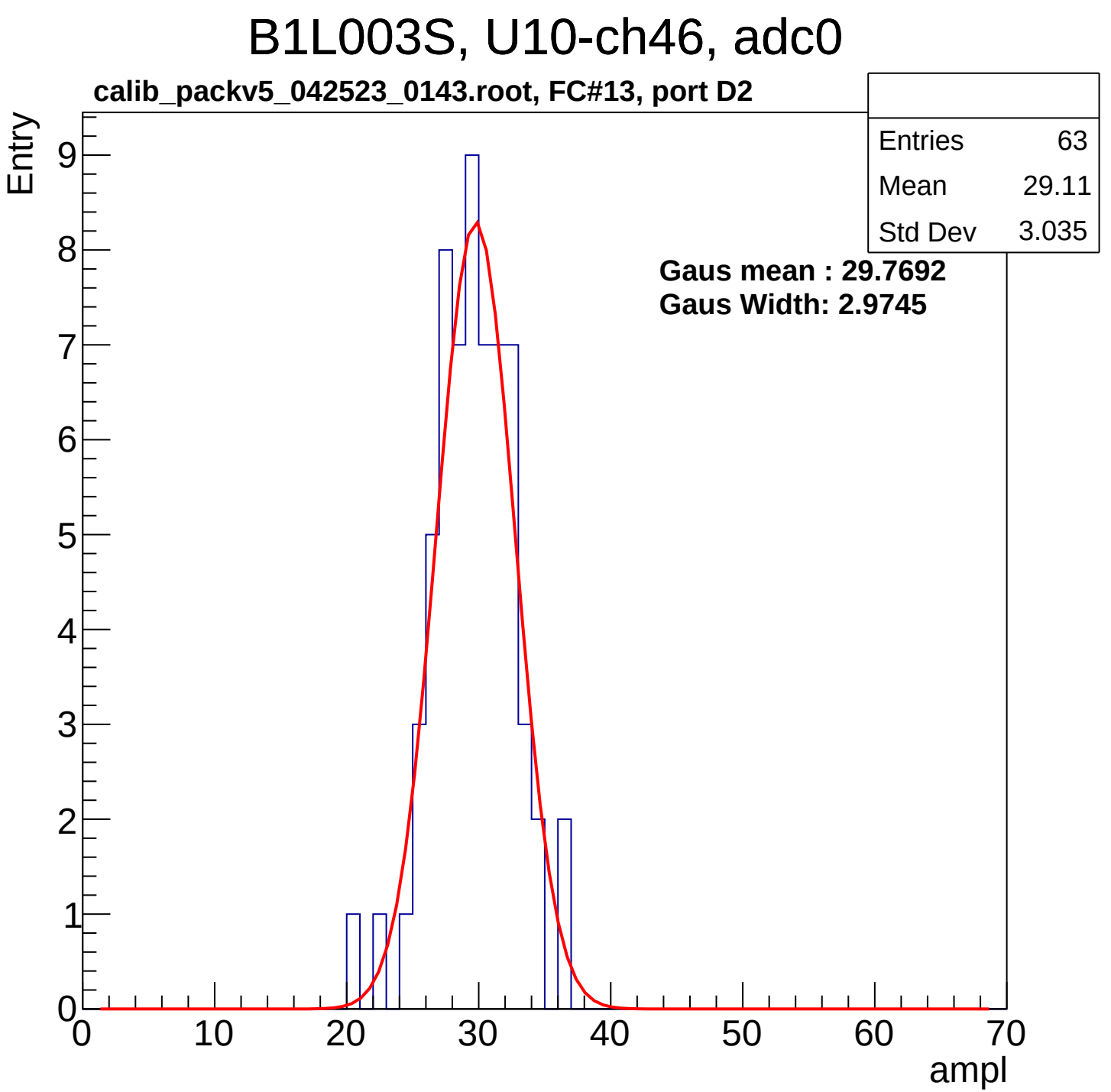
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch45, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch46, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29.11
Std Dev 3.035
Gaus mean : 29.7692
Gaus Width: 2.9745
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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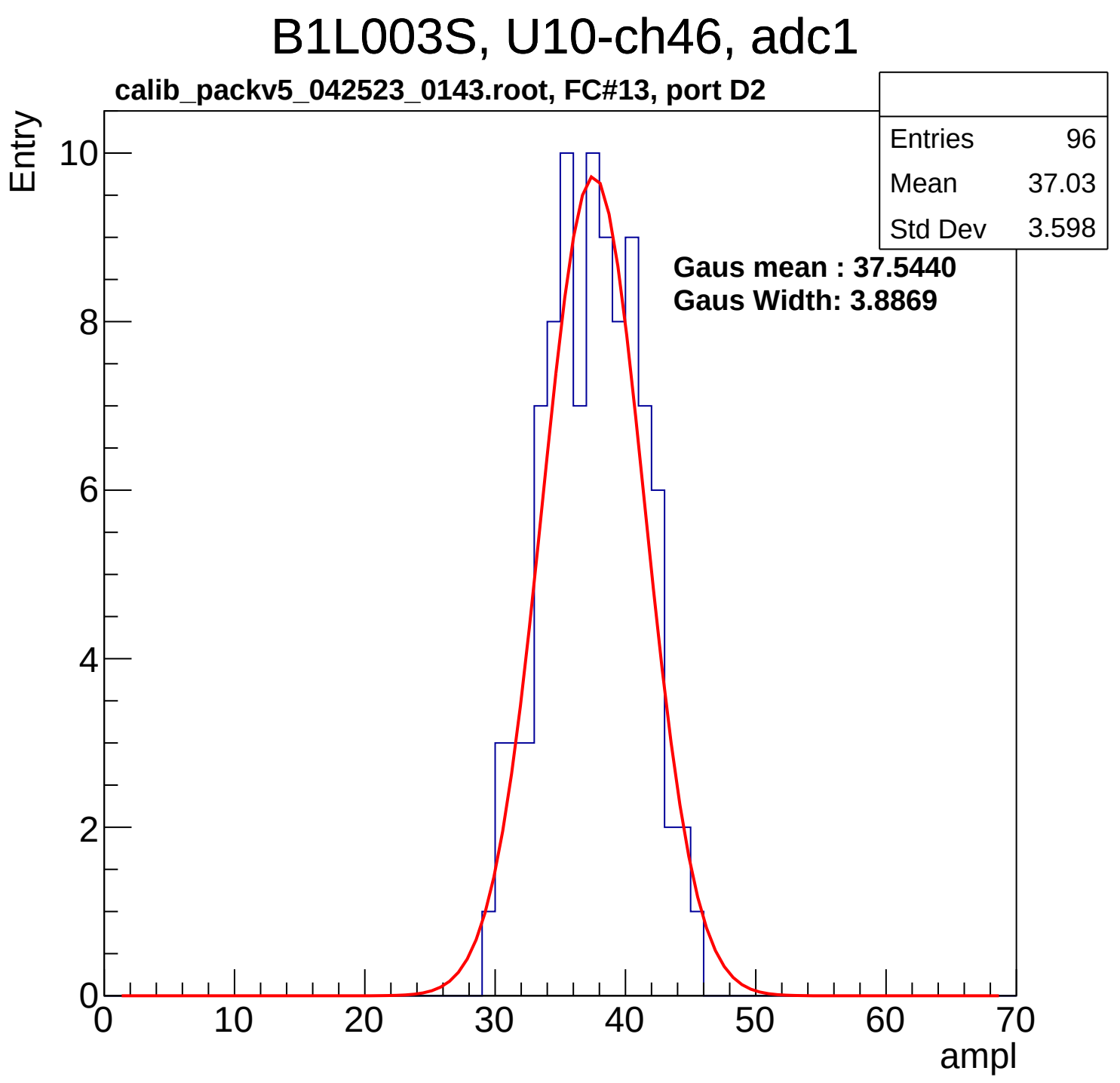
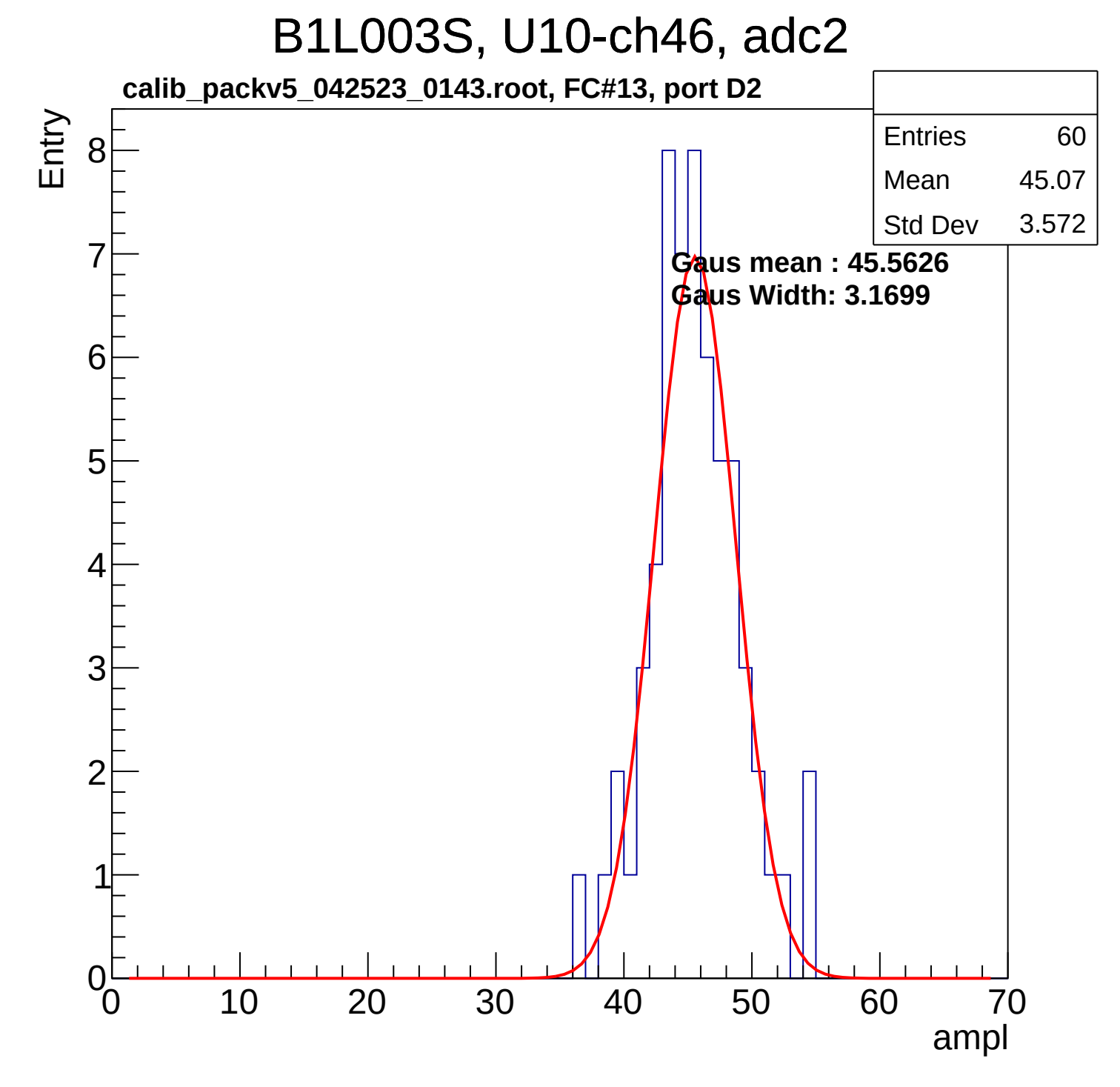
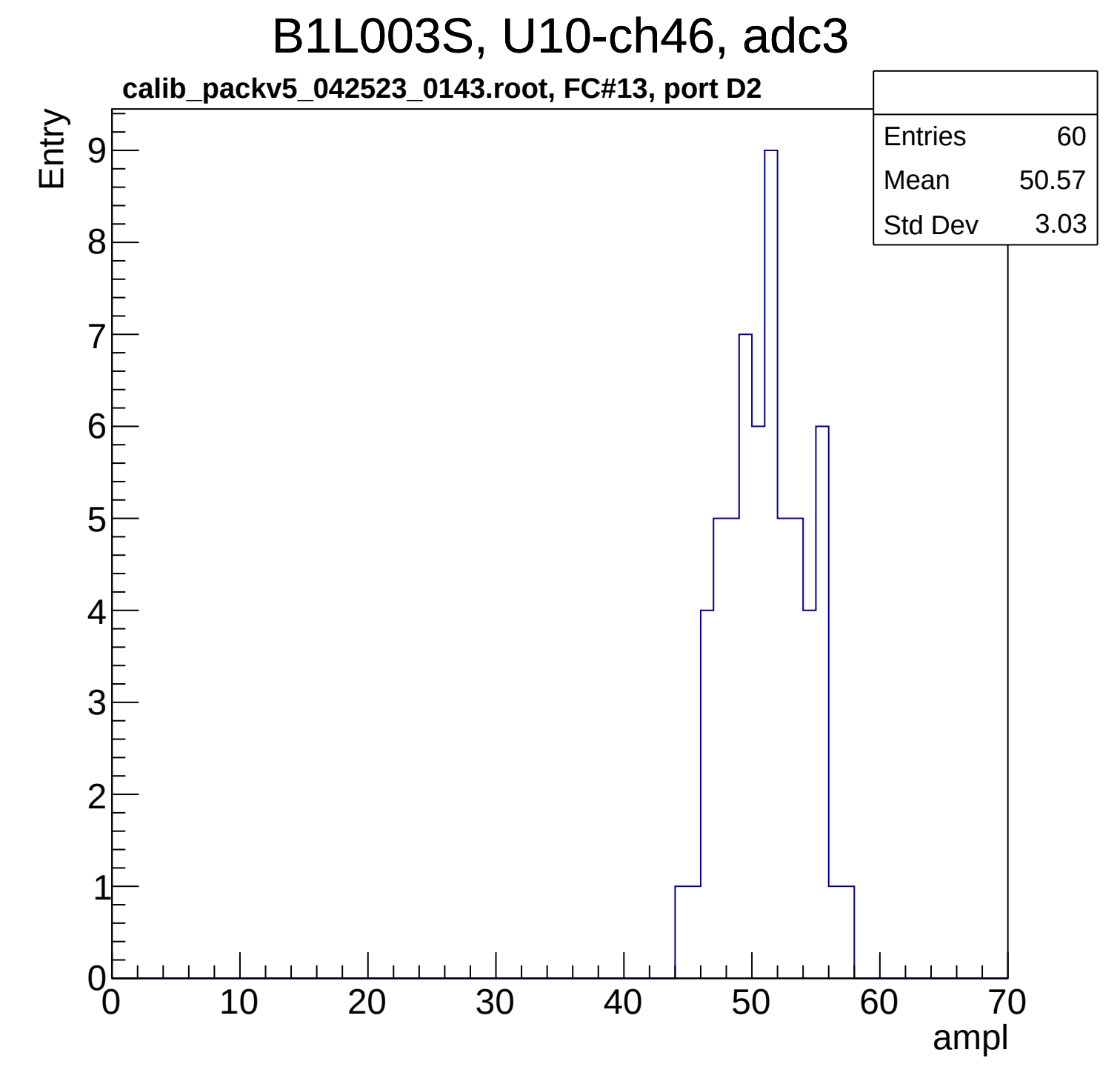

B1L003S, U10-ch46, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 96
Mean 37.03
Std Dev 3.598
Gaus mean : 37.5440
Gaus Width: 3.8869
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch46, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 45.07
Std Dev 3.572
Gaus mean : 45.5626
Gaus Width: 3.1699
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch46, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 50.57
Std Dev 3.03
Entry
ampl
0
1
2
3
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60
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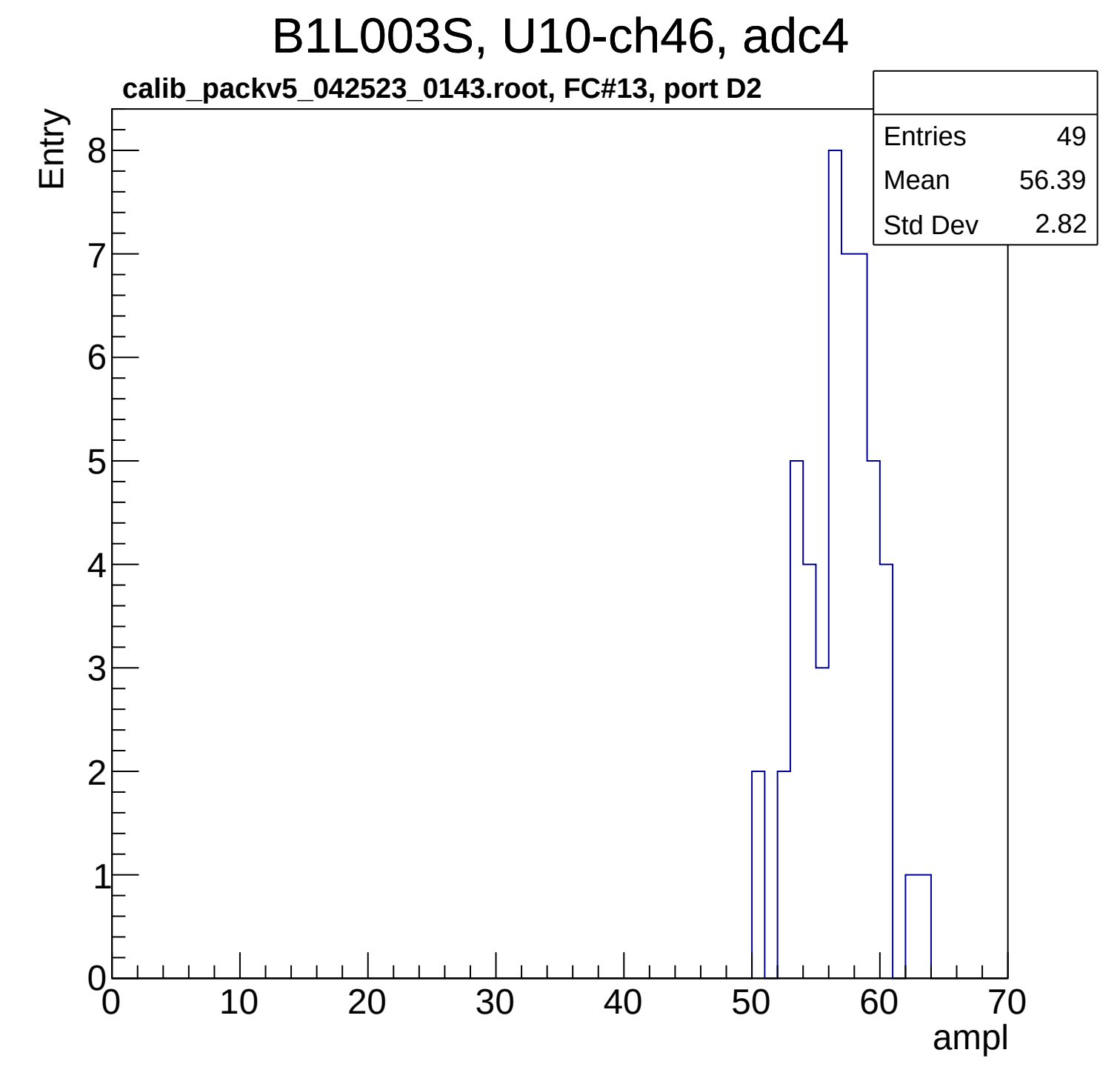
B1L003S, U10-ch46, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 56.39
Std Dev 2.82
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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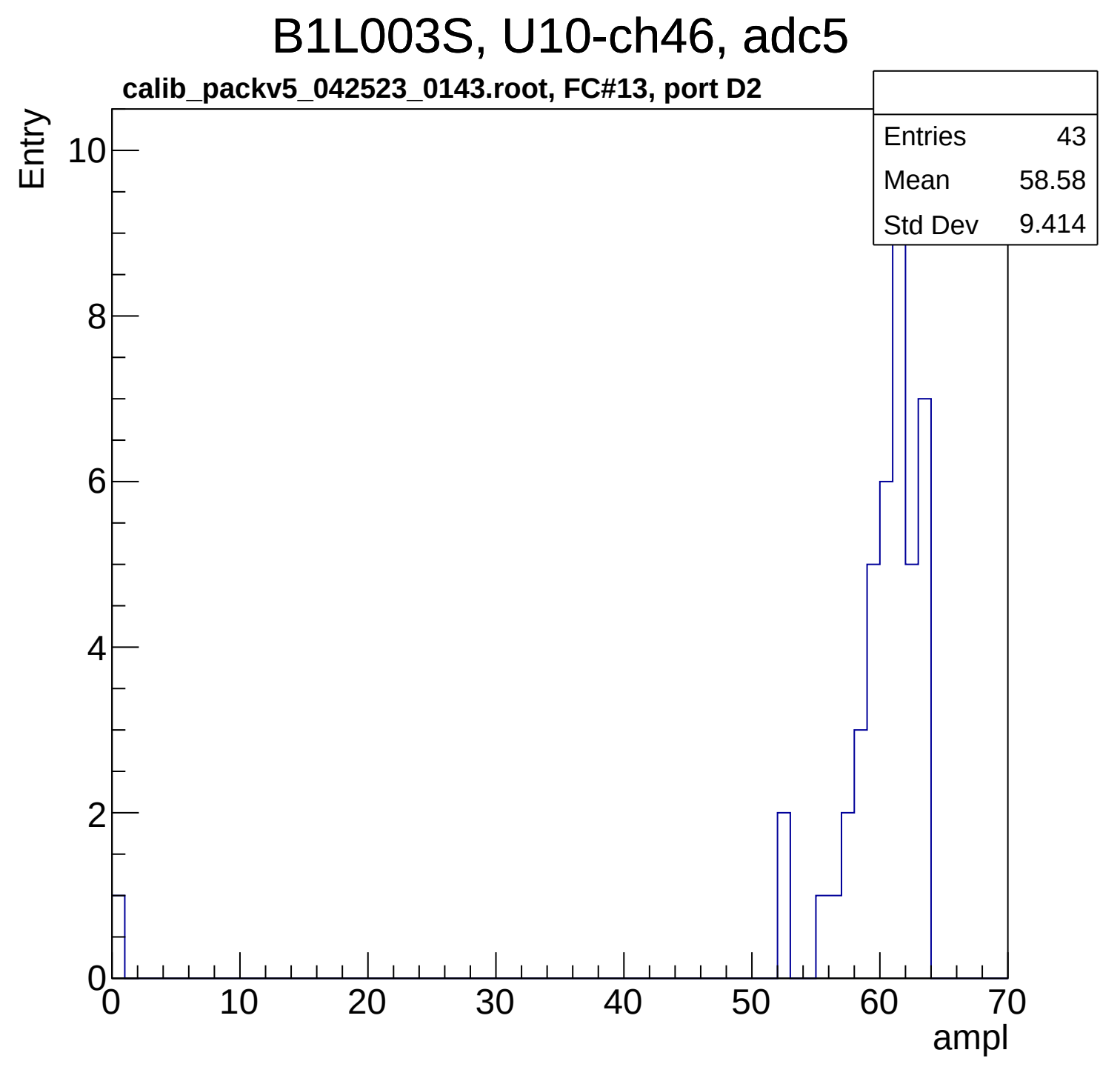
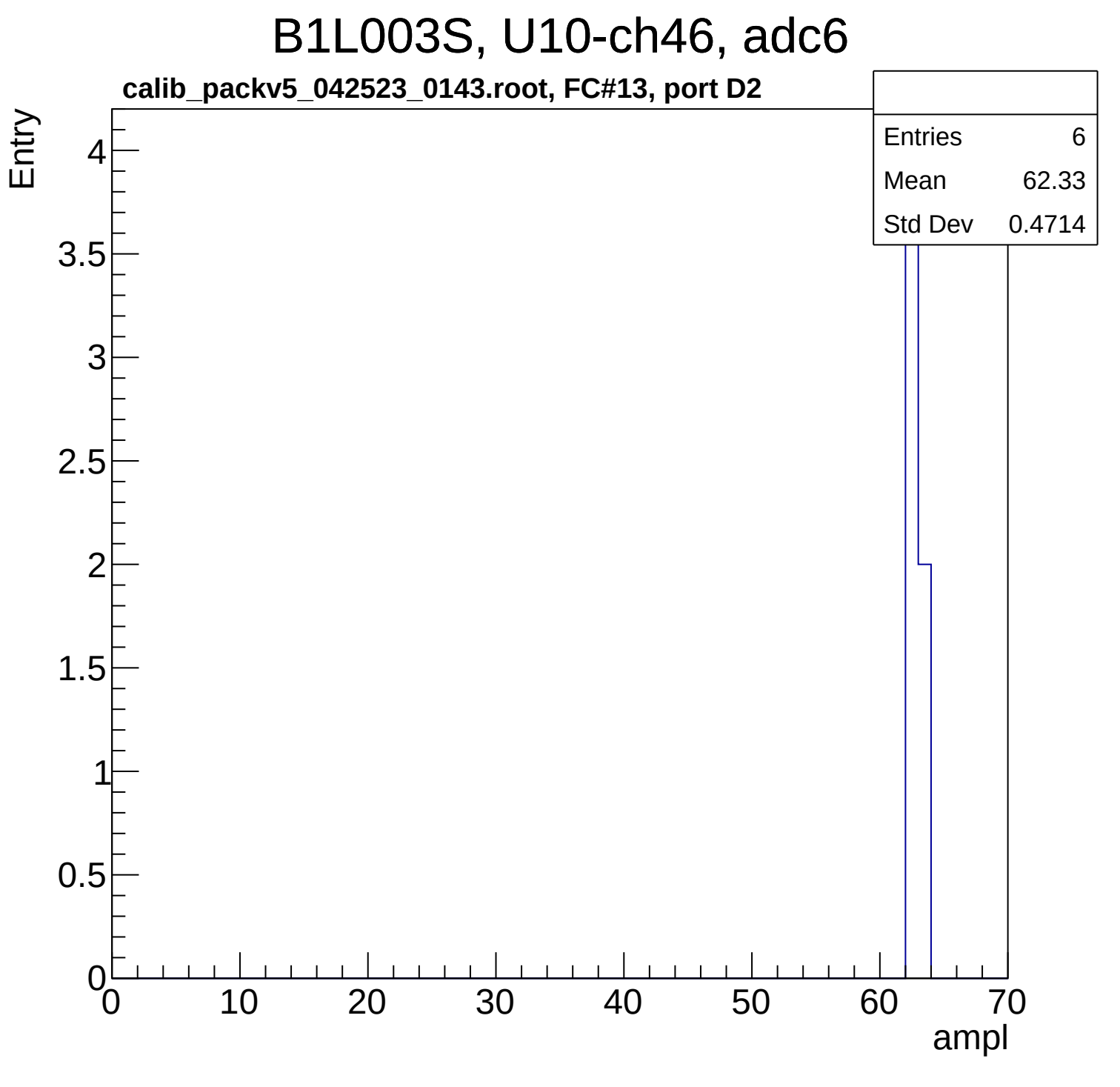
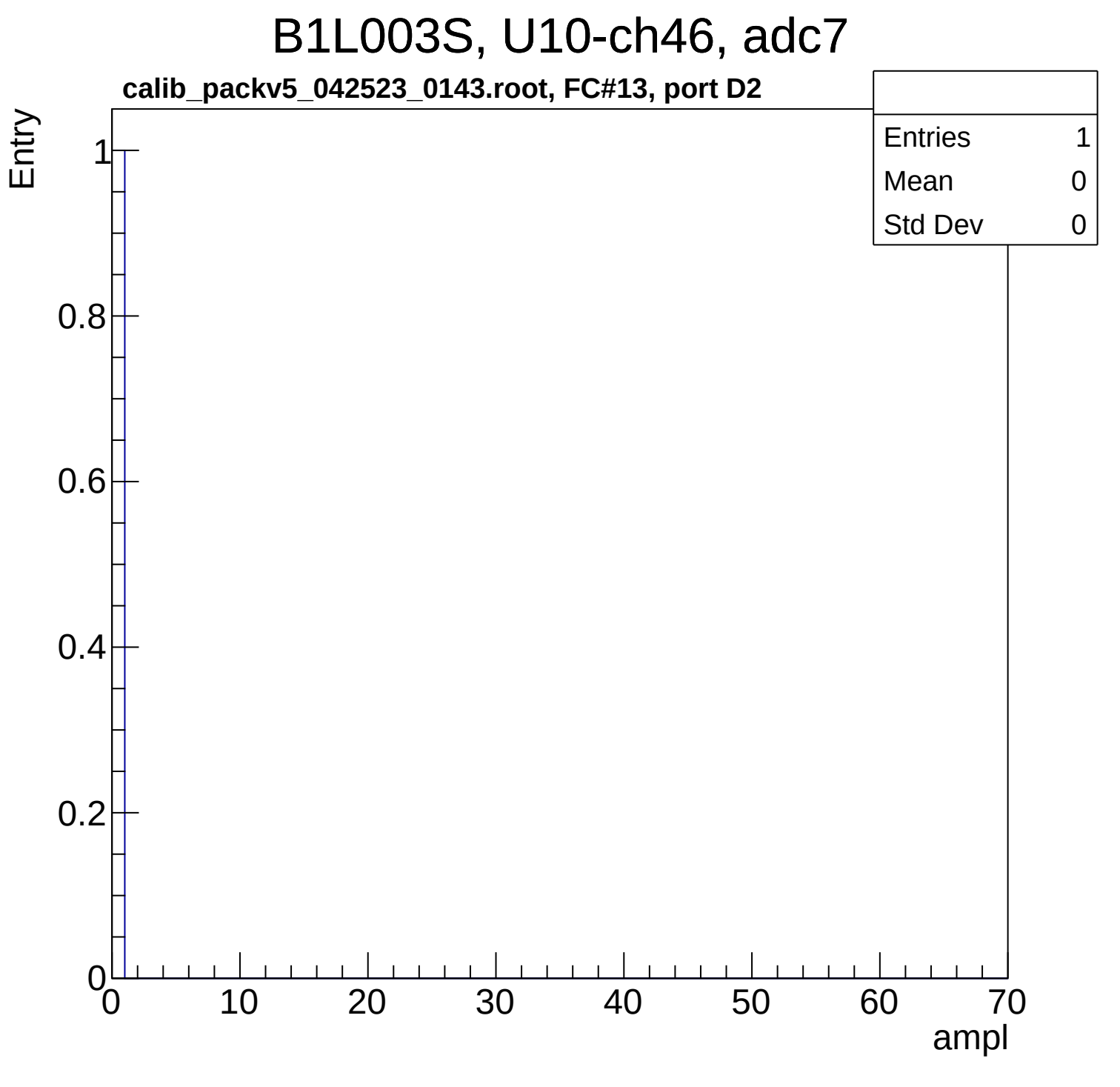
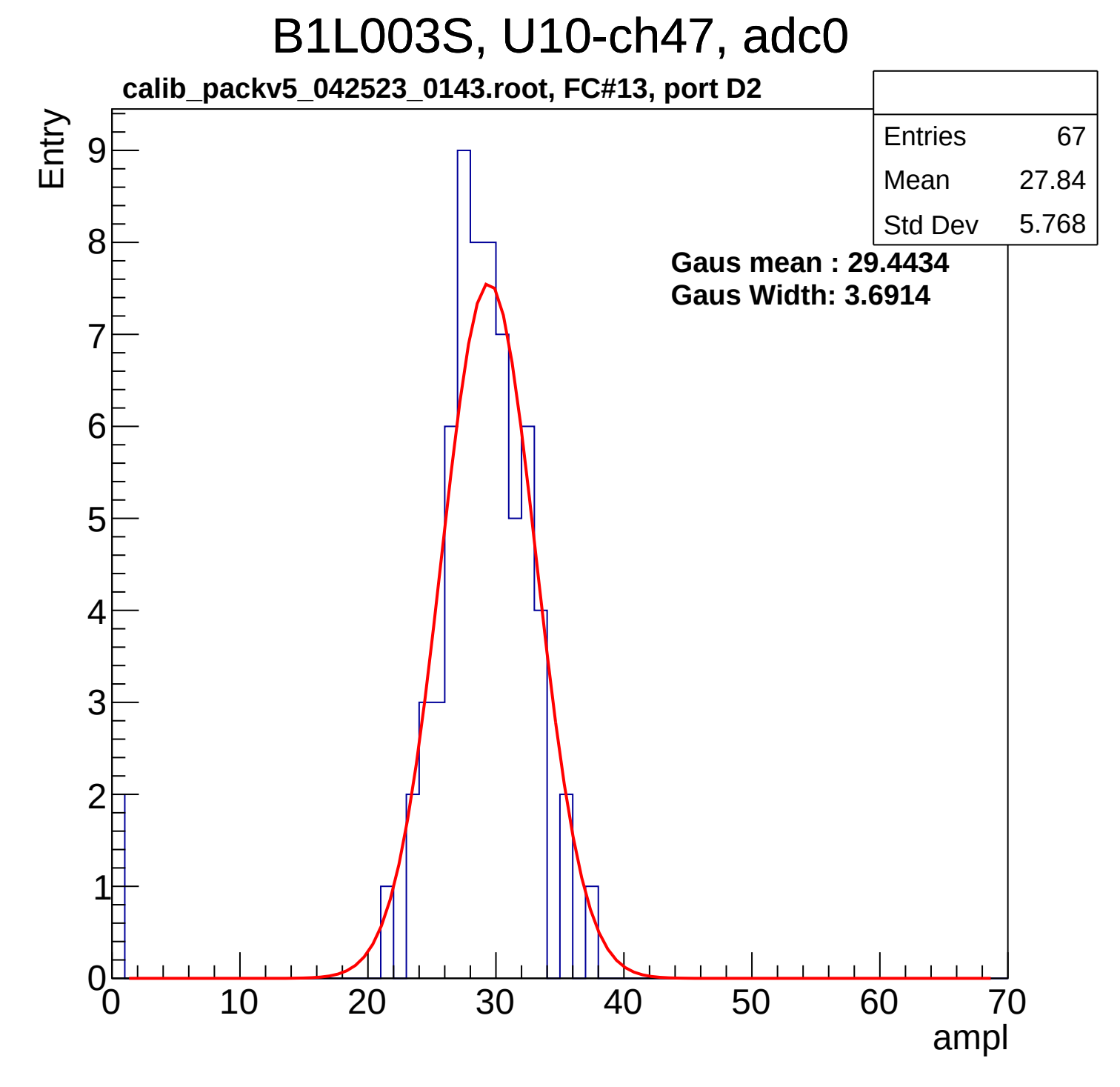
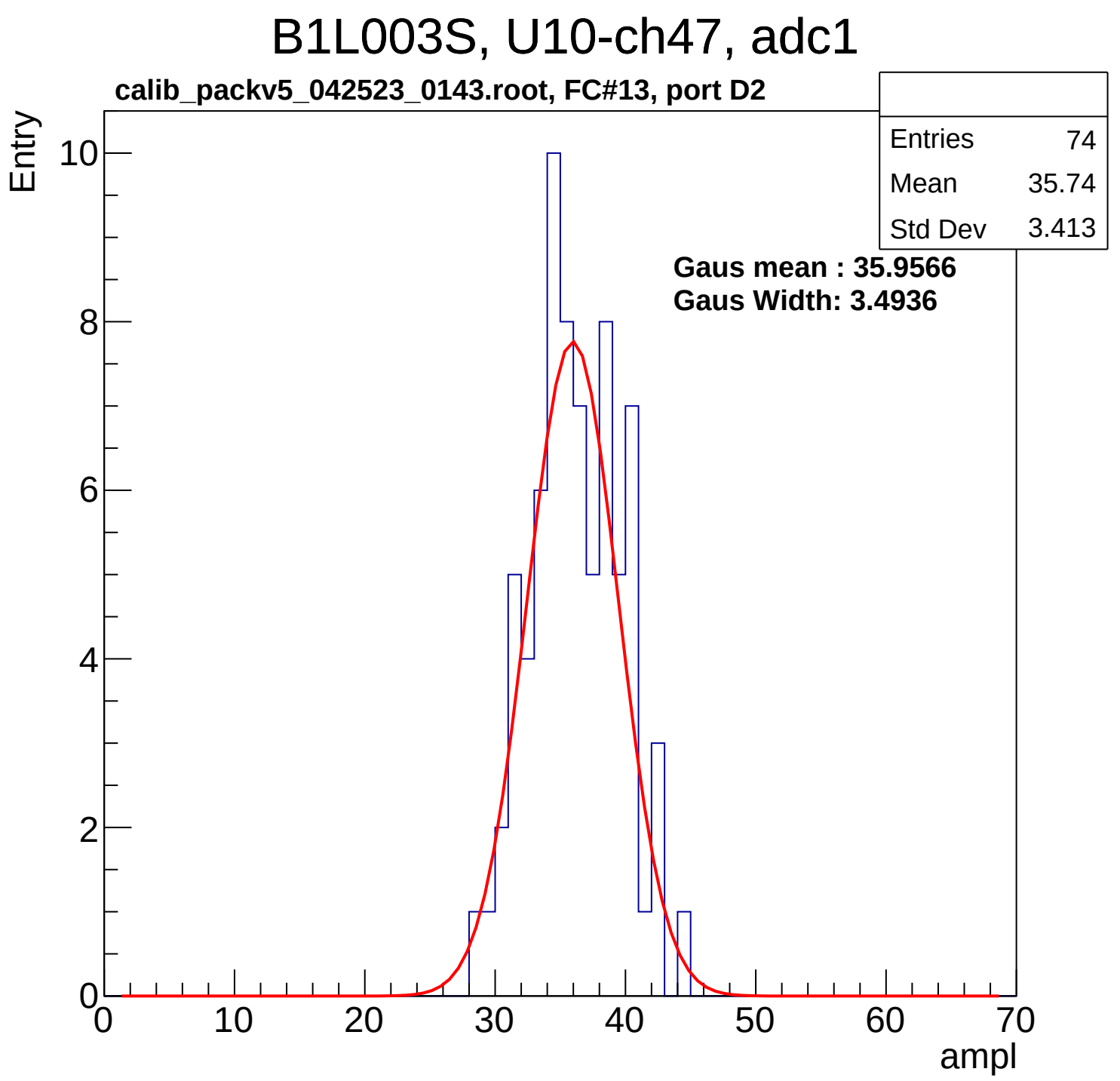
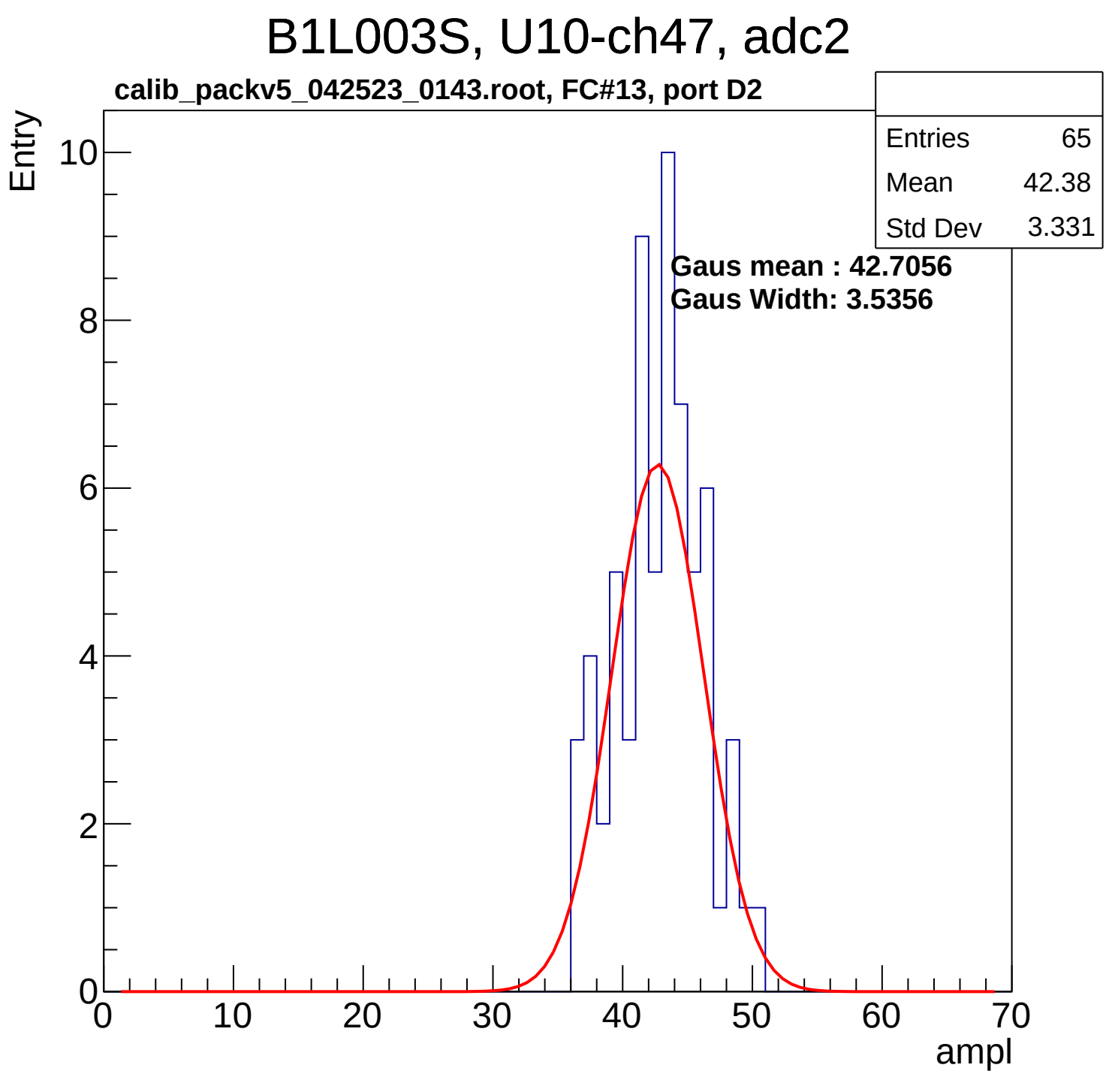

B1L003S, U10-ch46, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 58.58
Std Dev 9.414
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

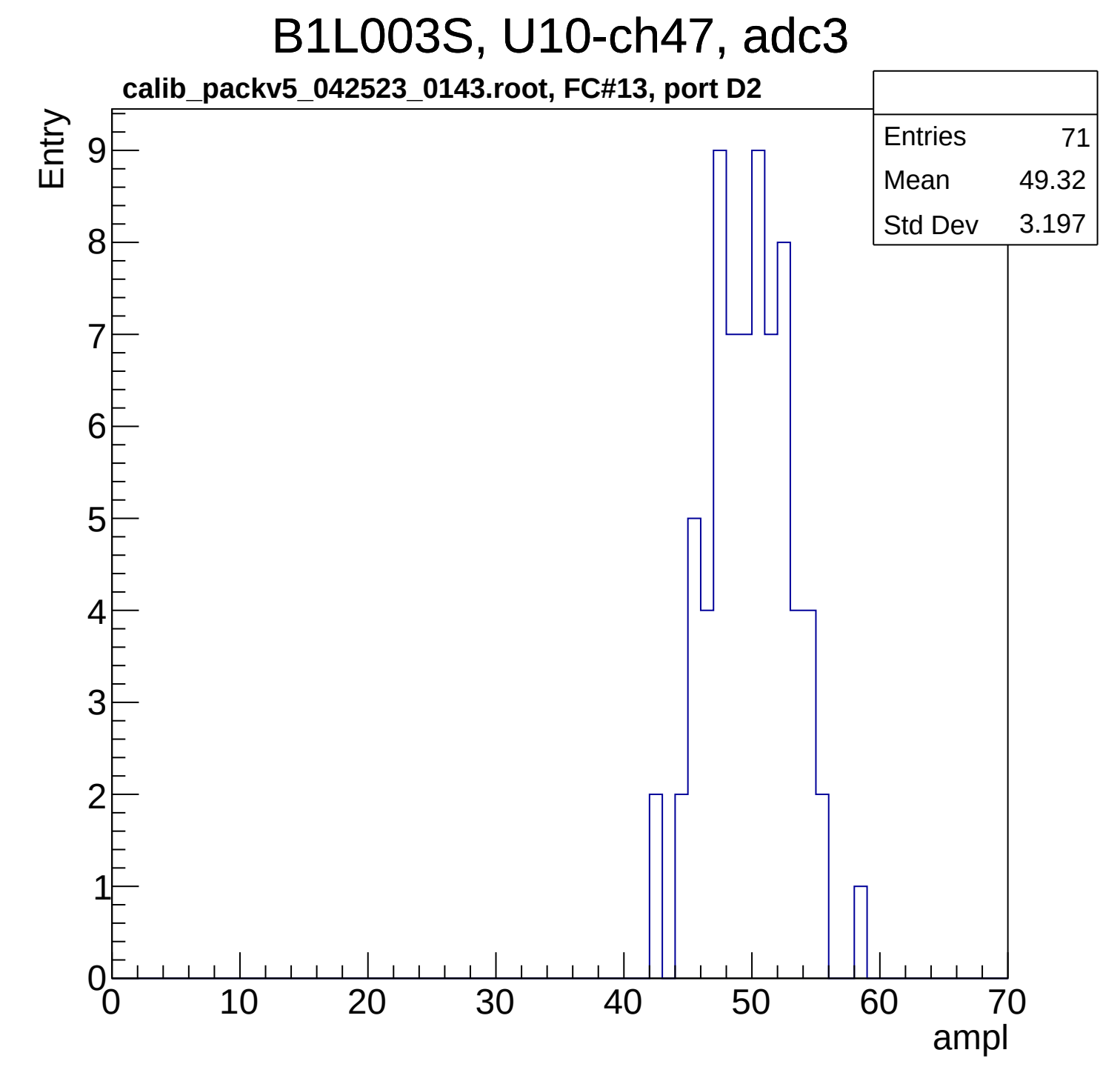

B1L003S, U10-ch46, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

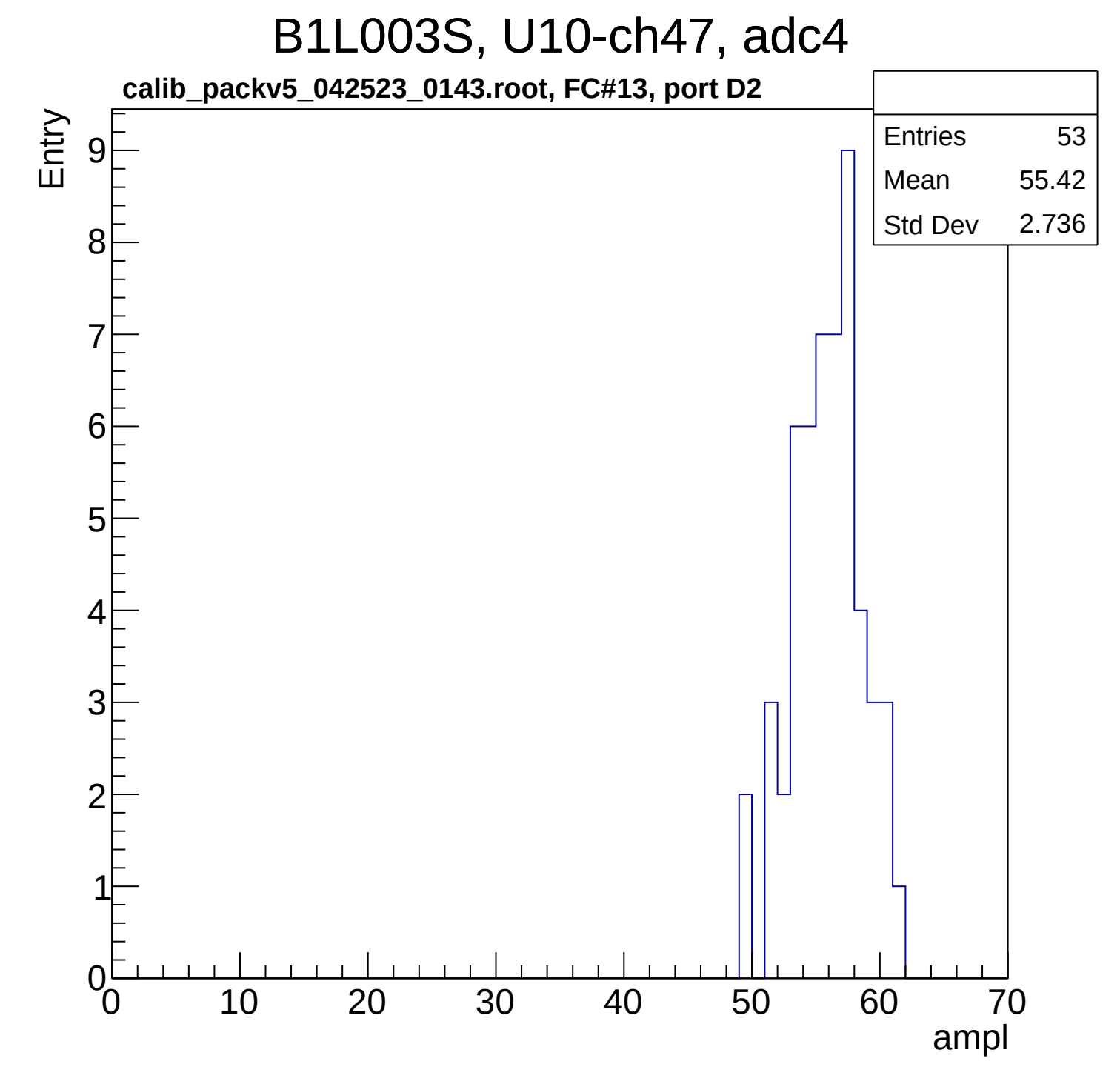

B1L003S, U10-ch46, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

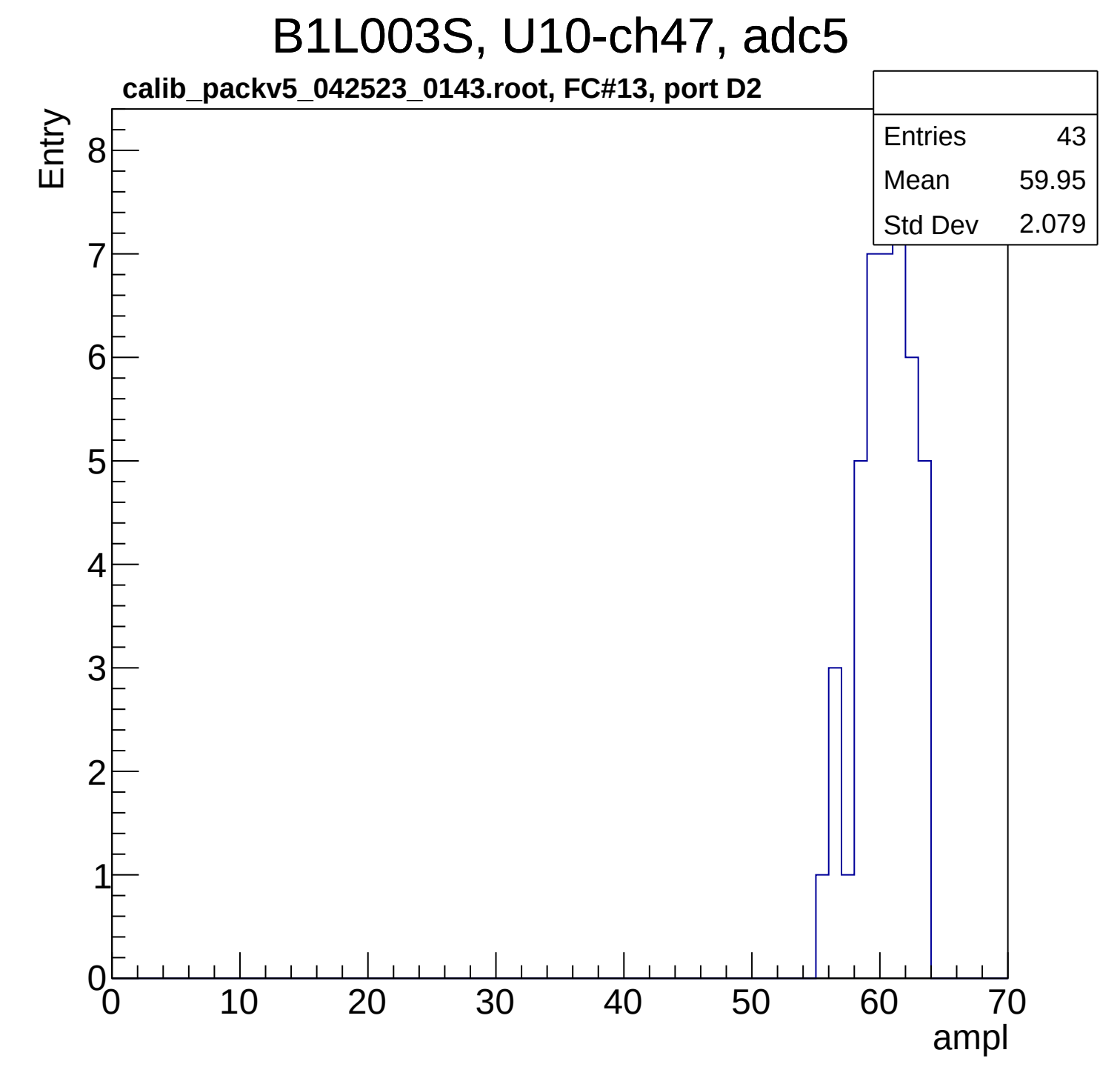

B1L003S, U10-ch47, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 27.84
Std Dev 5.768
Gaus mean : 29.4434
Gaus Width: 3.6914
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch47, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 35.74
Std Dev 3.413
Gaus mean : 35.9566
Gaus Width: 3.4936
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch47, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.38
Std Dev 3.331
Gaus mean : 42.7056
Gaus Width: 3.5356
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch47, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.32
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch47, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.42
Std Dev 2.736
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch47, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.95
Std Dev 2.079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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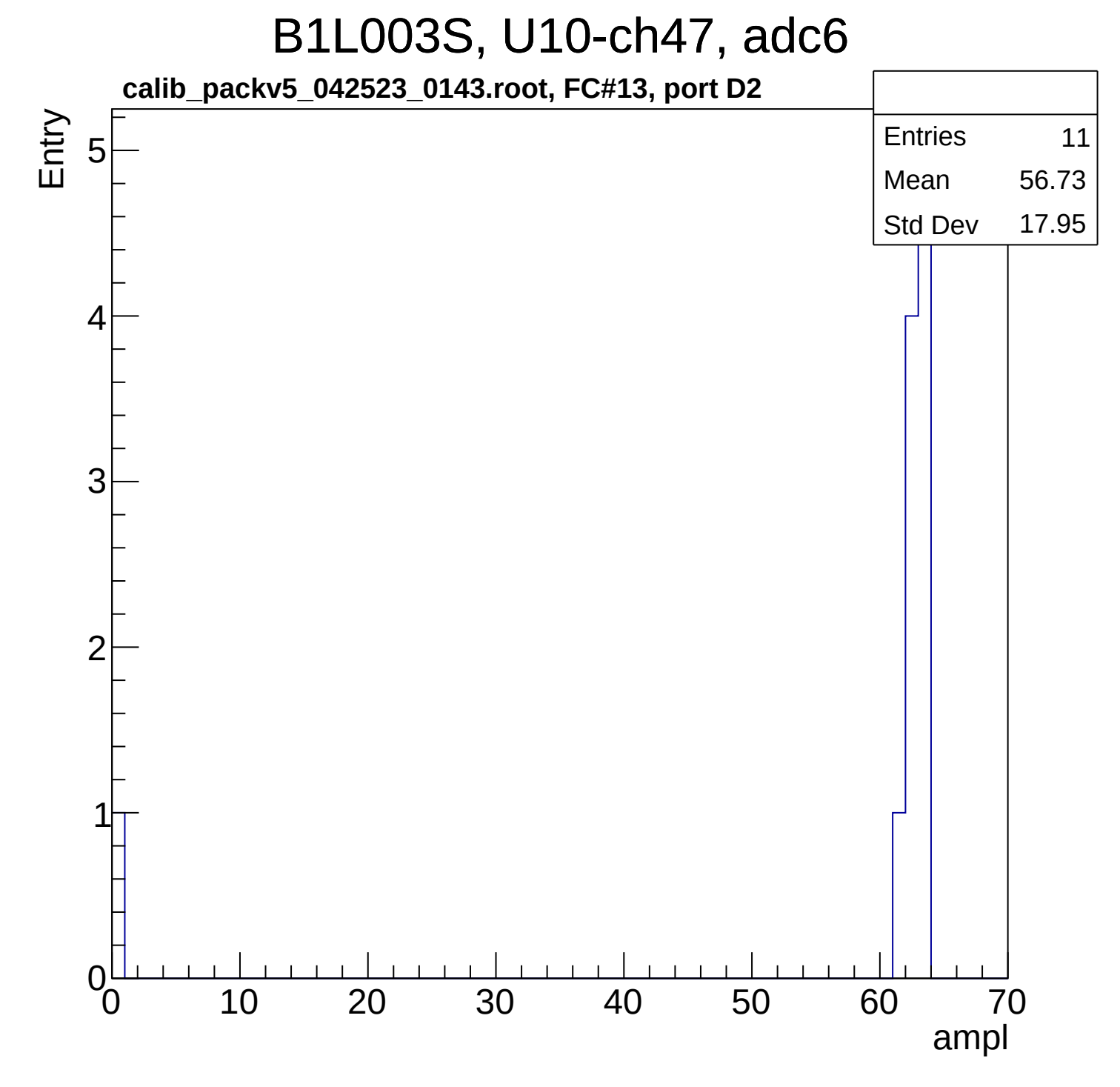
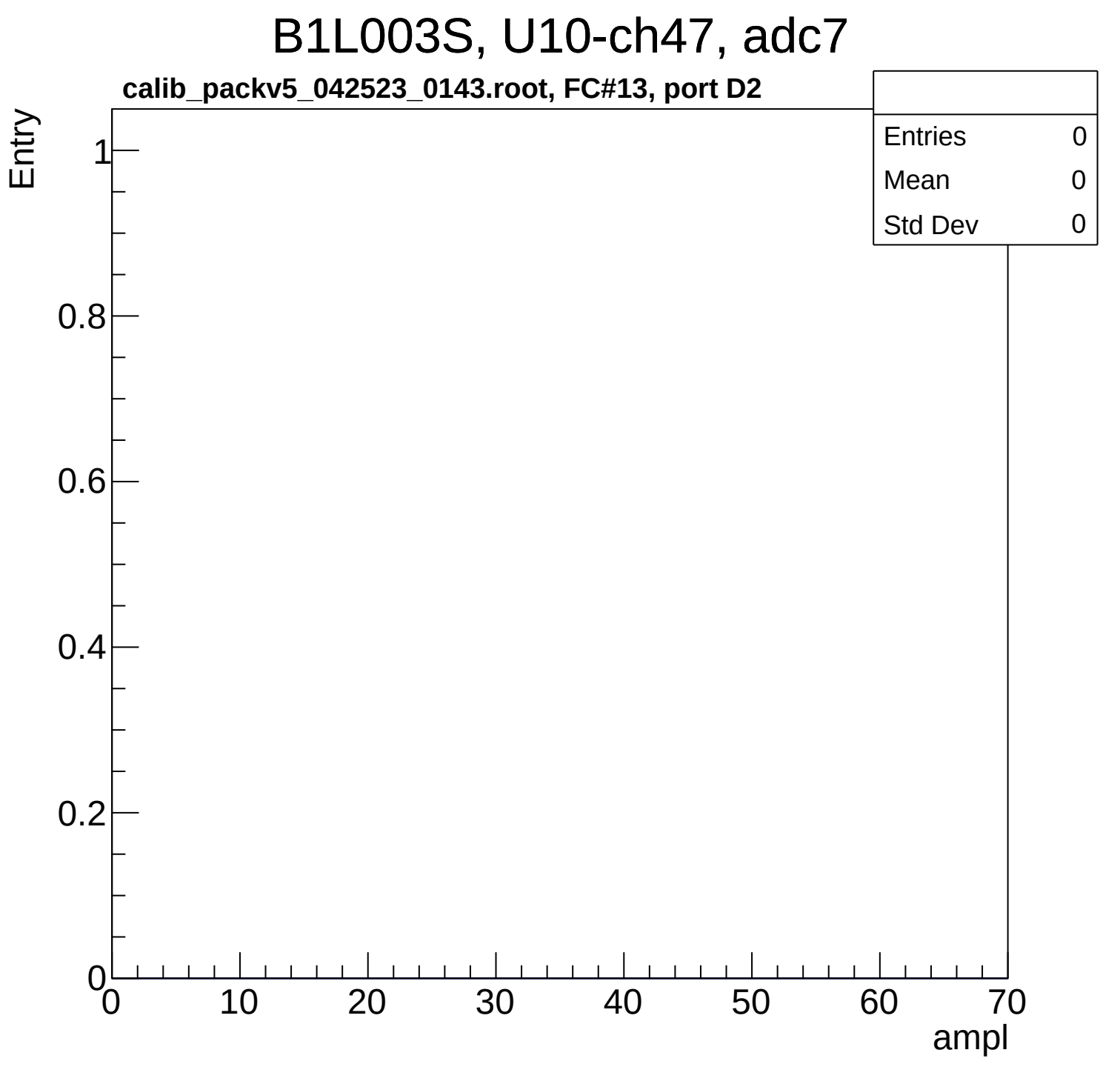
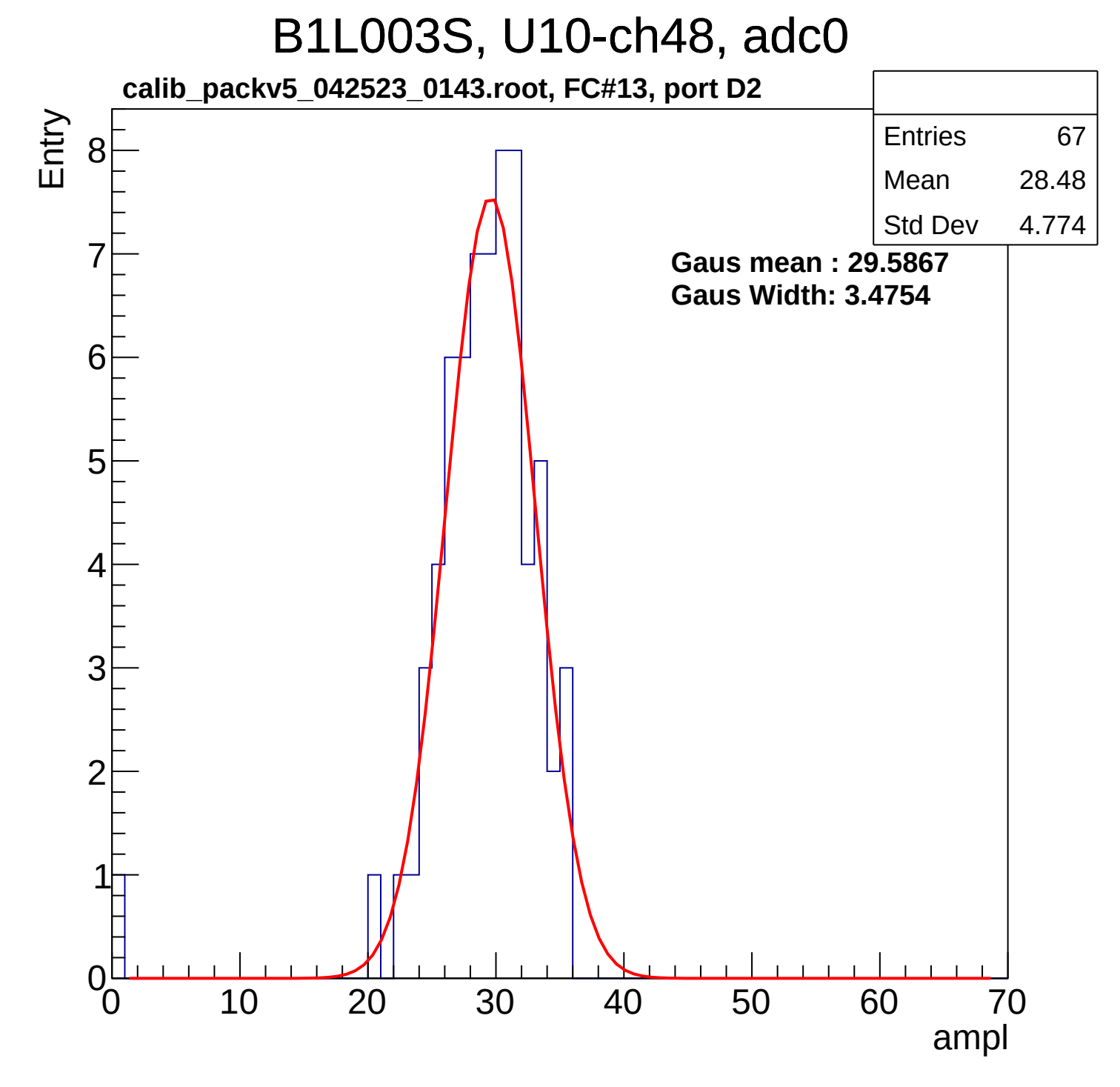
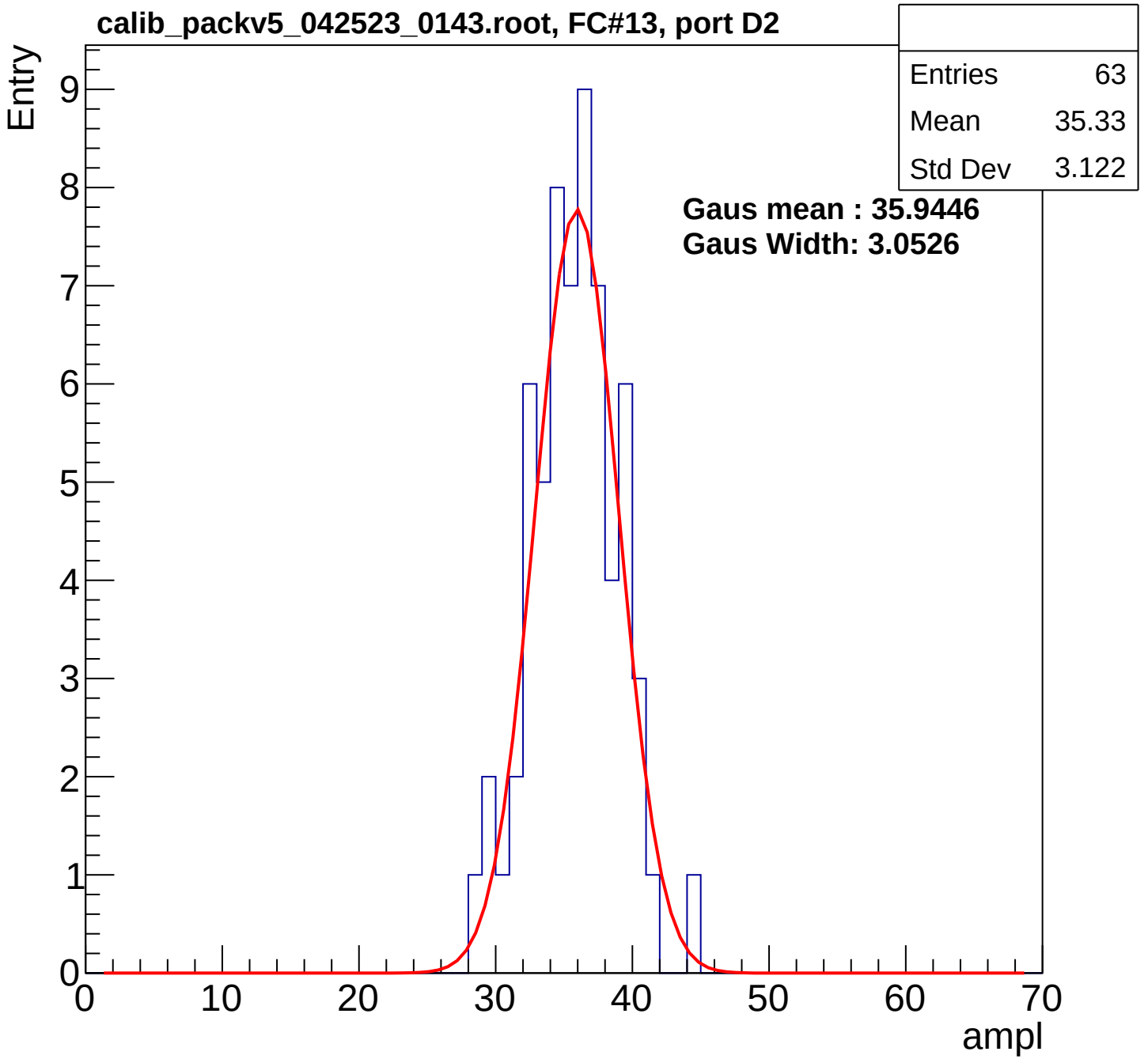
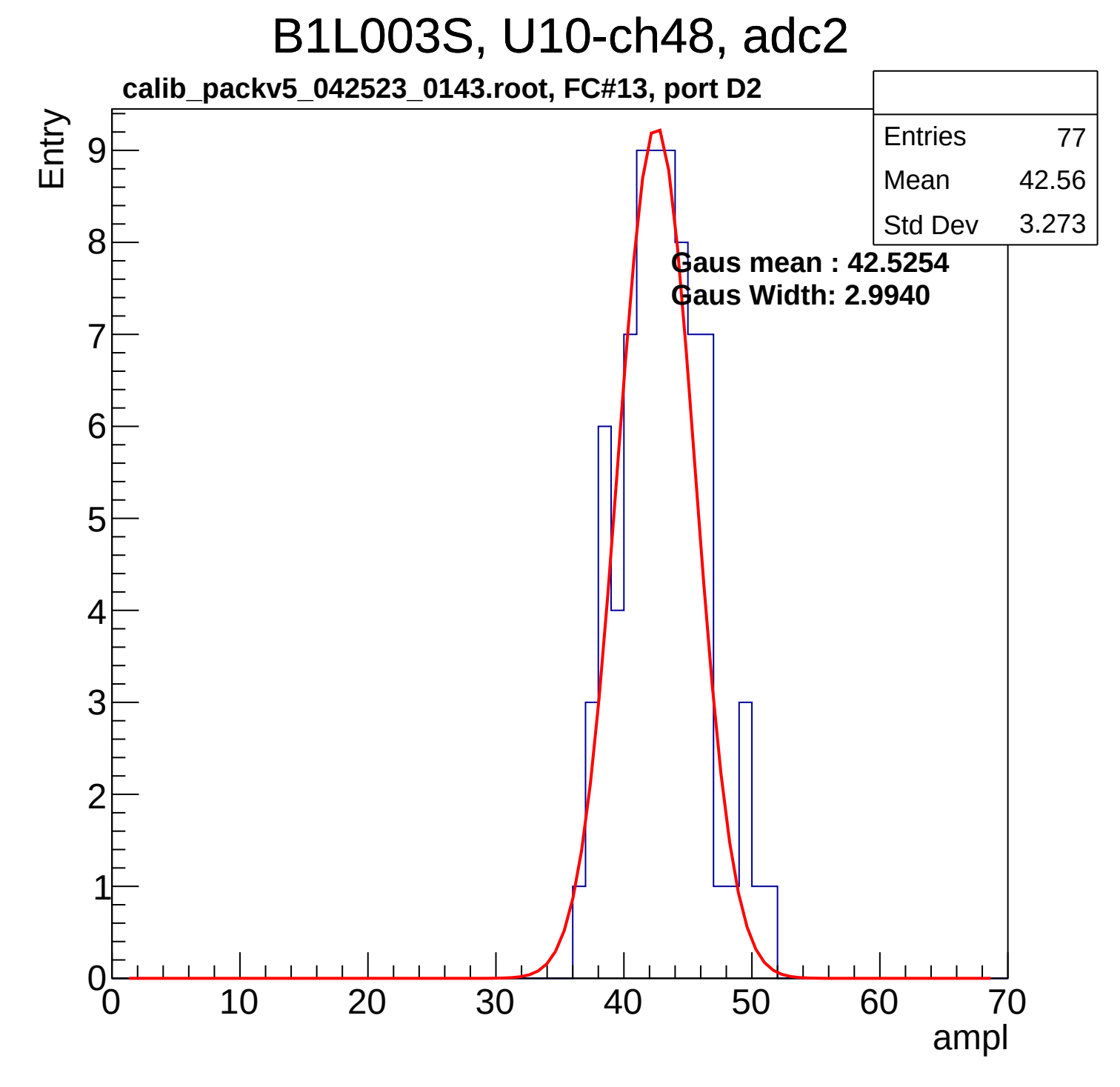
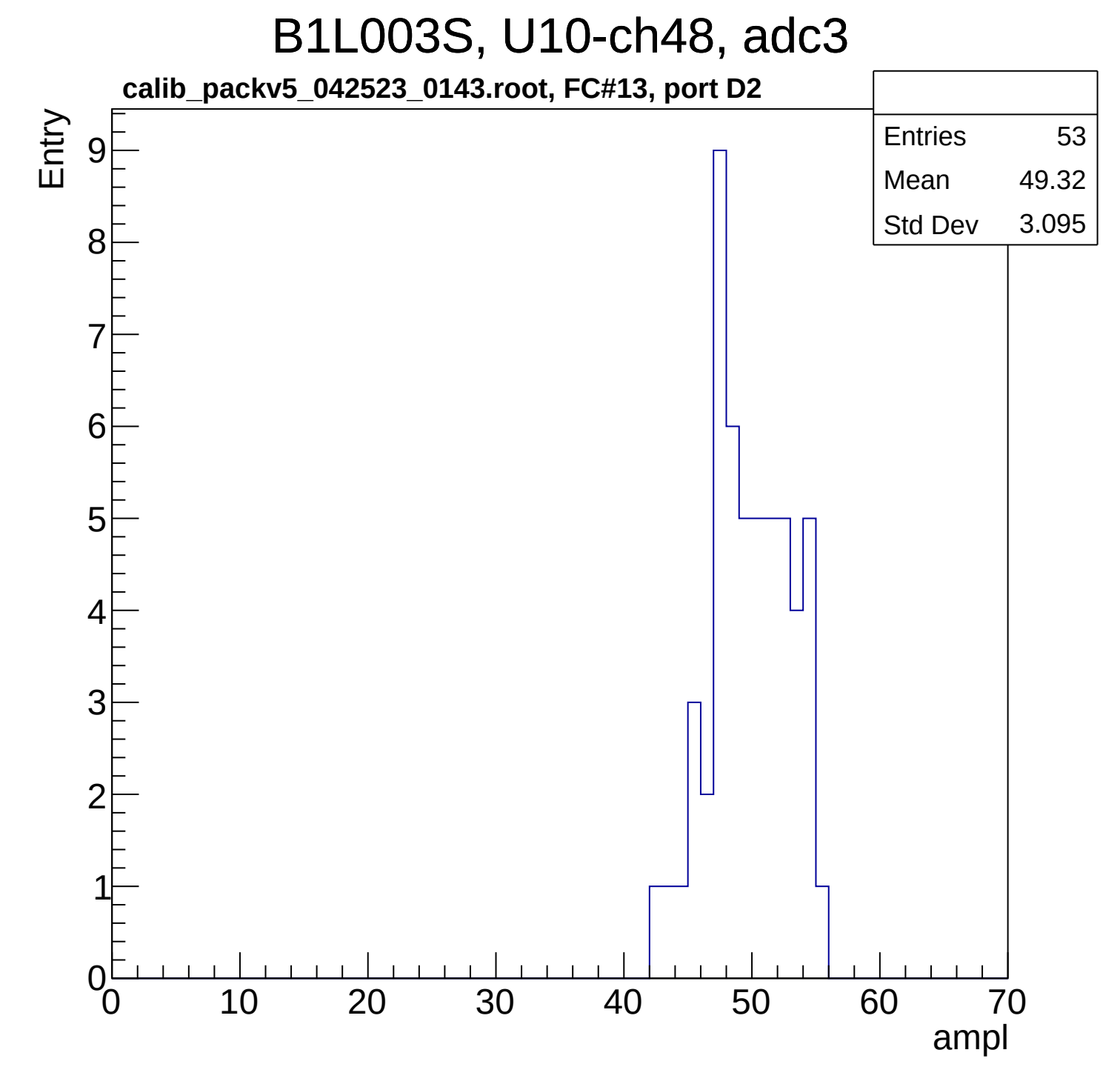

B1L003S, U10-ch47, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.73
Std Dev 17.95
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

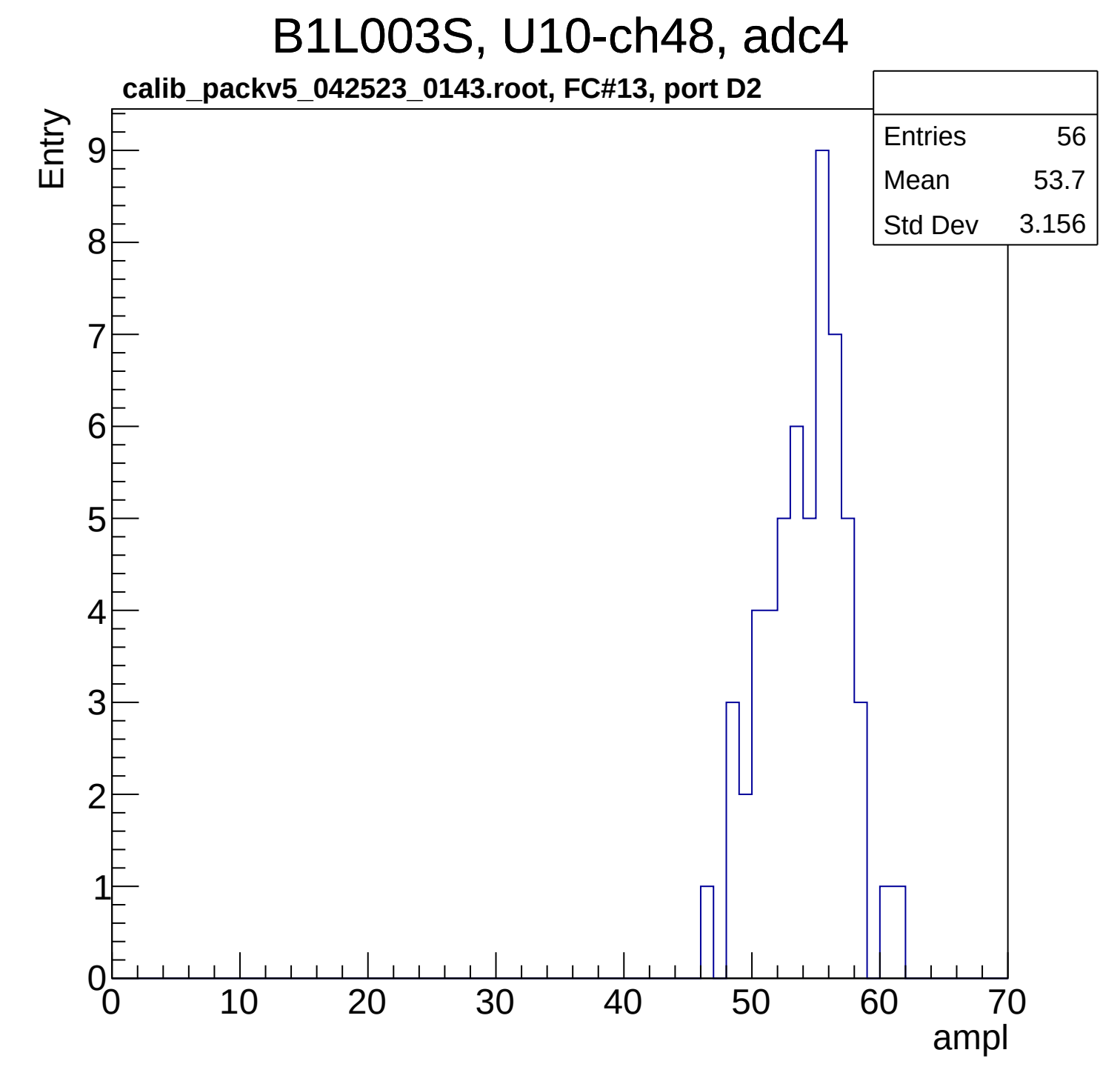

B1L003S, U10-ch47, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch48, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 28.48
Std Dev 4.774
Gaus mean : 29.5867
Gaus Width: 3.4754
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch48, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 35.33
Std Dev 3.122
Gaus mean : 35.9446
Gaus Width: 3.0526
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch48, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 42.56
Std Dev 3.273
Gaus mean : 42.5254
Gaus Width: 2.9940
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch48, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 49.32
Std Dev 3.095
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch48, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 53.7
Std Dev 3.156
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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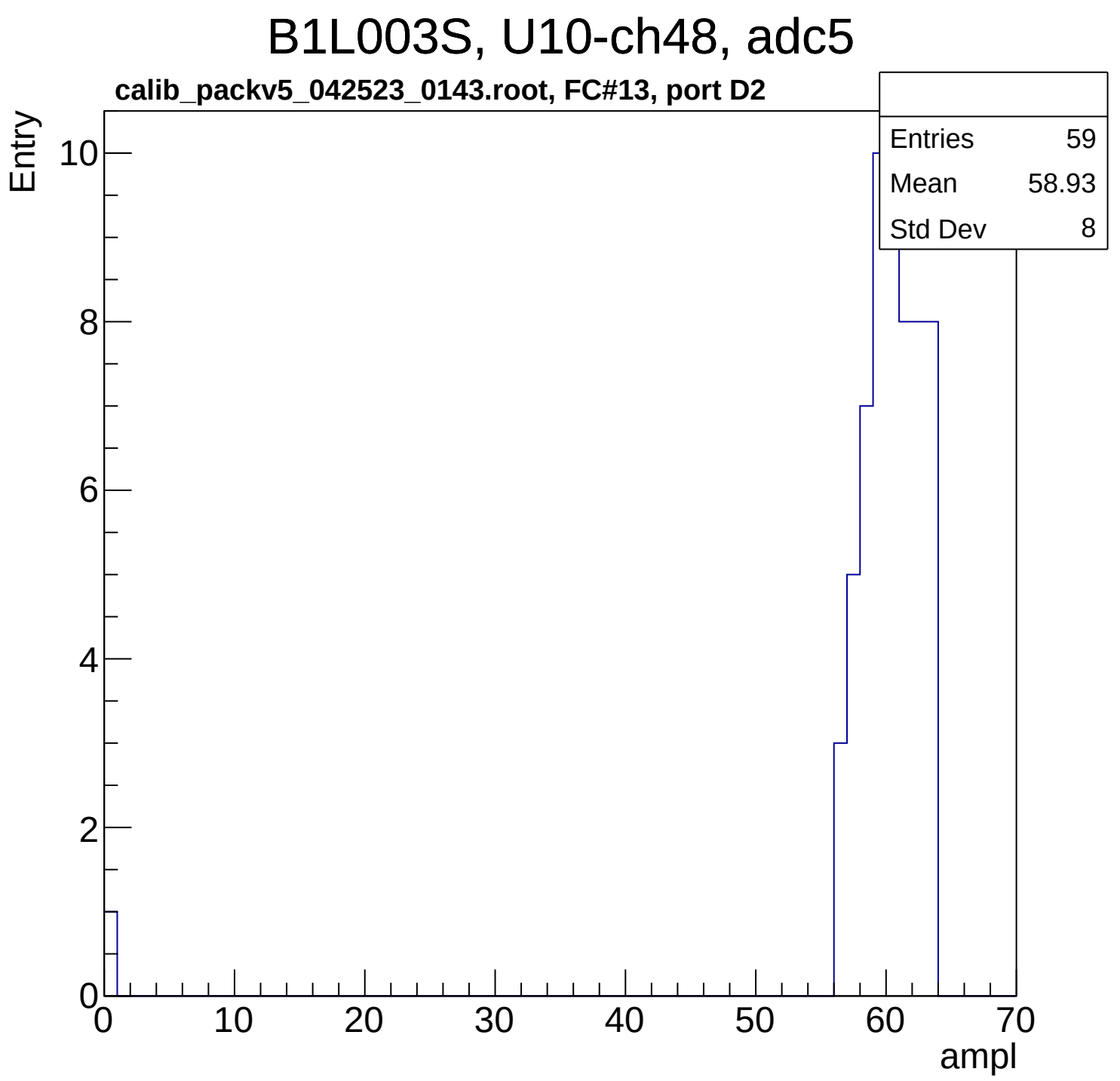
B1L003S, U10-ch48, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.93
Std Dev 8
Entry
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2
4
6
8
10
0
10
20
30
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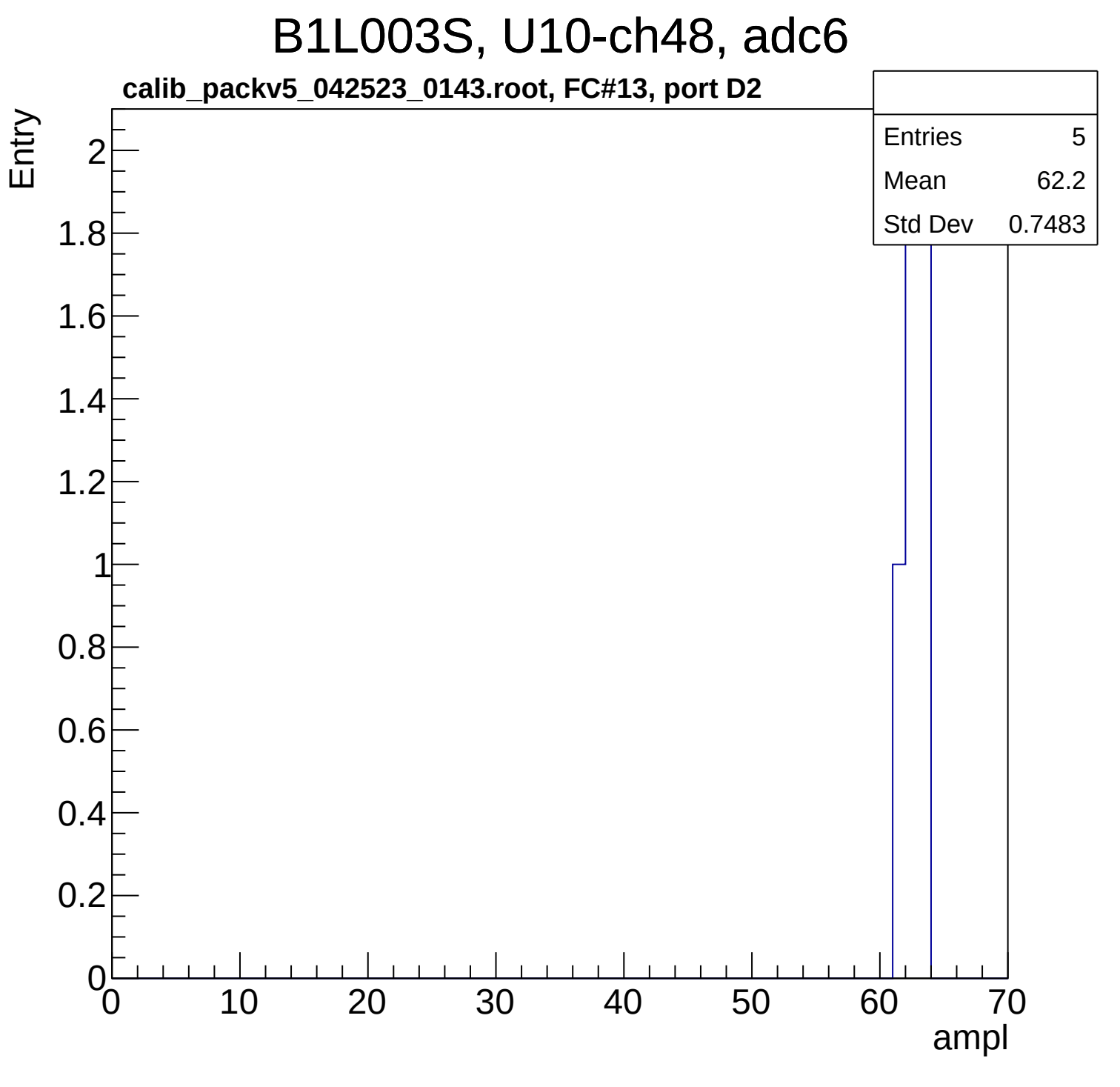
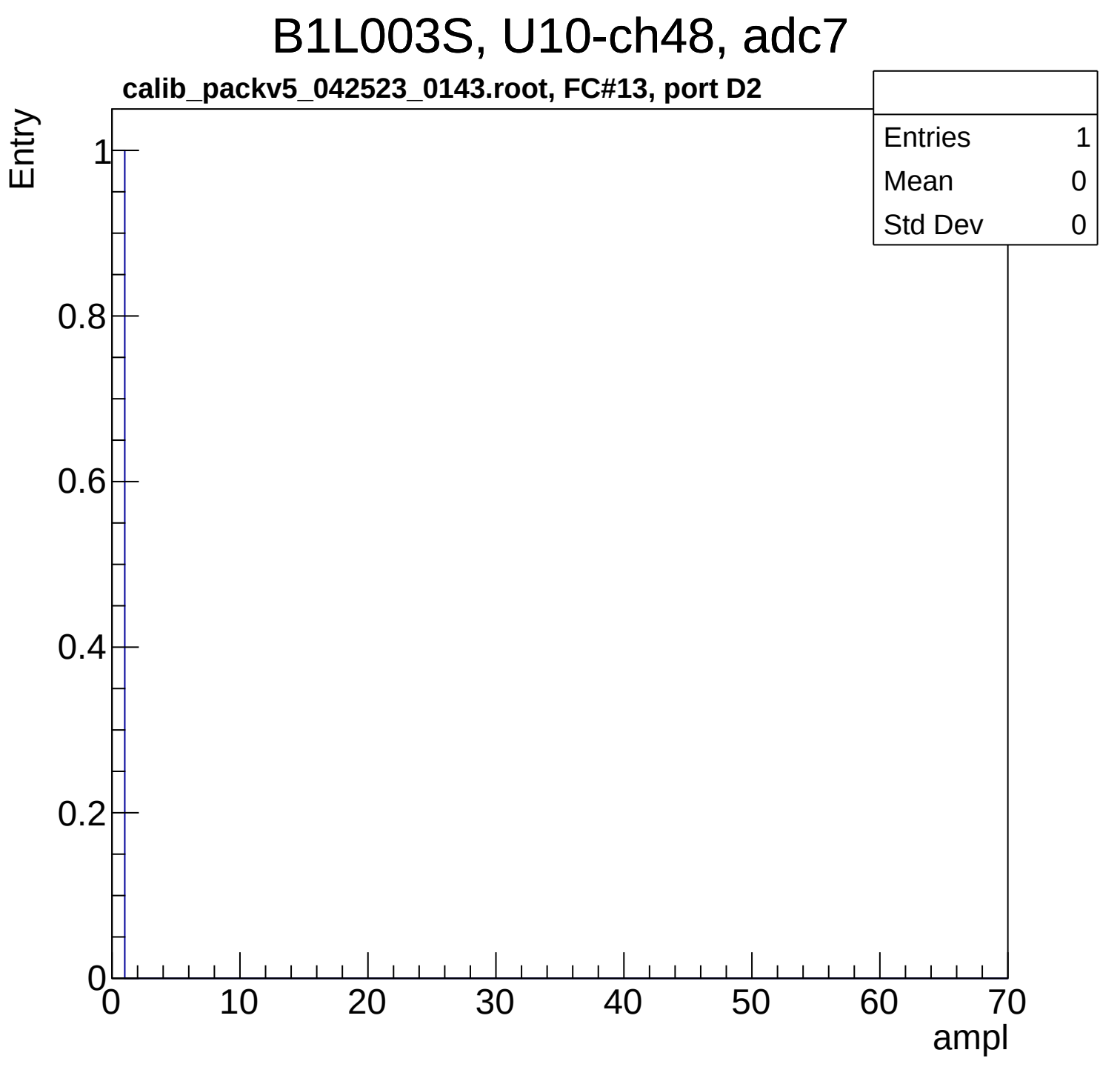
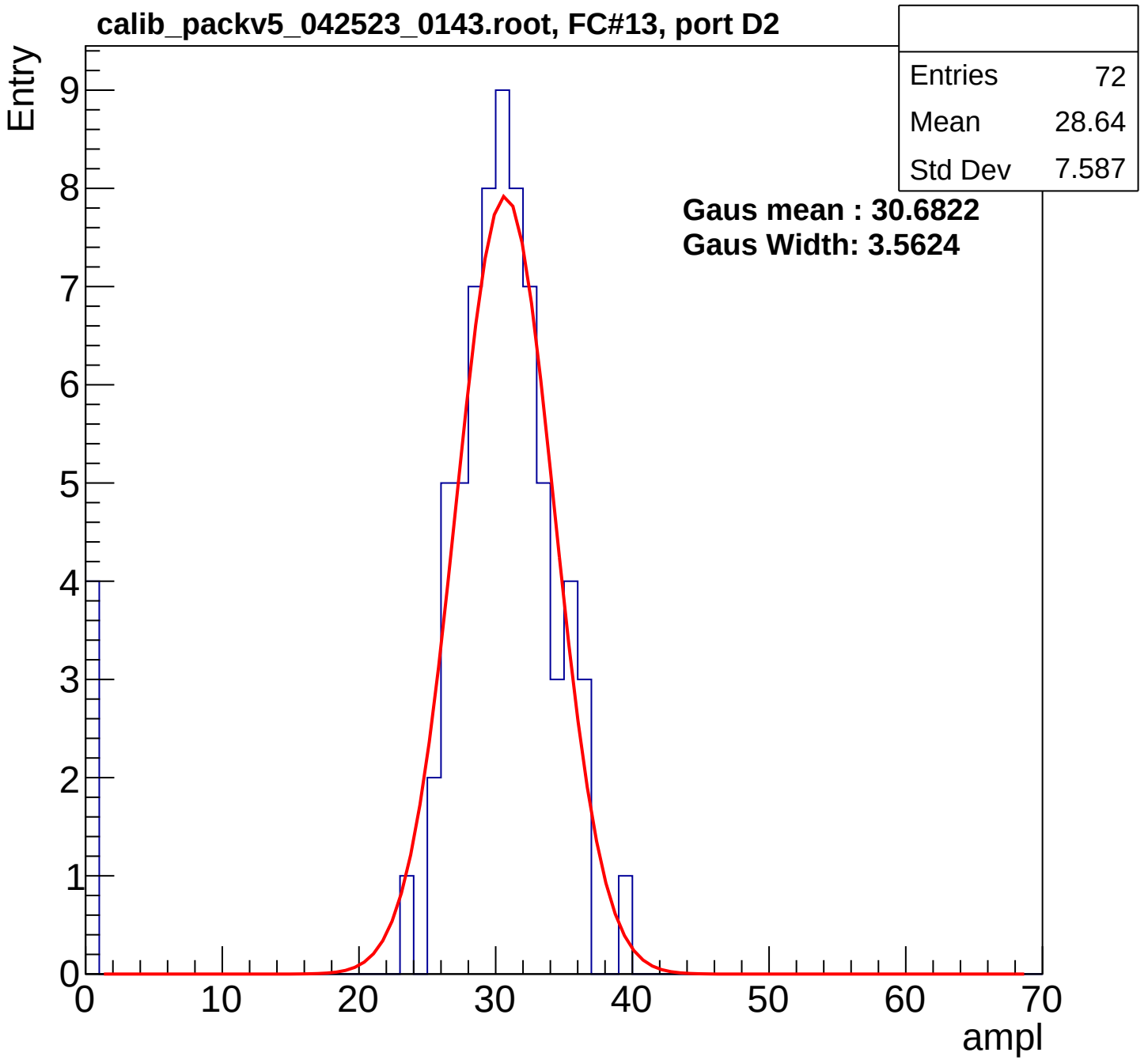
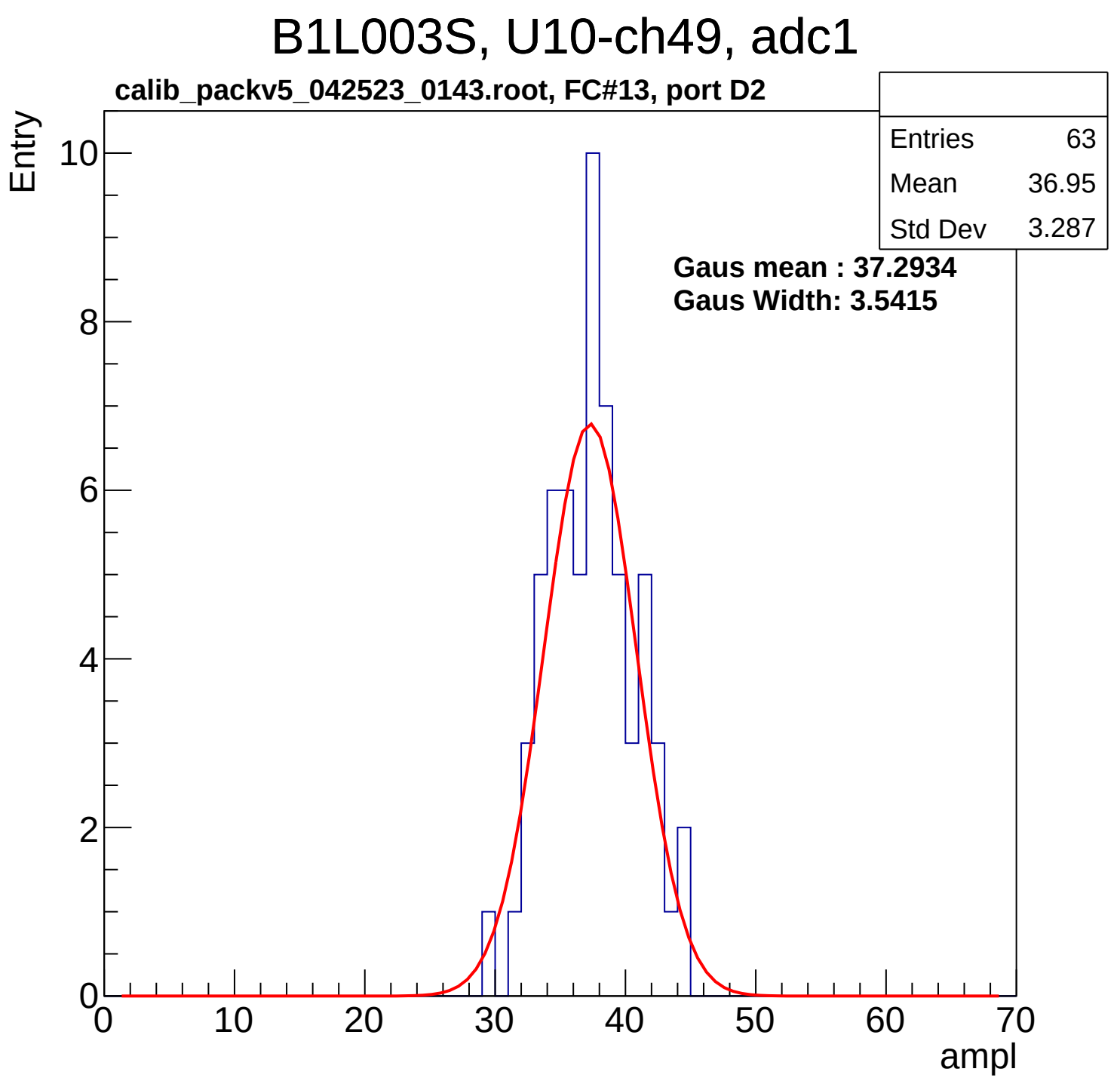
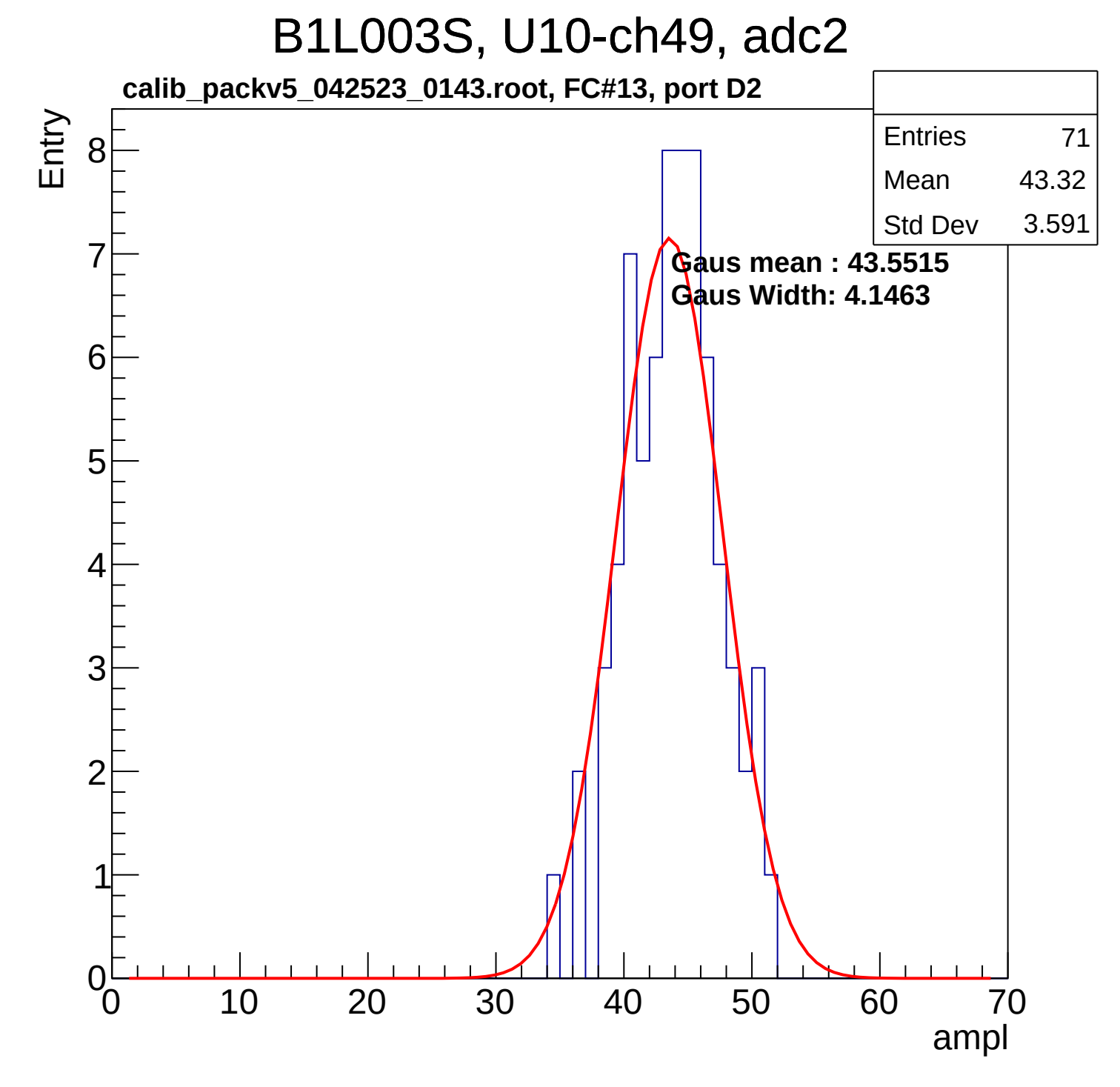
50
60
70
ampl

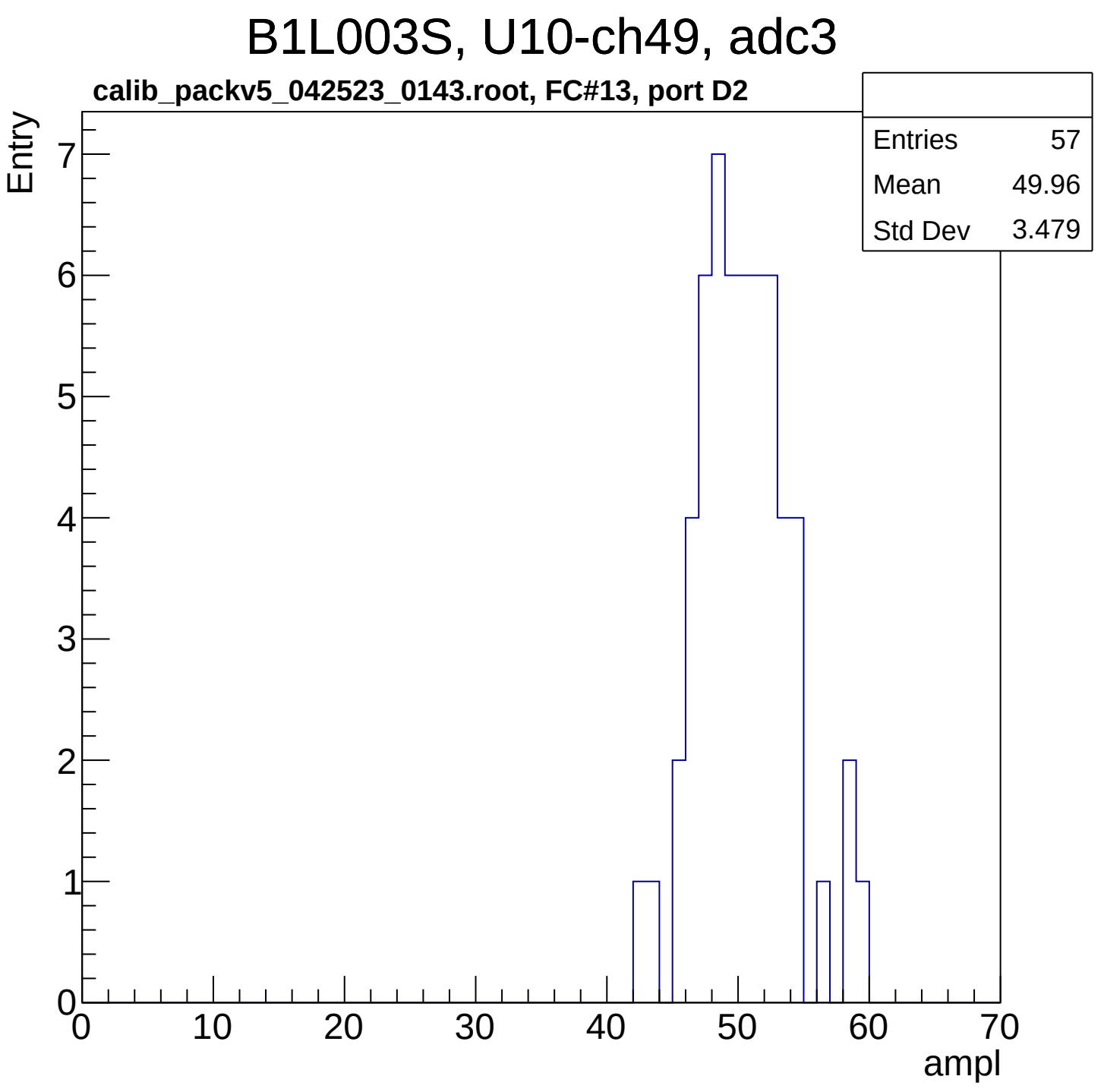

B1L003S, U10-ch48, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

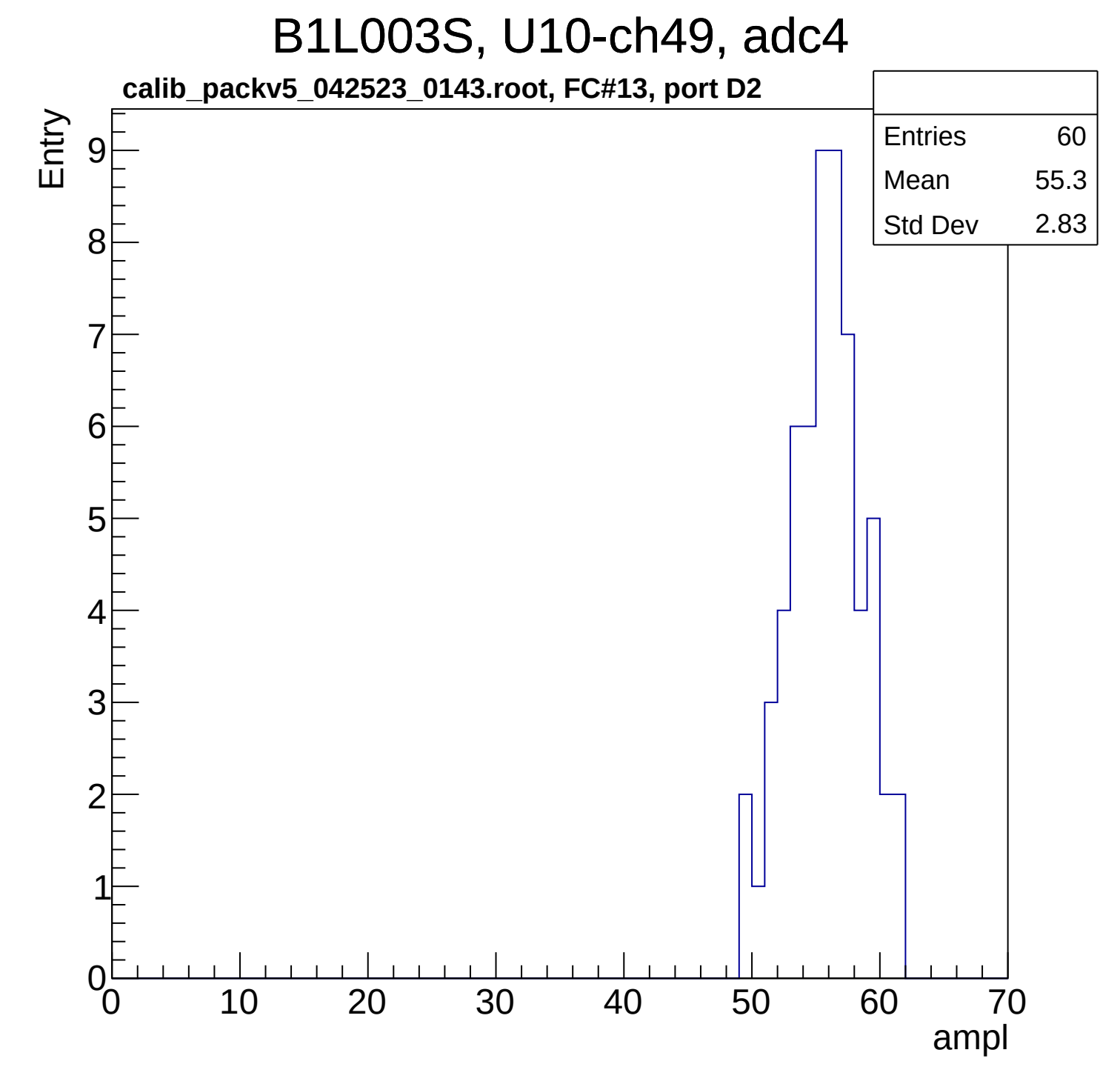

B1L003S, U10-ch48, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch49, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 28.64
Std Dev 7.587
Gaus mean : 30.6822
Gaus Width: 3.5624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch49, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.95
Std Dev 3.287
Gaus mean : 37.2934
Gaus Width: 3.5415
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch49, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 43.32
Std Dev 3.591
Gaus mean : 43.5515
Gaus Width: 4.1463
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch49, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 49.96
Std Dev 3.479
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch49, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.3
Std Dev 2.83
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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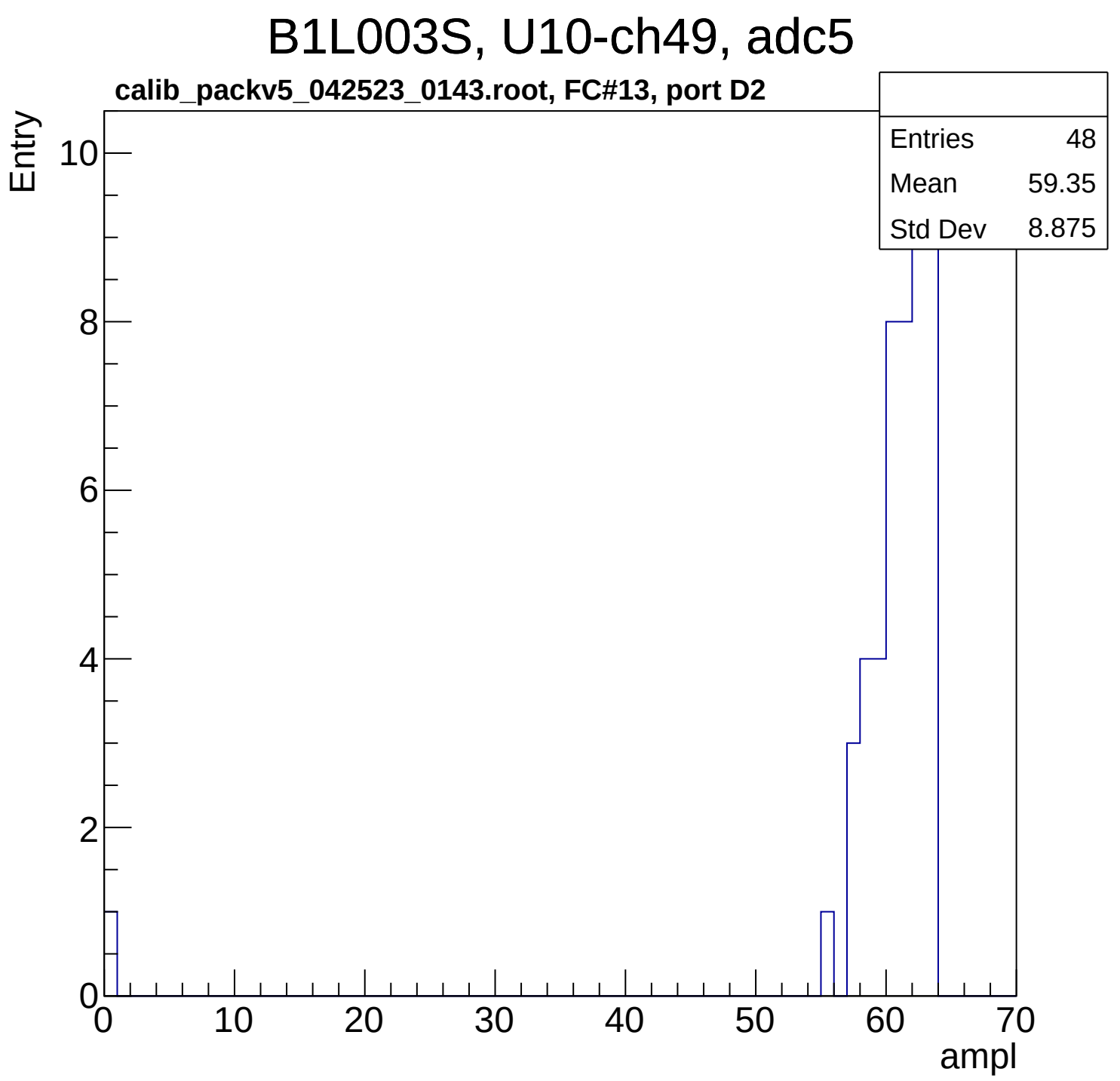
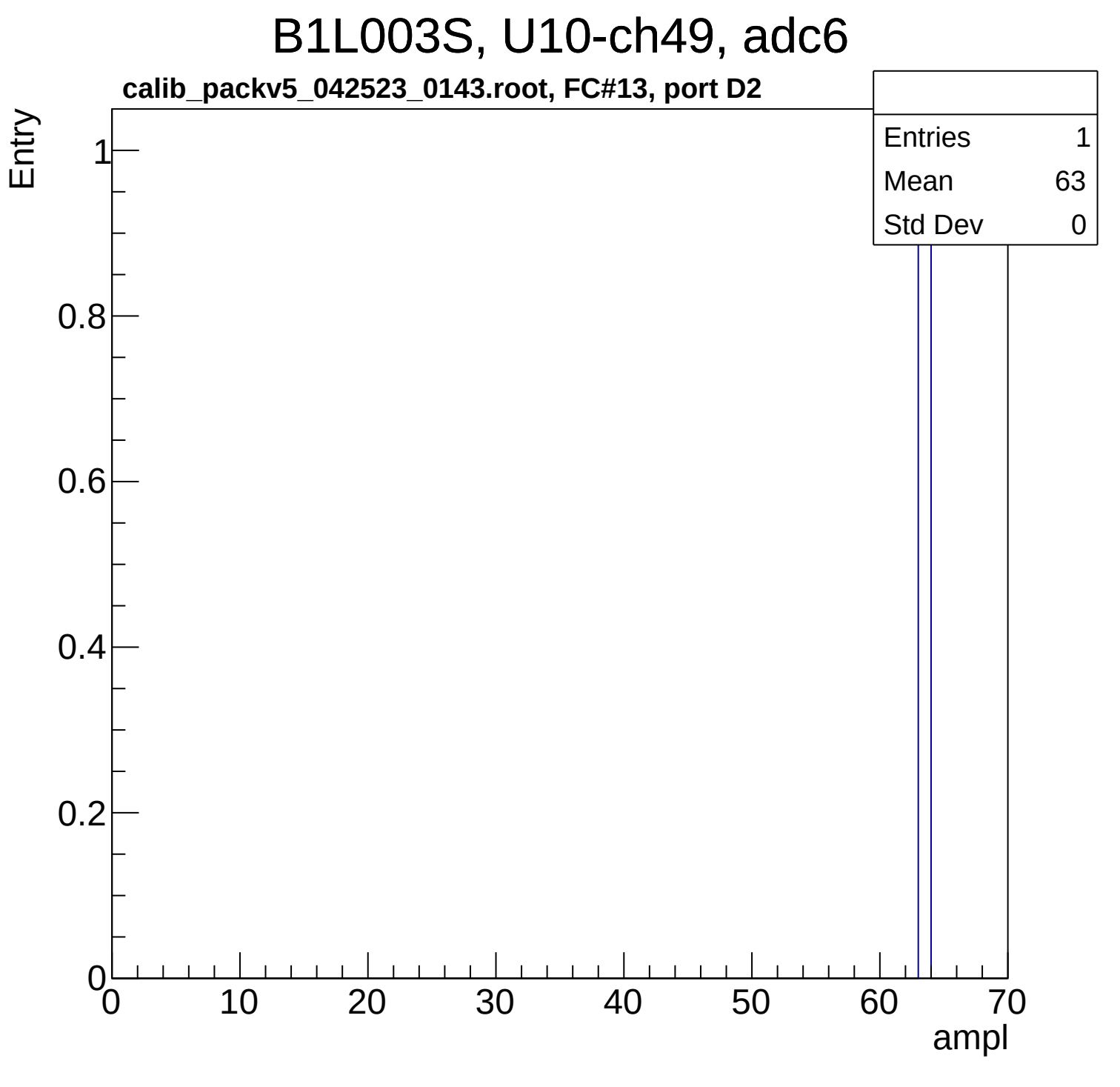
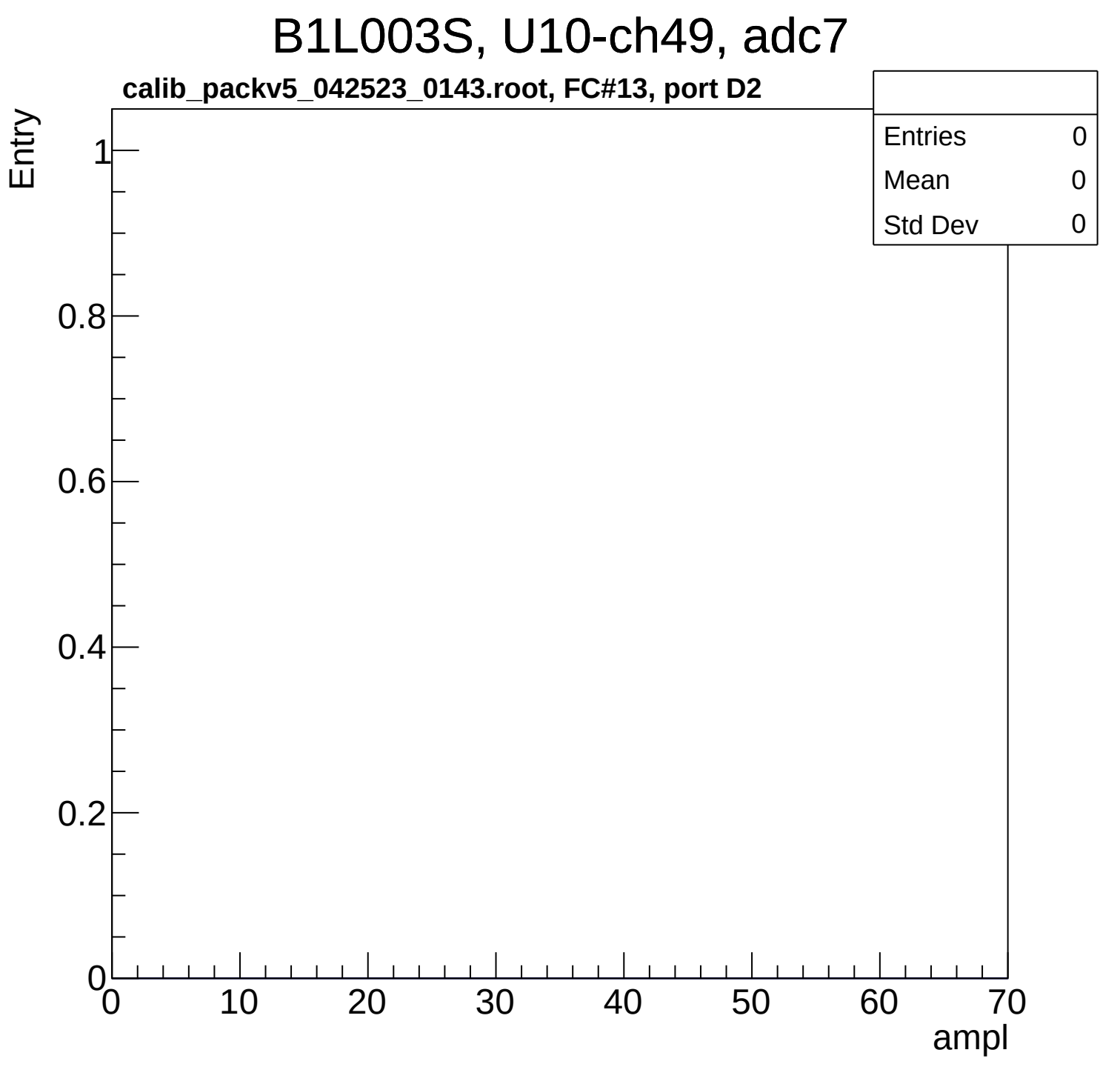
60
70


B1L003S, U10-ch49, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.35
Std Dev 8.875
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch49, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch49, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
20
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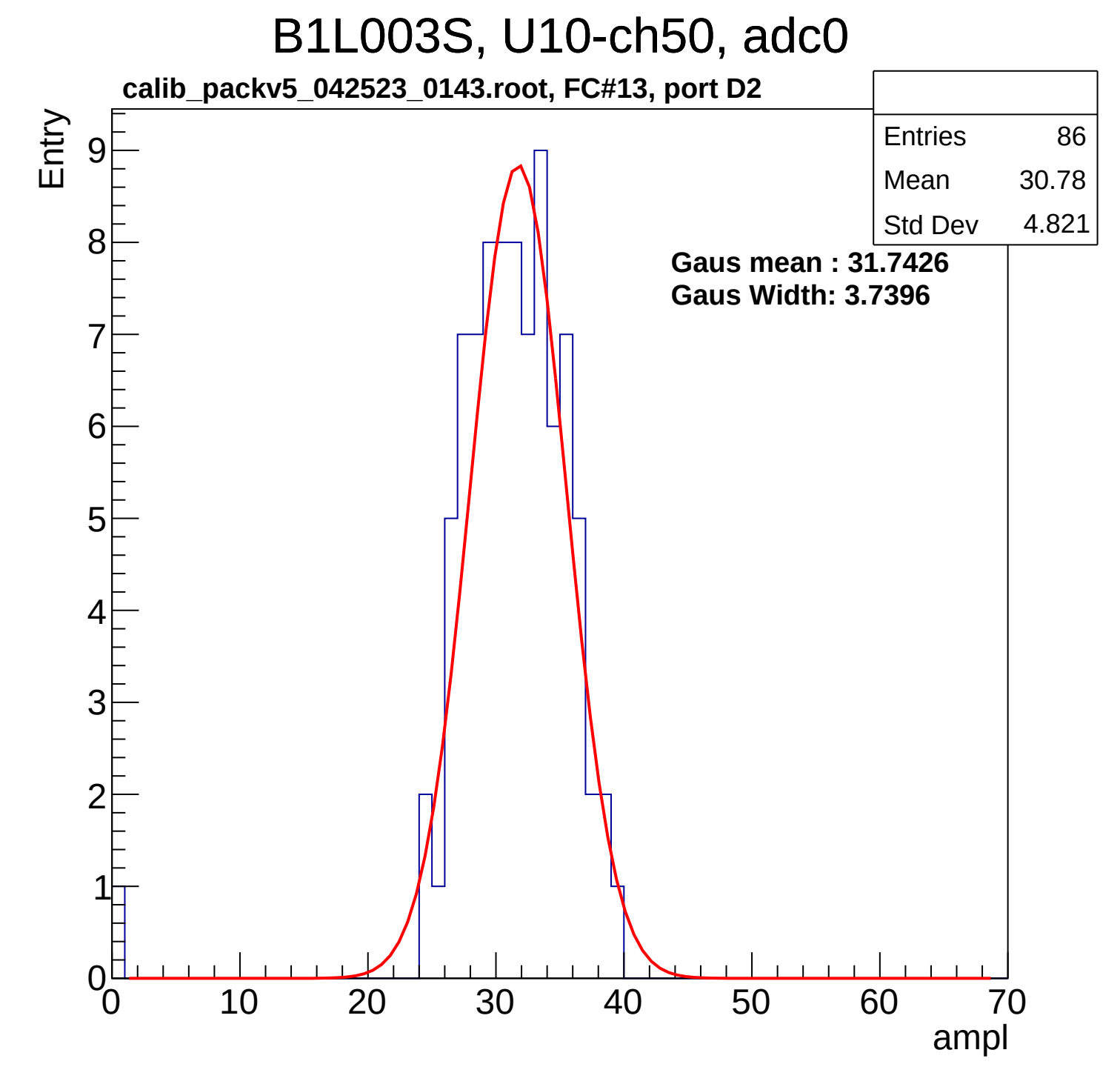
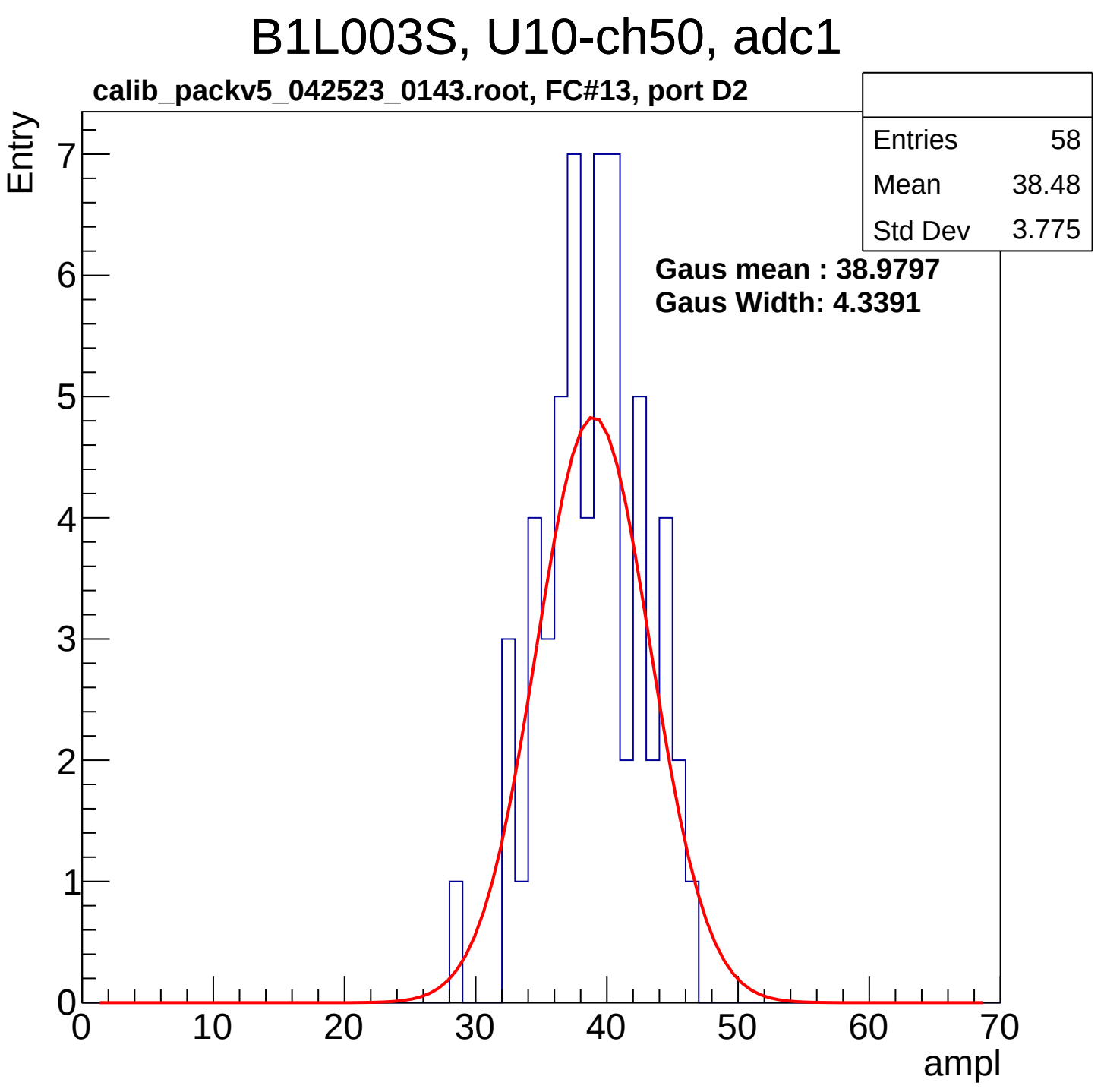
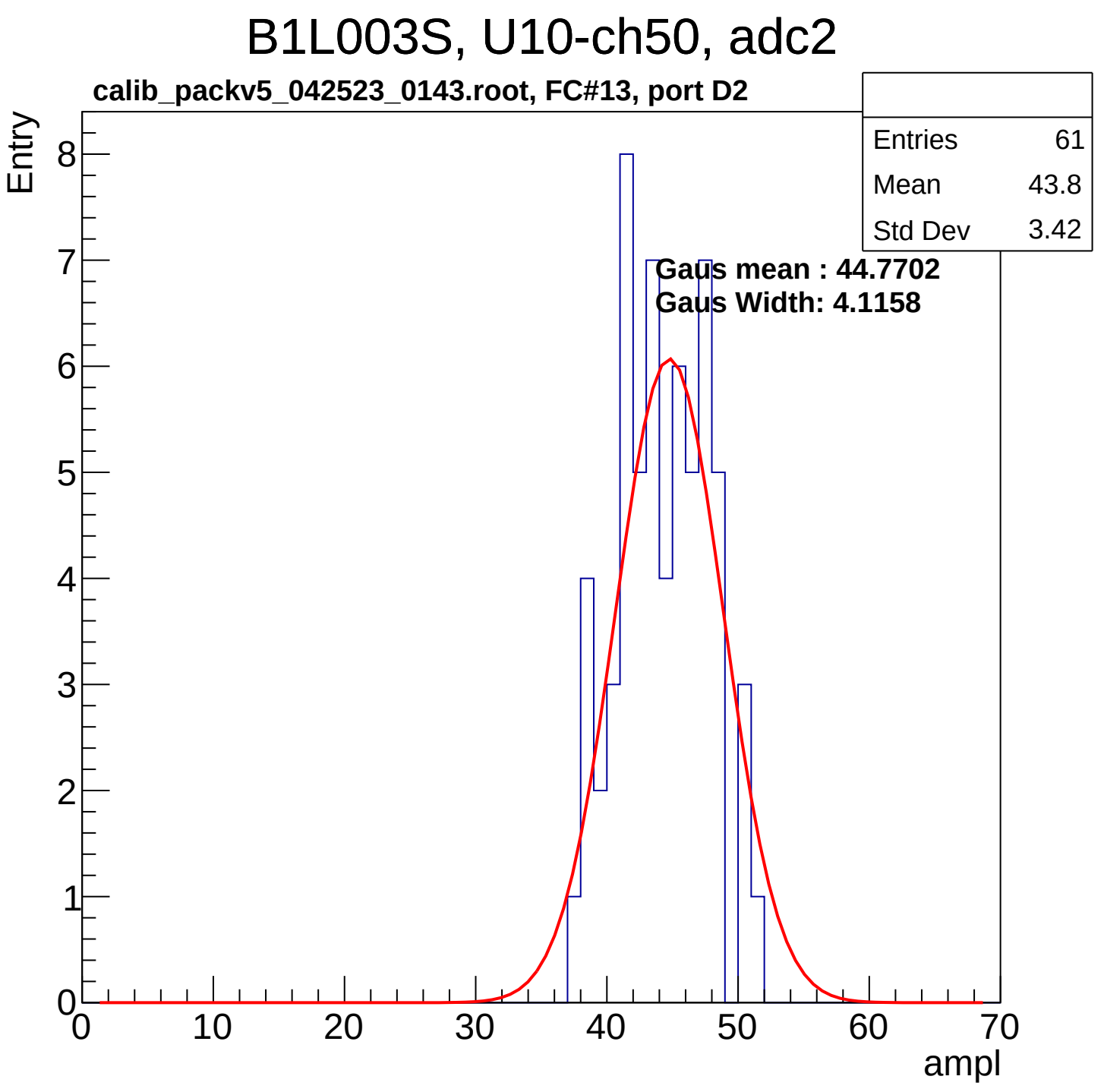
60
70
ampl


B1L003S, U10-ch50, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 30.78
Std Dev 4.821
Gaus mean : 31.7426
Gaus Width: 3.7396
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch50, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 38.48
Std Dev 3.775
Gaus mean : 38.9797
Gaus Width: 4.3391
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch50, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 43.8
Std Dev 3.42
Gaus mean : 44.7702
Gaus Width: 4.1158
Entry
ampl
0
1
2
3
4
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0
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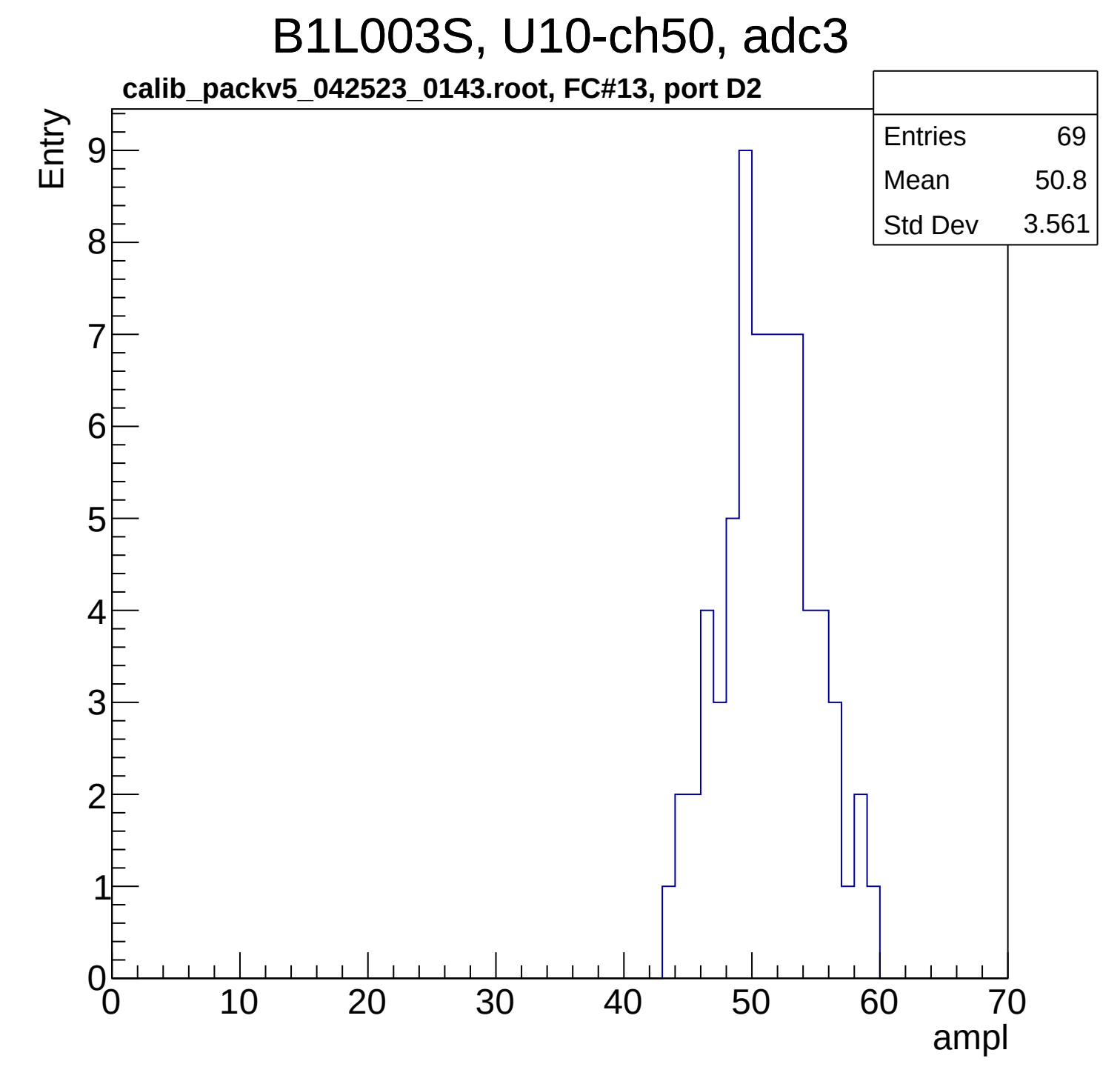
B1L003S, U10-ch50, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.8
Std Dev 3.561
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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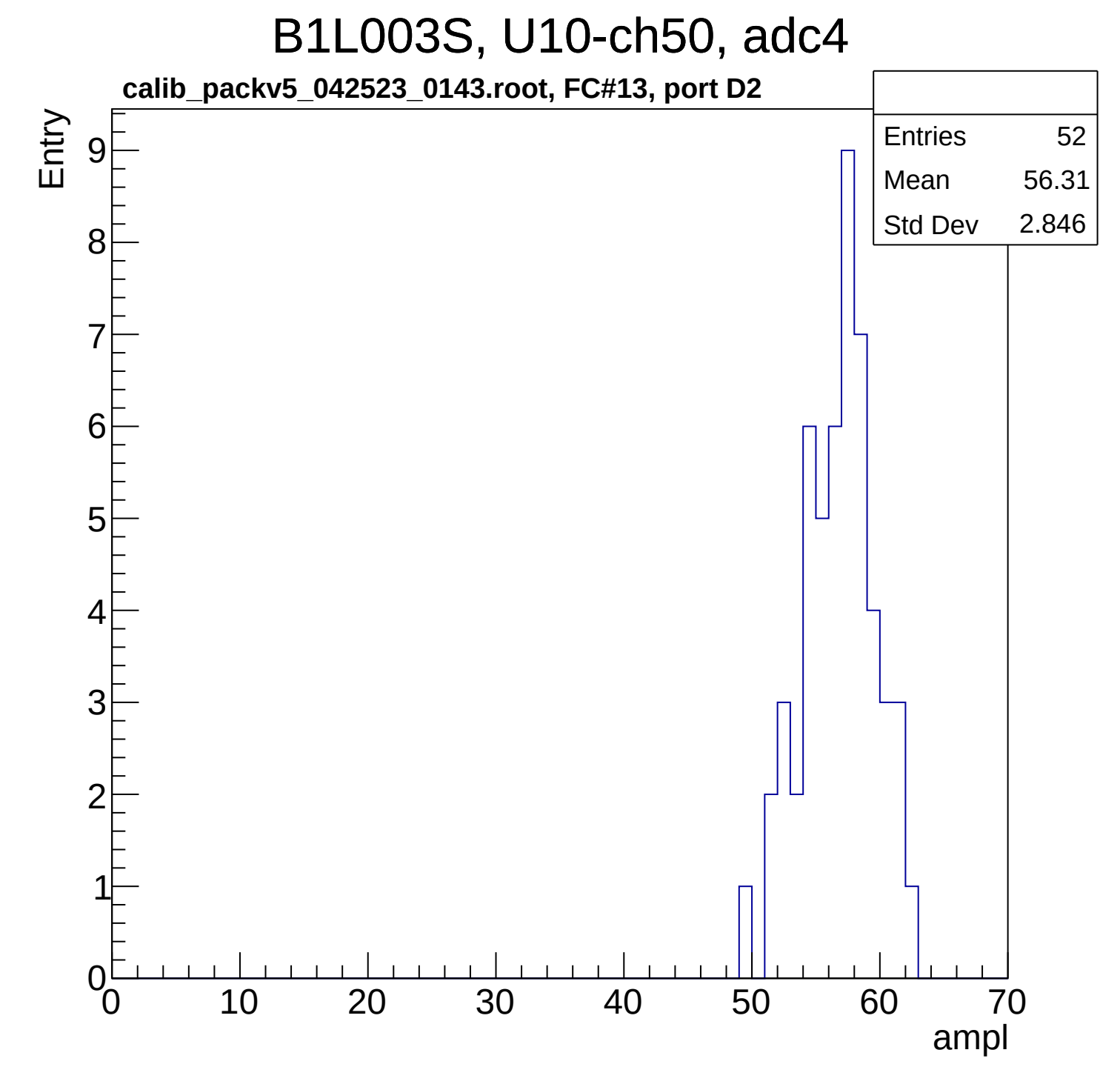
B1L003S, U10-ch50, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 56.31
Std Dev 2.846
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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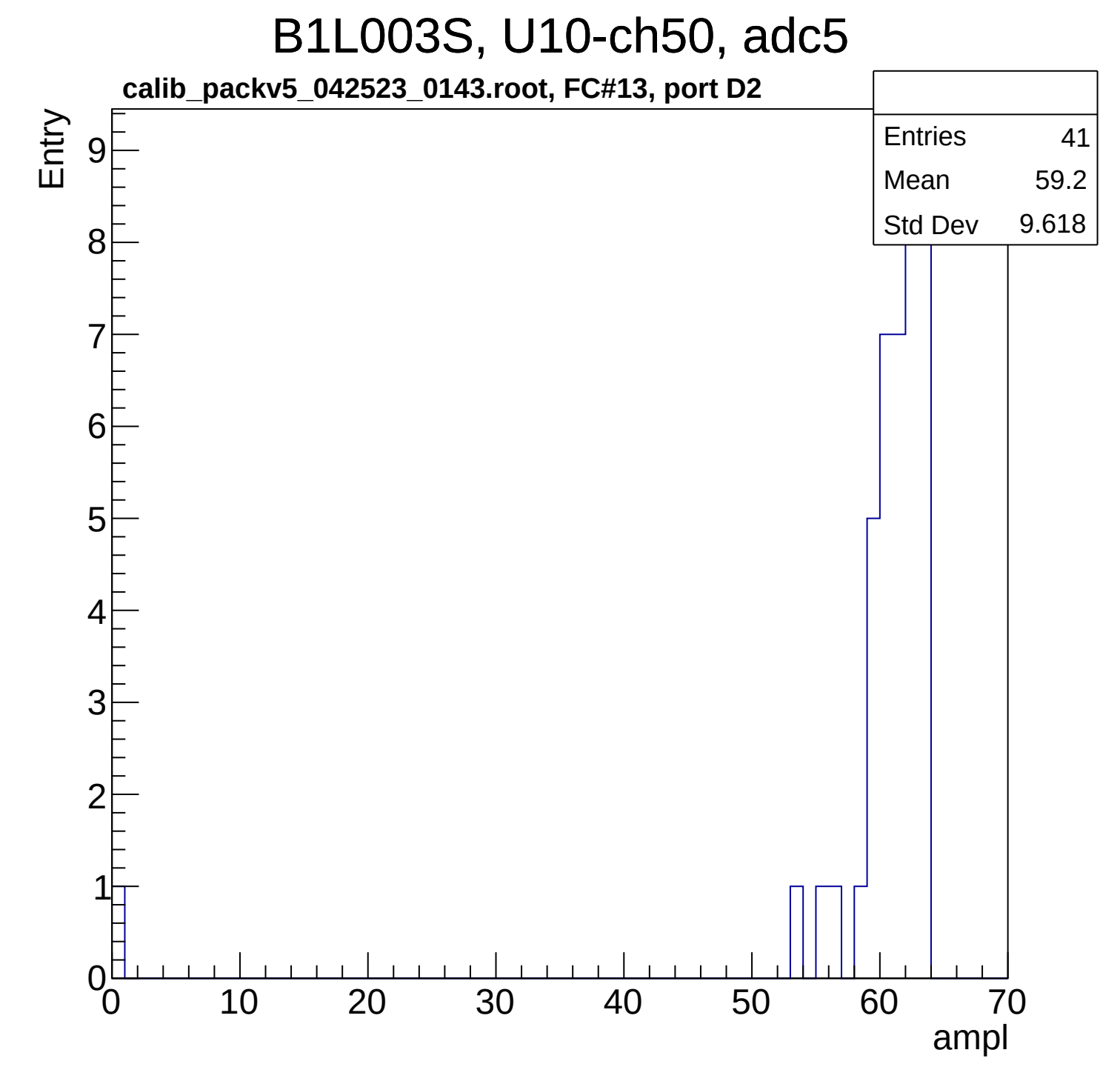
B1L003S, U10-ch50, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59.2
Std Dev 9.618
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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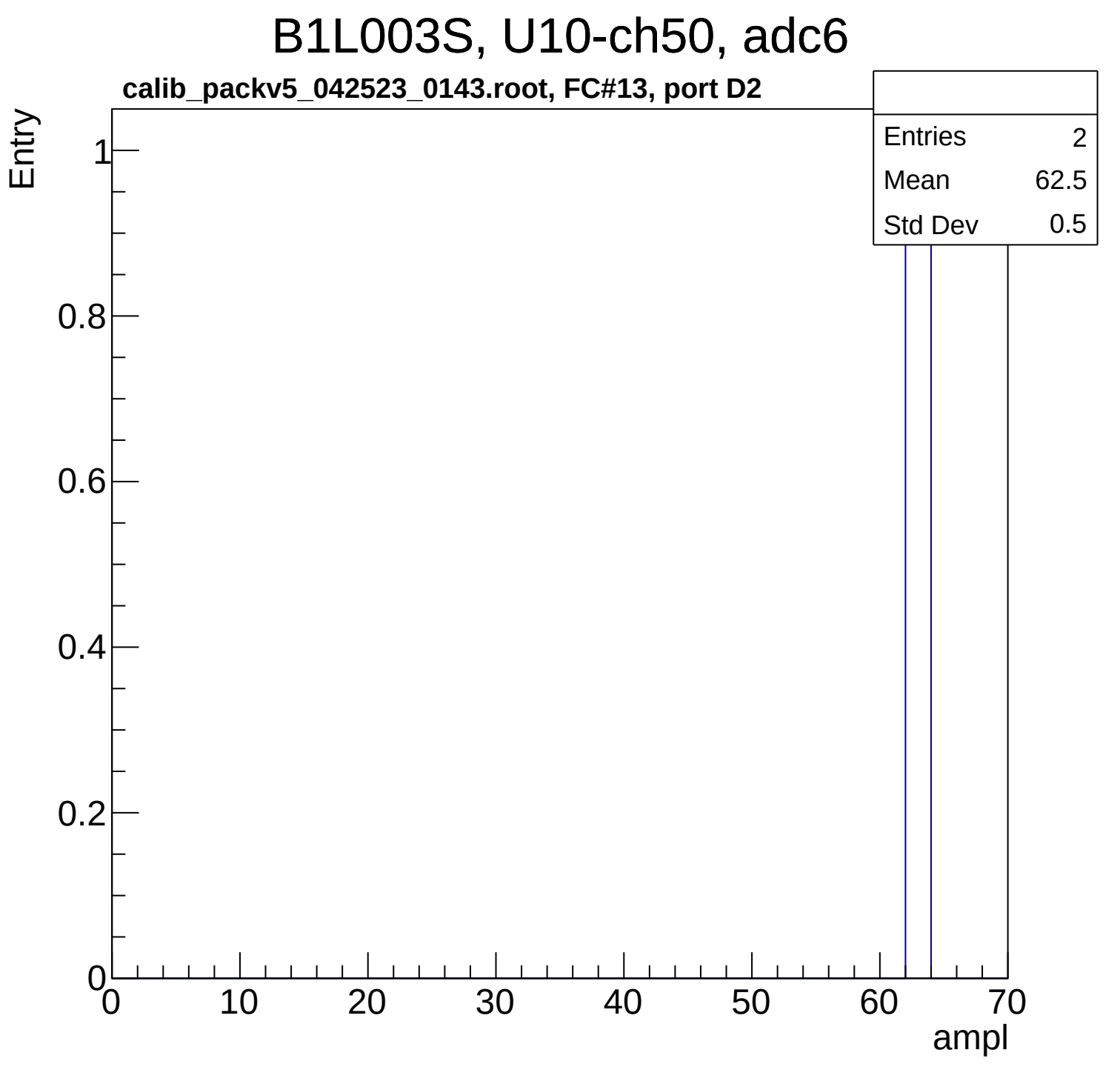
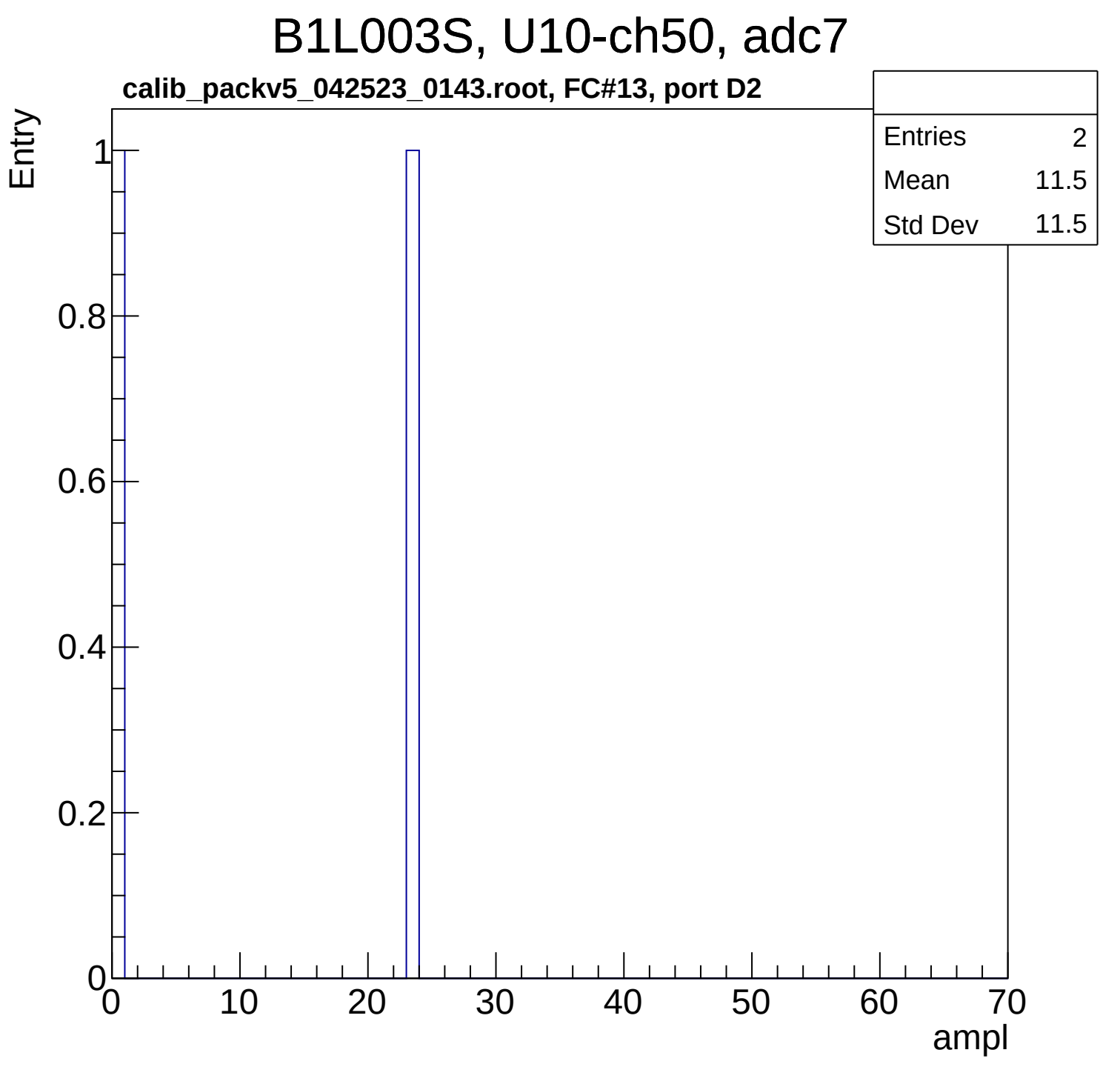
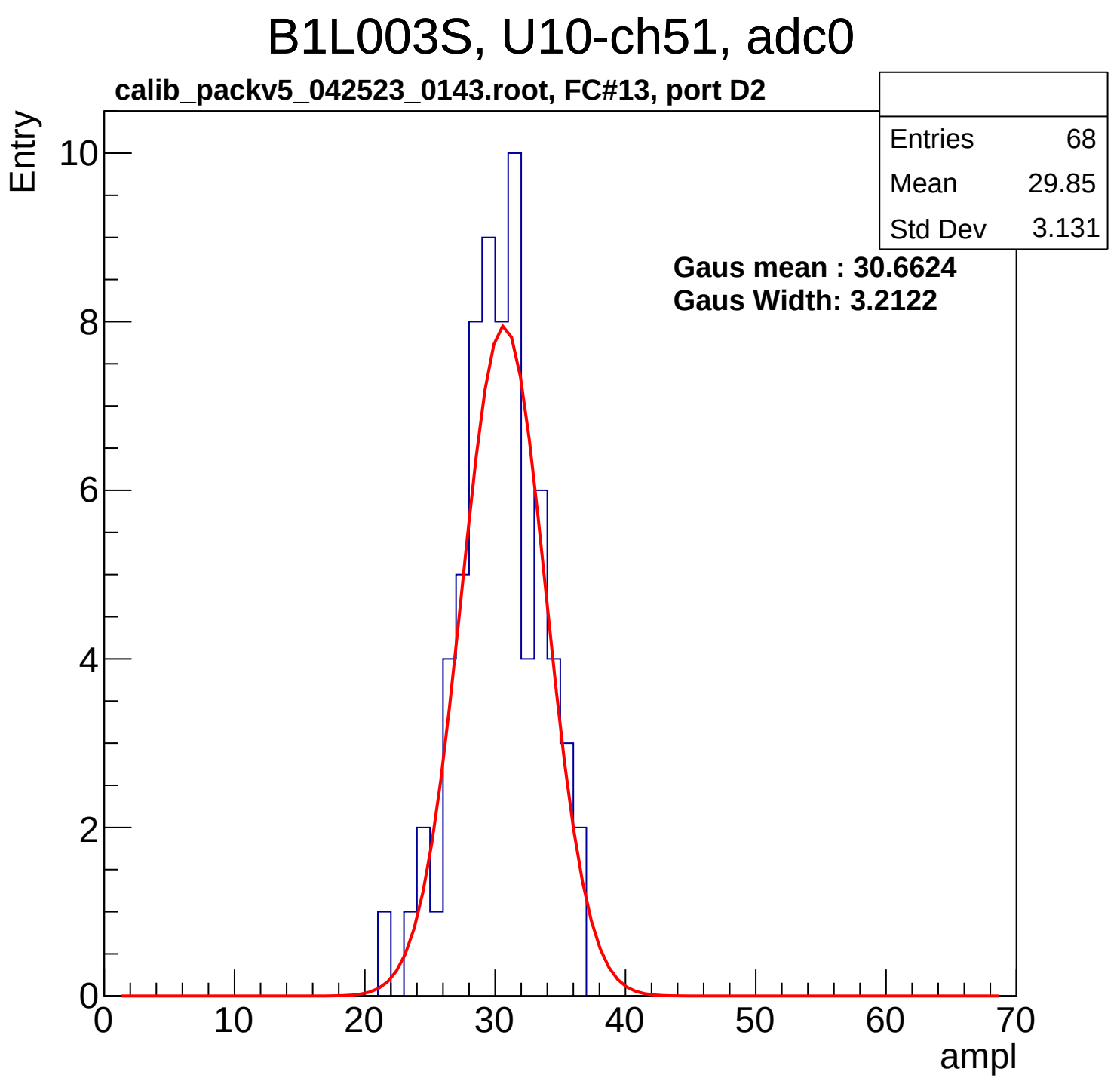
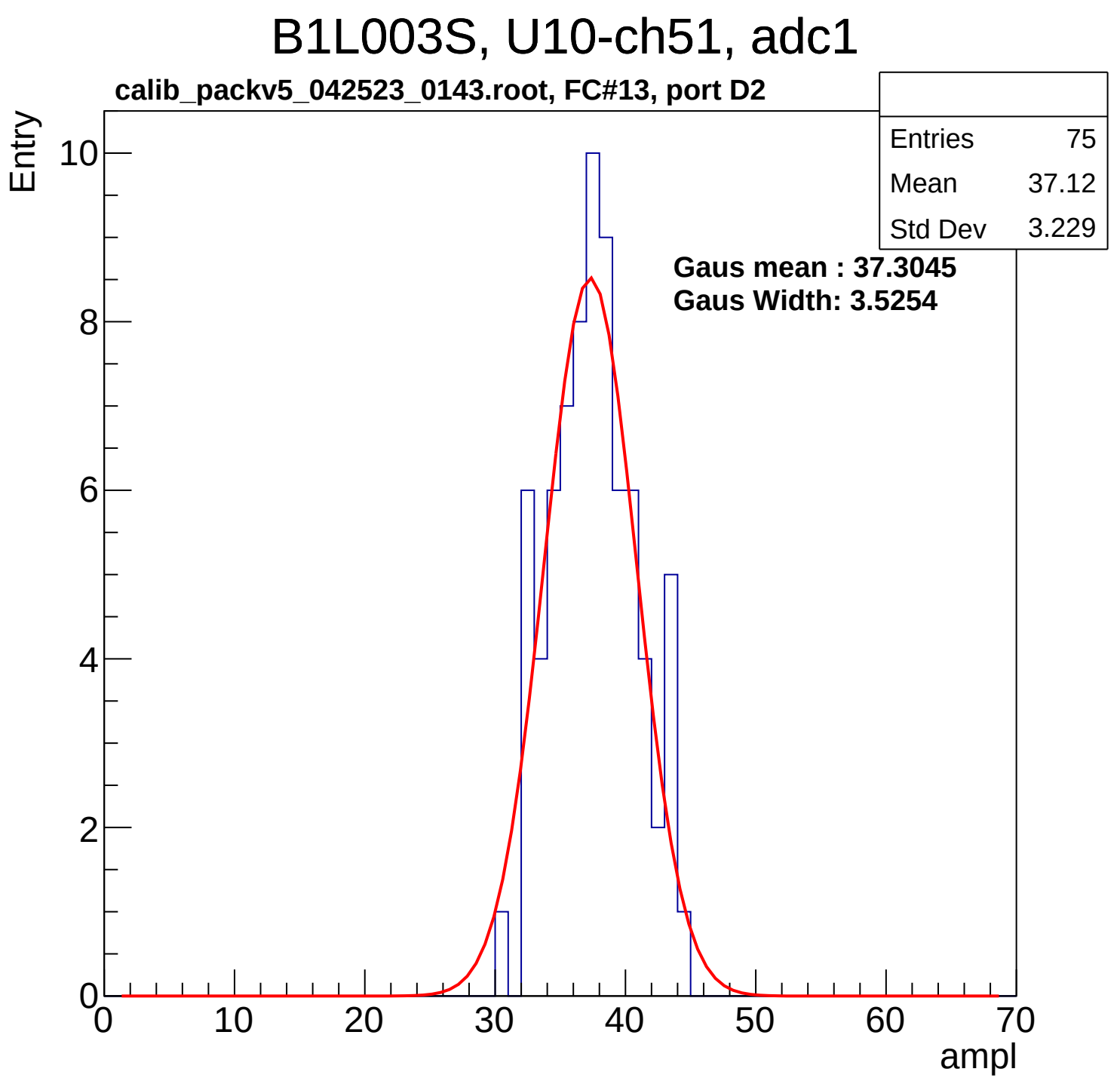

B1L003S, U10-ch50, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch50, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch51, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.85
Std Dev 3.131
Gaus mean : 30.6624
Gaus Width: 3.2122
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch51, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 37.12
Std Dev 3.229
Gaus mean : 37.3045
Gaus Width: 3.5254
Entry
ampl
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2
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8
10
0
10
20
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40
50
60
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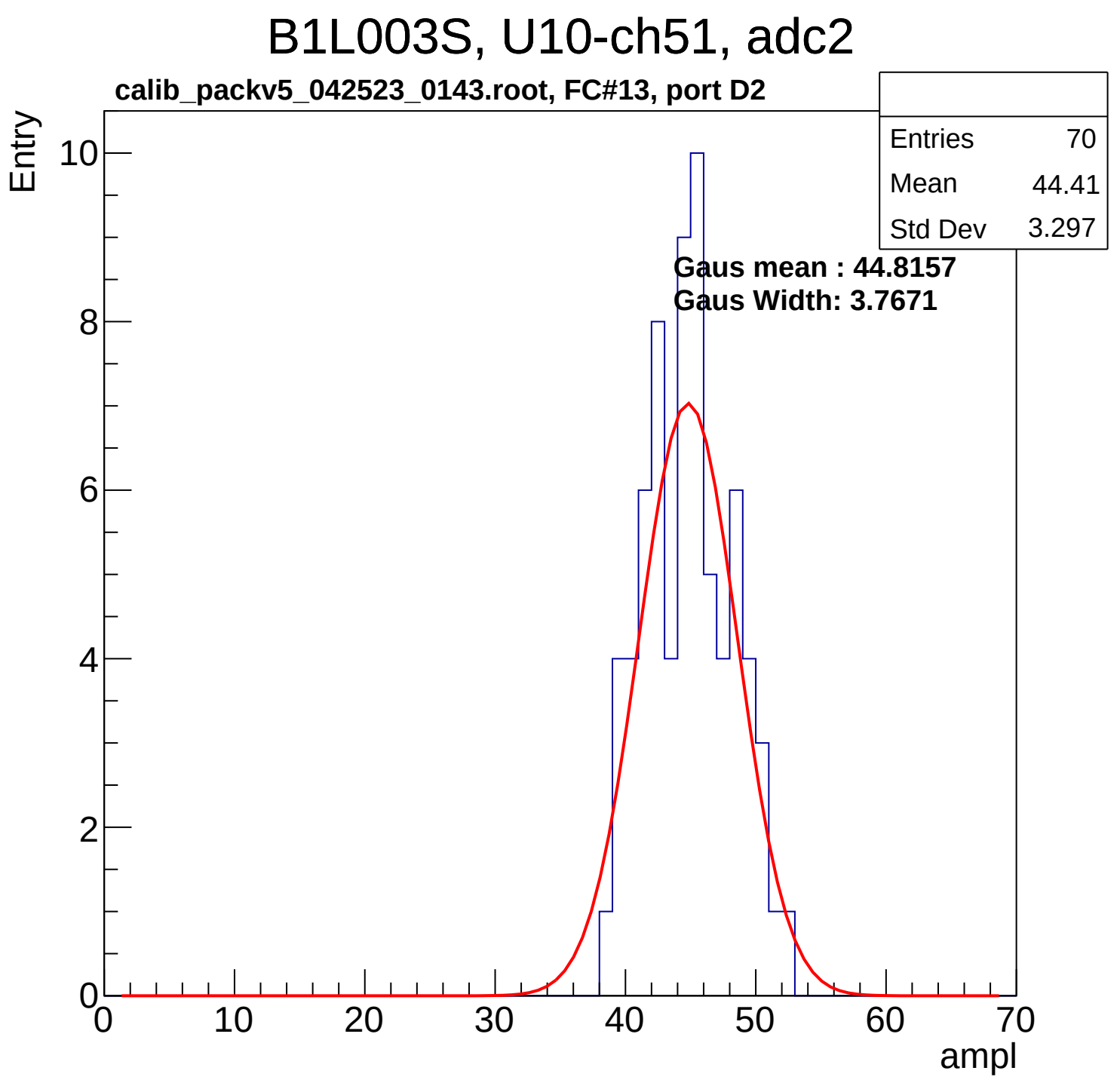
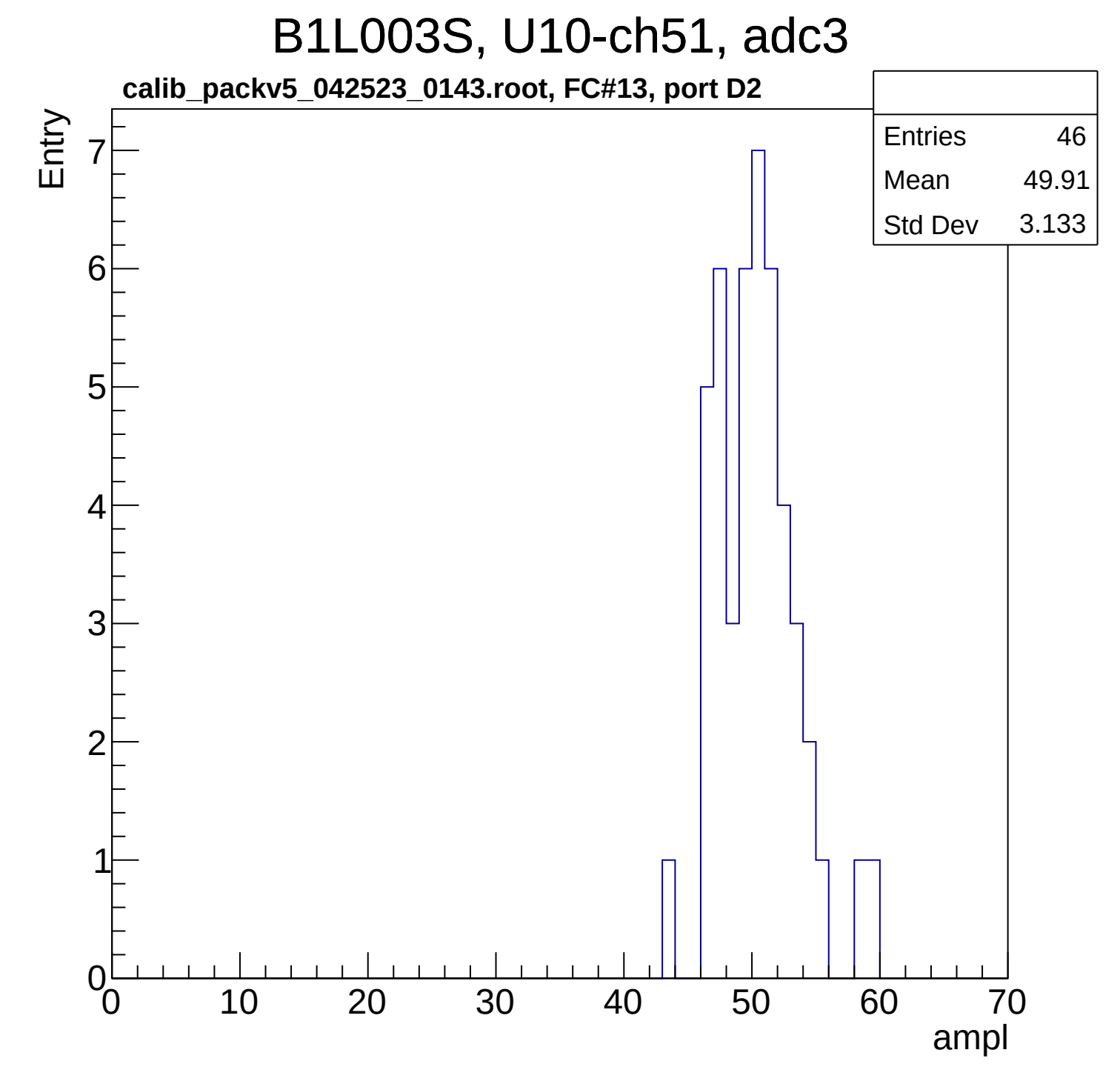
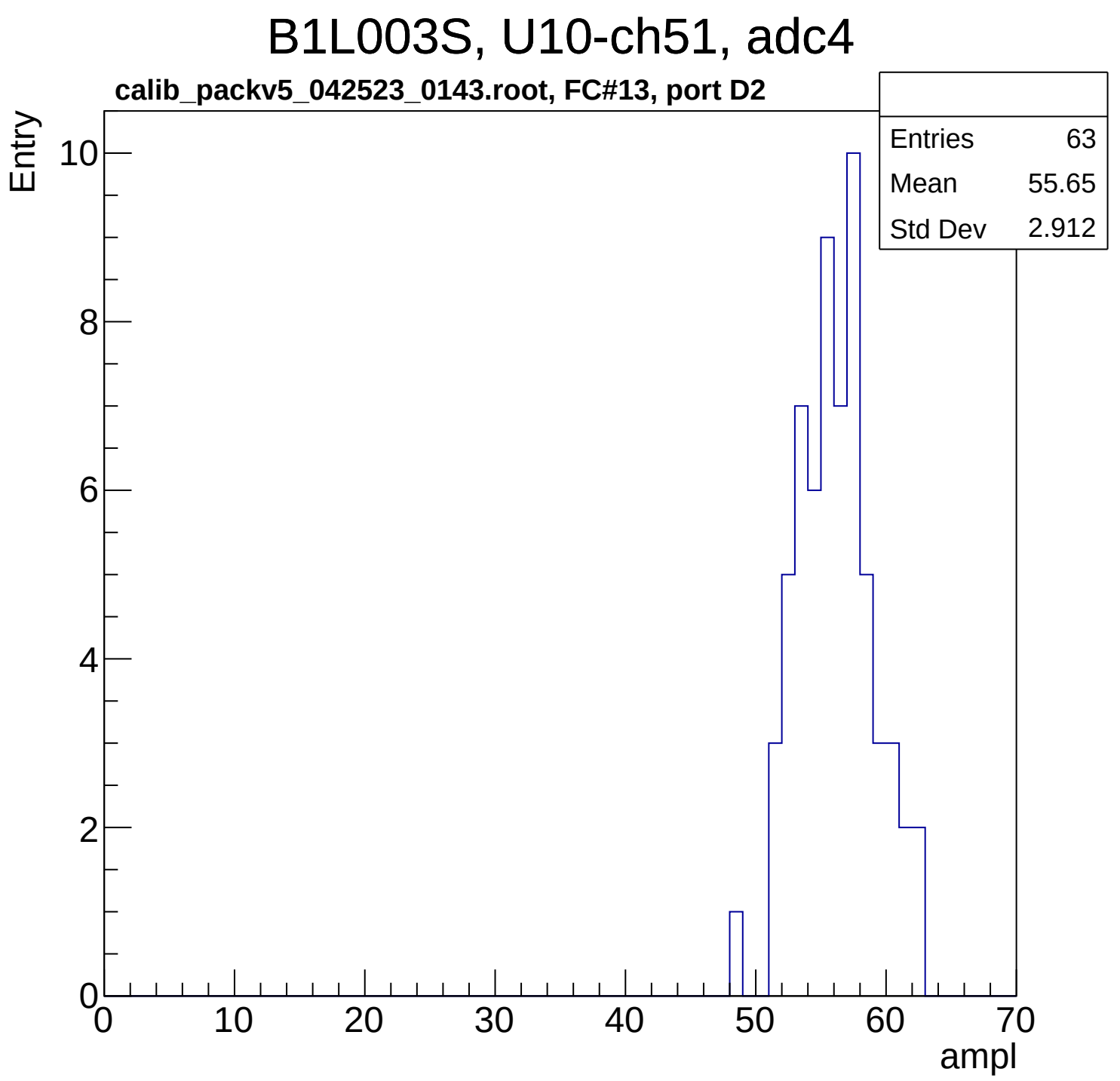
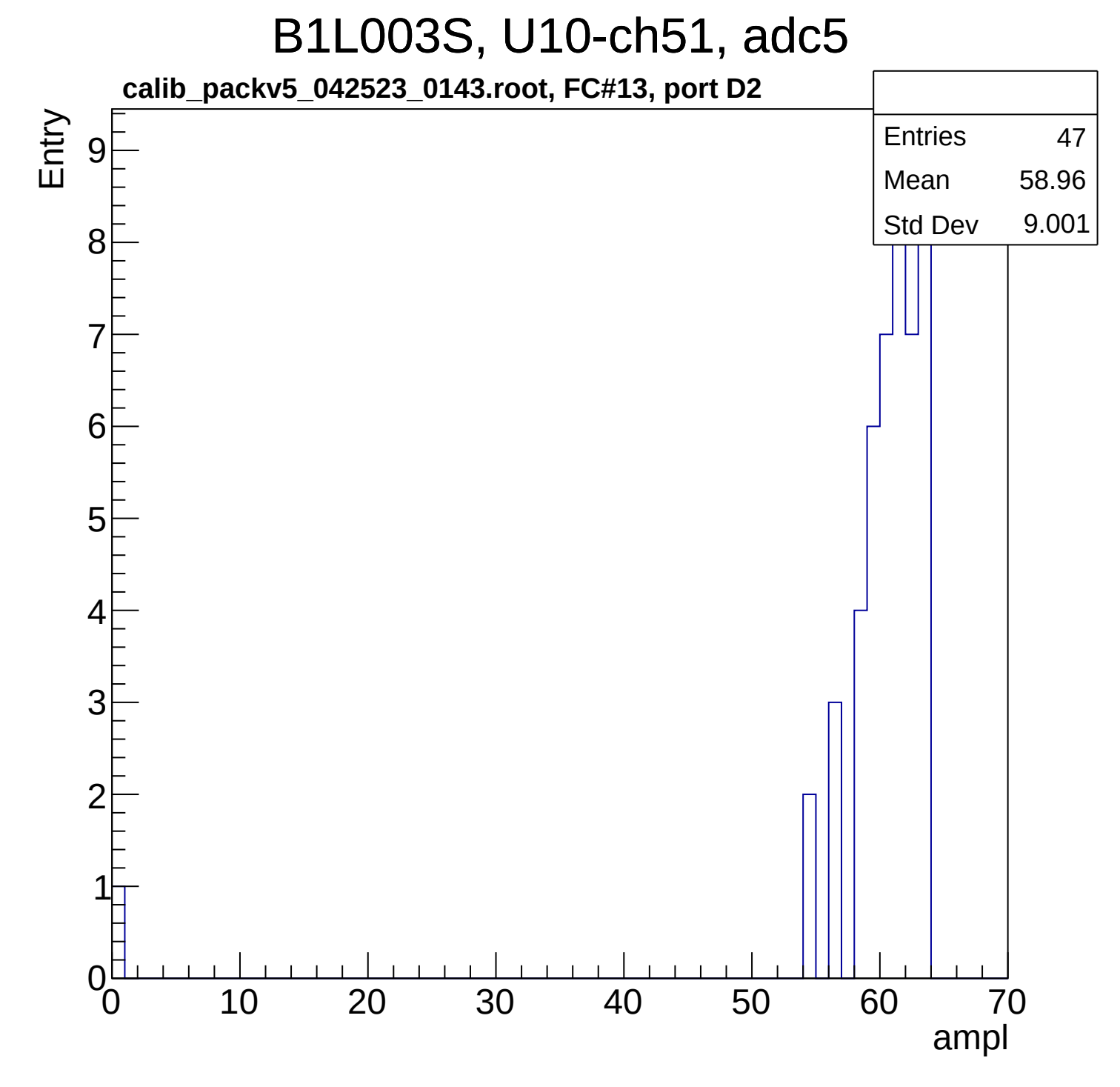

B1L003S, U10-ch51, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 44.41
Std Dev 3.297
Gaus mean : 44.8157
Gaus Width: 3.7671
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch51, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 49.91
Std Dev 3.133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch51, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.65
Std Dev 2.912
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch51, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.96
Std Dev 9.001
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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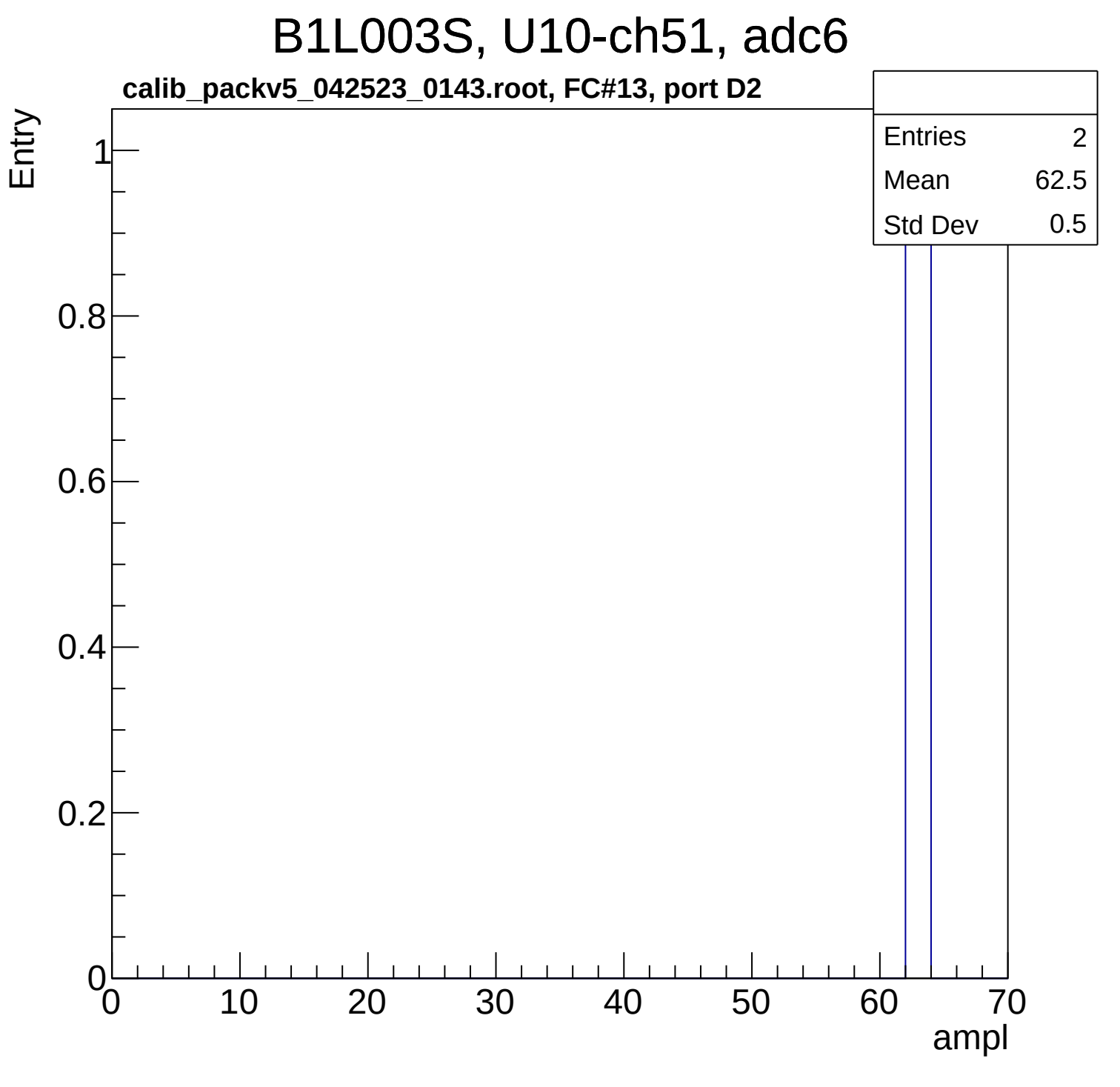
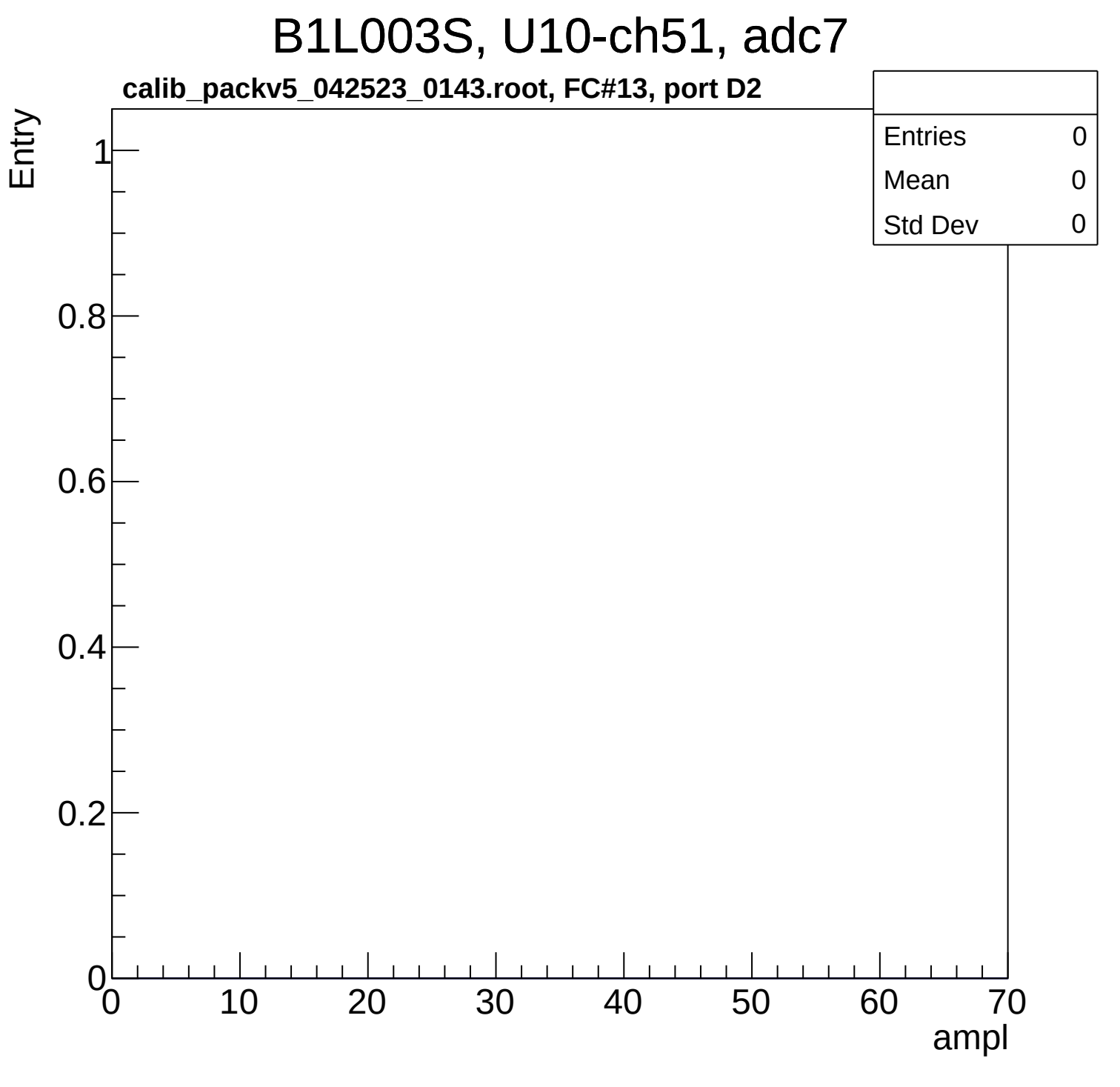
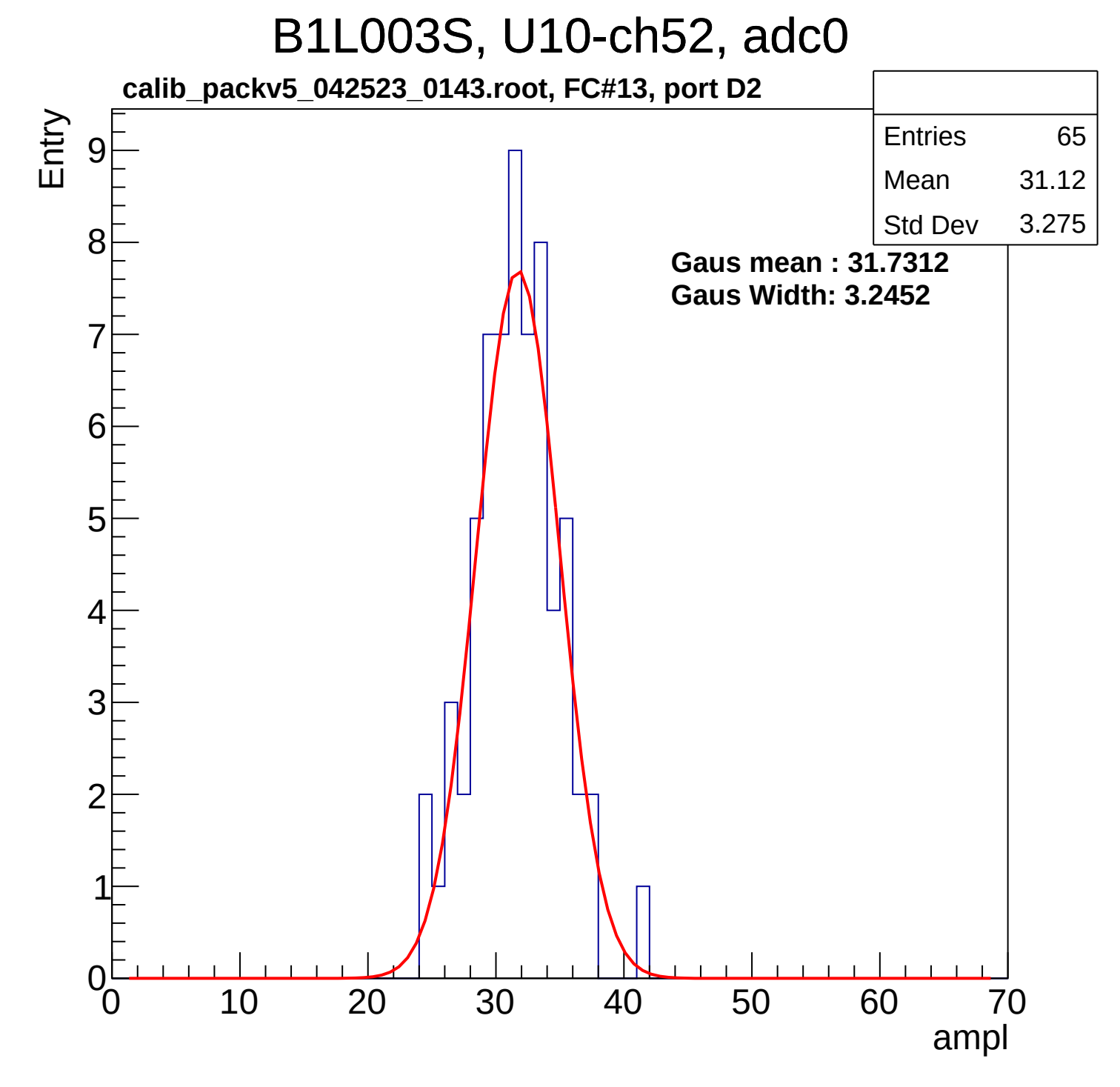
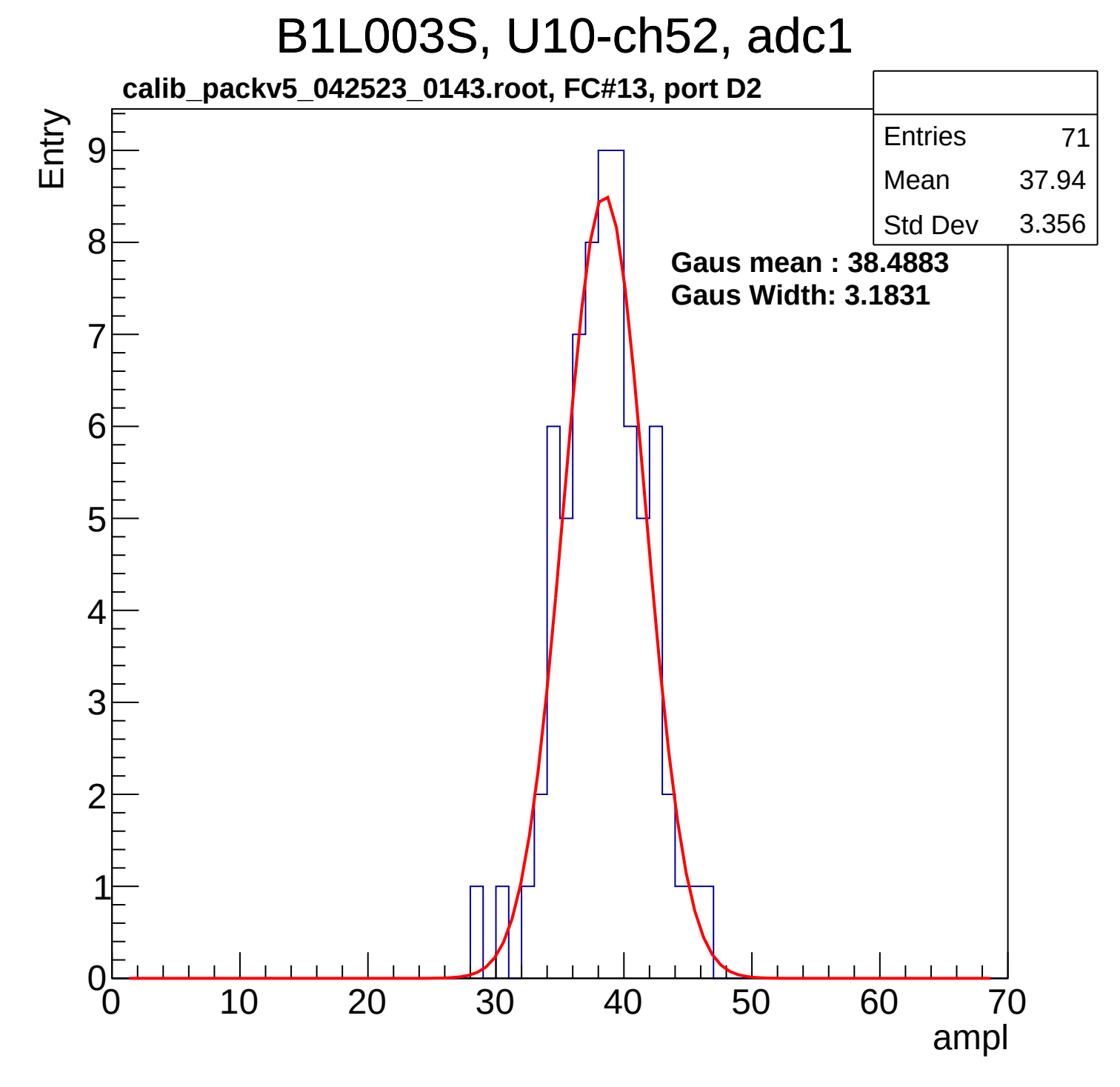

B1L003S, U10-ch51, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch51, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch52, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 31.12
Std Dev 3.275
Gaus mean : 31.7312
Gaus Width: 3.2452
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch52, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 37.94
Std Dev 3.356
Gaus mean : 38.4883
Gaus Width: 3.1831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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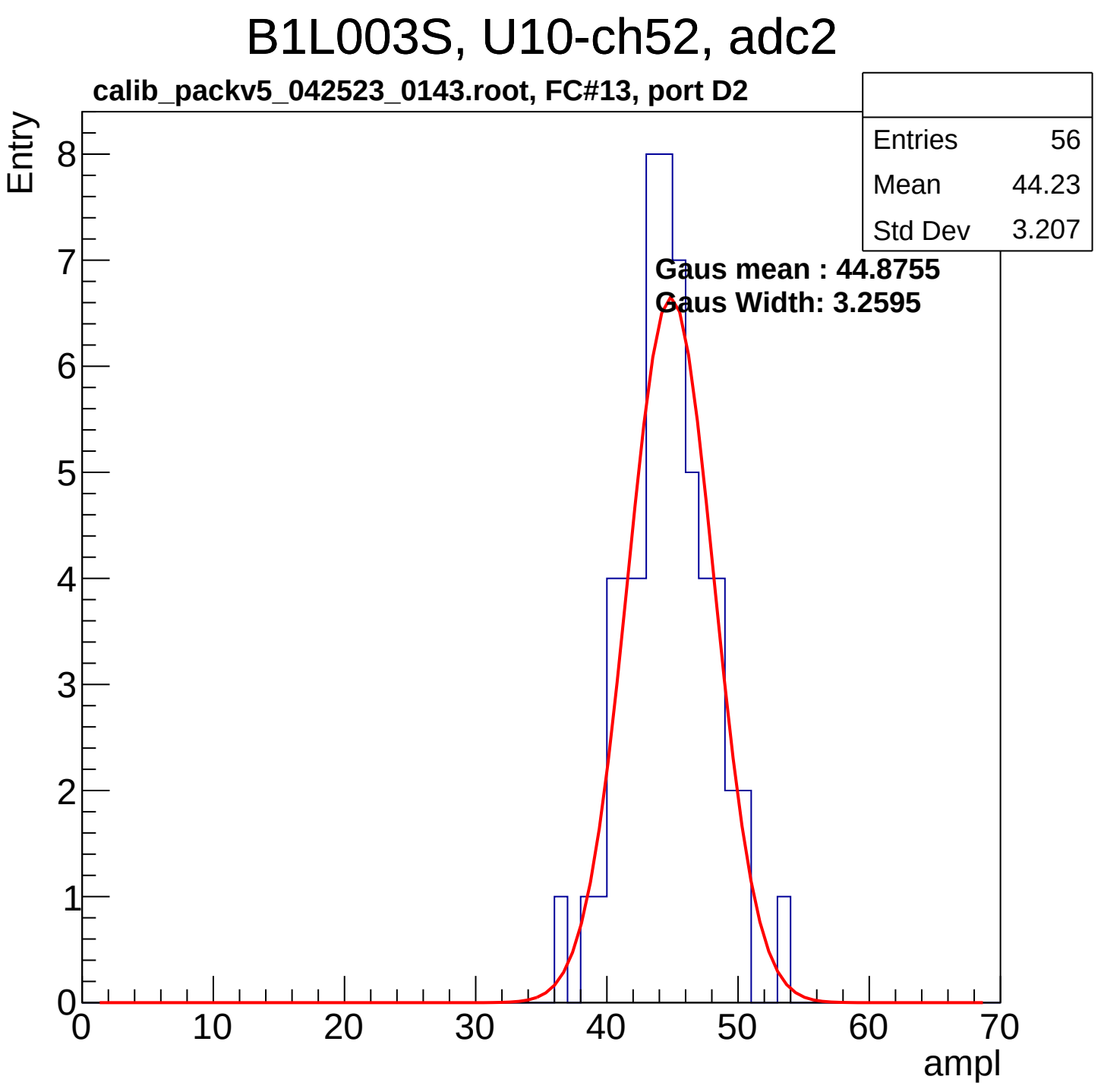
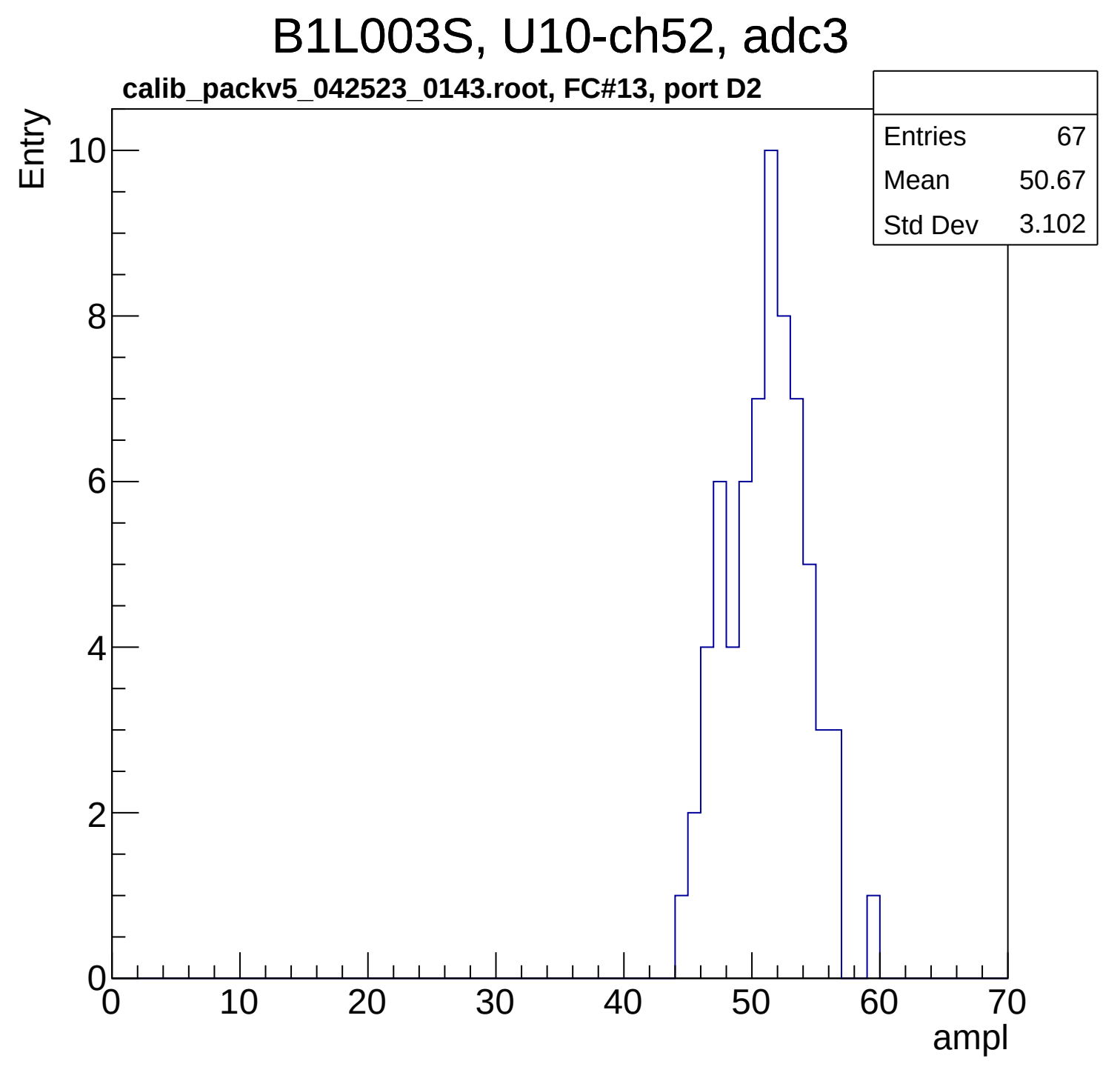
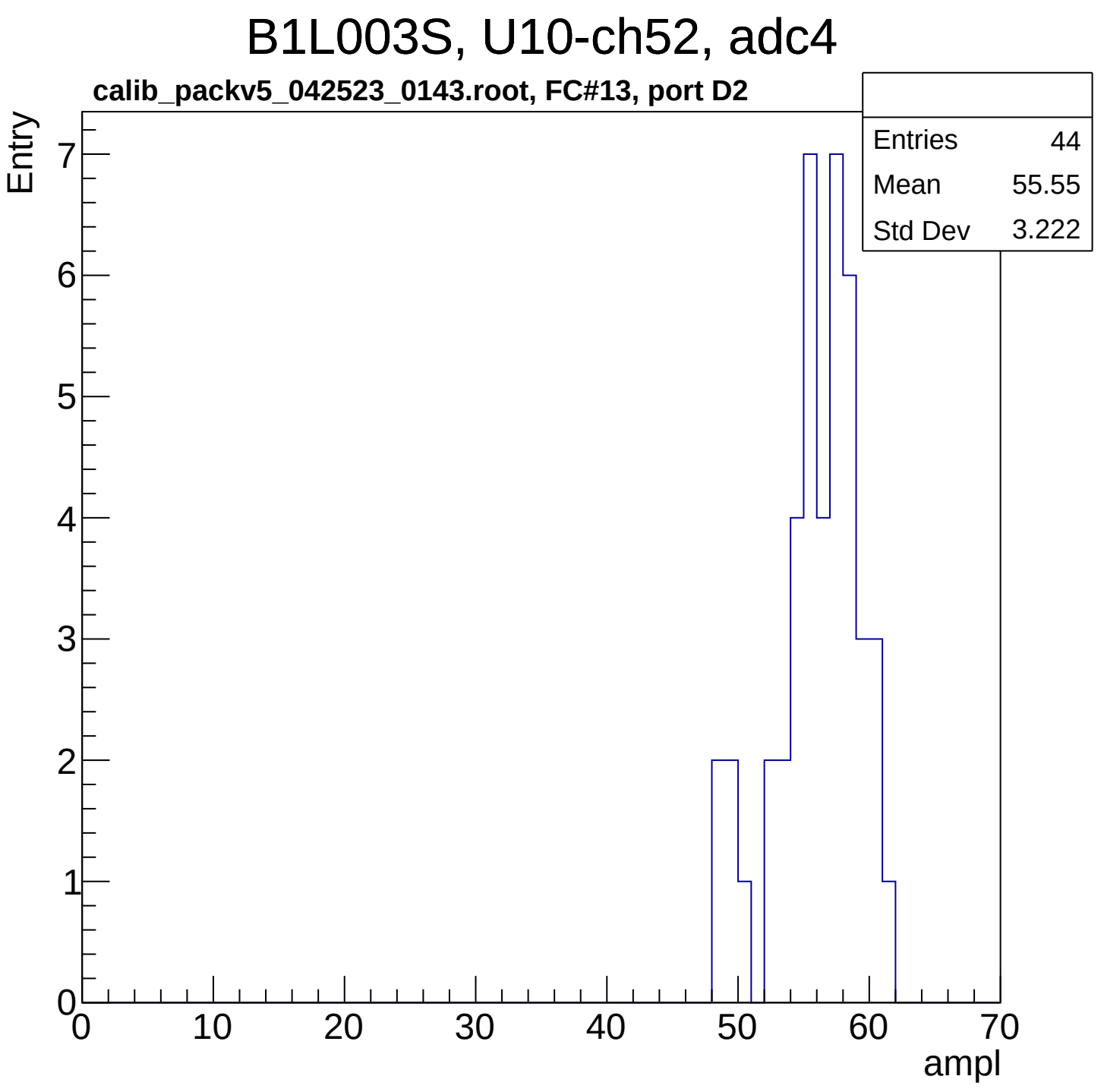

B1L003S, U10-ch52, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 44.23
Std Dev 3.207
Gaus mean : 44.8755
Gaus Width: 3.2595
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch52, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 50.67
Std Dev 3.102
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch52, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 55.55
Std Dev 3.222
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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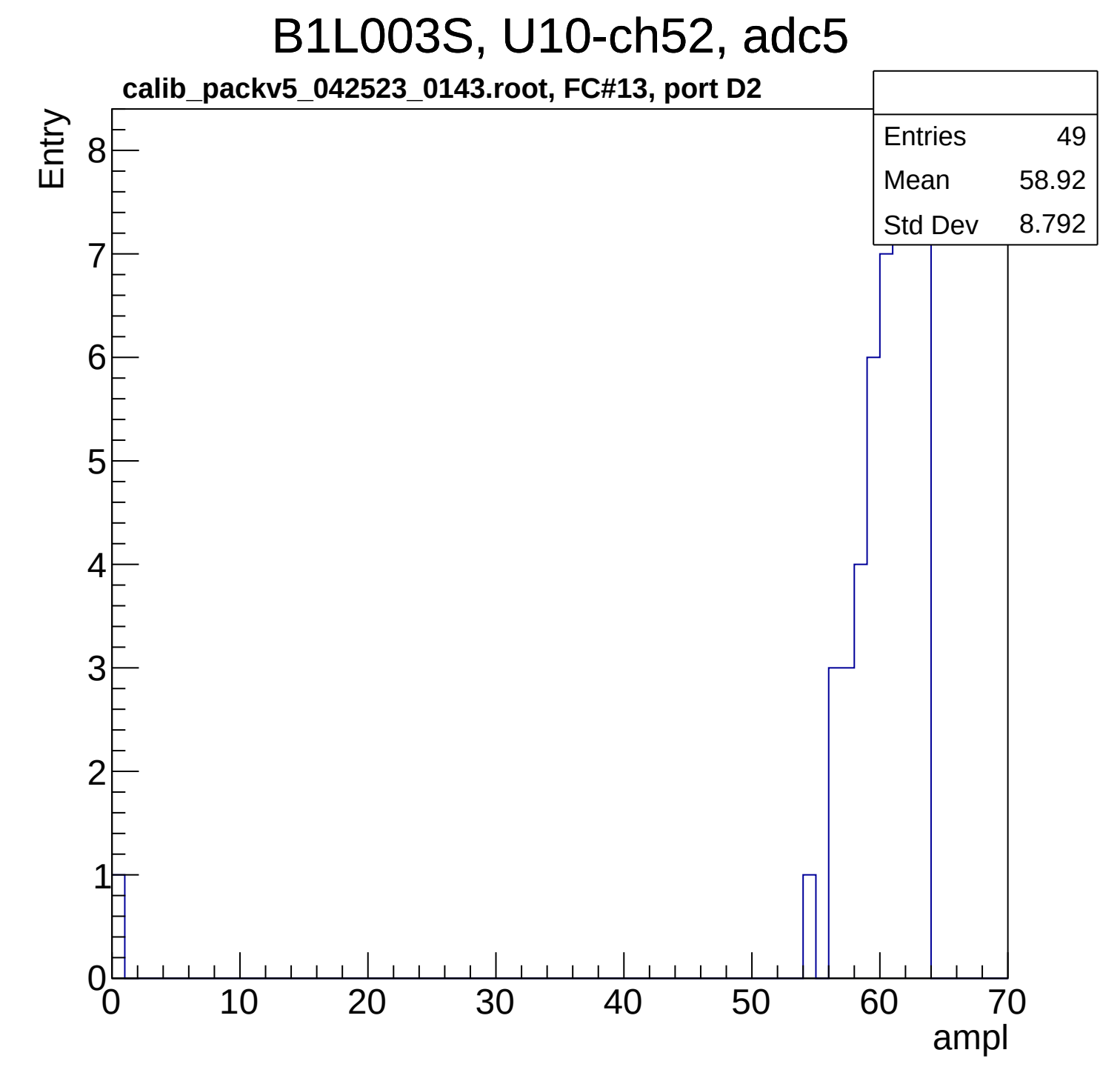
B1L003S, U10-ch52, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.92
Std Dev 8.792
Entry
ampl
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1
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0
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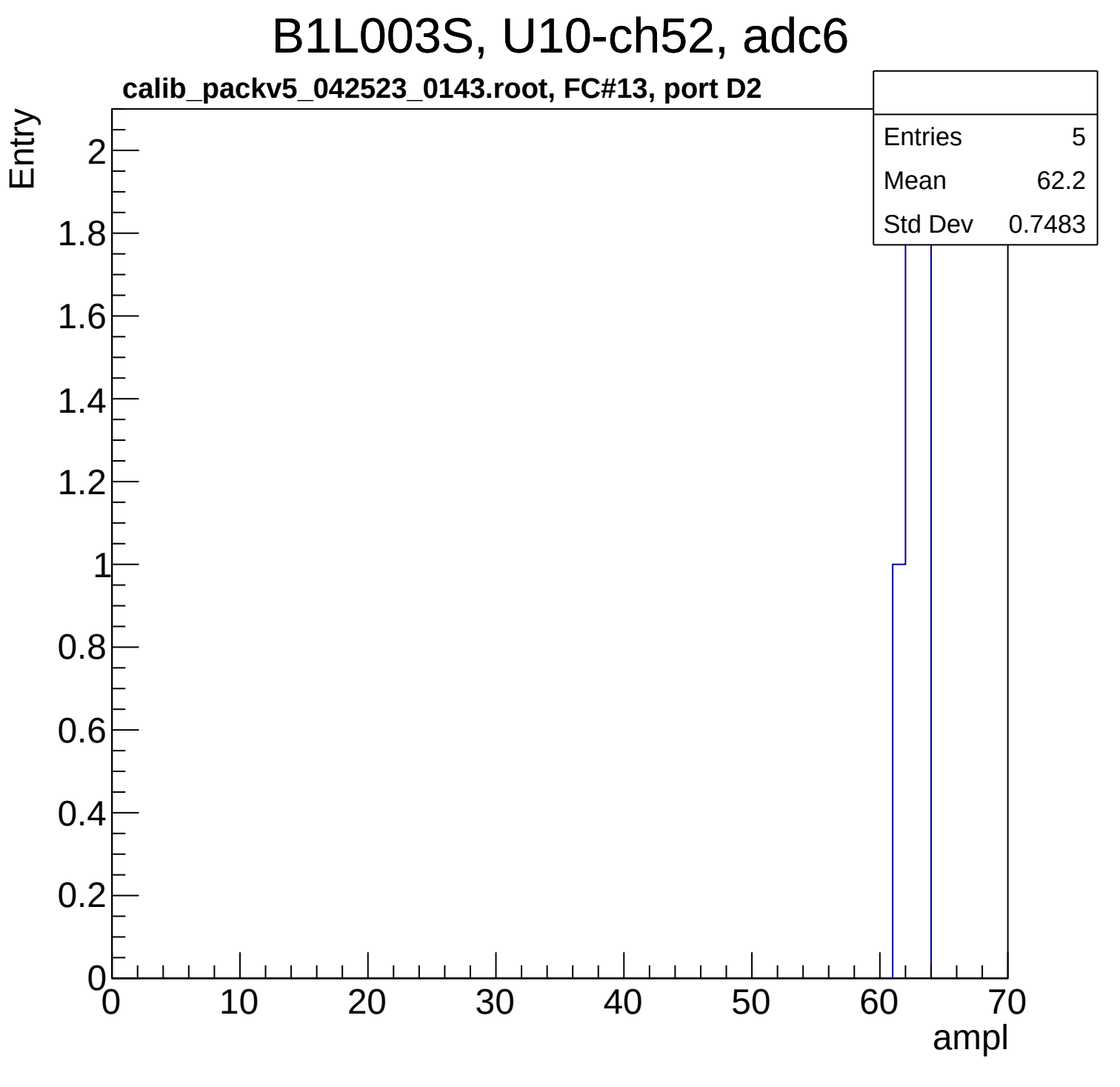
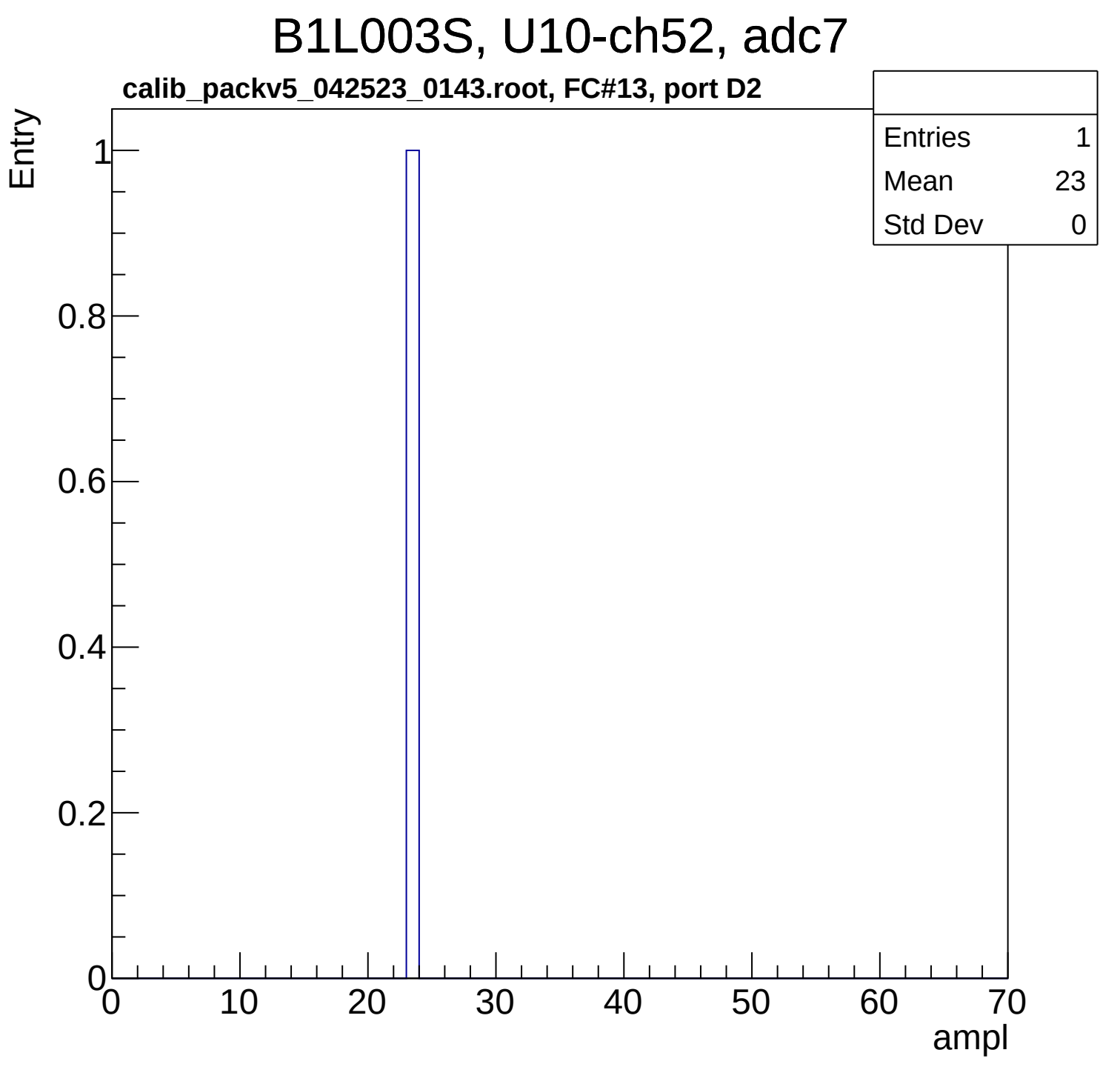

B1L003S, U10-ch52, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch52, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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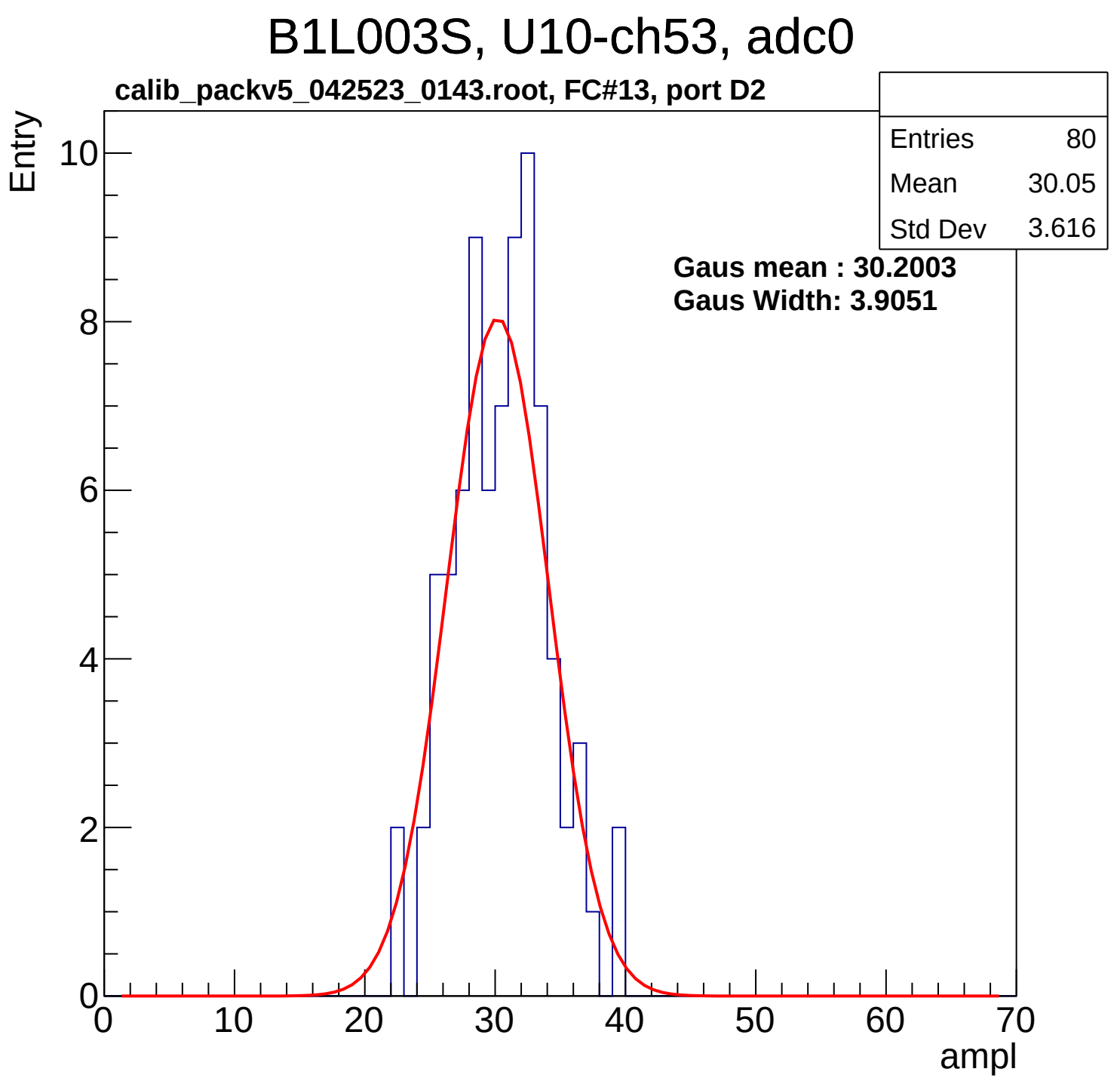
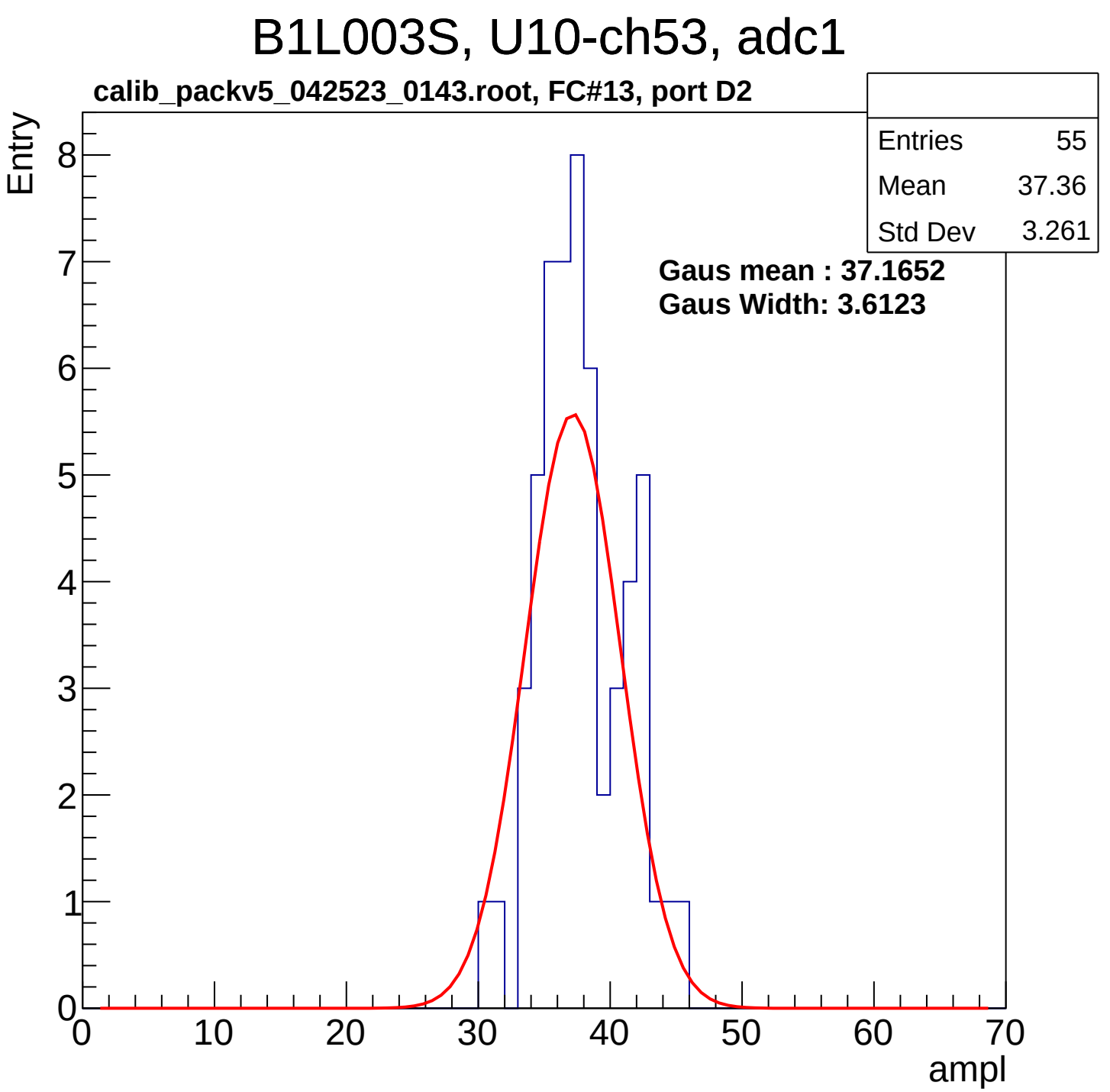
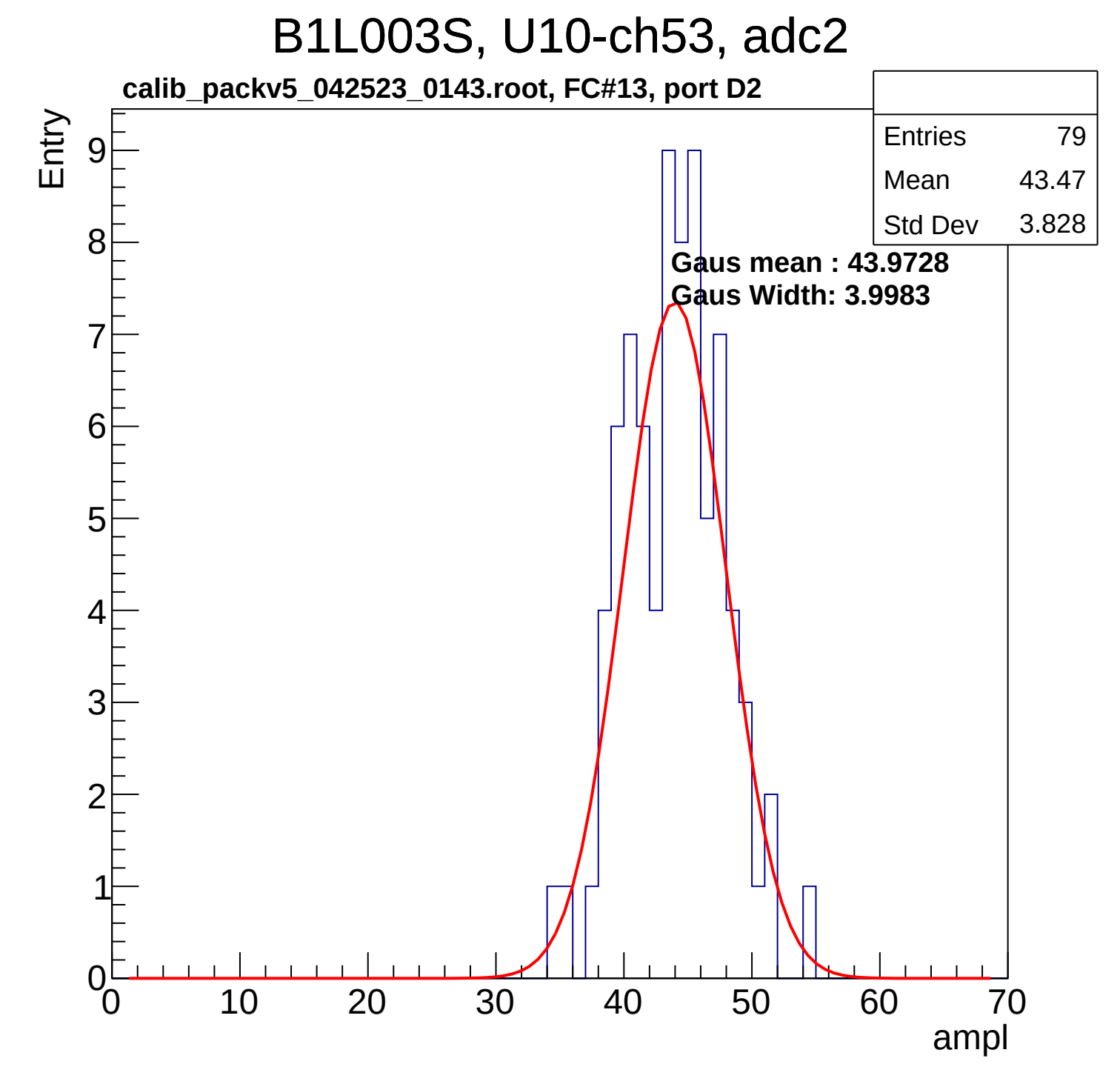
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch53, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 30.05
Std Dev 3.616
Gaus mean : 30.2003
Gaus Width: 3.9051
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch53, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 37.36
Std Dev 3.261
Gaus mean : 37.1652
Gaus Width: 3.6123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch53, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 43.47
Std Dev 3.828
Gaus mean : 43.9728
Gaus Width: 3.9983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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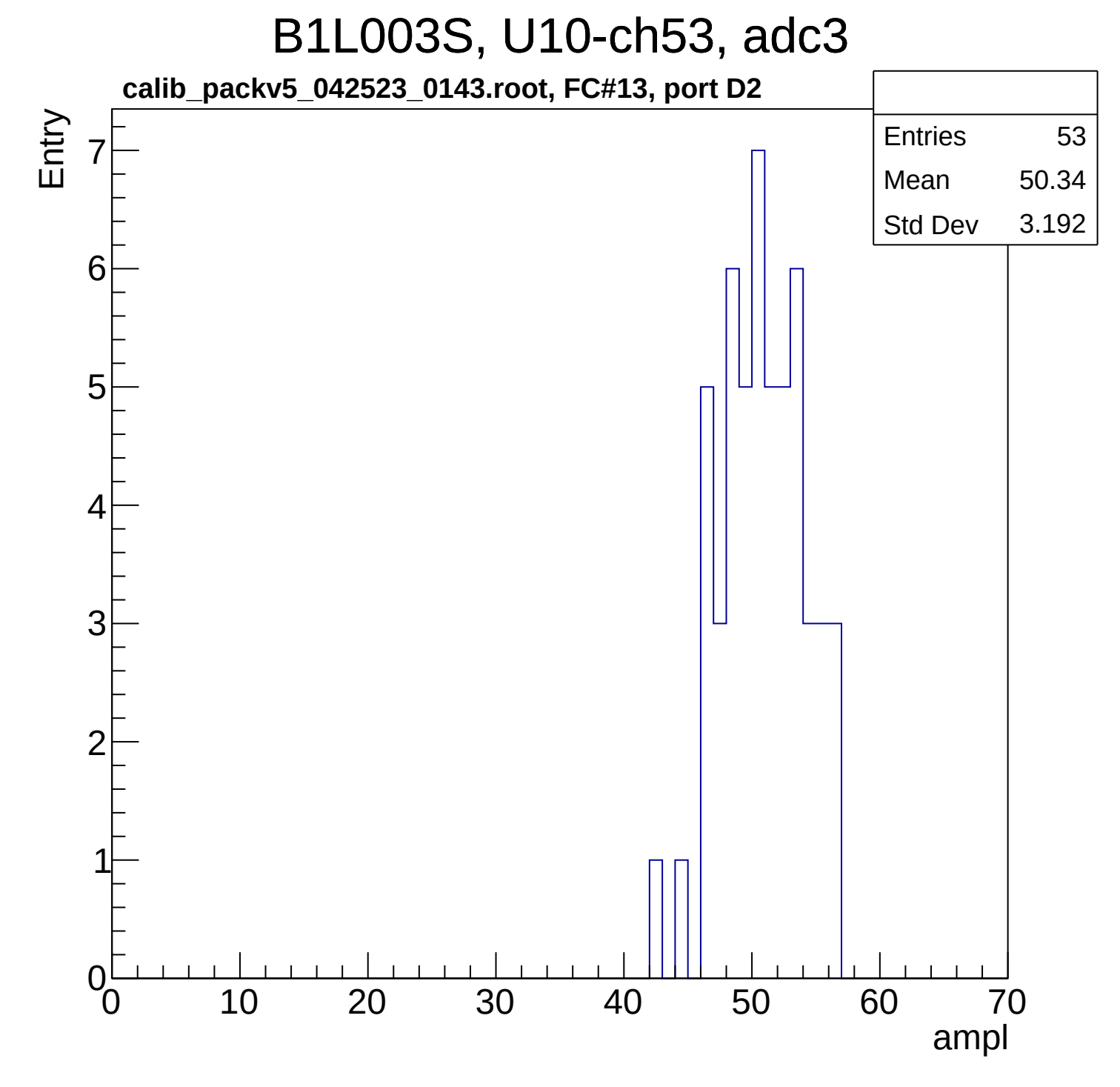
B1L003S, U10-ch53, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 50.34
Std Dev 3.192
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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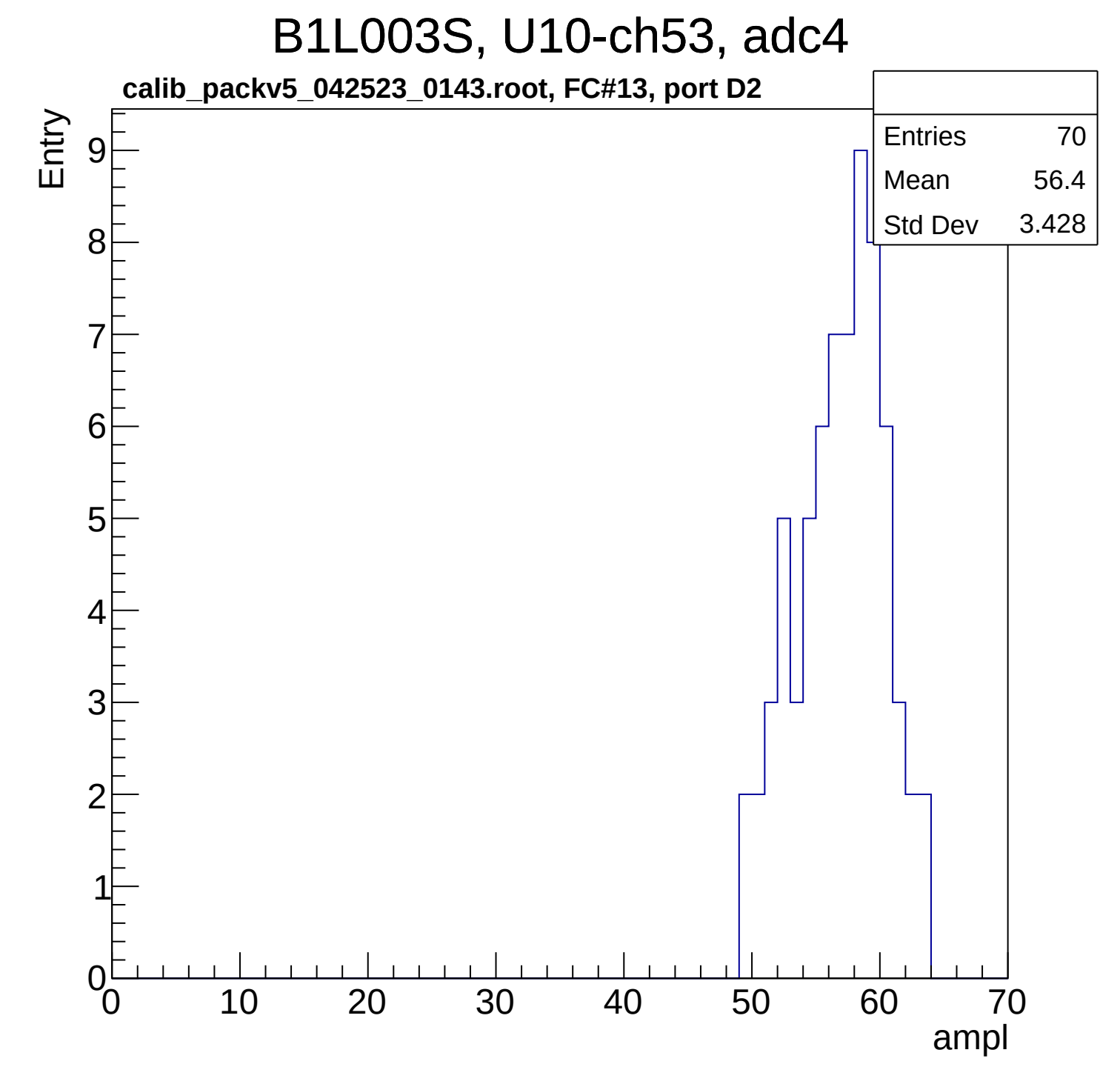
B1L003S, U10-ch53, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 56.4
Std Dev 3.428
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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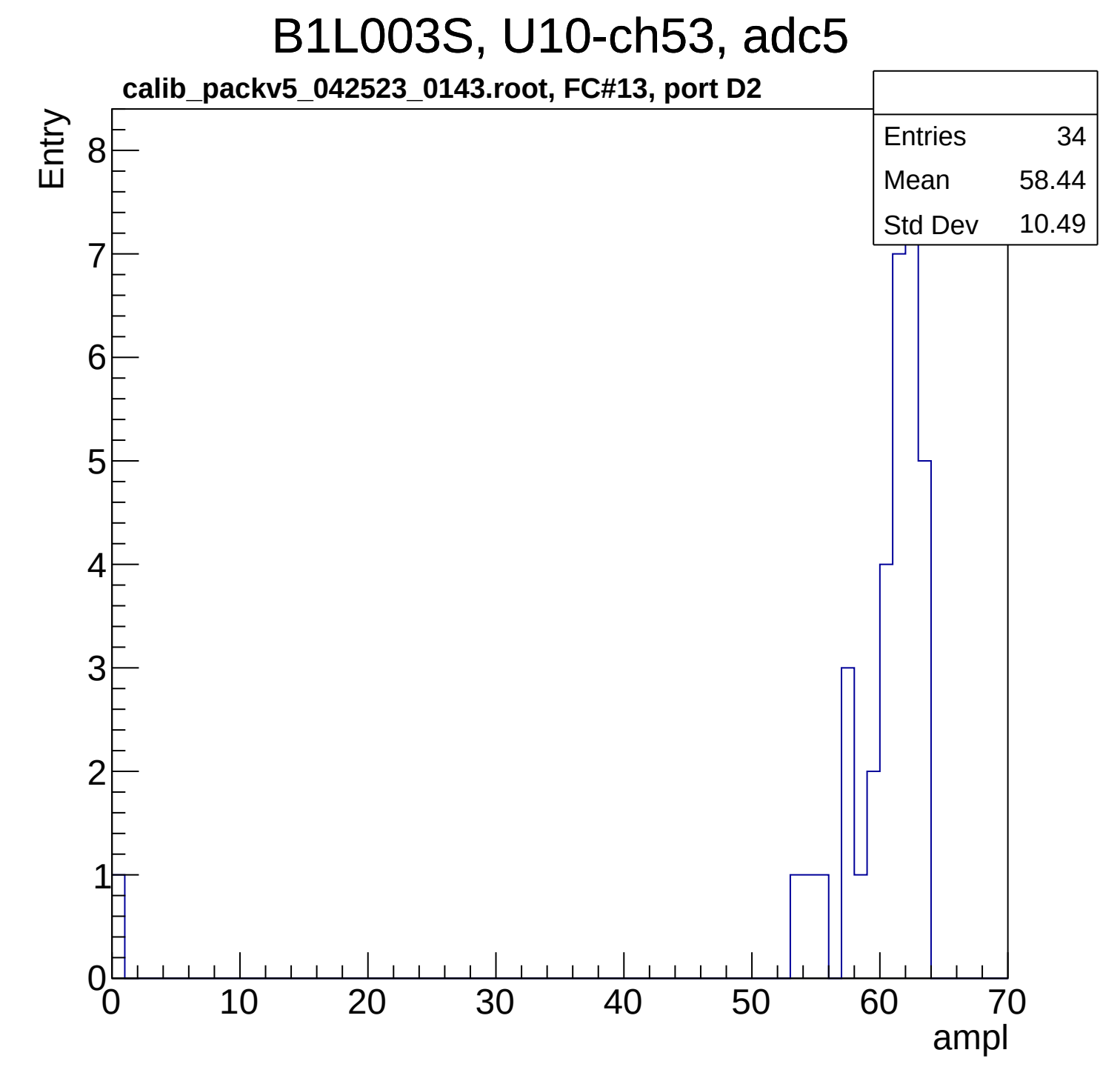
B1L003S, U10-ch53, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 58.44
Std Dev 10.49
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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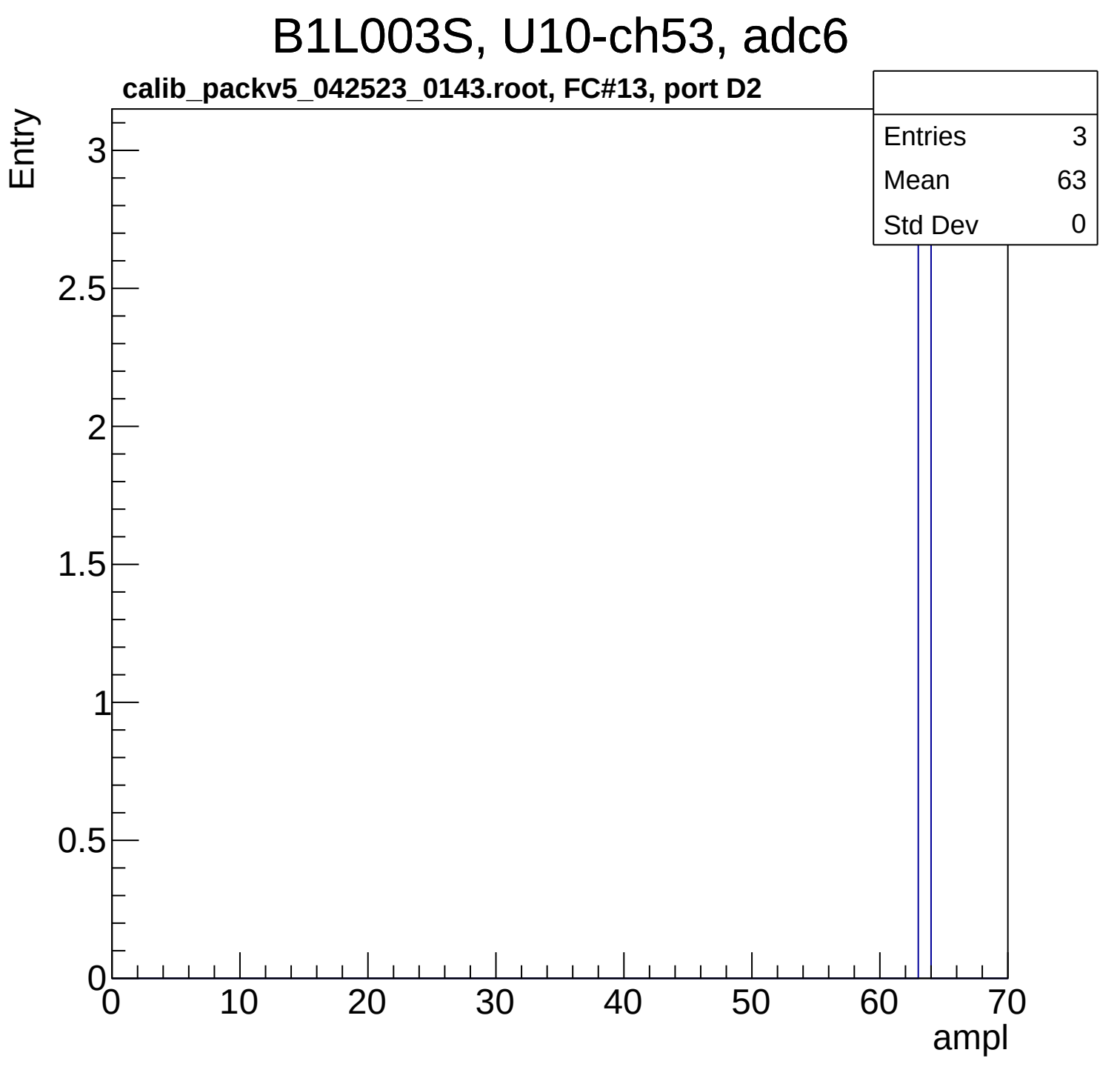
B1L003S, U10-ch53, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 63
Std Dev 0
Entry
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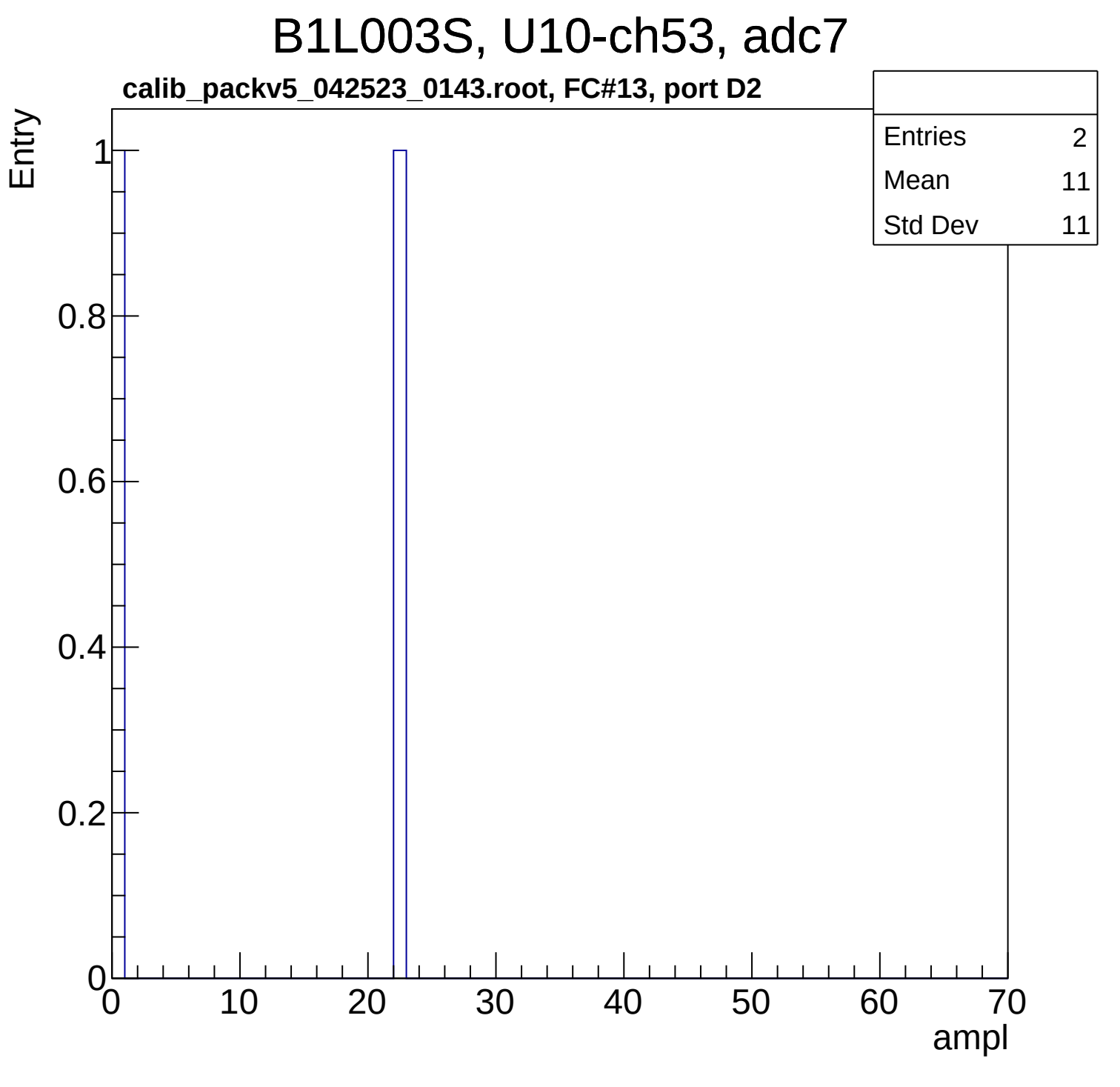
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch53, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
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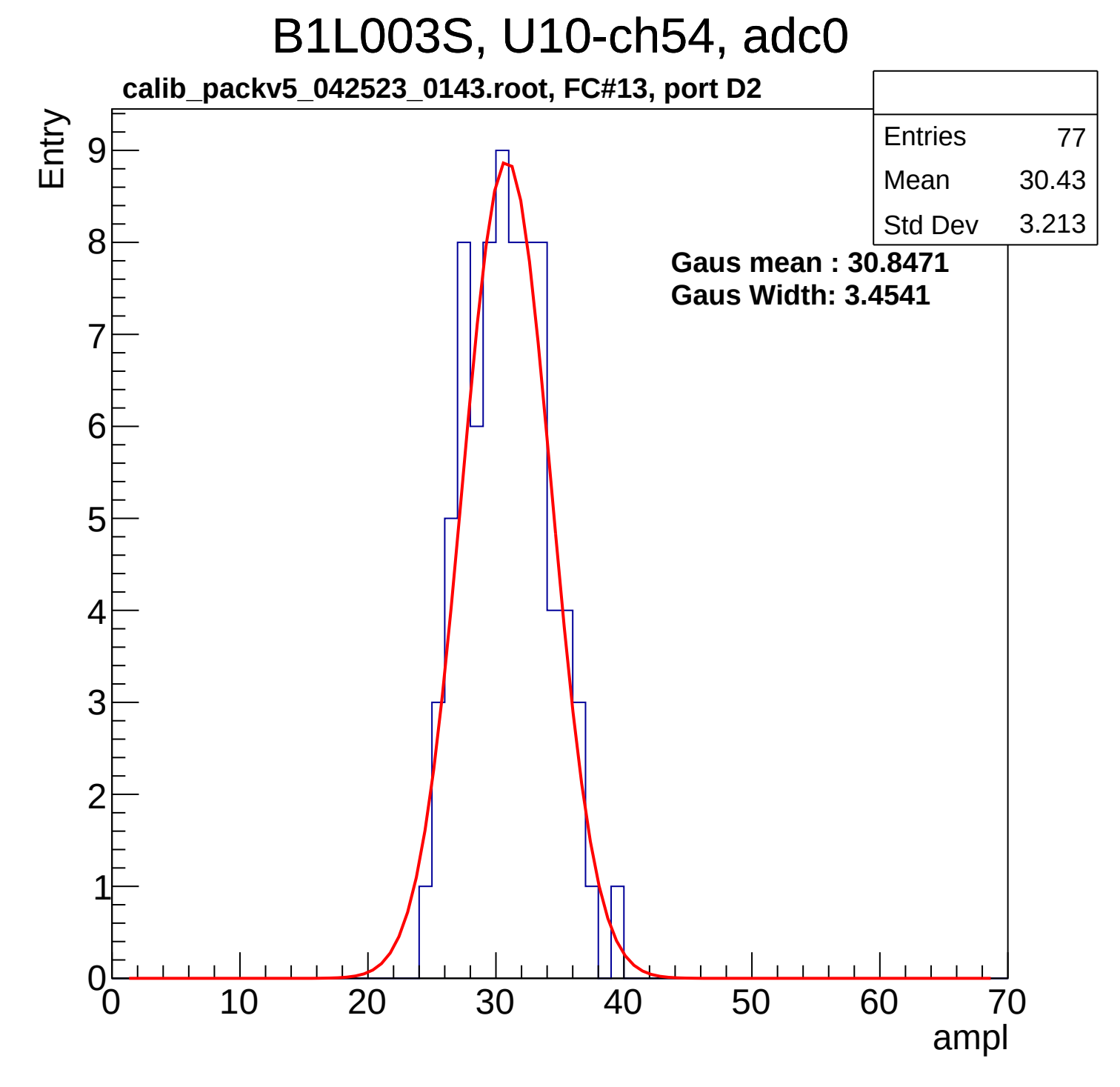
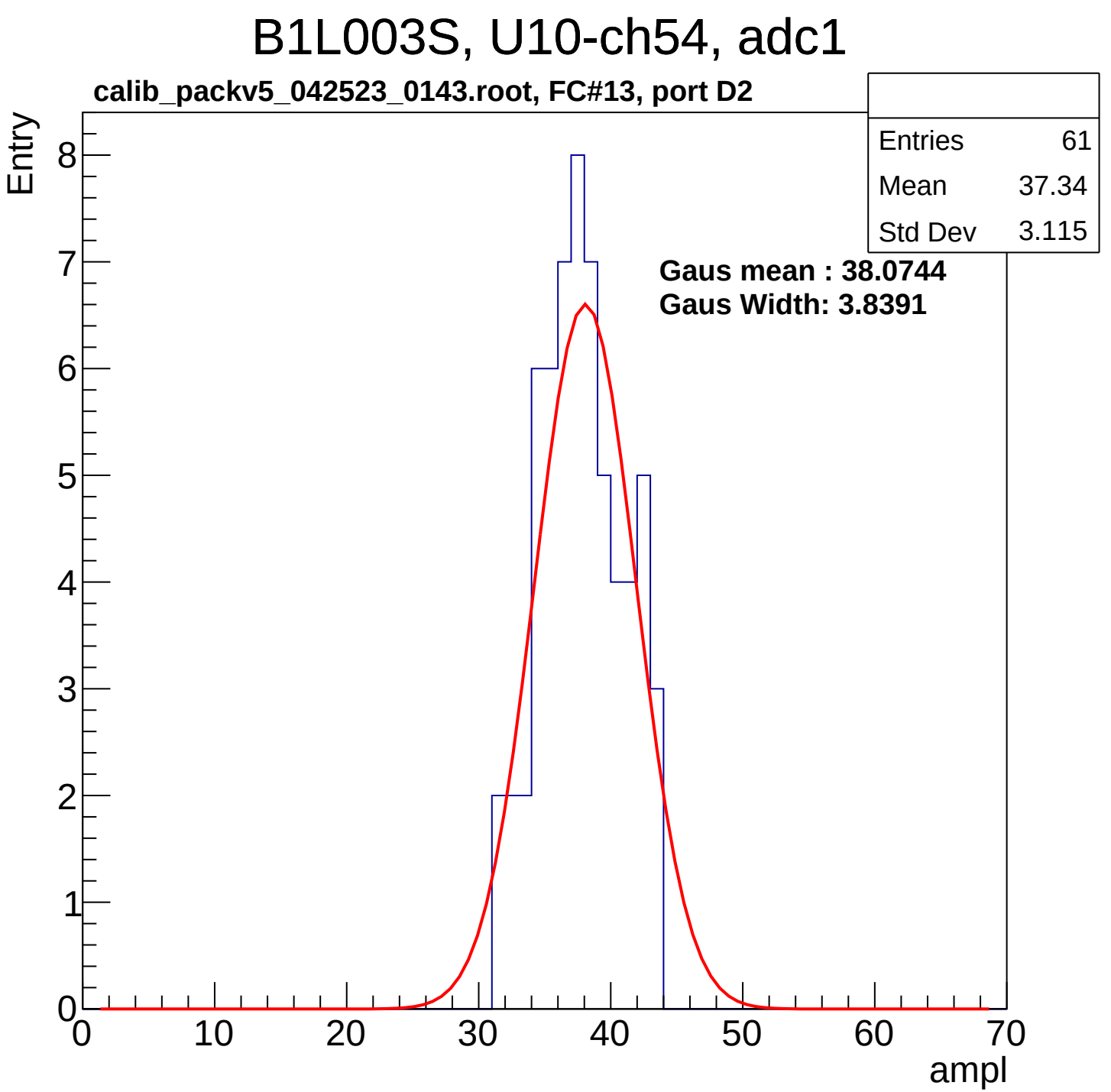
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch54, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 30.43
Std Dev 3.213
Gaus mean : 30.8471
Gaus Width: 3.4541
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch54, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 37.34
Std Dev 3.115
Gaus mean : 38.0744
Gaus Width: 3.8391
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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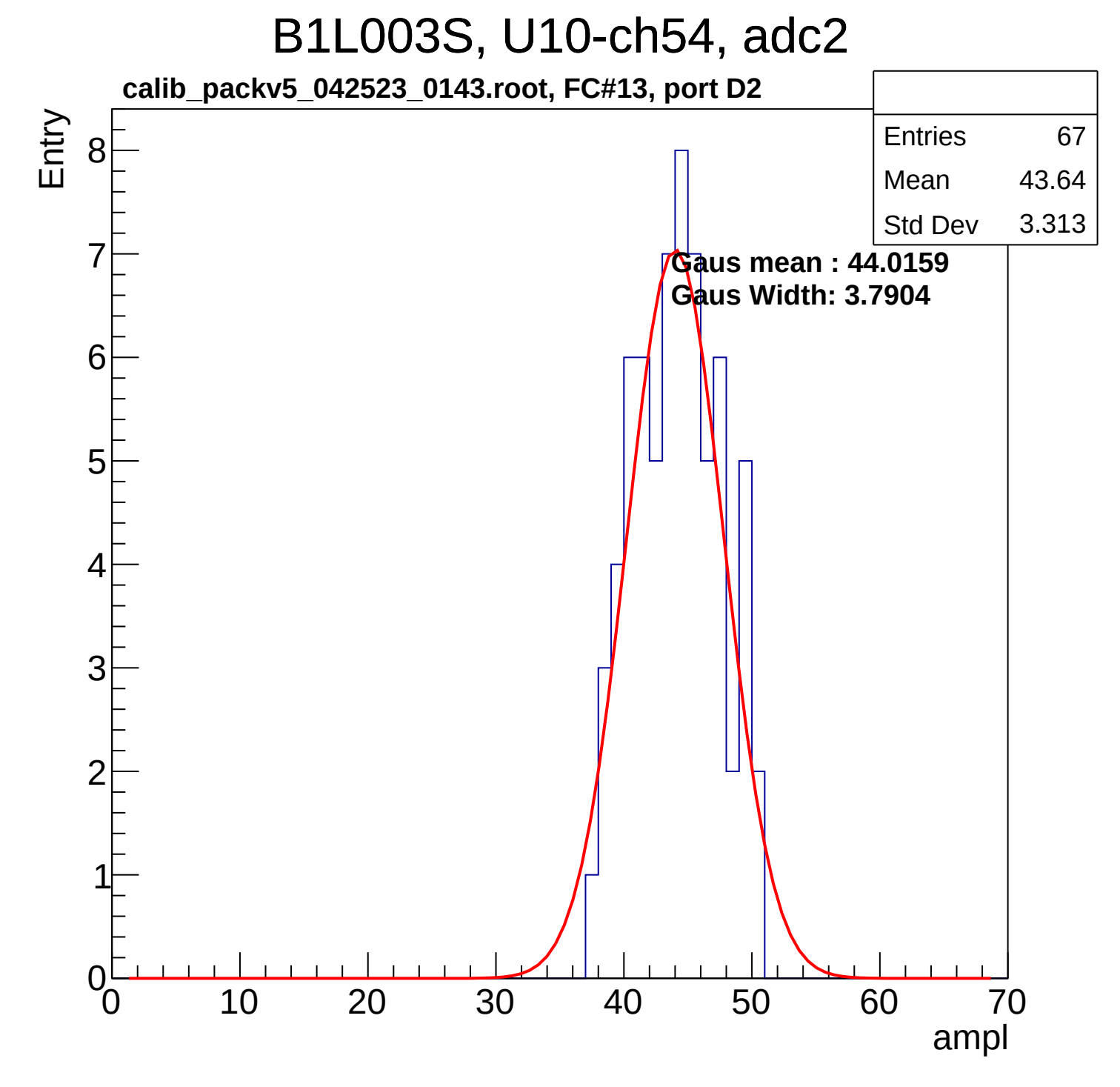
B1L003S, U10-ch54, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.64
Std Dev 3.313
Gaus mean : 44.0159
Gaus Width: 3.7904
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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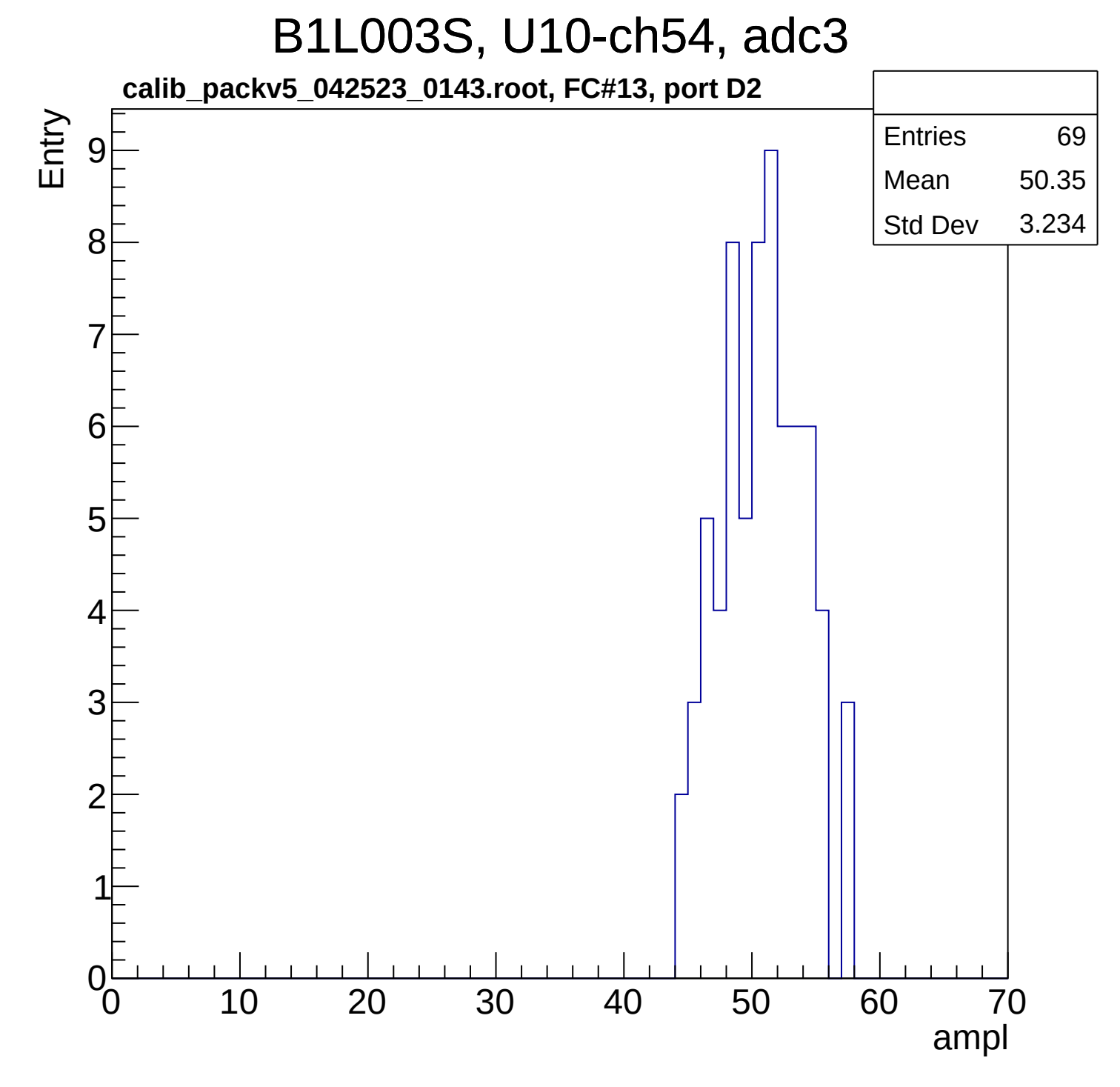
B1L003S, U10-ch54, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.35
Std Dev 3.234
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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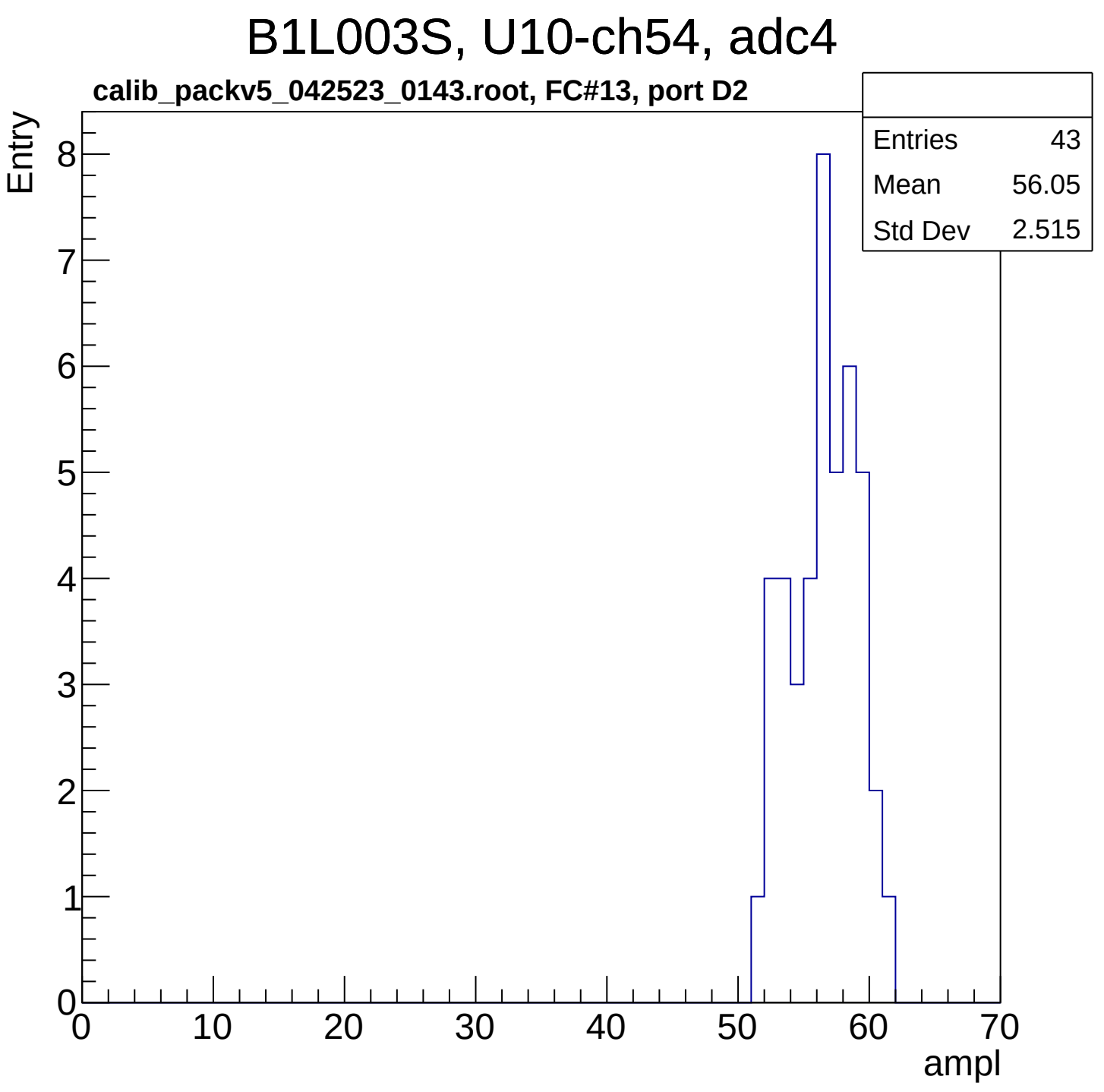
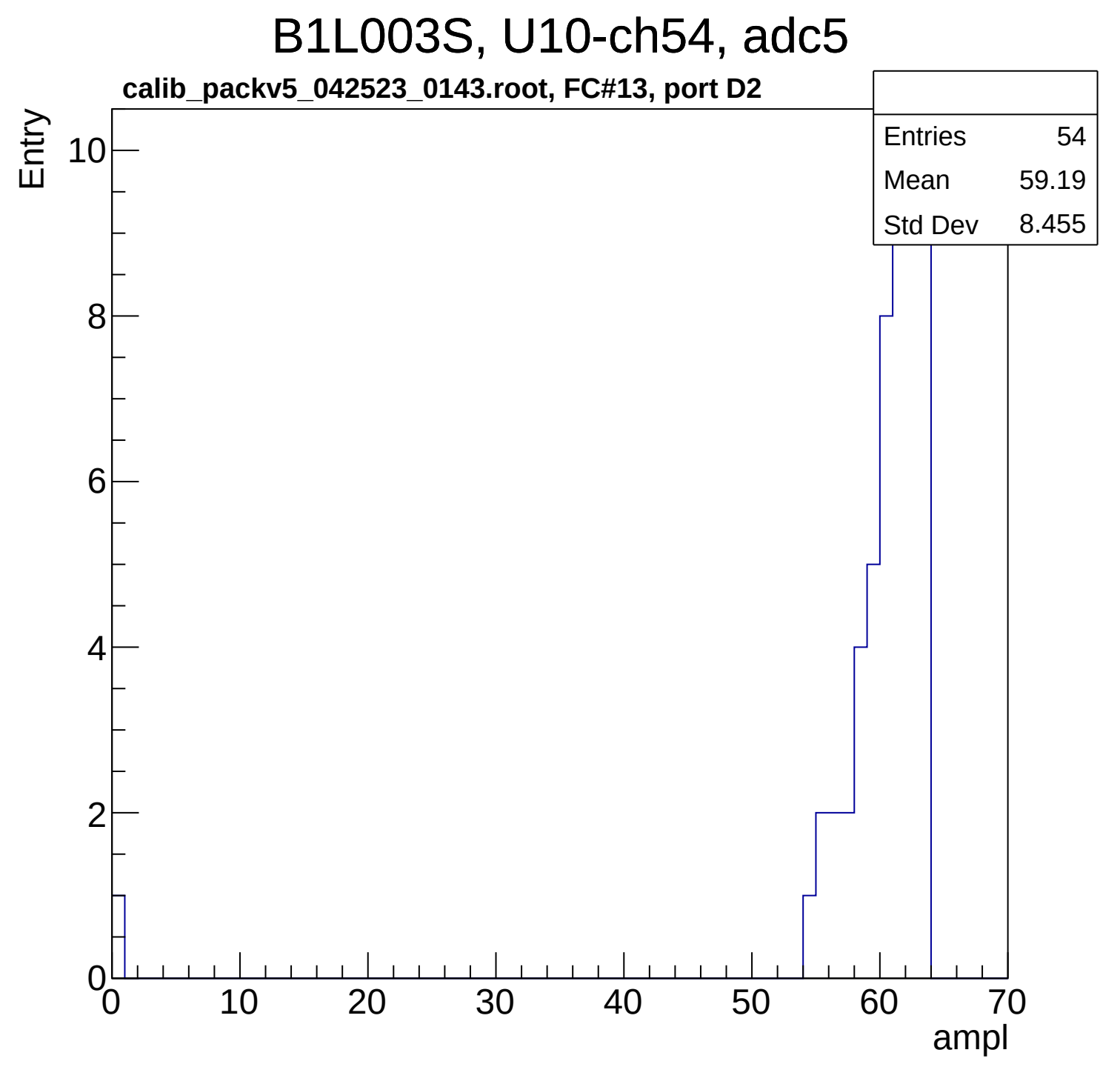
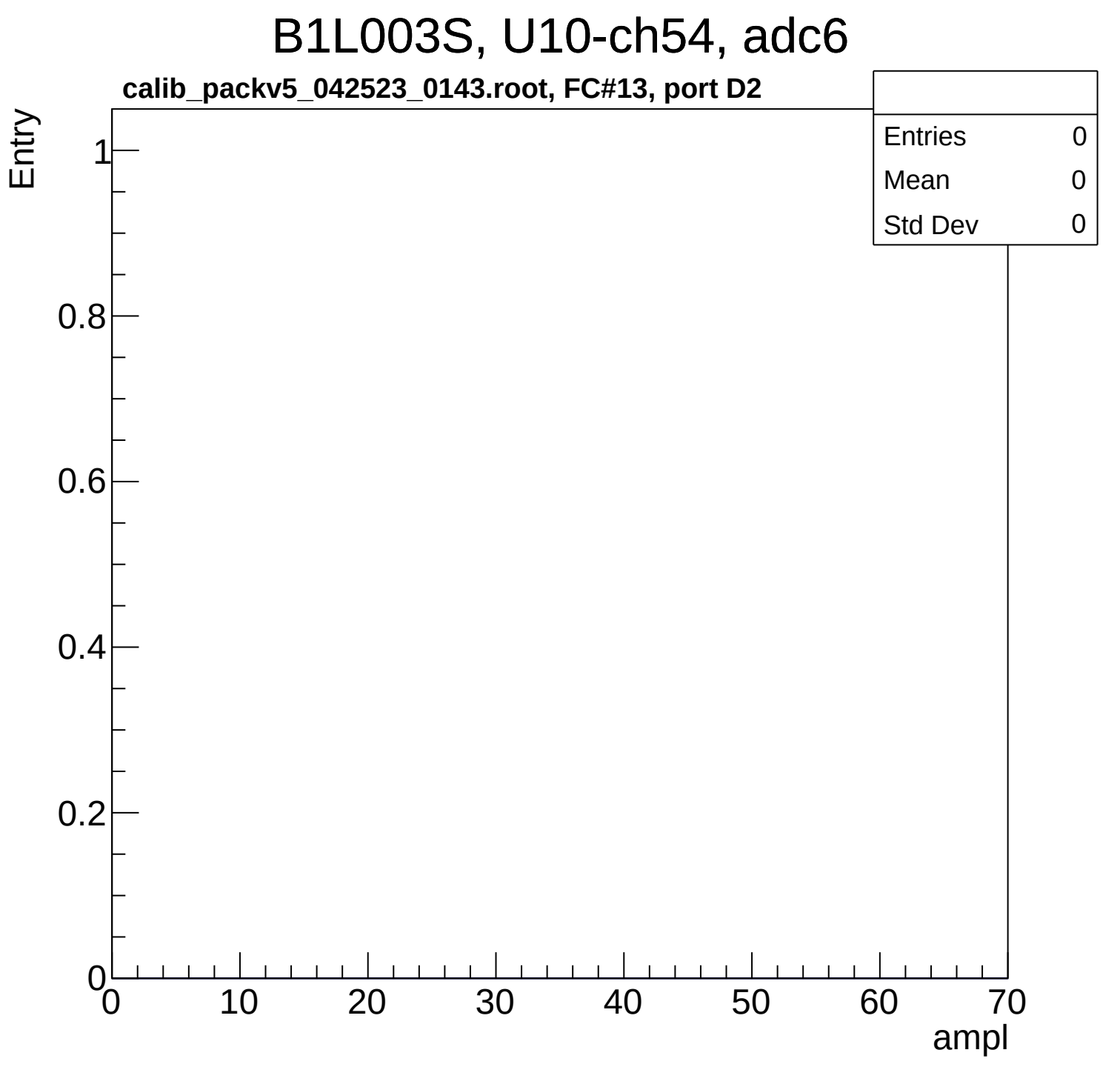
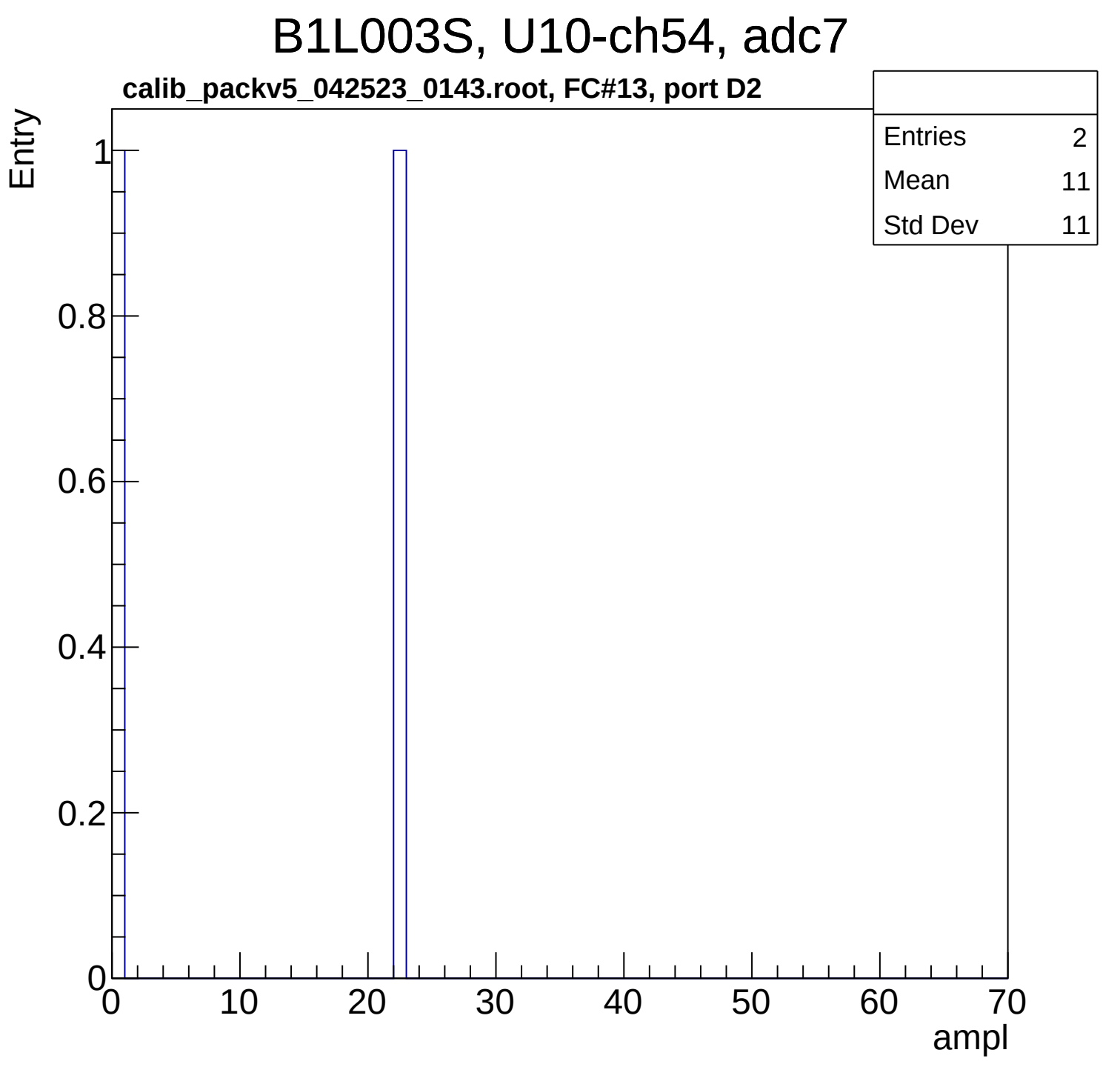
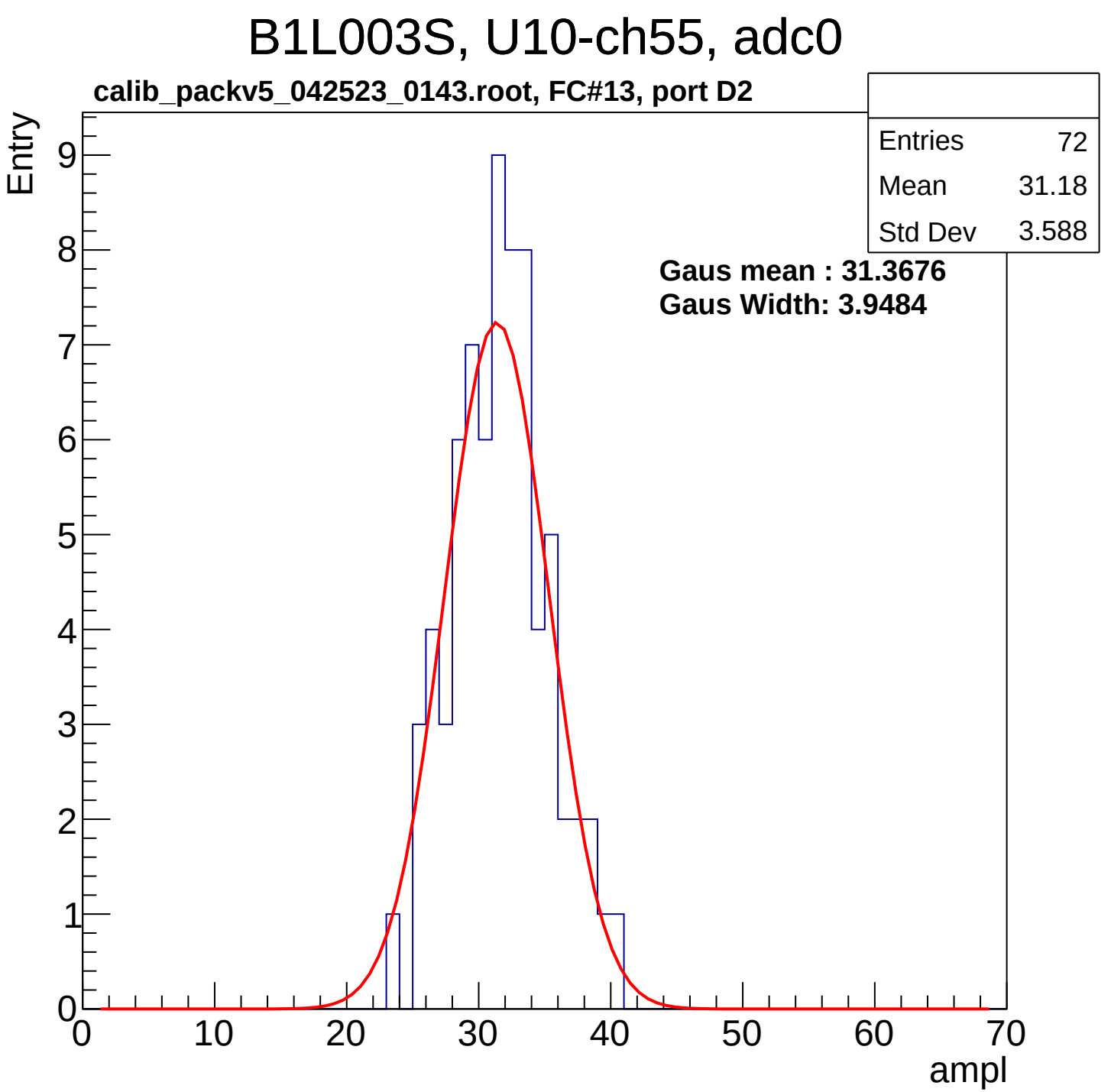

B1L003S, U10-ch54, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 56.05
Std Dev 2.515
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch54, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.19
Std Dev 8.455
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch54, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch54, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch55, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 31.18
Std Dev 3.588
Gaus mean : 31.3676
Gaus Width: 3.9484
Entry
ampl
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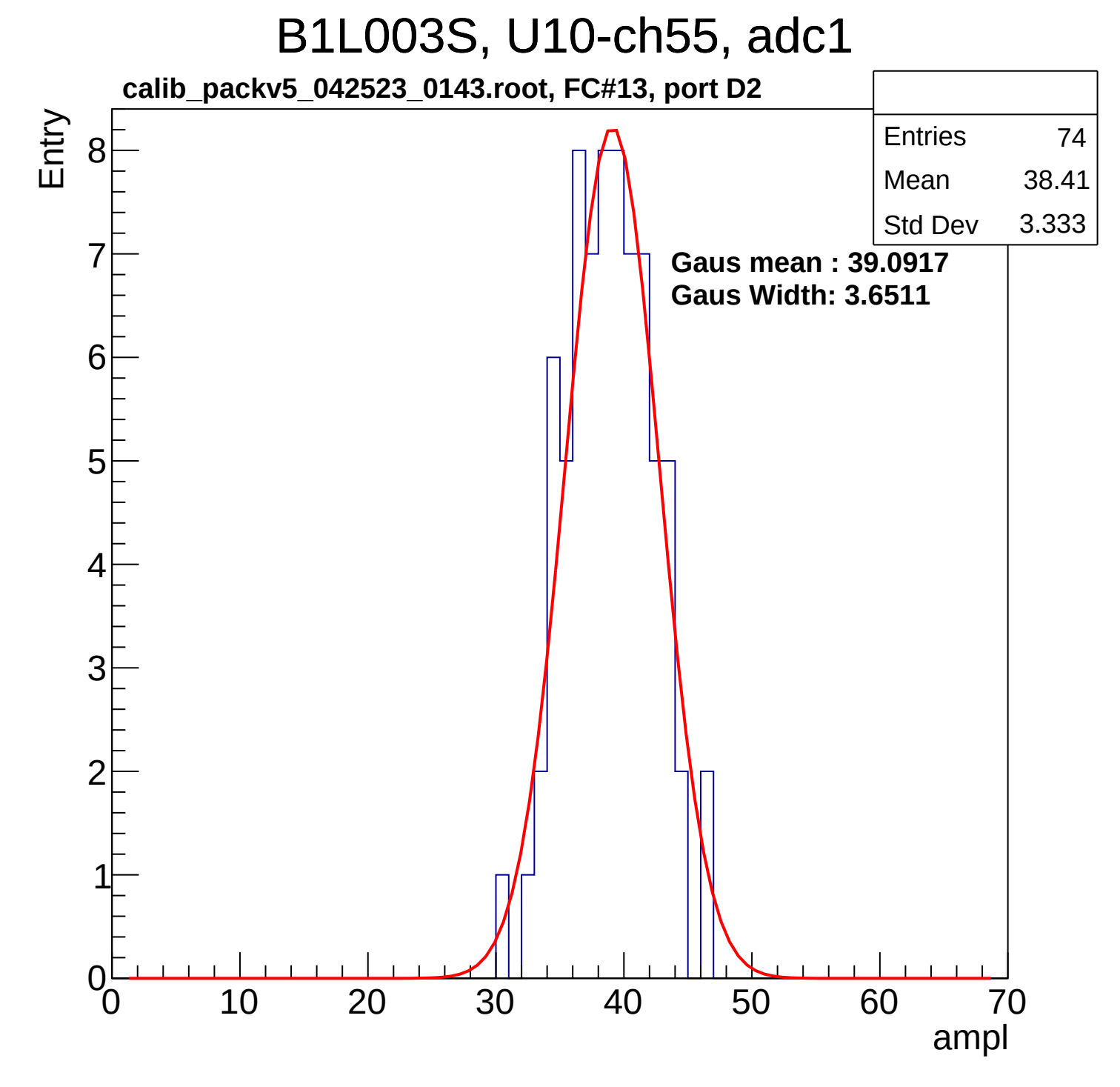
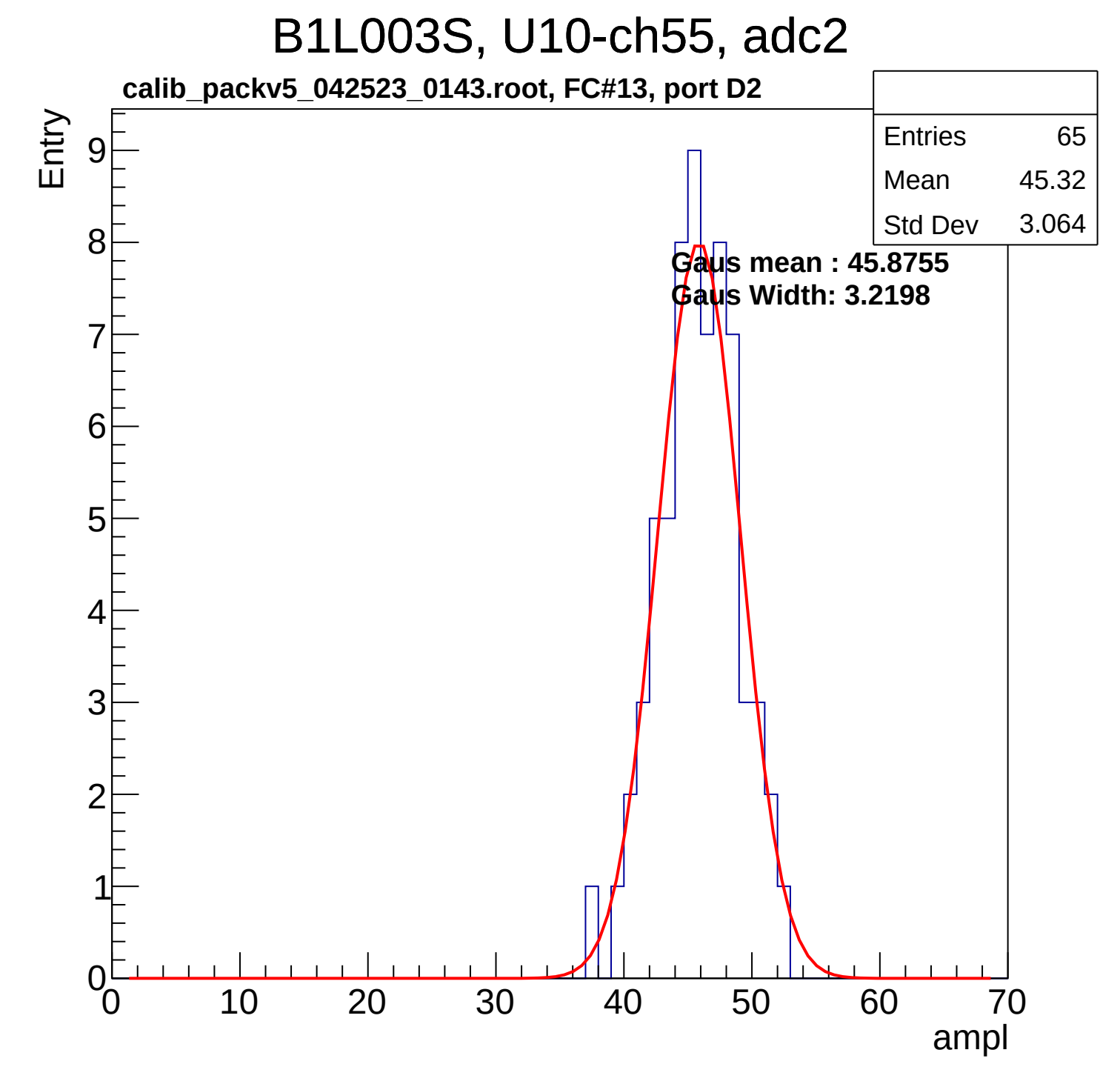
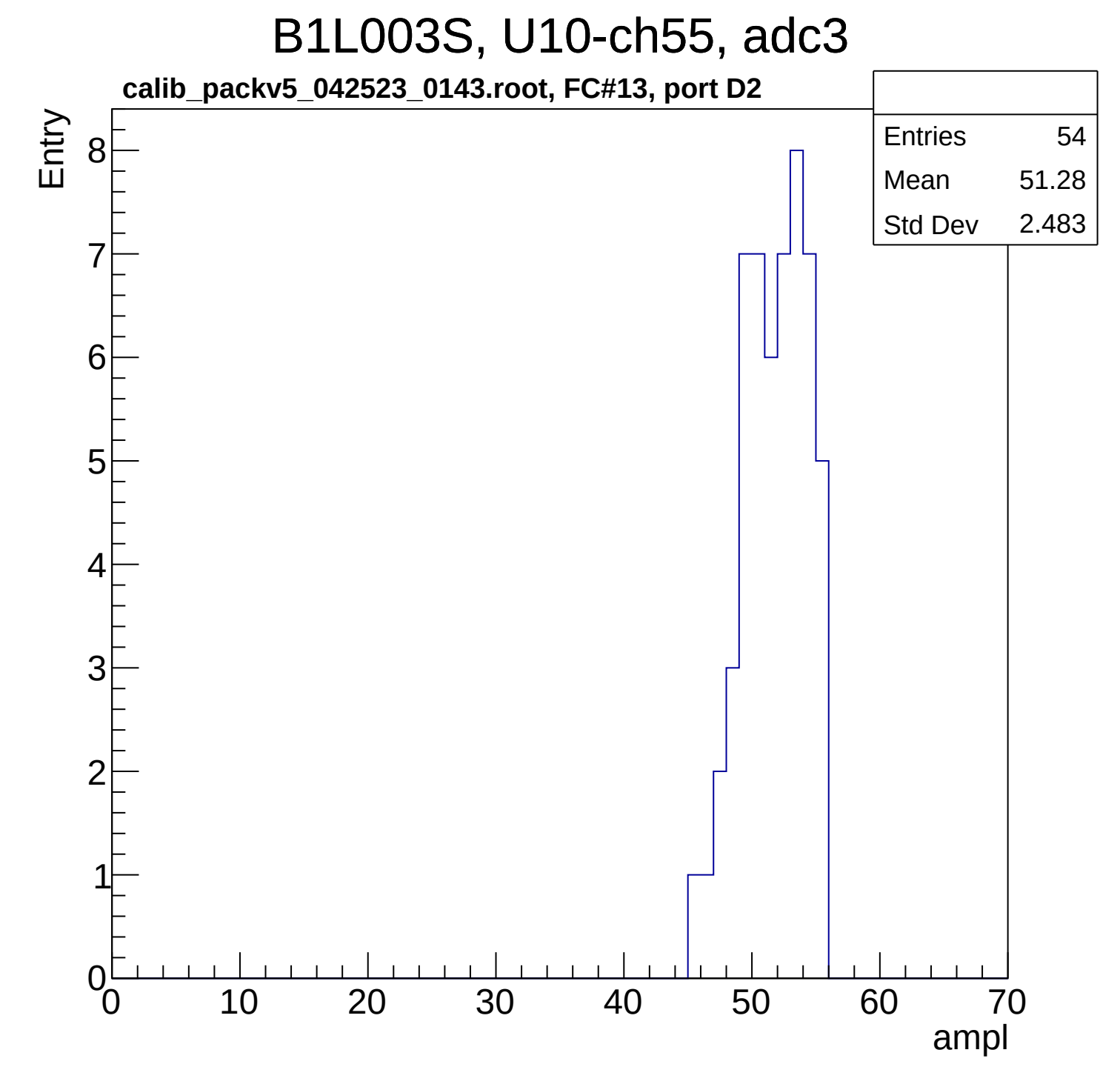
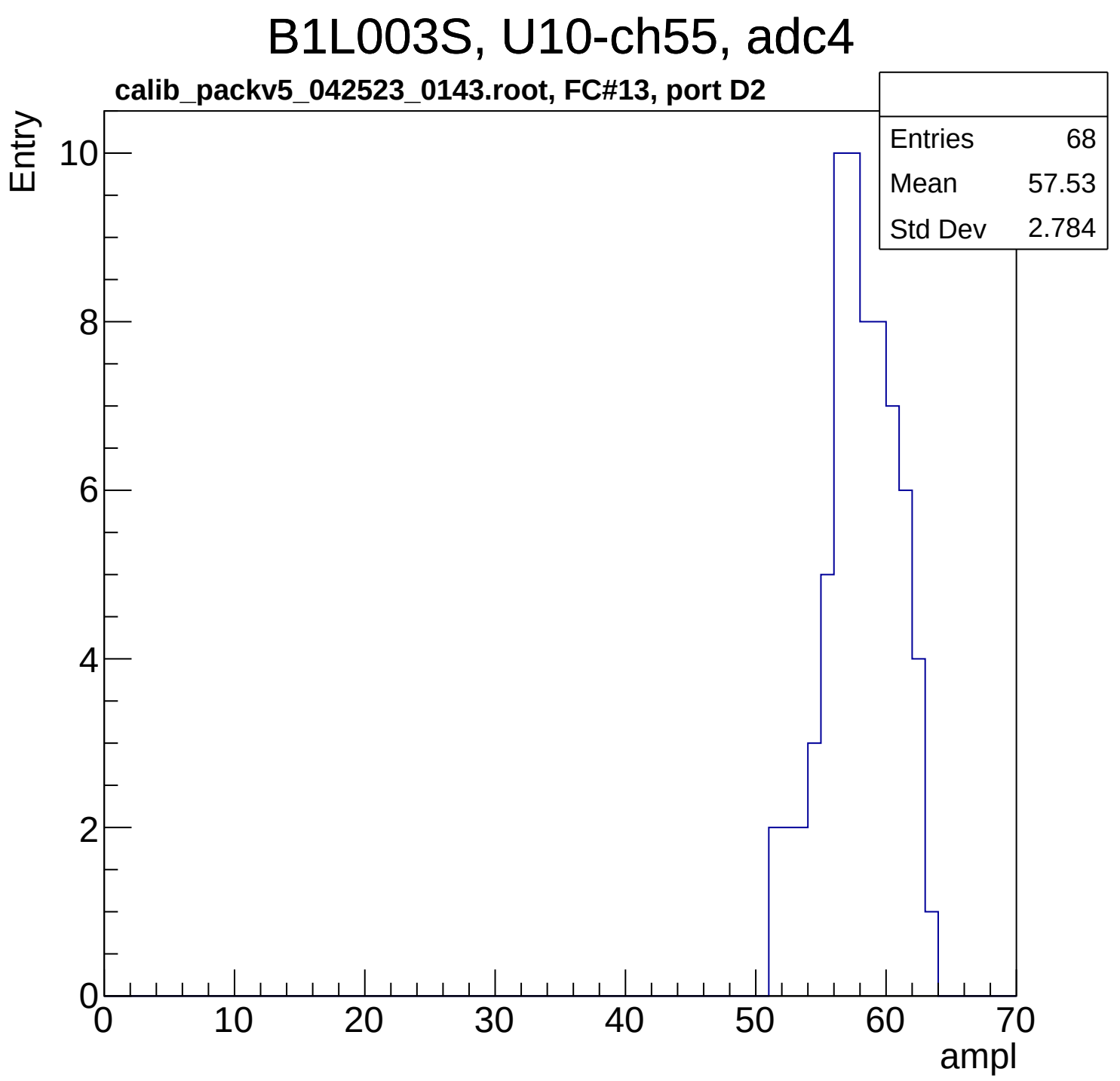
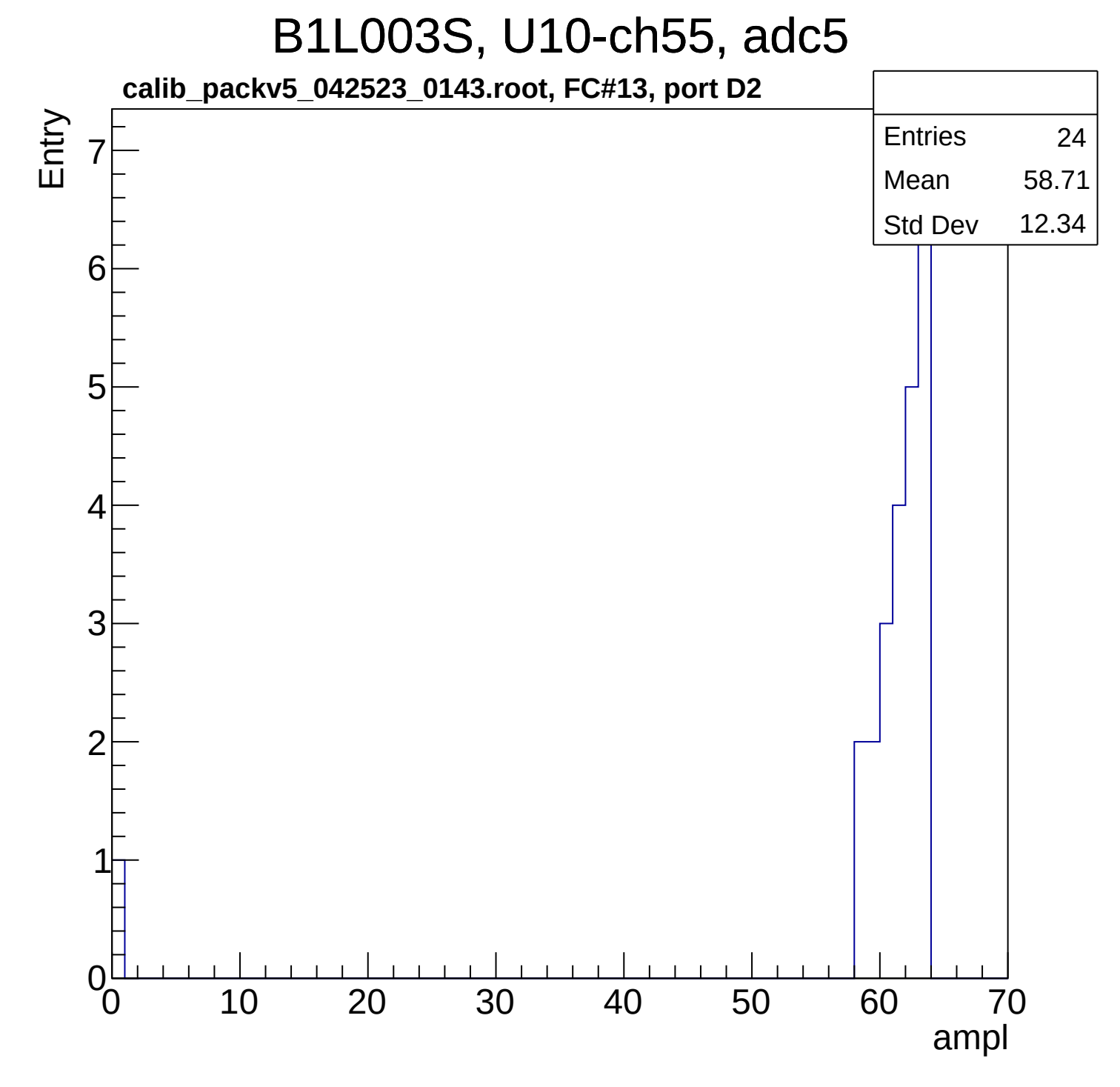
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

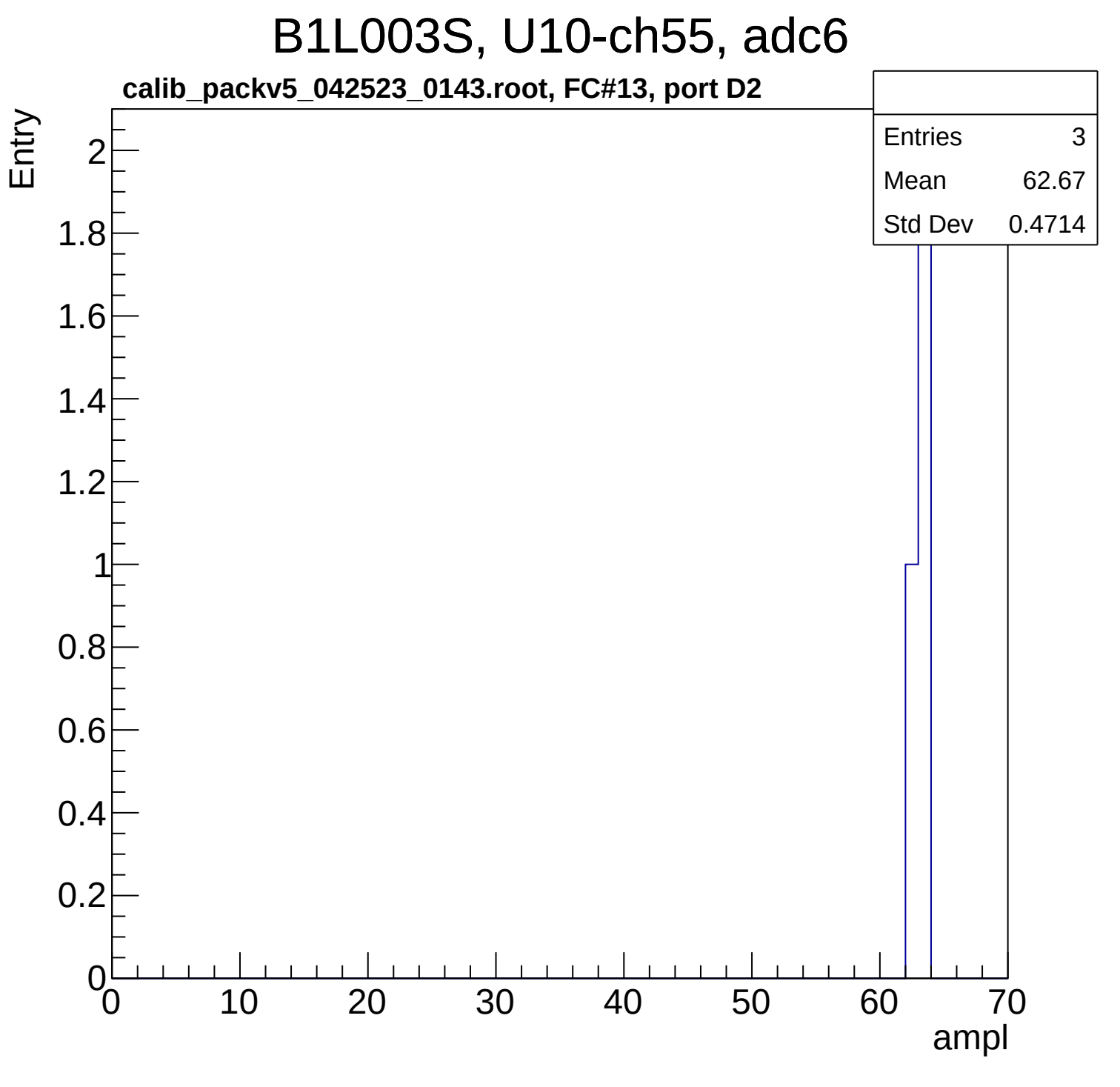

B1L003S, U10-ch55, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 38.41
Std Dev 3.333
Gaus mean : 39.0917
Gaus Width: 3.6511
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

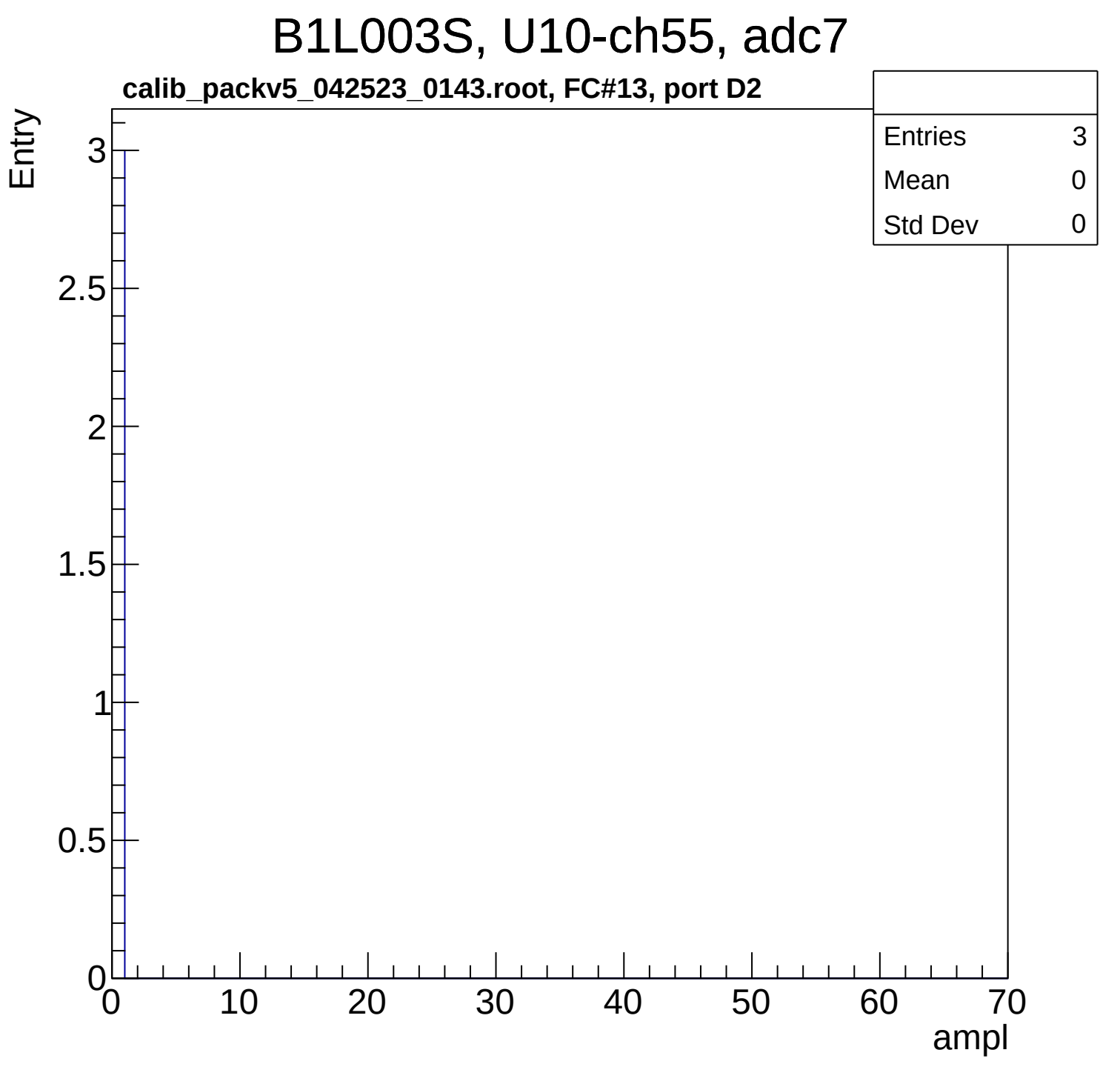

B1L003S, U10-ch55, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 45.32
Std Dev 3.064
Gaus mean : 45.8755
Gaus Width: 3.2198
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

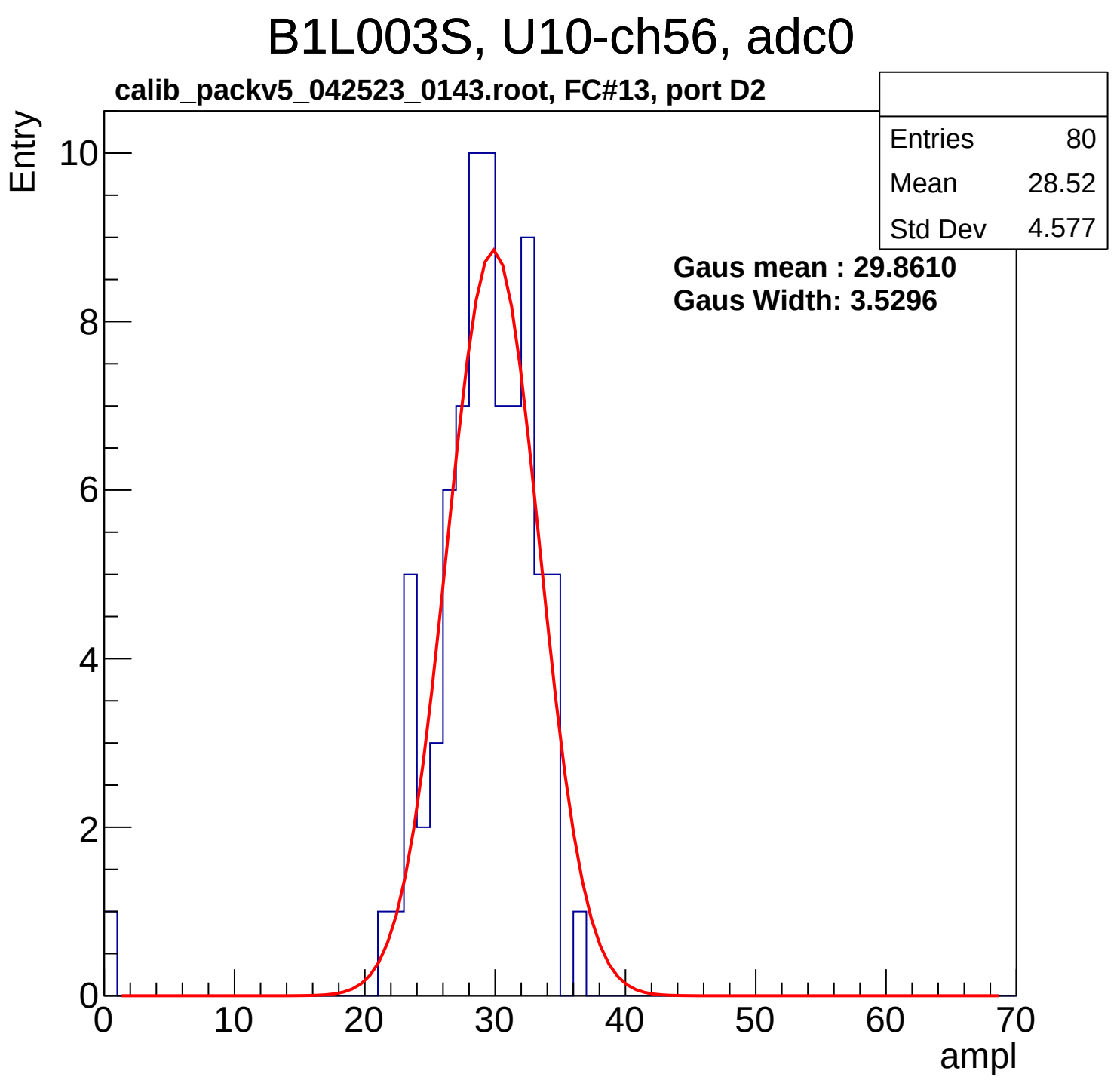

B1L003S, U10-ch55, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 51.28
Std Dev 2.483
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

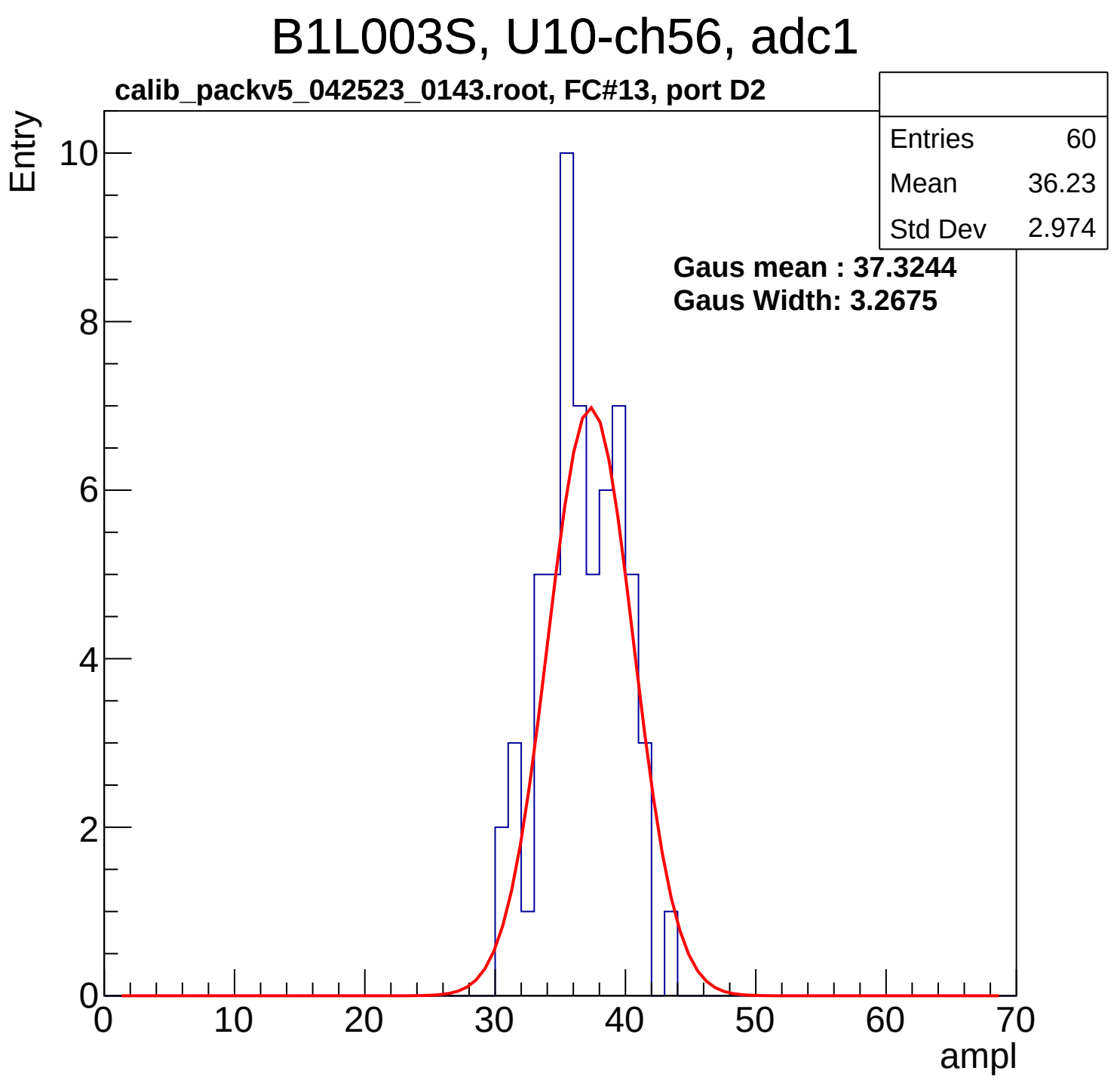

B1L003S, U10-ch55, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 57.53
Std Dev 2.784
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

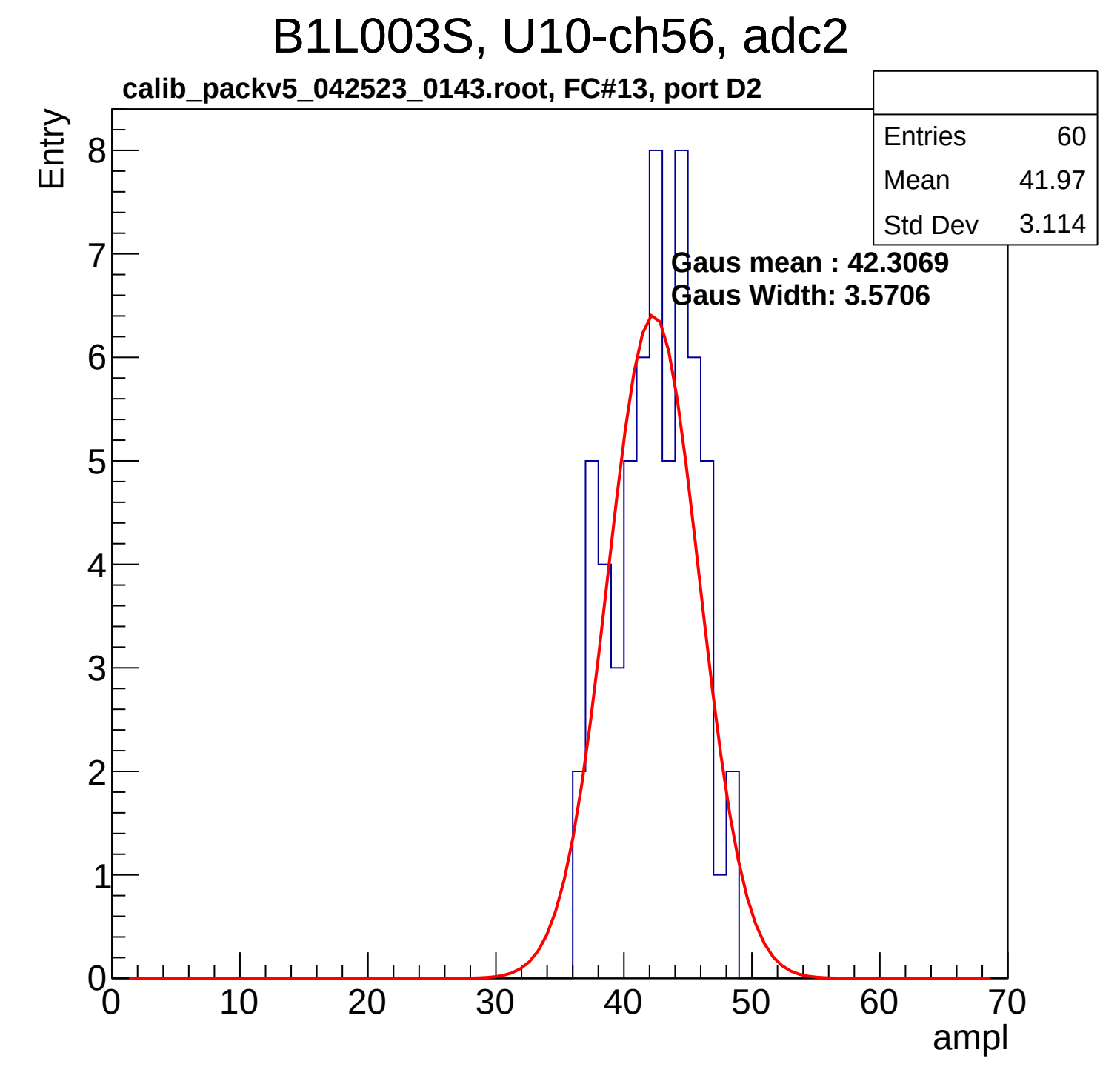

B1L003S, U10-ch55, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 58.71
Std Dev 12.34
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

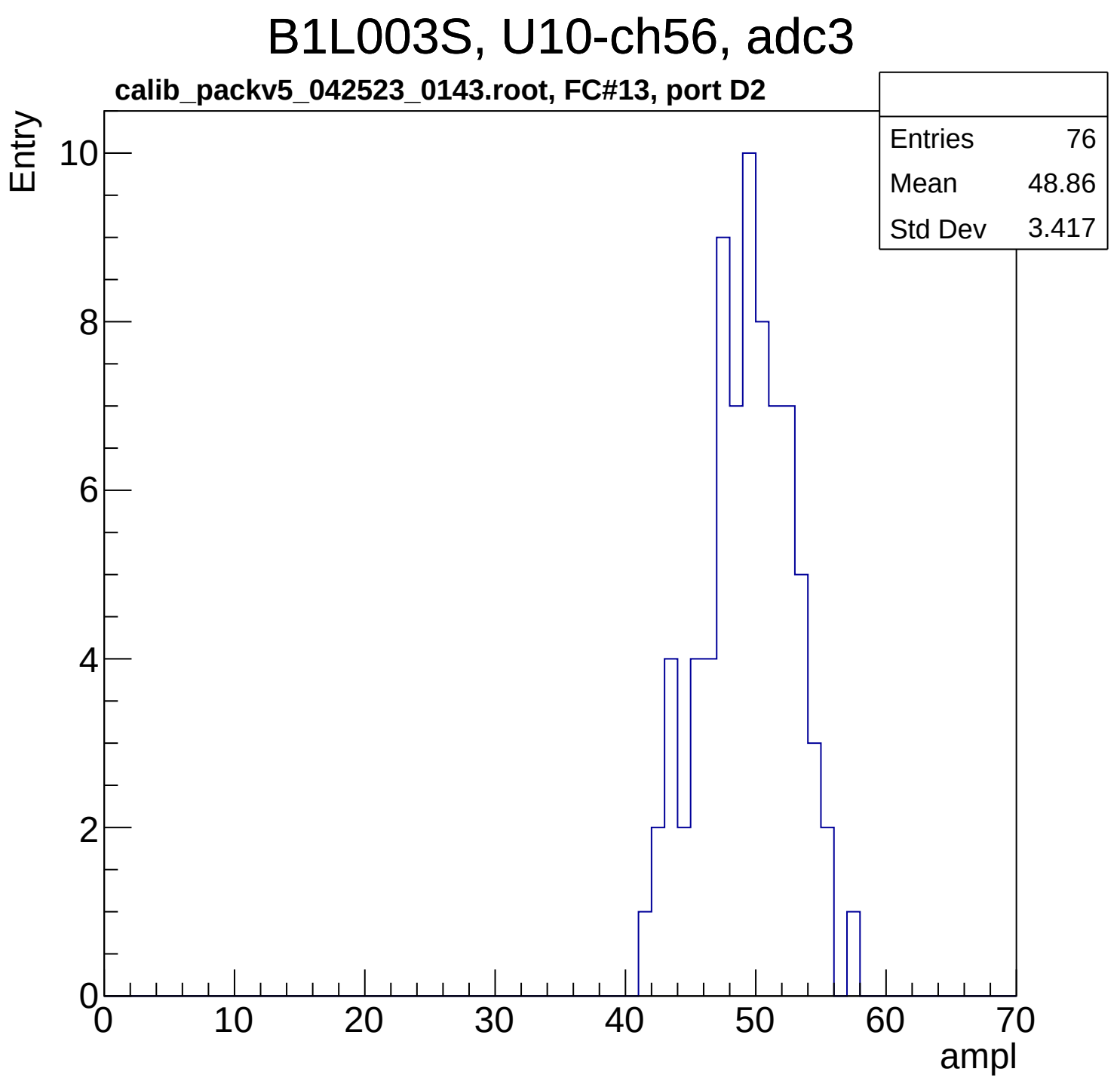

B1L003S, U10-ch55, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

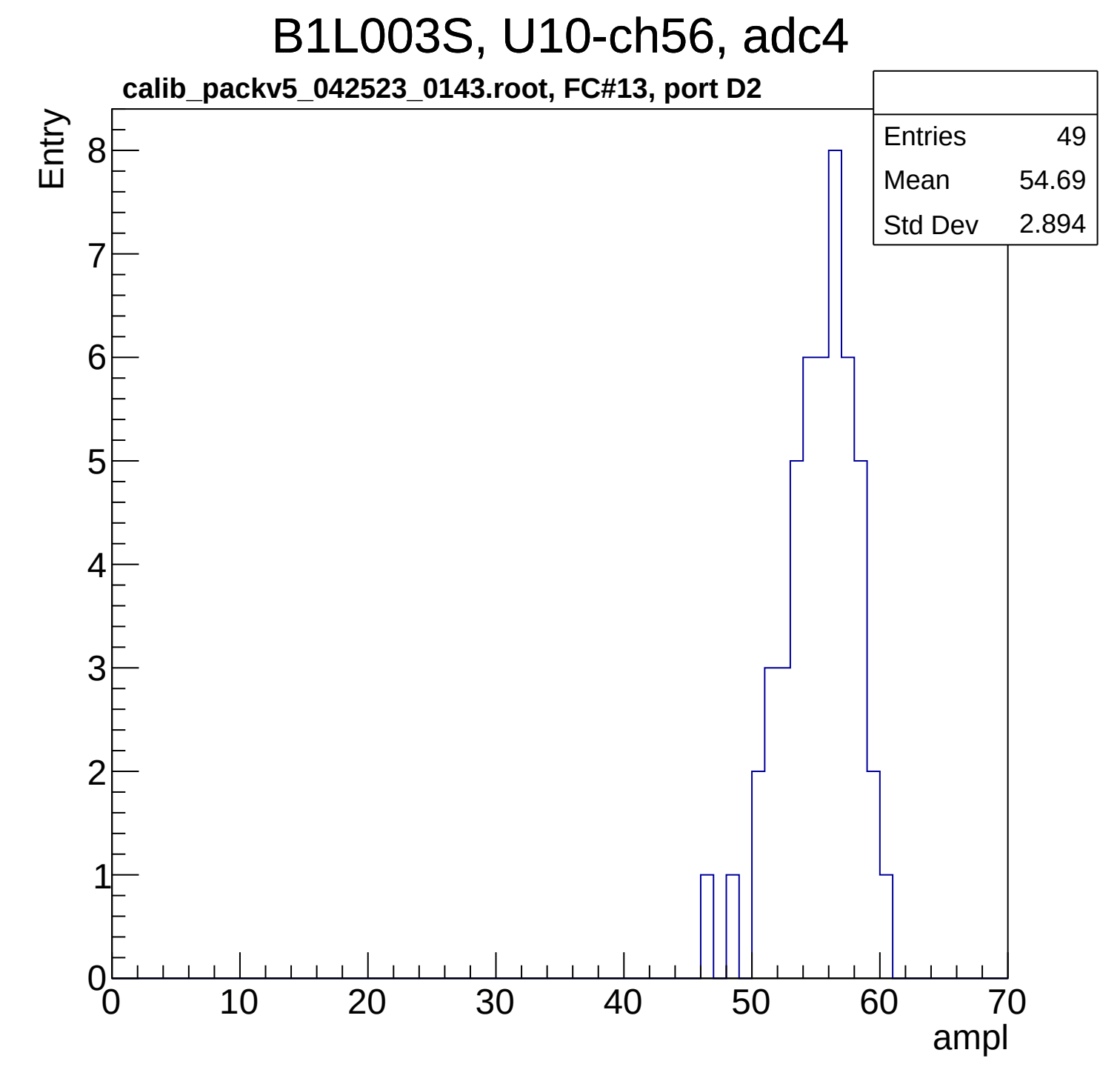

B1L003S, U10-ch55, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch56, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 28.52
Std Dev 4.577
Gaus mean : 29.8610
Gaus Width: 3.5296
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch56, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.23
Std Dev 2.974
Gaus mean : 37.3244
Gaus Width: 3.2675
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch56, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.97
Std Dev 3.114
Gaus mean : 42.3069
Gaus Width: 3.5706
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch56, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 48.86
Std Dev 3.417
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch56, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.69
Std Dev 2.894
Entry
ampl
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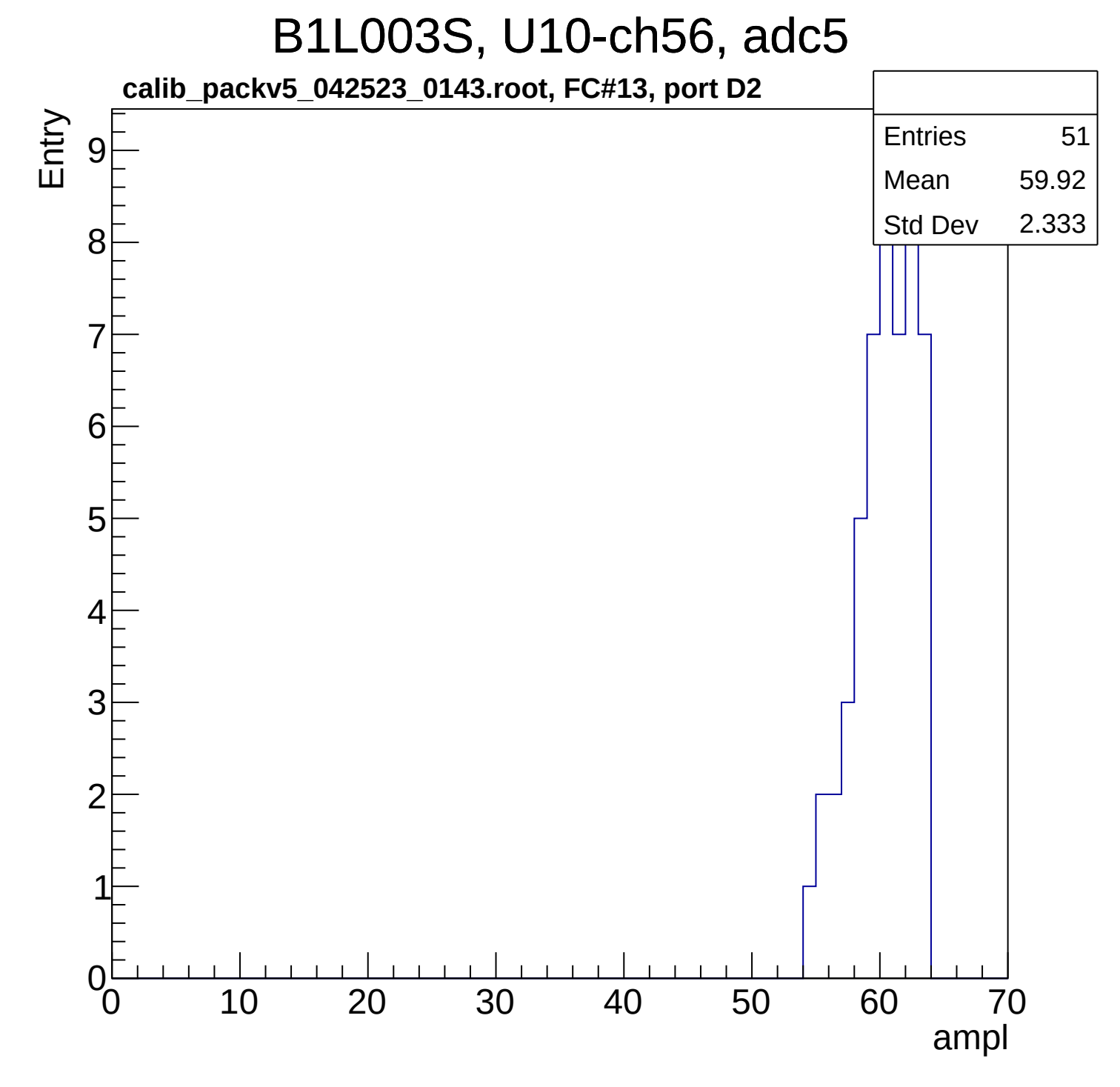
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch56, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.92
Std Dev 2.333
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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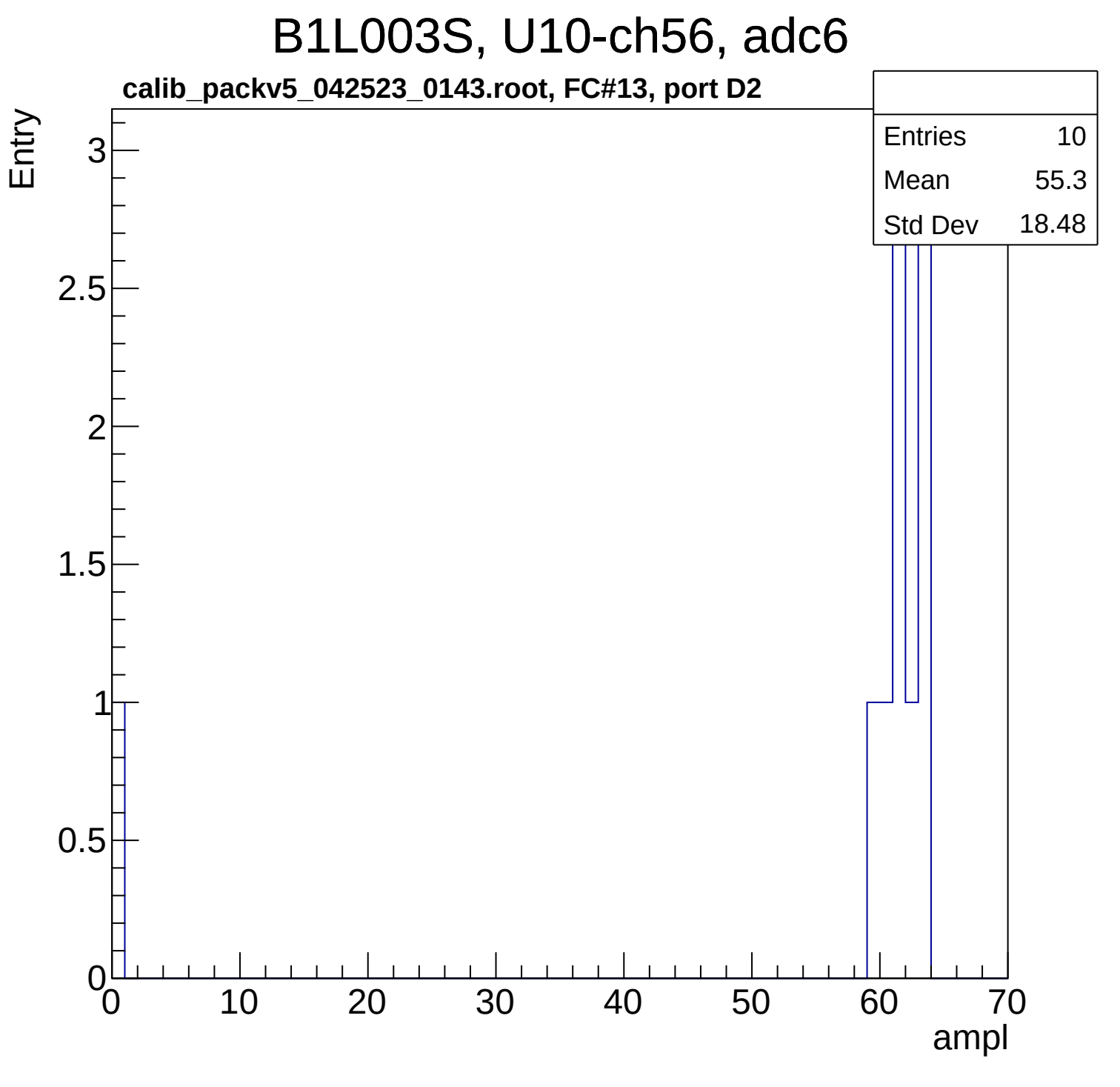
B1L003S, U10-ch56, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.3
Std Dev 18.48
Entry
ampl
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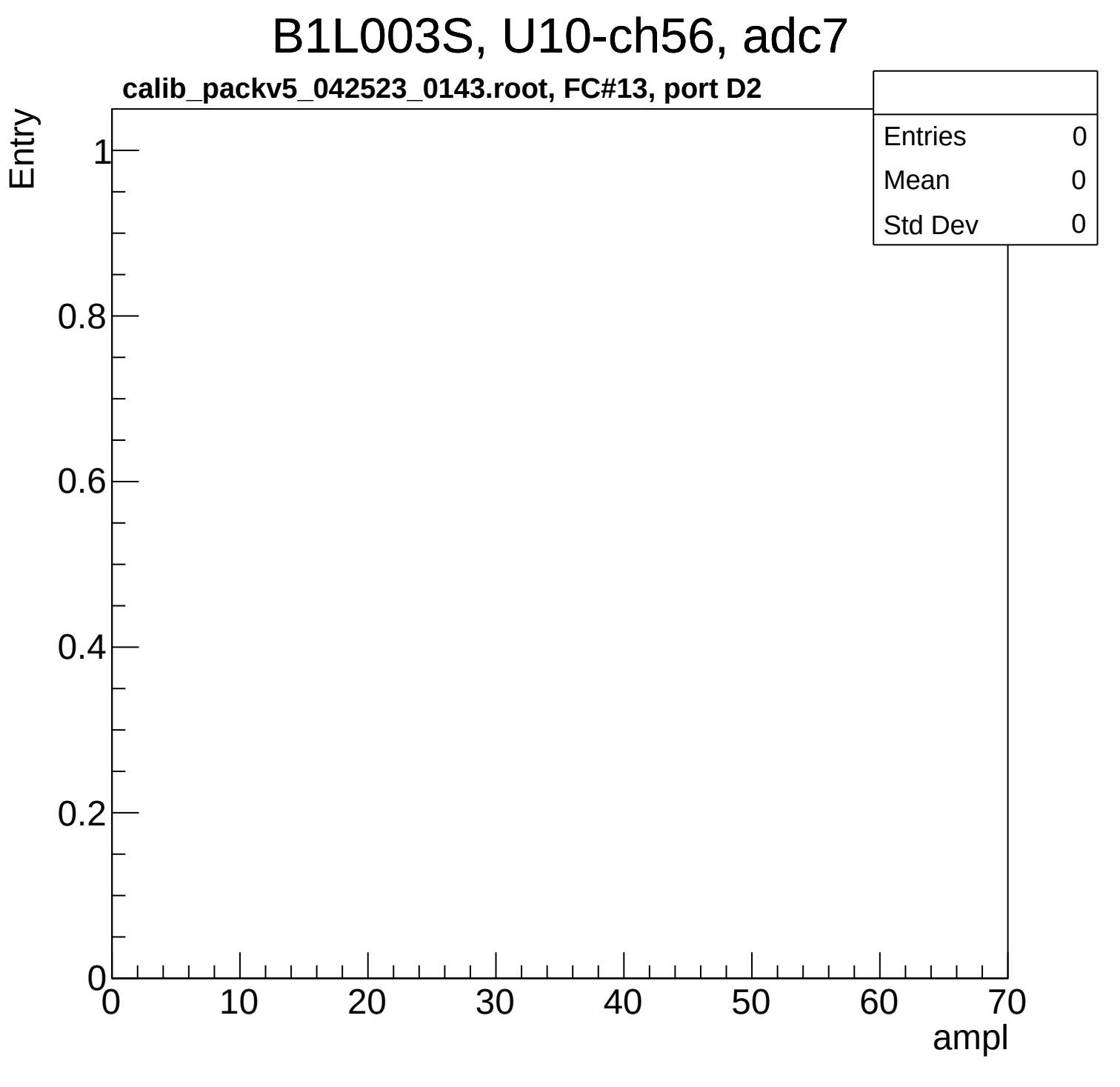
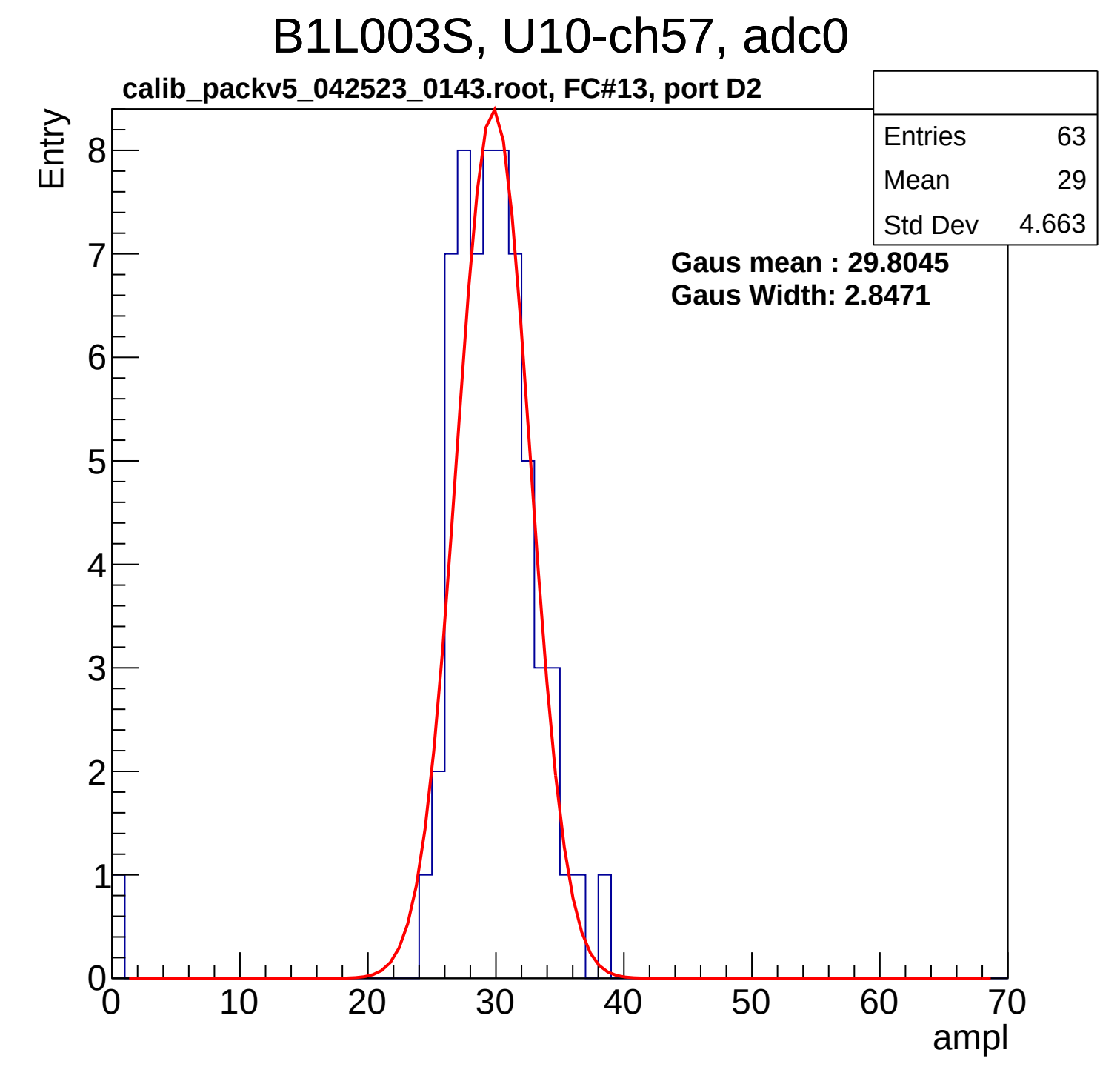
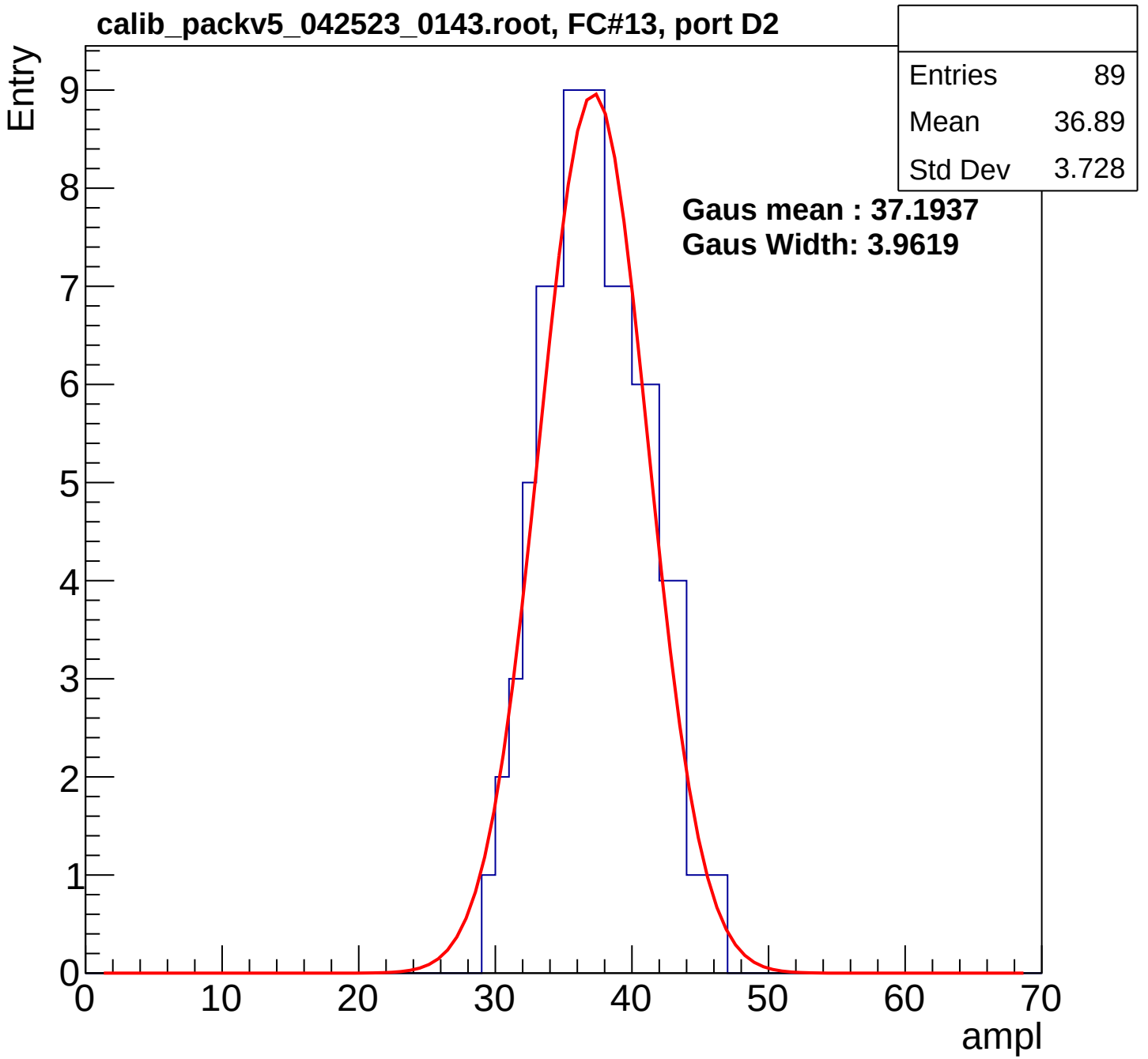
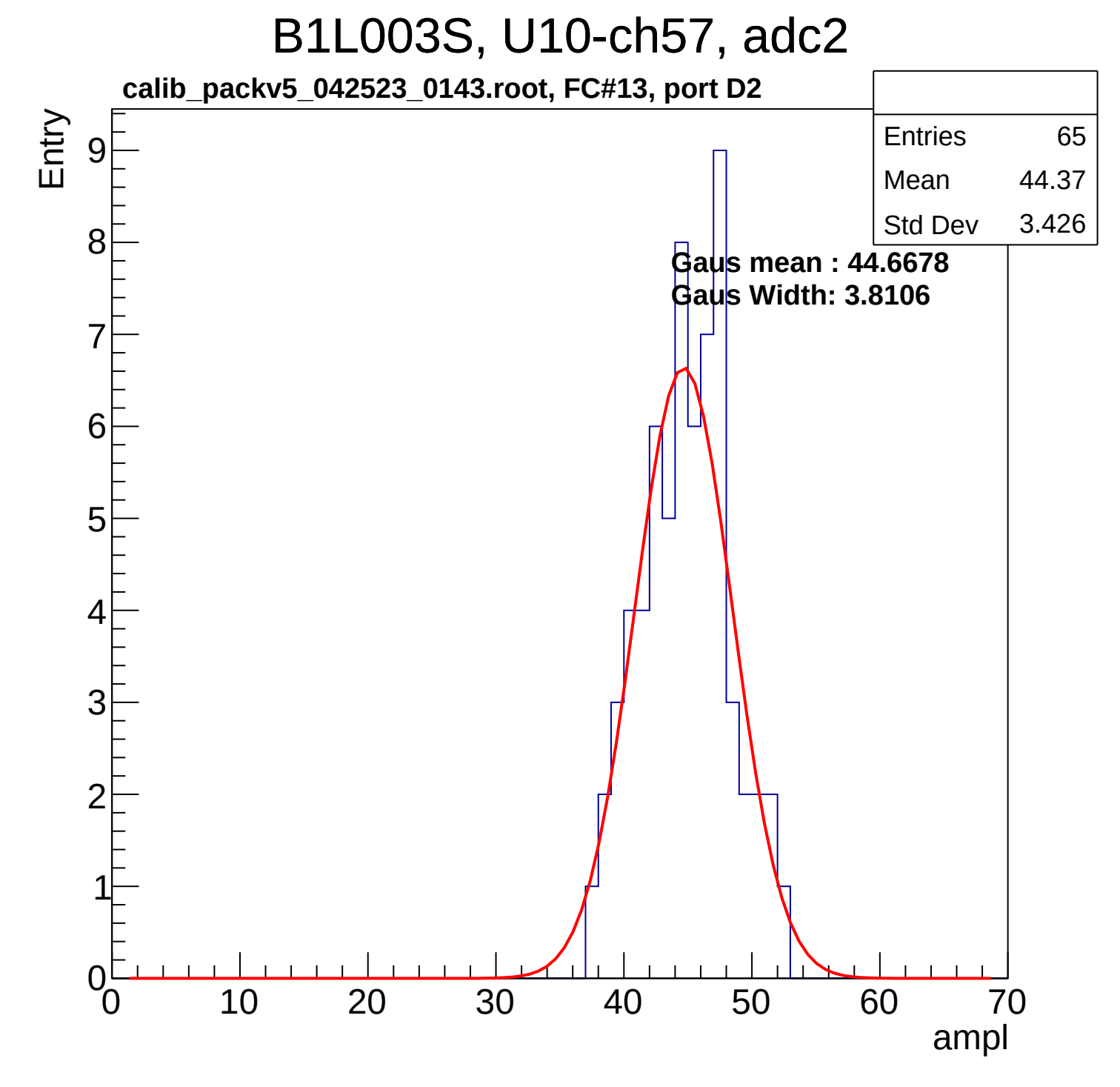
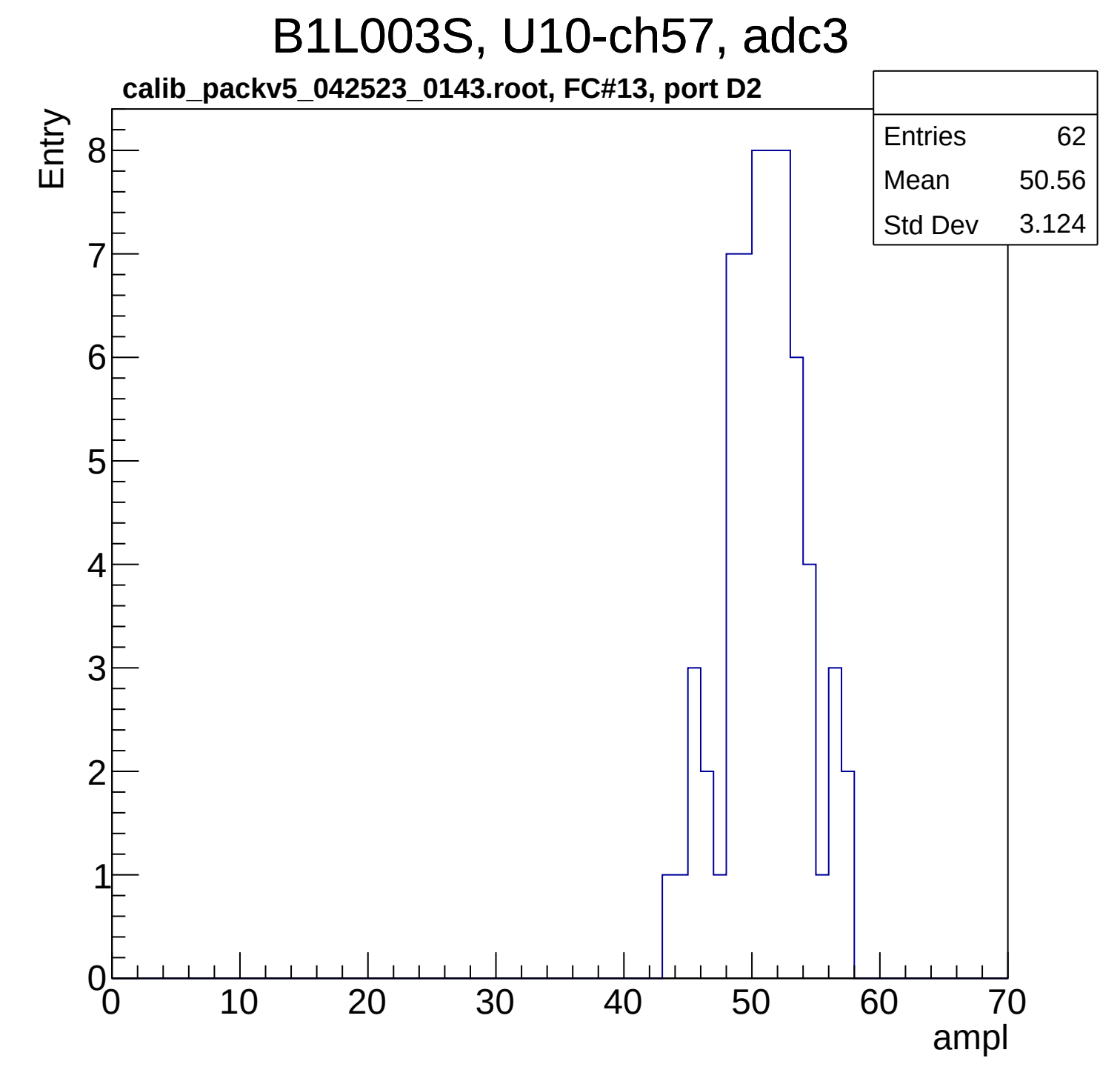
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch56, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch57, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29
Std Dev 4.663
Gaus mean : 29.8045
Gaus Width: 2.8471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch57, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 89
Mean 36.89
Std Dev 3.728
Gaus mean : 37.1937
Gaus Width: 3.9619
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch57, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 44.37
Std Dev 3.426
Gaus mean : 44.6678
Gaus Width: 3.8106
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch57, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 50.56
Std Dev 3.124
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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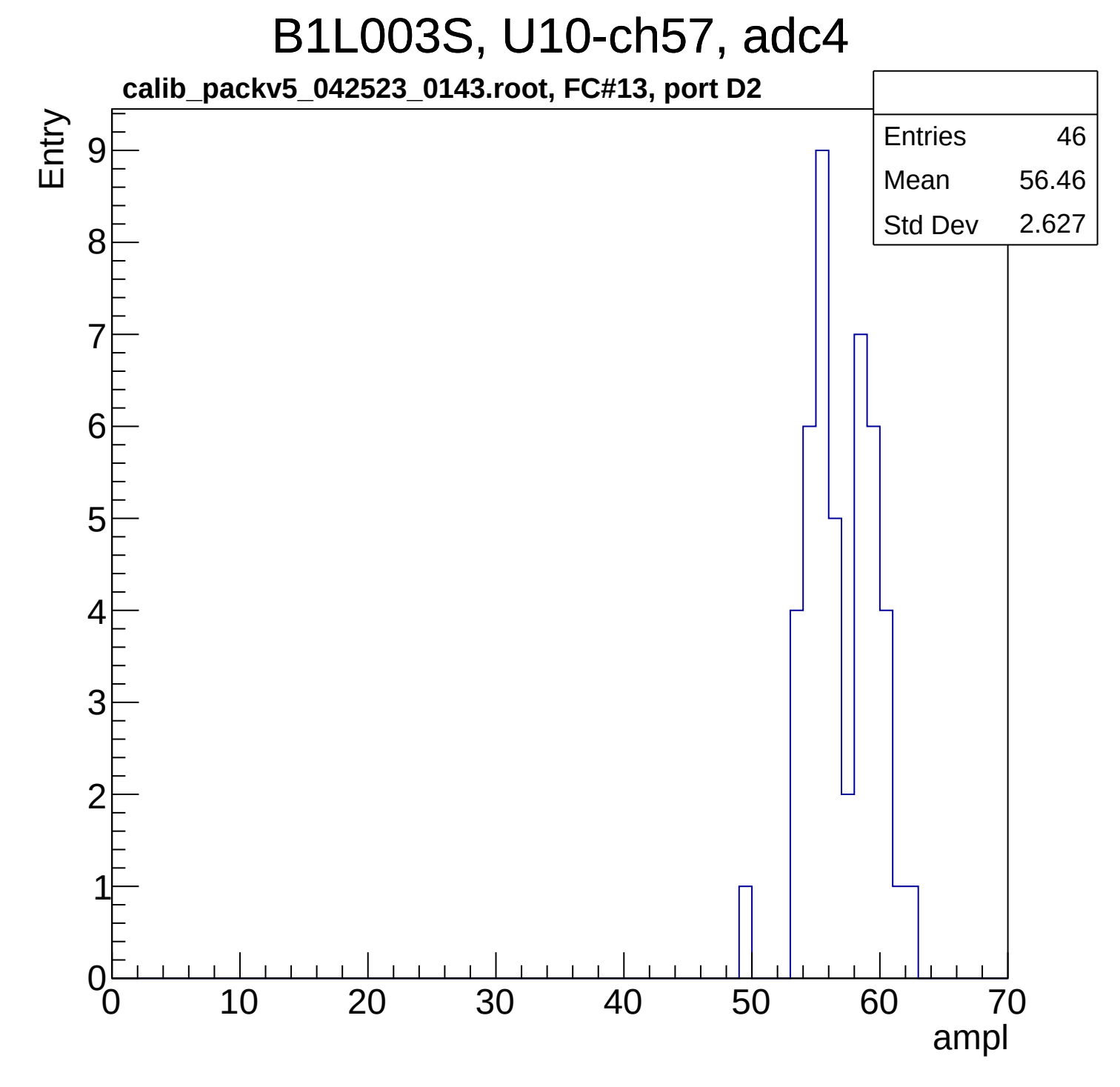
B1L003S, U10-ch57, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 56.46
Std Dev 2.627
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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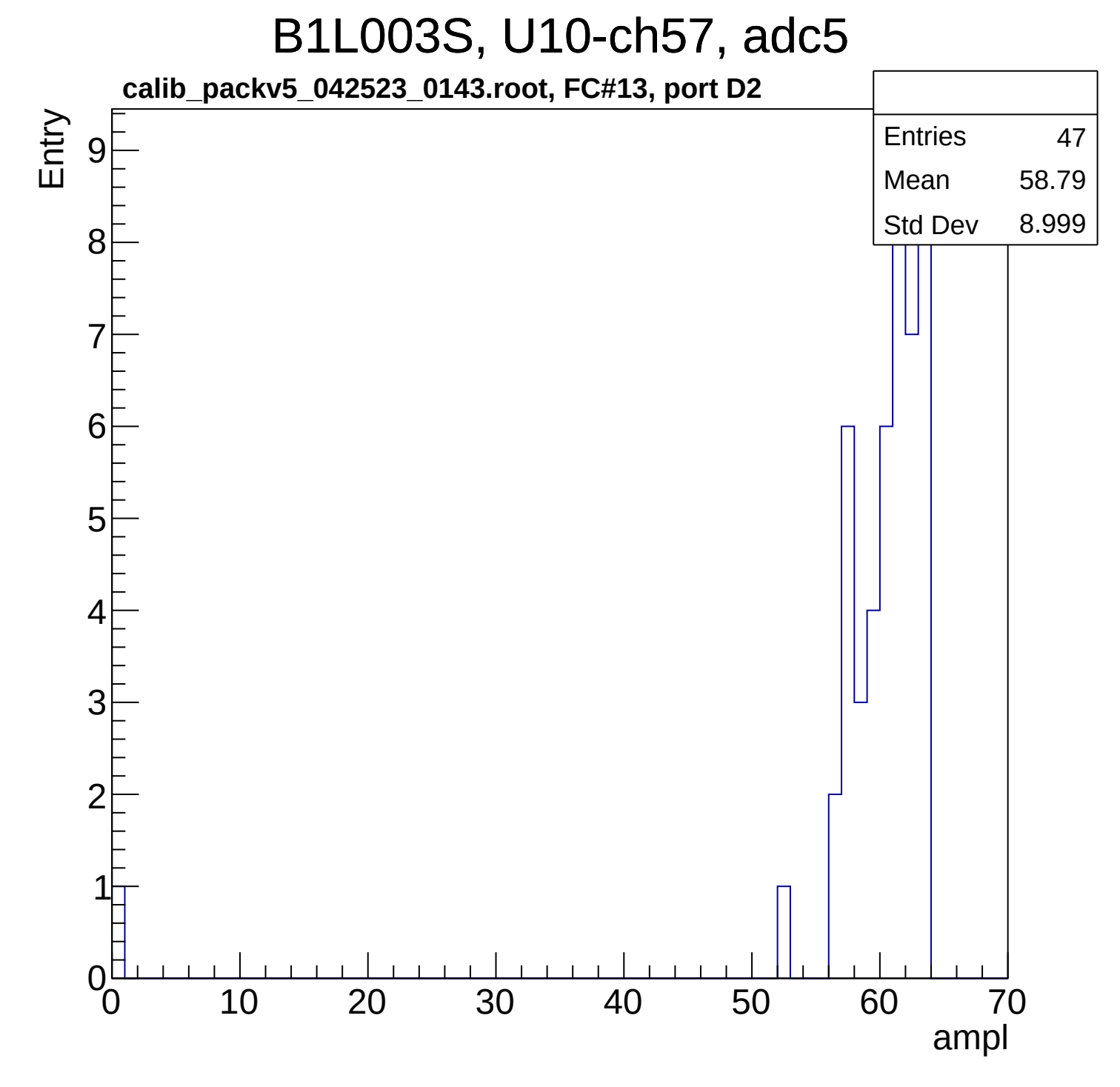
B1L003S, U10-ch57, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.79
Std Dev 8.999
Entry
ampl
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3
4
5
6
7
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0
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60
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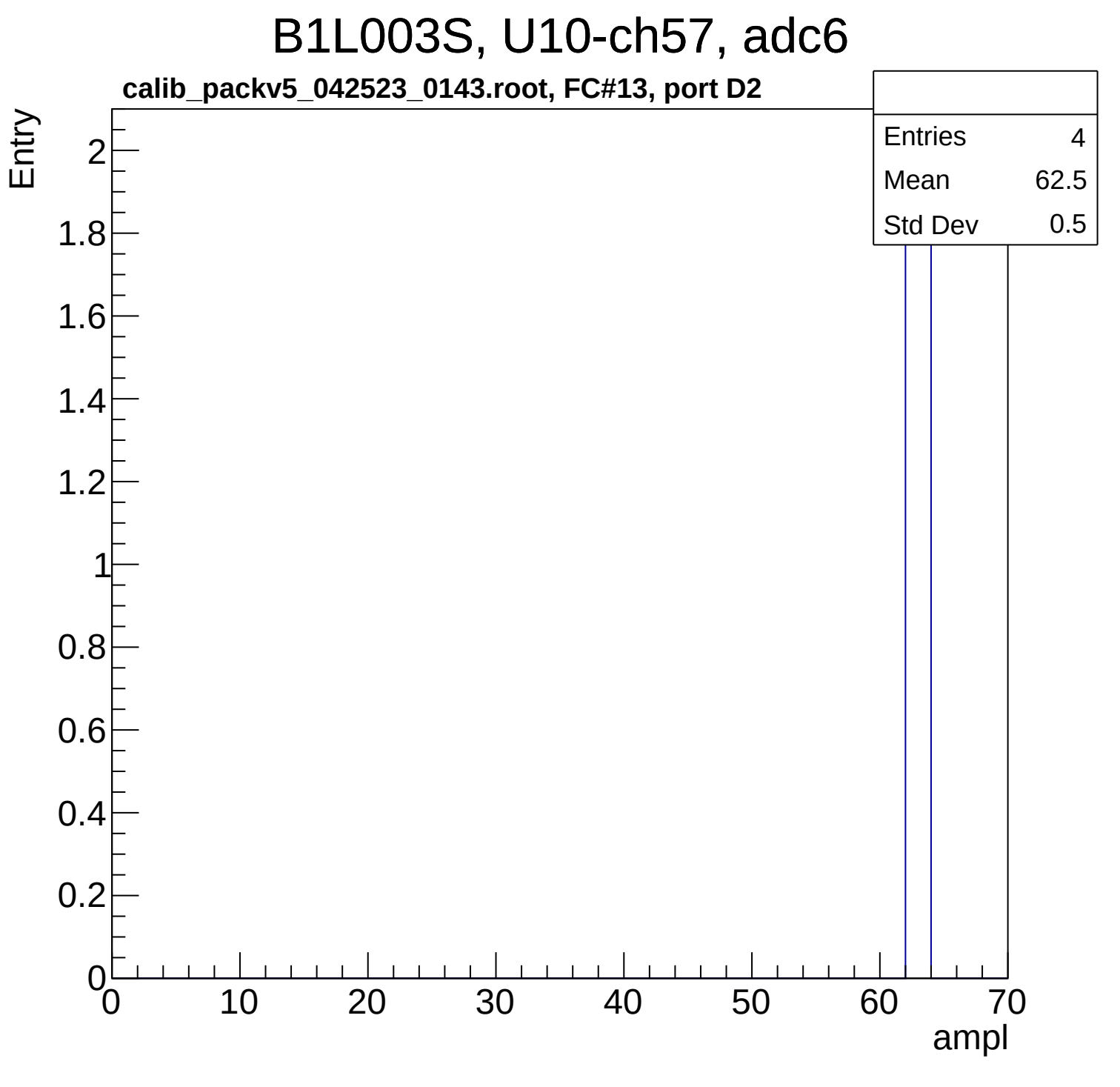
B1L003S, U10-ch57, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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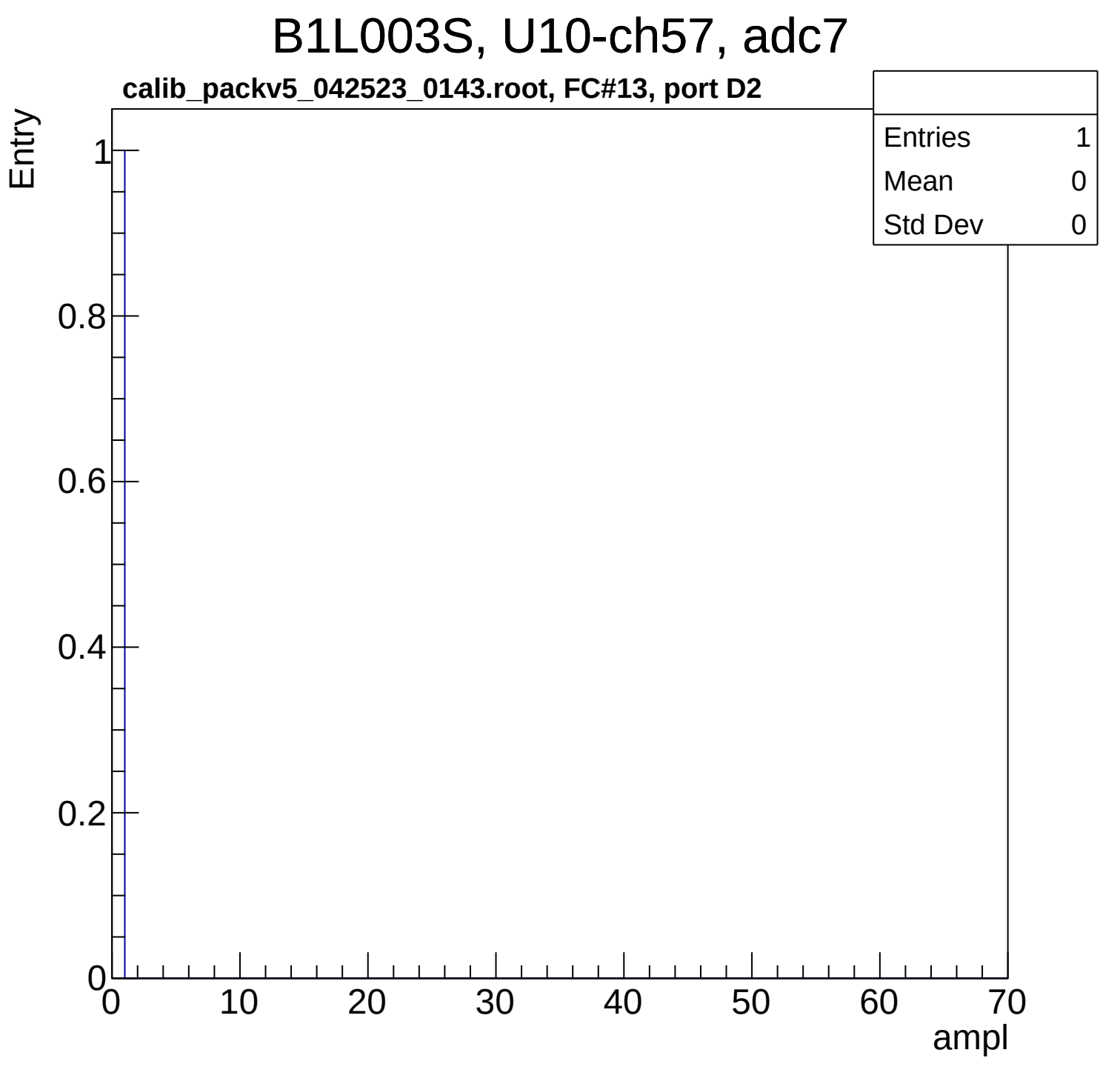
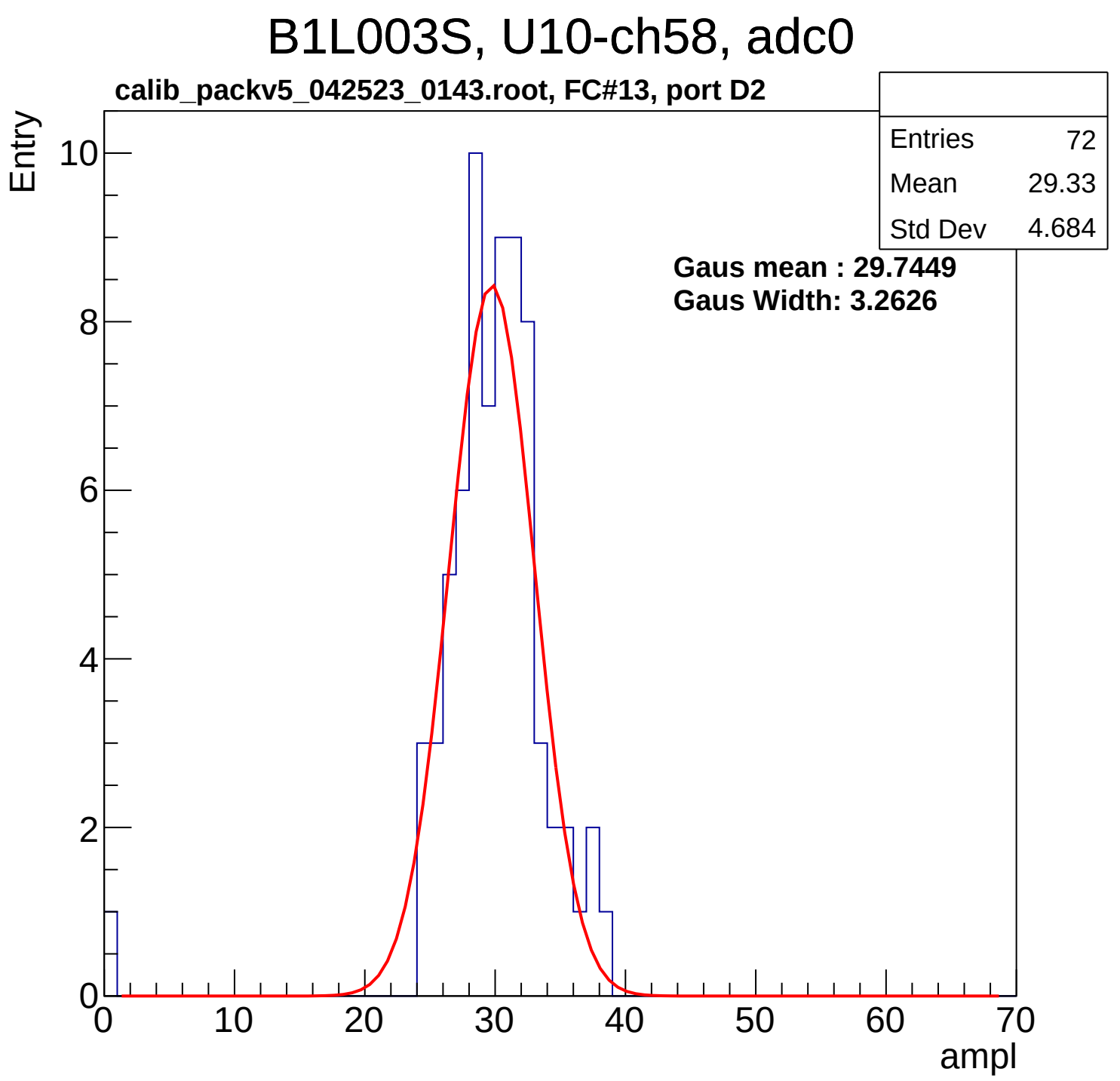
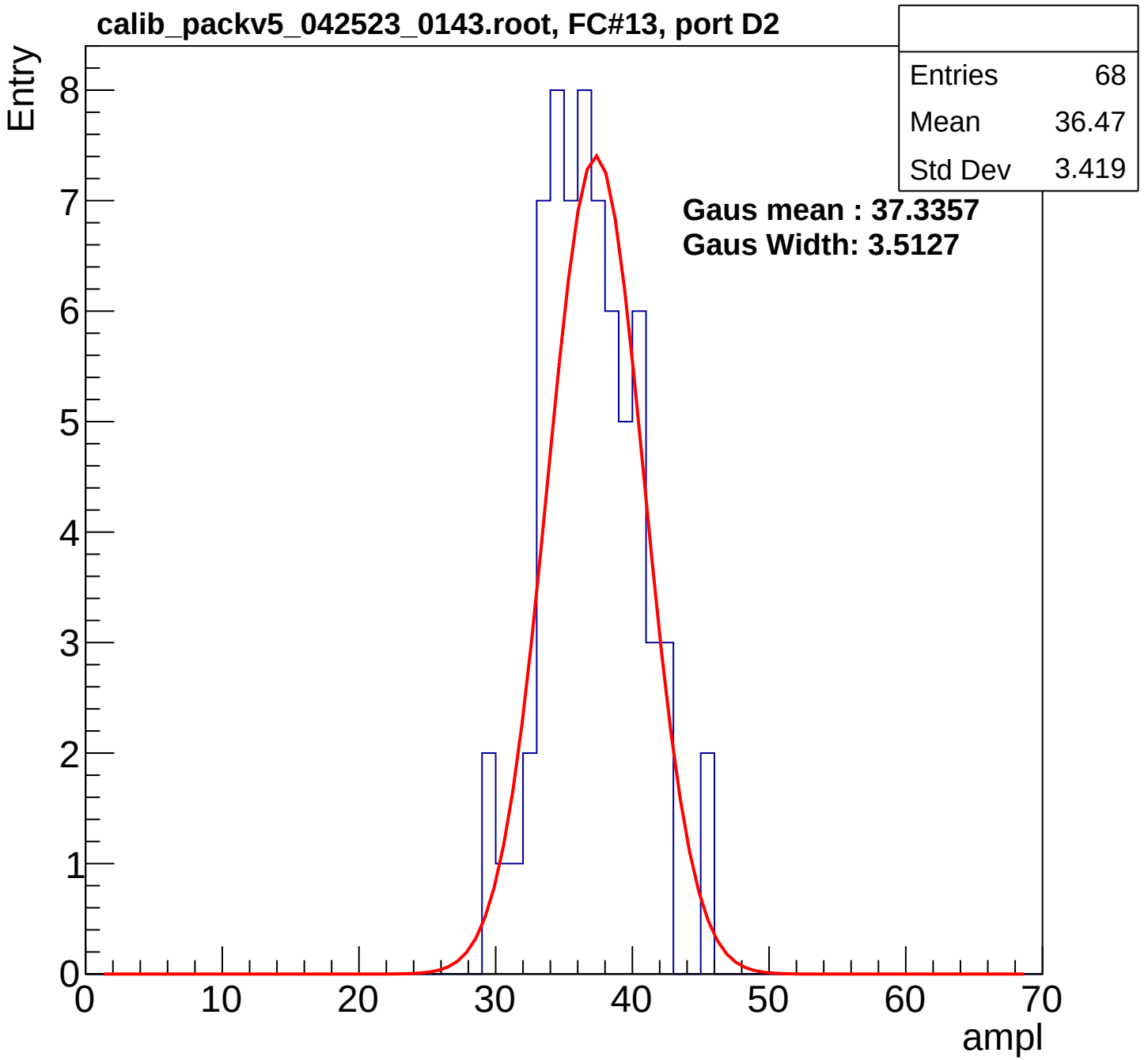
40
50
60
70


B1L003S, U10-ch57, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch58, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 29.33
Std Dev 4.684
Gaus mean : 29.7449
Gaus Width: 3.2626
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch58, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.47
Std Dev 3.419
Gaus mean : 37.3357
Gaus Width: 3.5127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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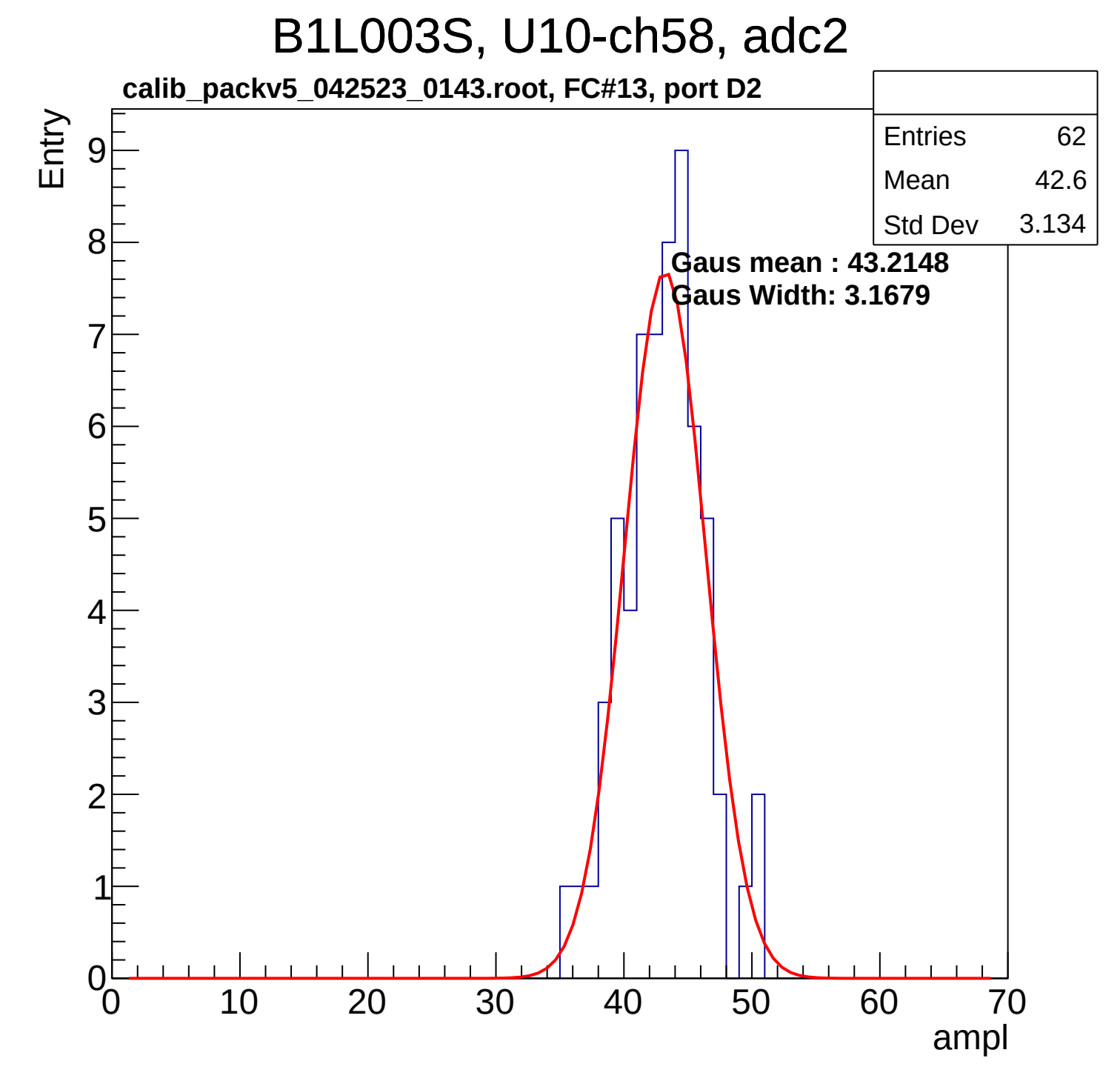
B1L003S, U10-ch58, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.6
Std Dev 3.134
Gaus mean : 43.2148
Gaus Width: 3.1679
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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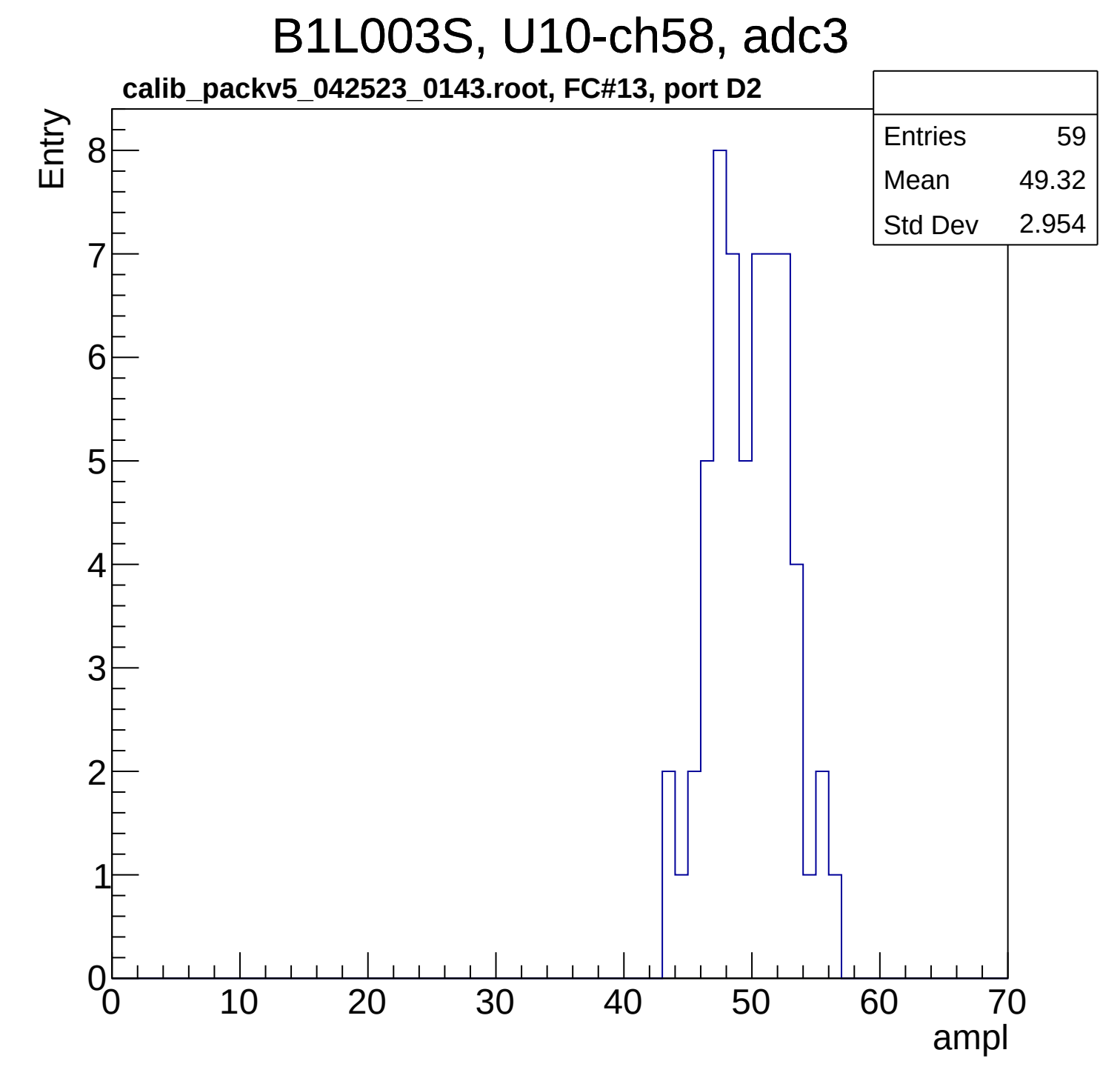
B1L003S, U10-ch58, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.32
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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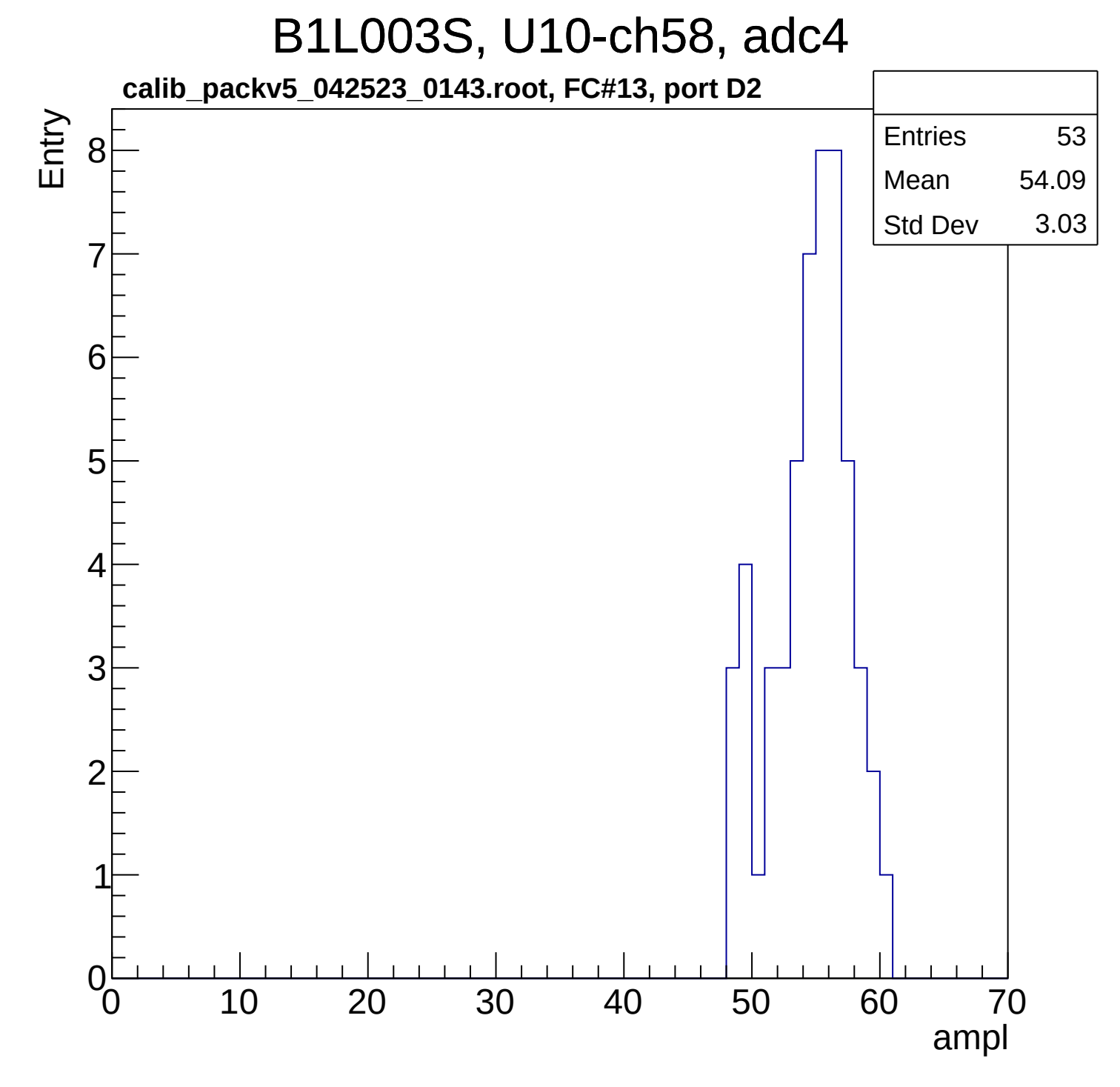
B1L003S, U10-ch58, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.09
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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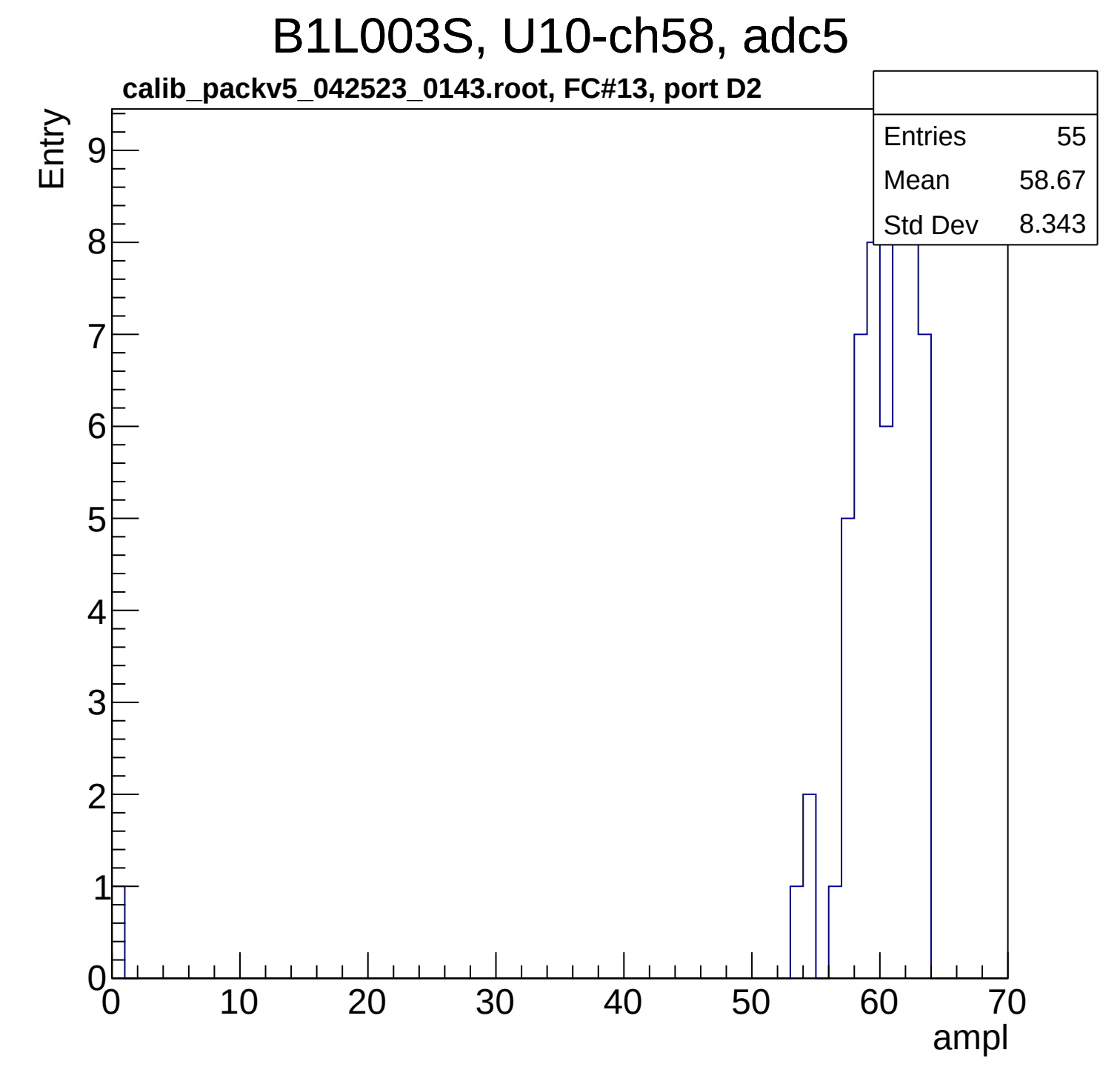
B1L003S, U10-ch58, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.67
Std Dev 8.343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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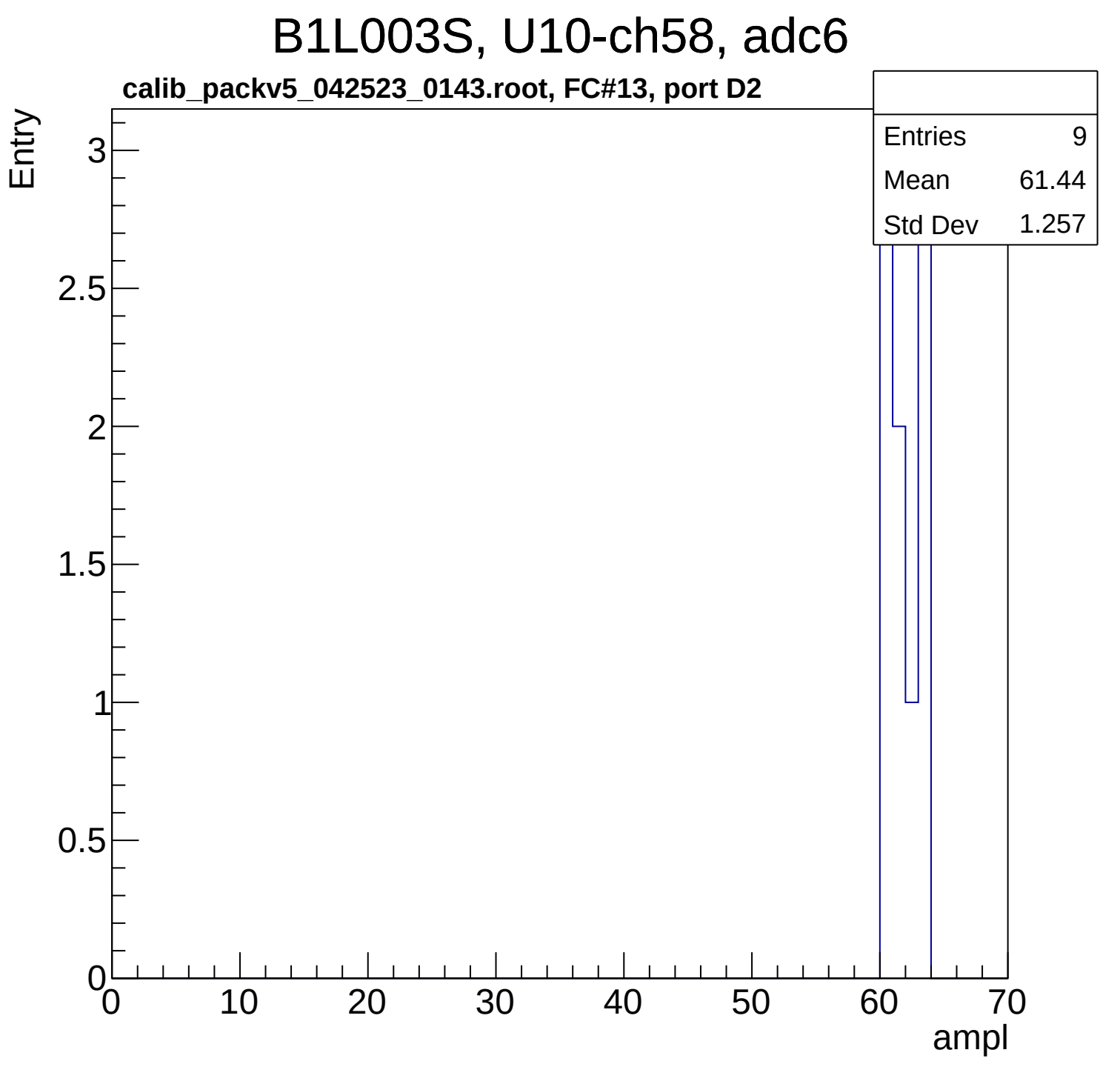
B1L003S, U10-ch58, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.44
Std Dev 1.257
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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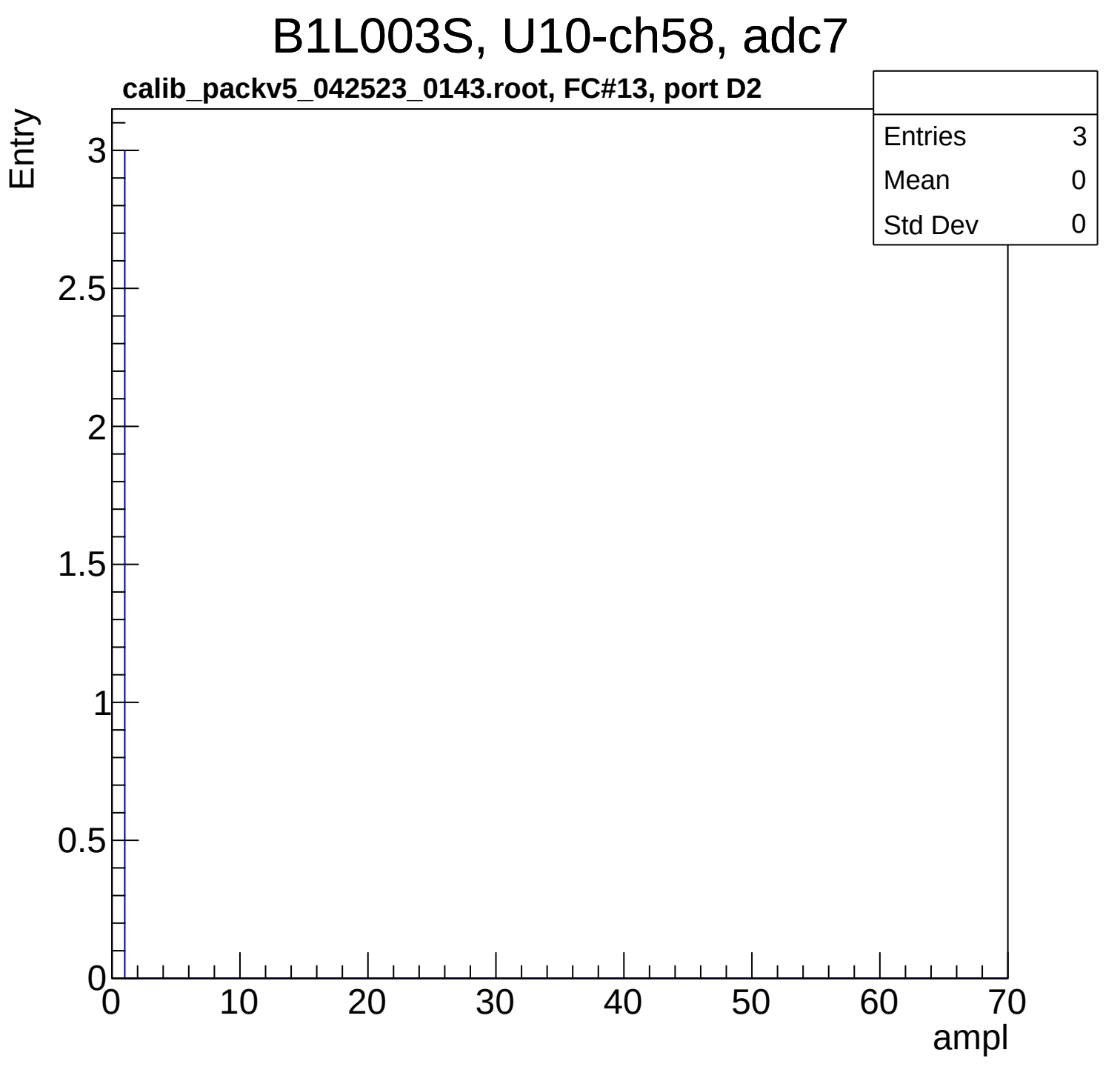
40
50
60
70


B1L003S, U10-ch58, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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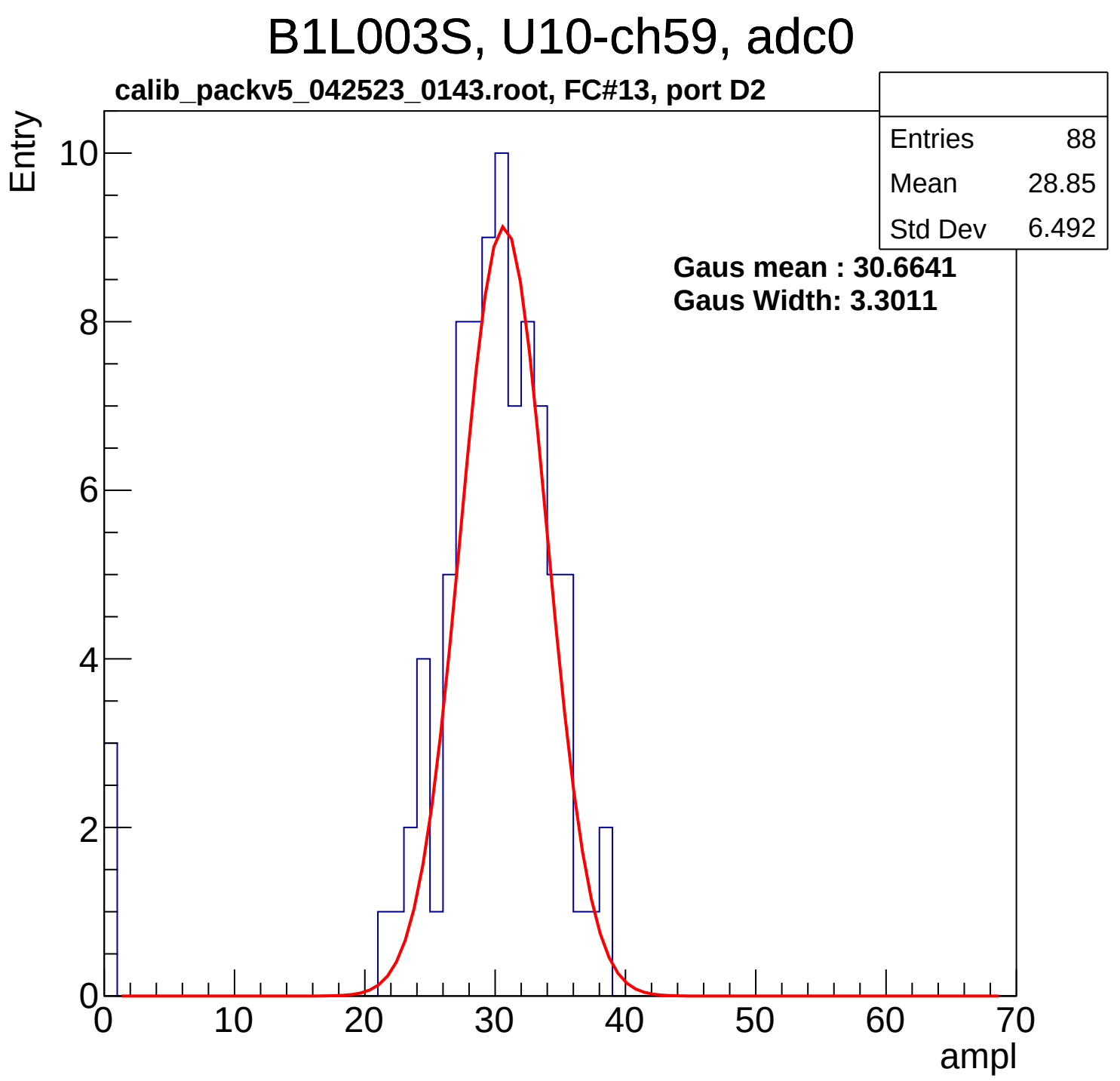
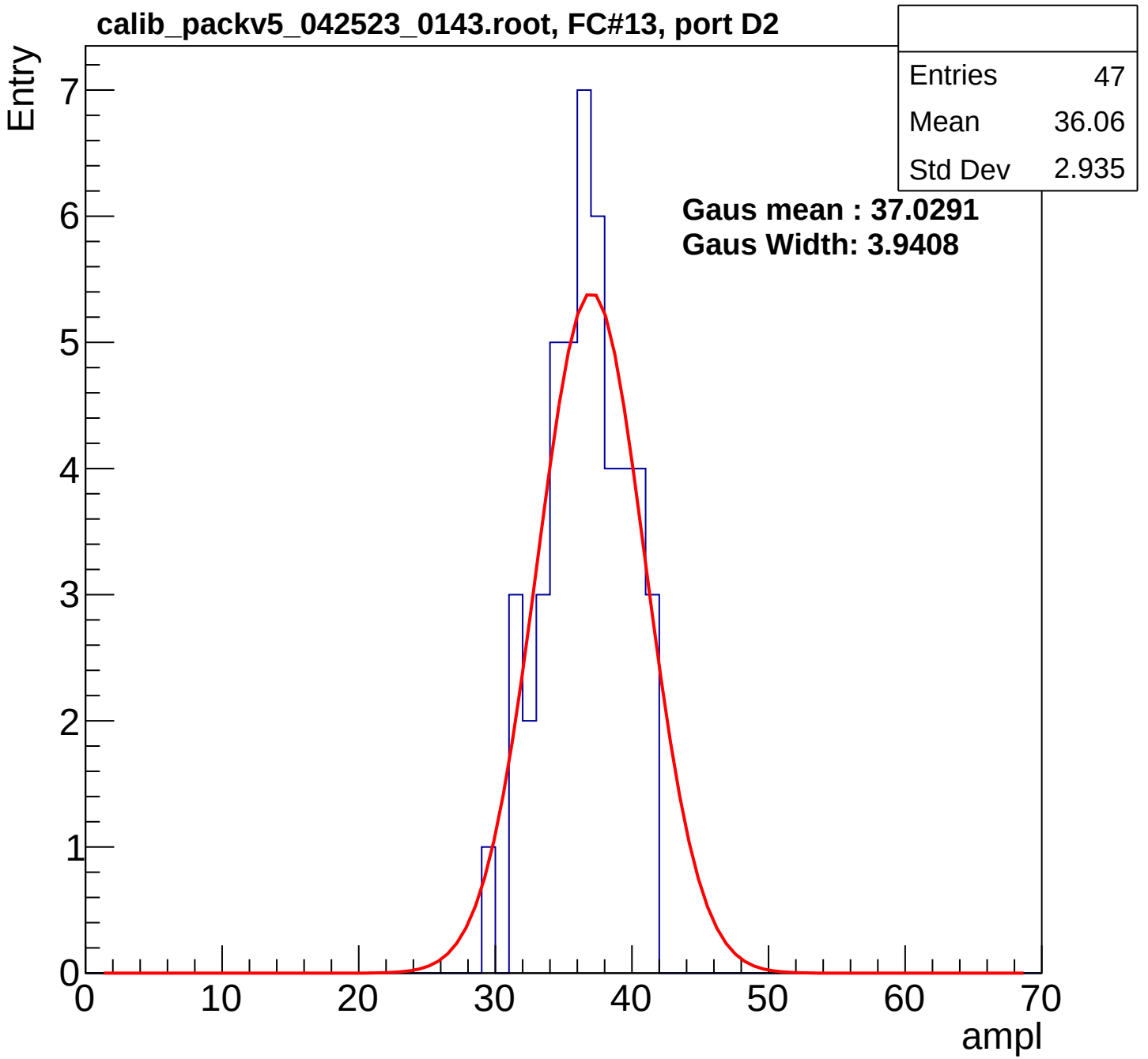
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch59, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 88
Mean 28.85
Std Dev 6.492
Gaus mean : 30.6641
Gaus Width: 3.3011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch59, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 36.06
Std Dev 2.935
Gaus mean : 37.0291
Gaus Width: 3.9408
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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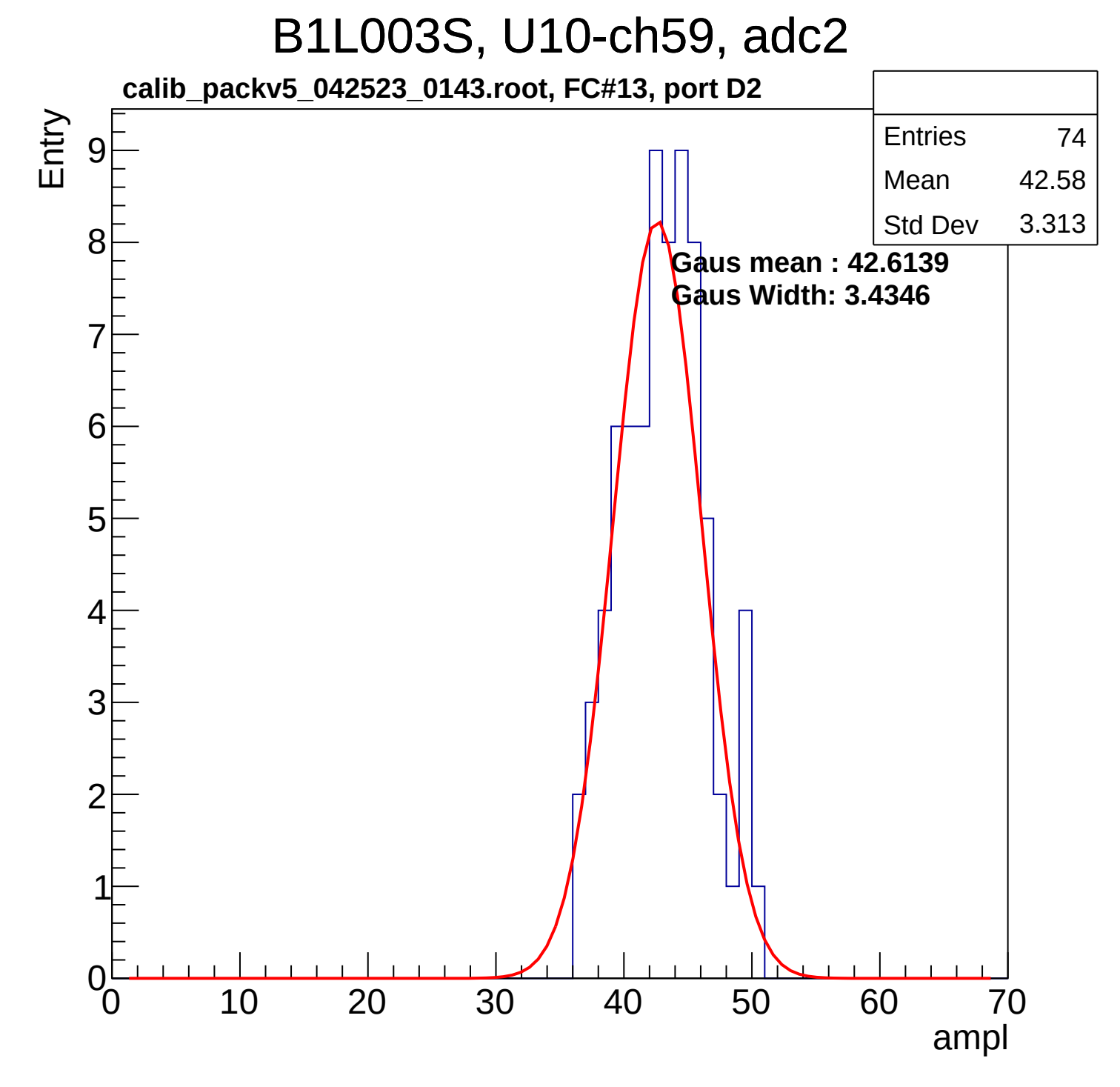
B1L003S, U10-ch59, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 42.58
Std Dev 3.313
Gaus mean : 42.6139
Gaus Width: 3.4346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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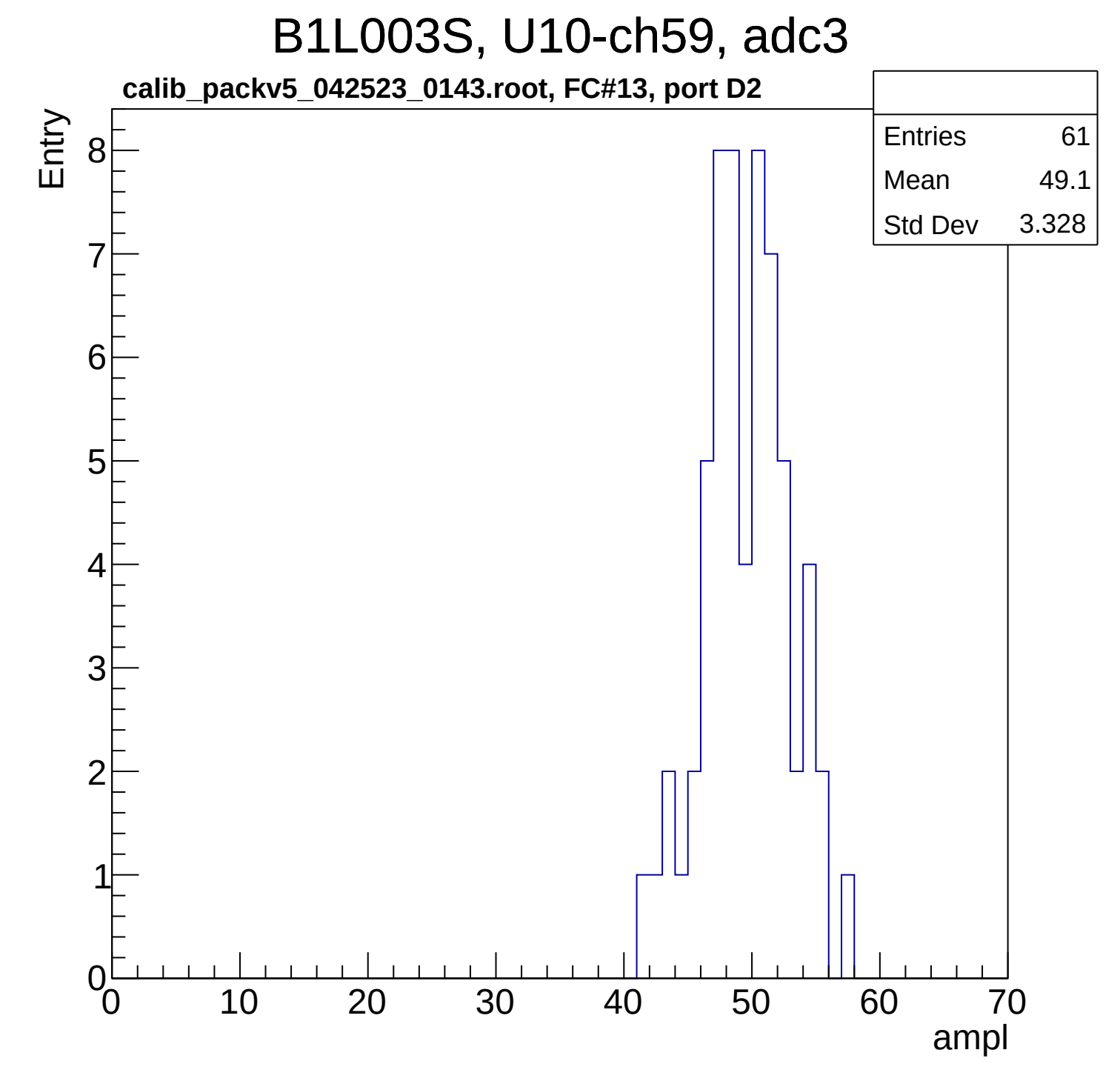
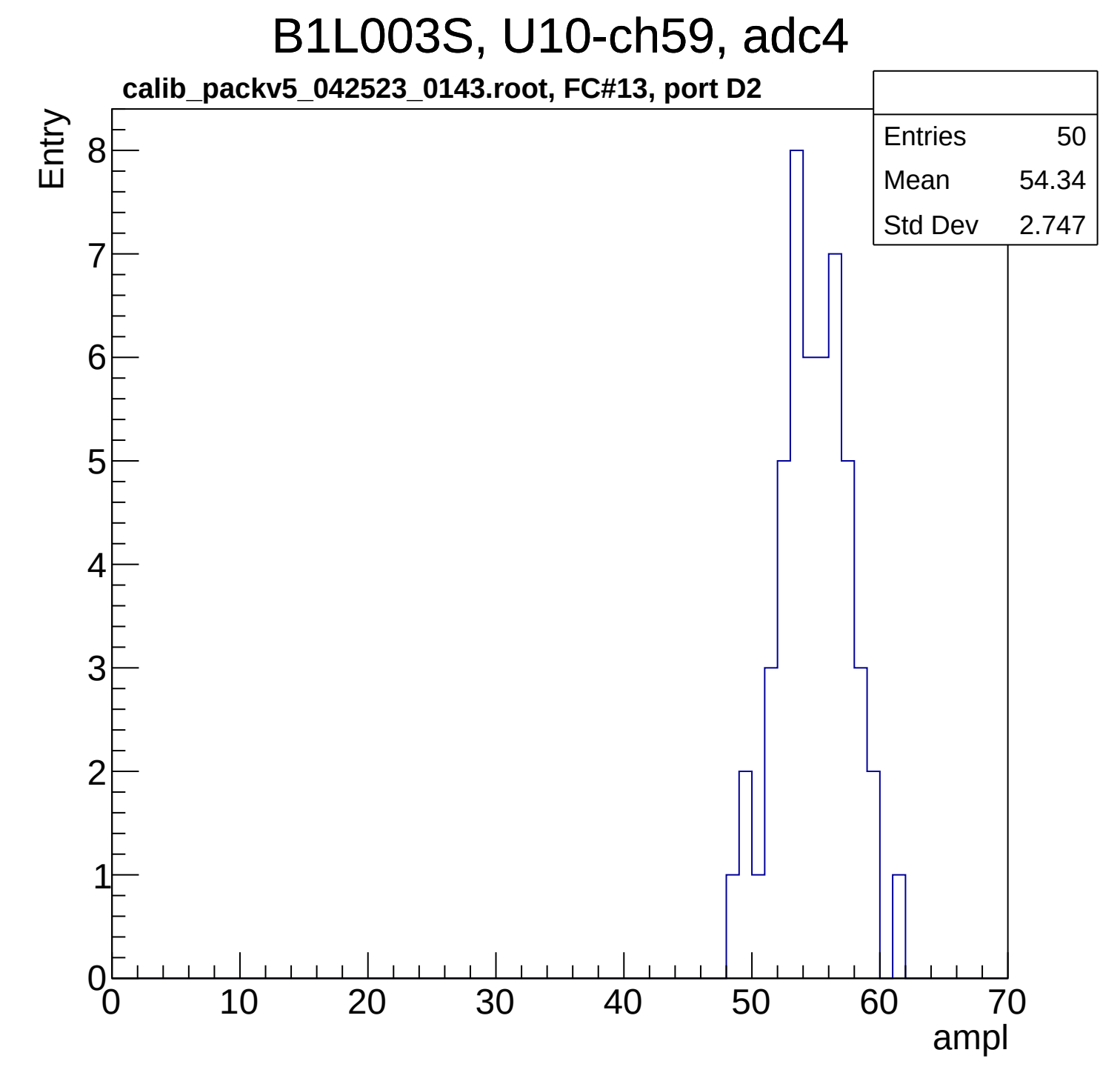

B1L003S, U10-ch59, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.1
Std Dev 3.328
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch59, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.34
Std Dev 2.747
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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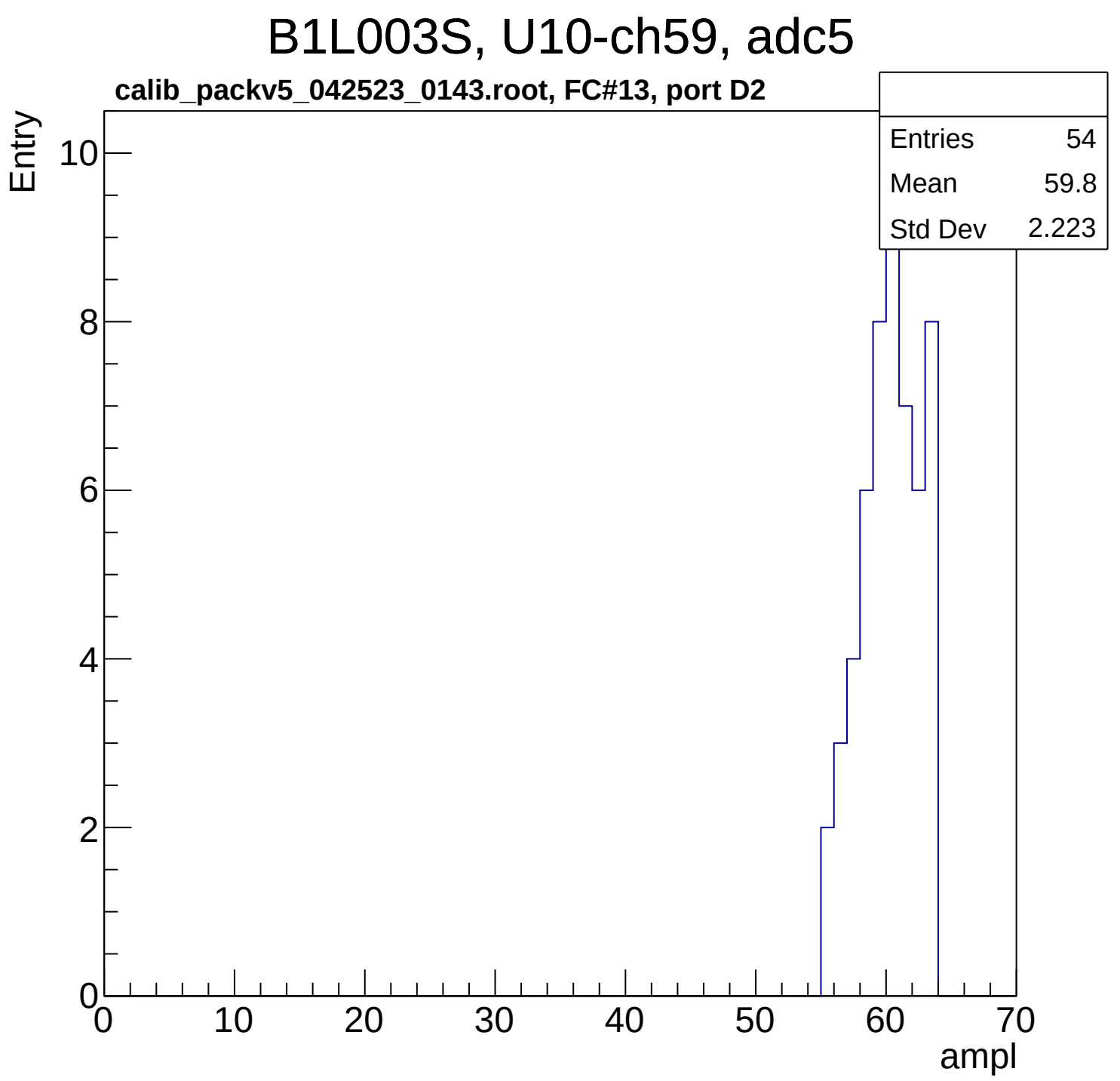
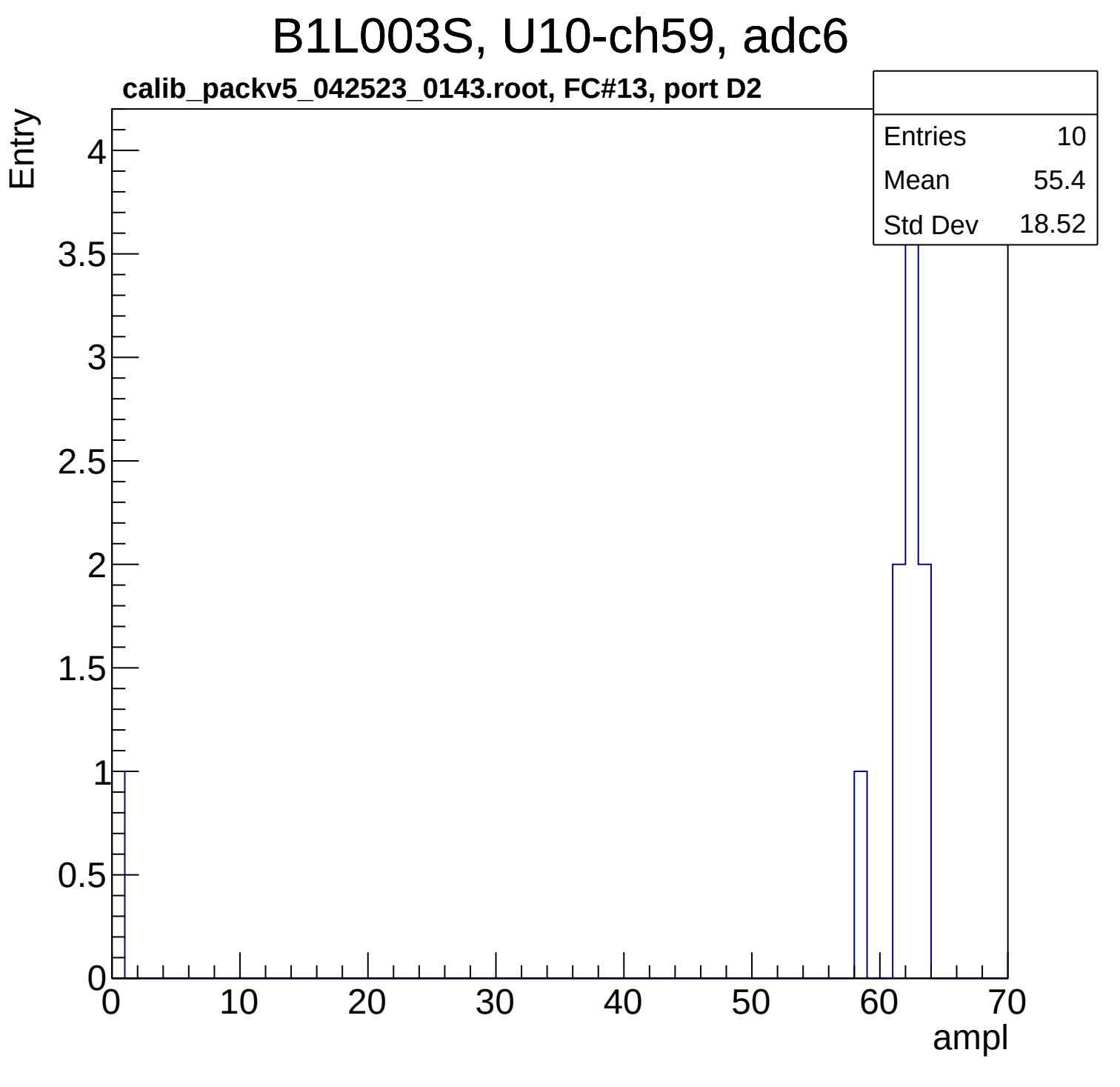

B1L003S, U10-ch59, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.8
Std Dev 2.223
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch59, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.4
Std Dev 18.52
Entry
ampl
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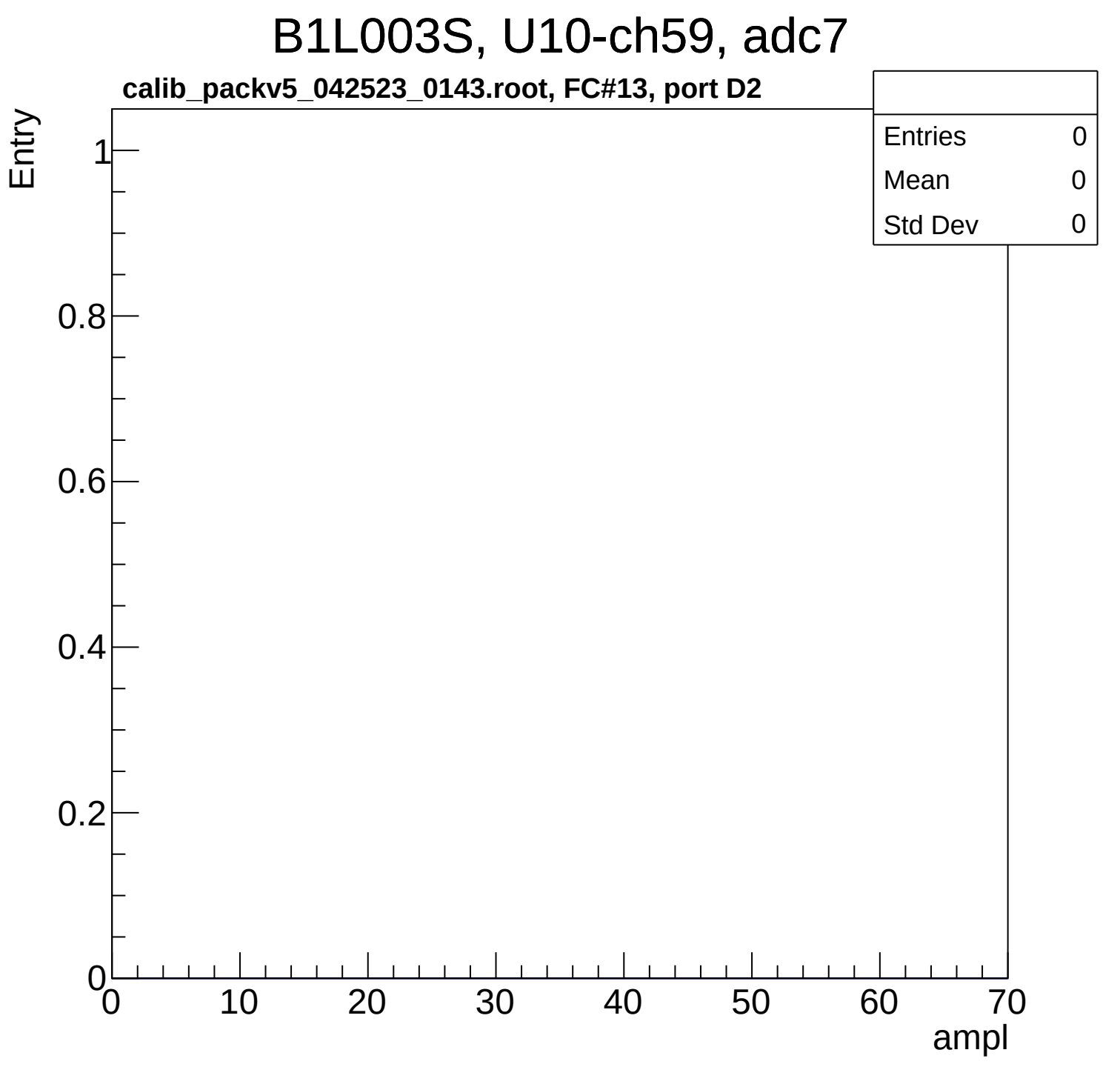
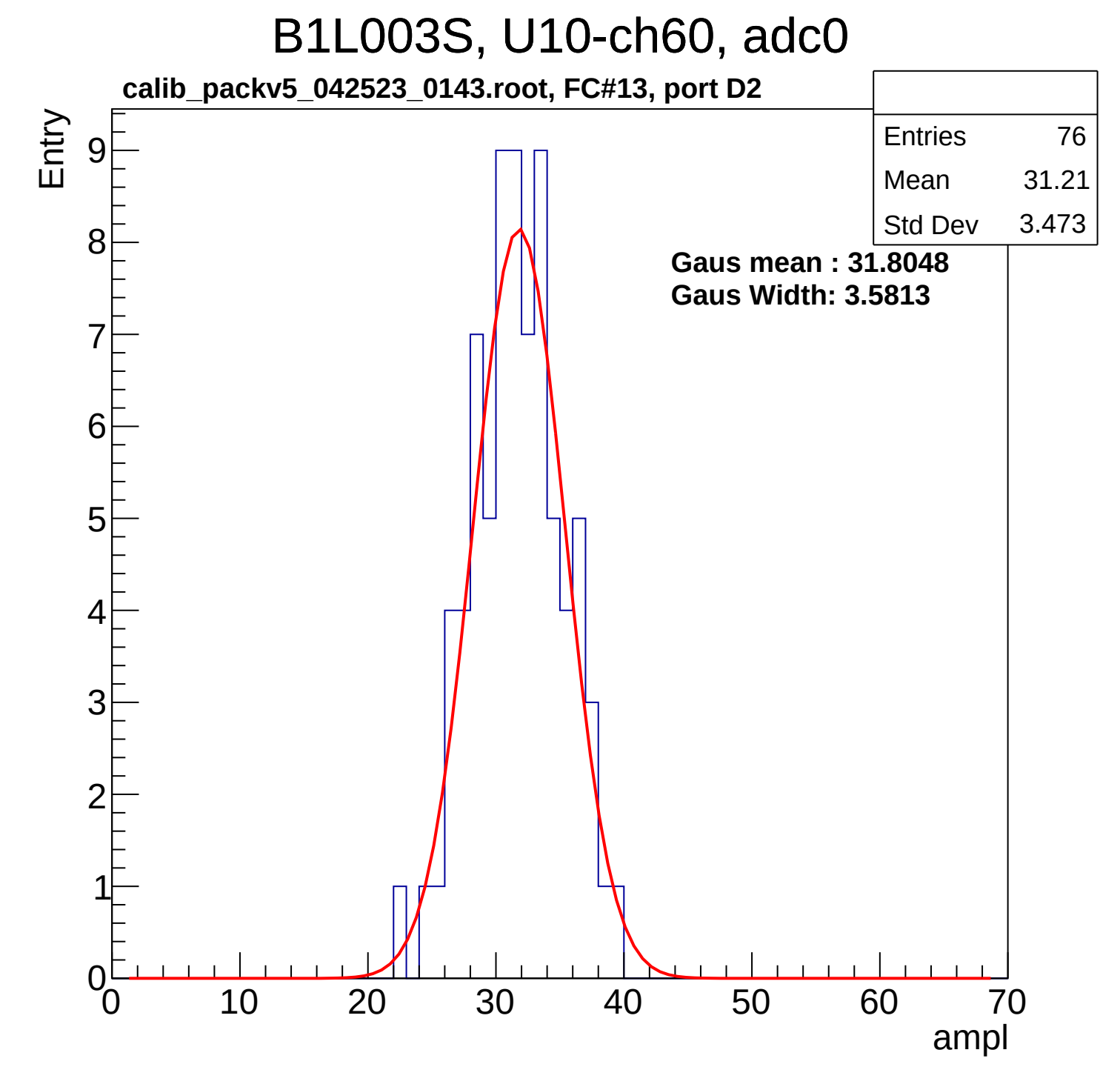
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch59, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch60, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 31.21
Std Dev 3.473
Gaus mean : 31.8048
Gaus Width: 3.5813
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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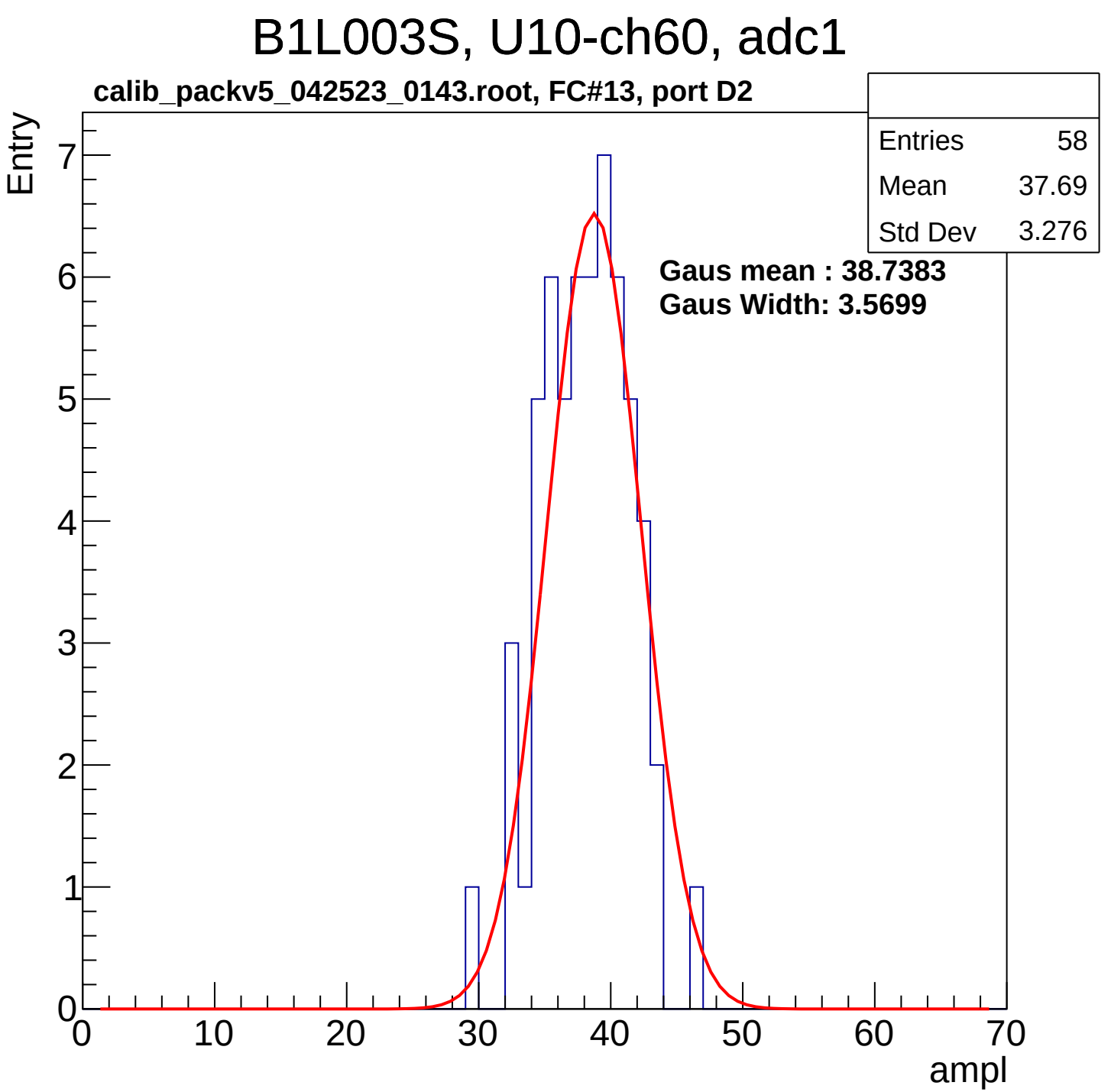
B1L003S, U10-ch60, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 37.69
Std Dev 3.276
Gaus mean : 38.7383
Gaus Width: 3.5699
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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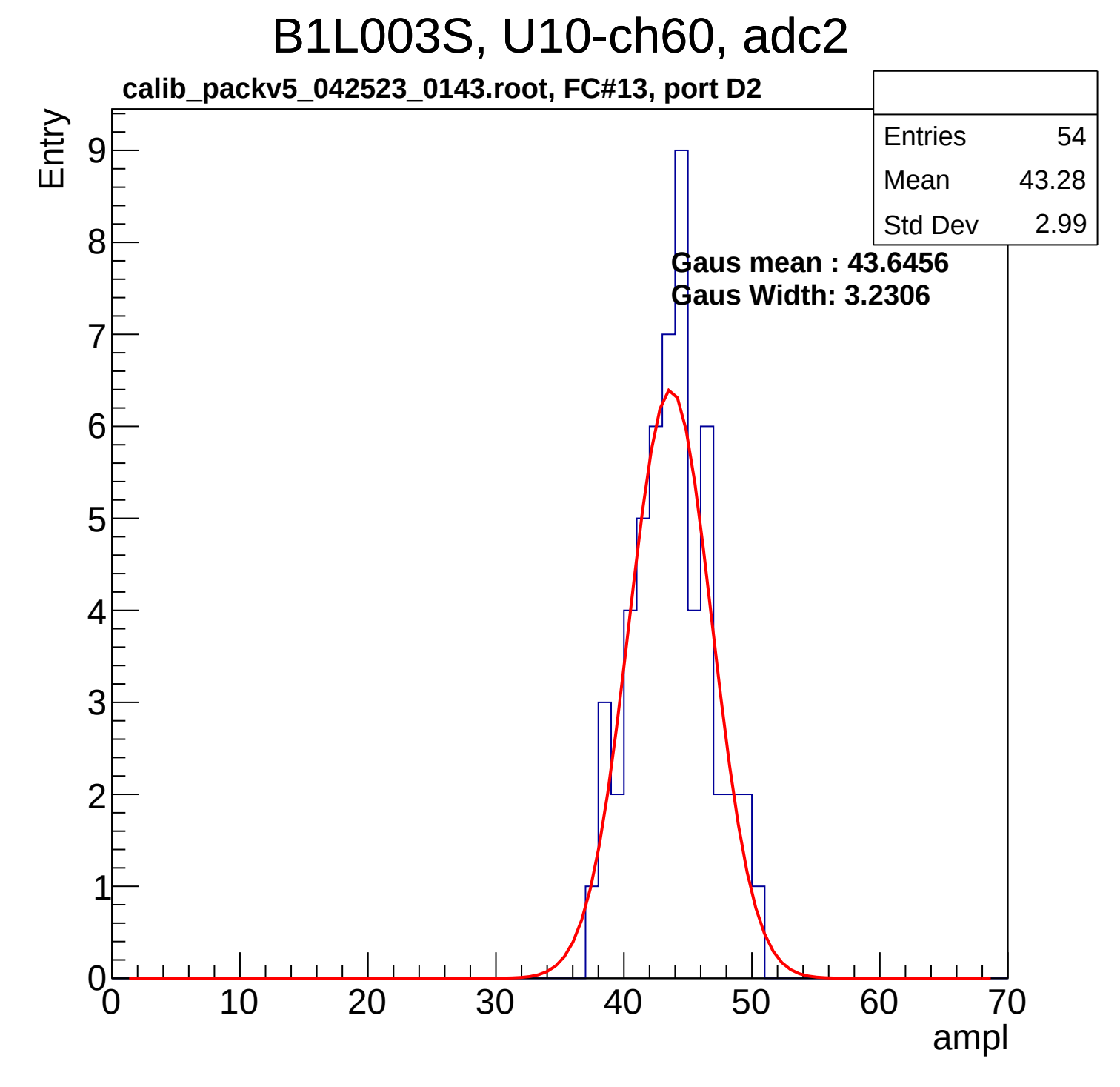
B1L003S, U10-ch60, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 43.28
Std Dev 2.99
Gaus mean : 43.6456
Gaus Width: 3.2306
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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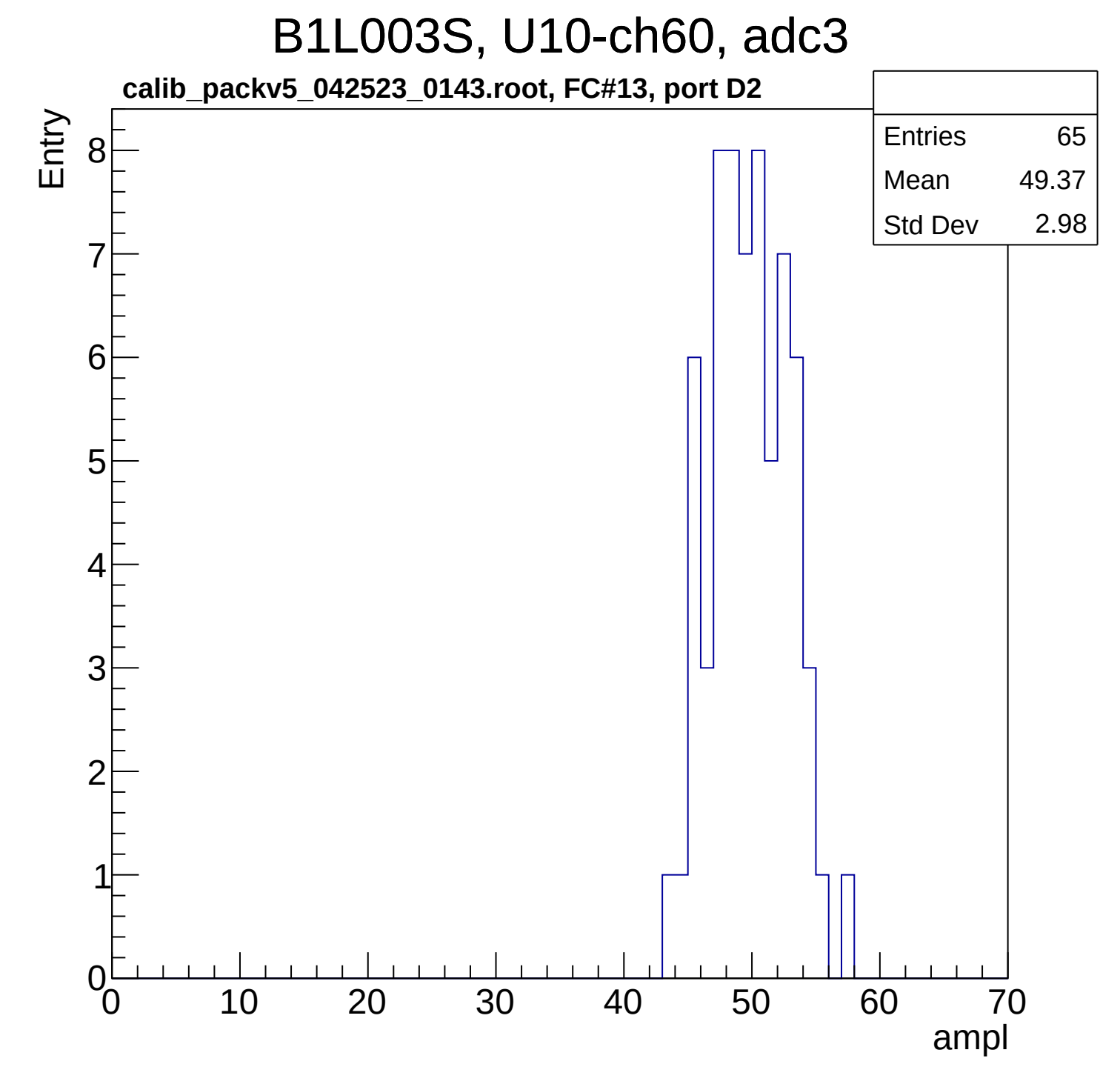
B1L003S, U10-ch60, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 49.37
Std Dev 2.98
Entry
ampl
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5
6
7
8
0
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60
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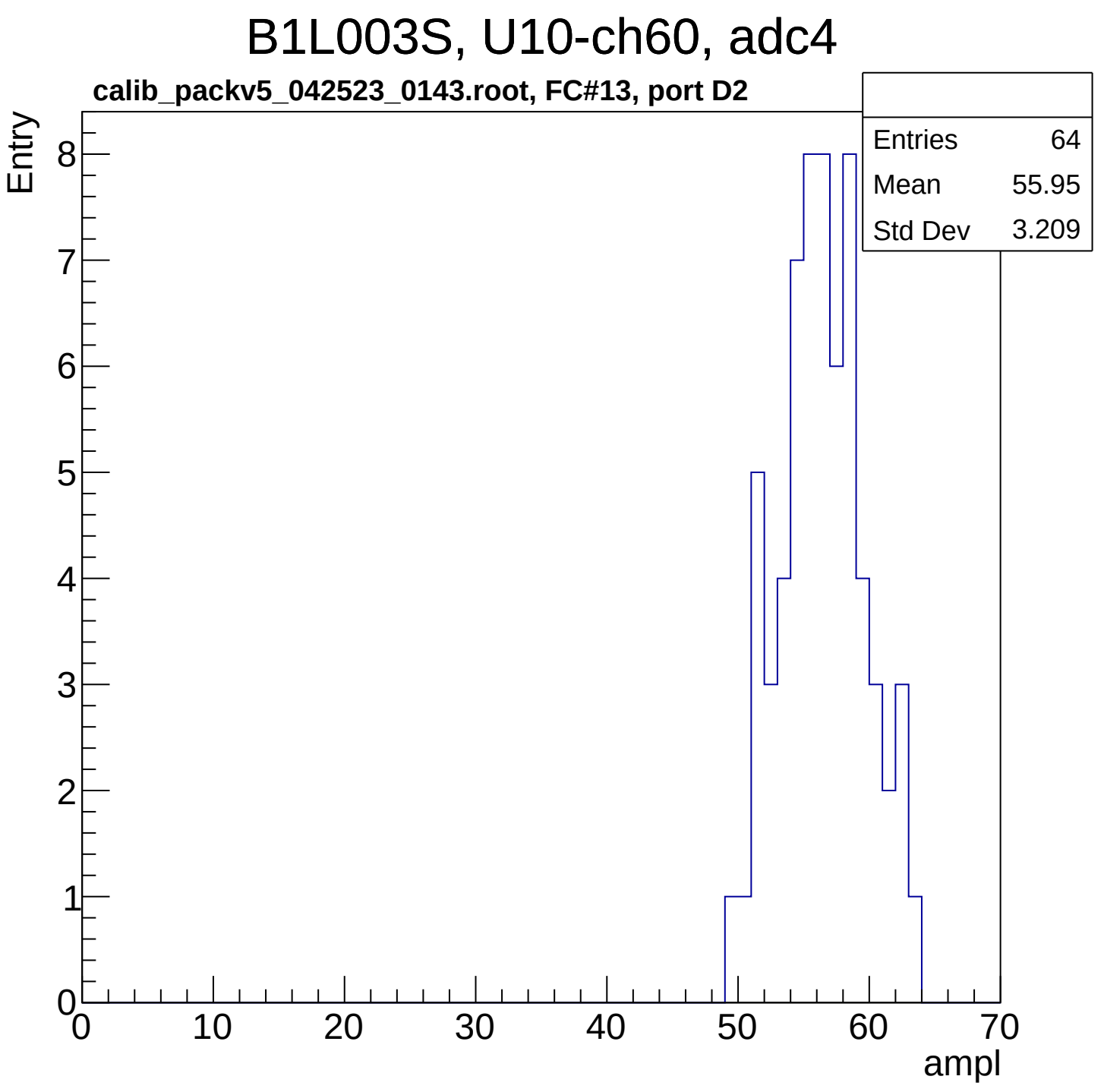
B1L003S, U10-ch60, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 55.95
Std Dev 3.209
Entry
ampl
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2
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4
5
6
7
8
0
10
20
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50
60
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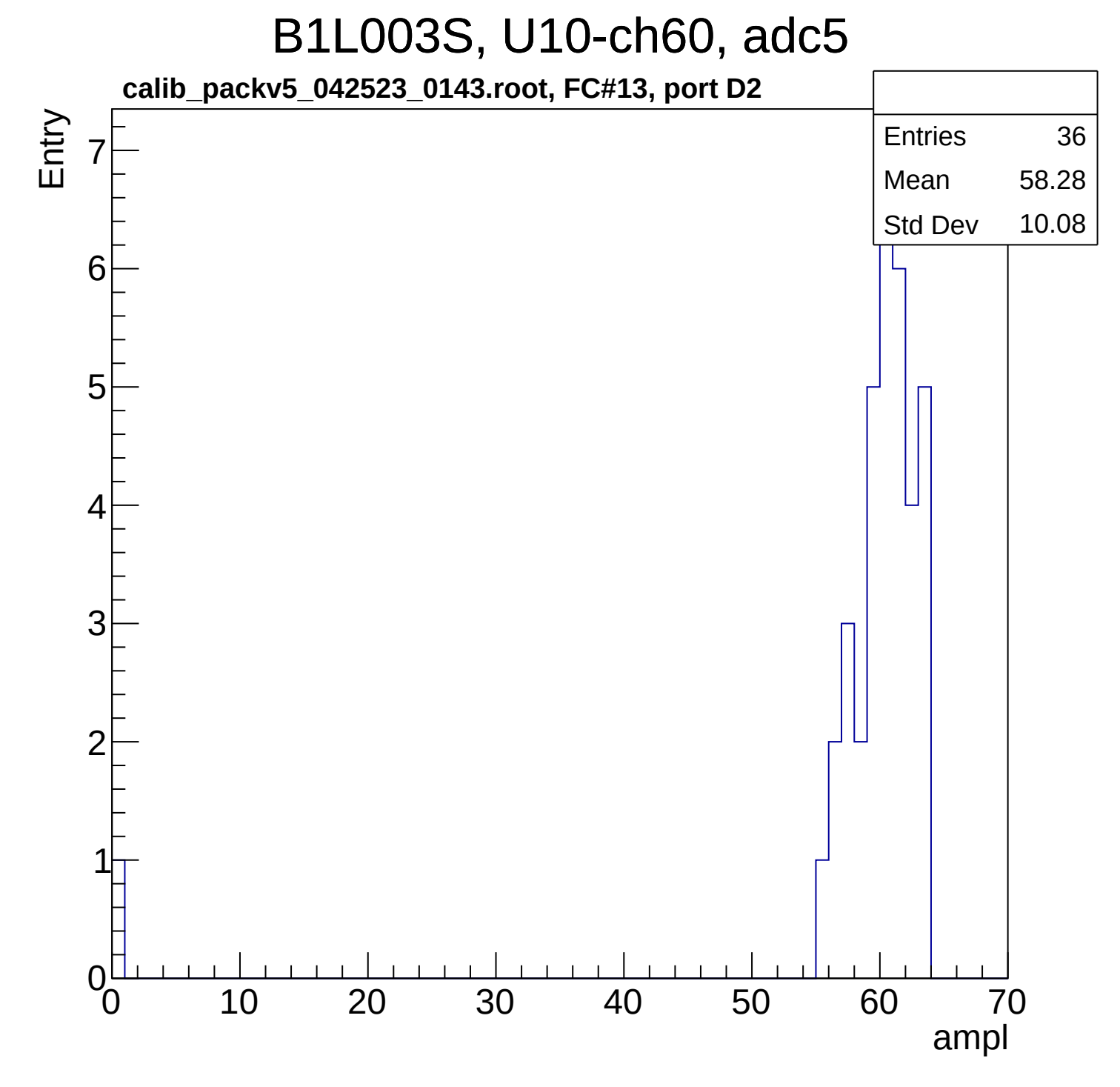
B1L003S, U10-ch60, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.28
Std Dev 10.08
Entry
ampl
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3
4
5
6
7
0
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20
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40
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60
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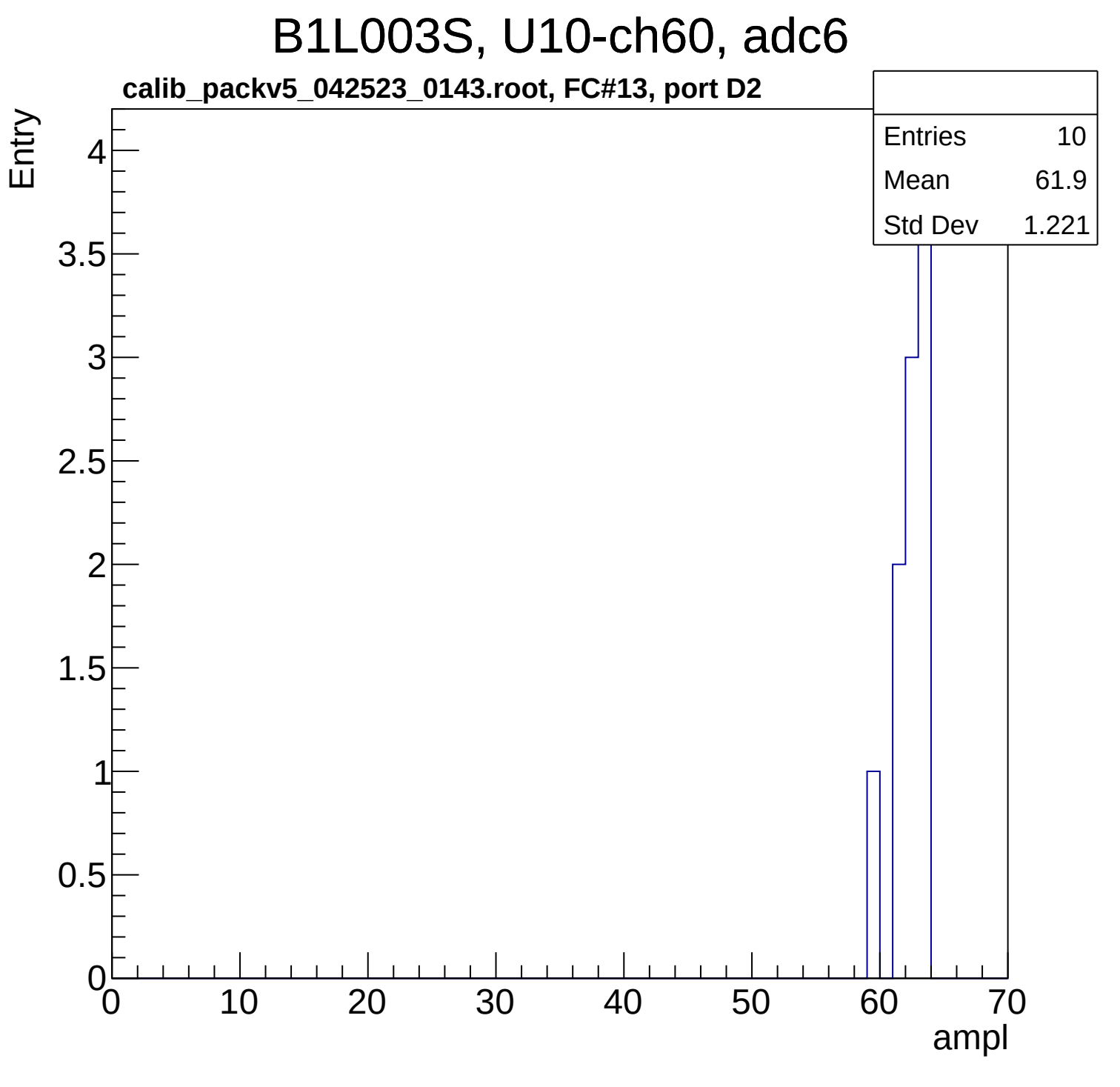
B1L003S, U10-ch60, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.9
Std Dev 1.221
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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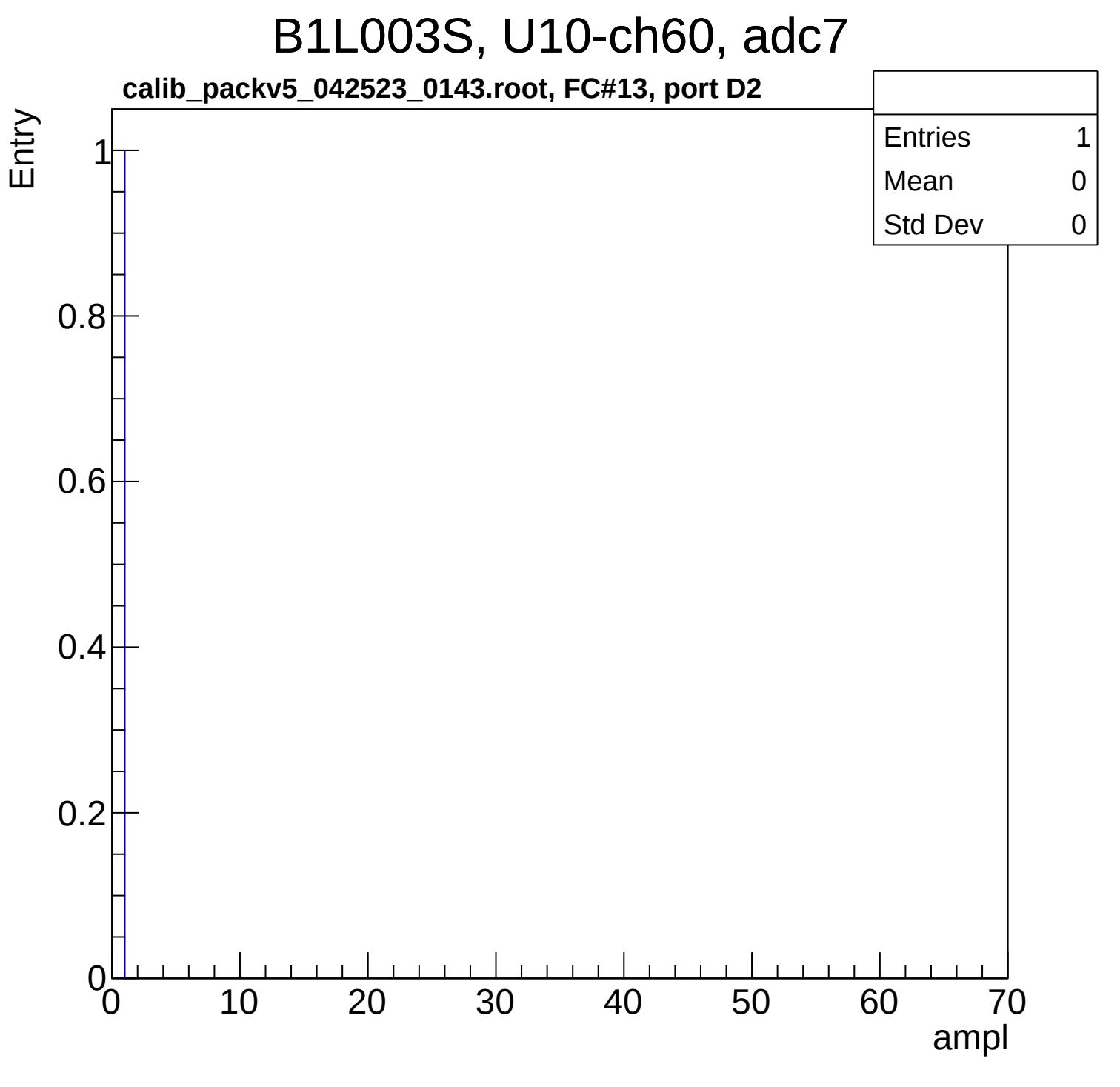
B1L003S, U10-ch60, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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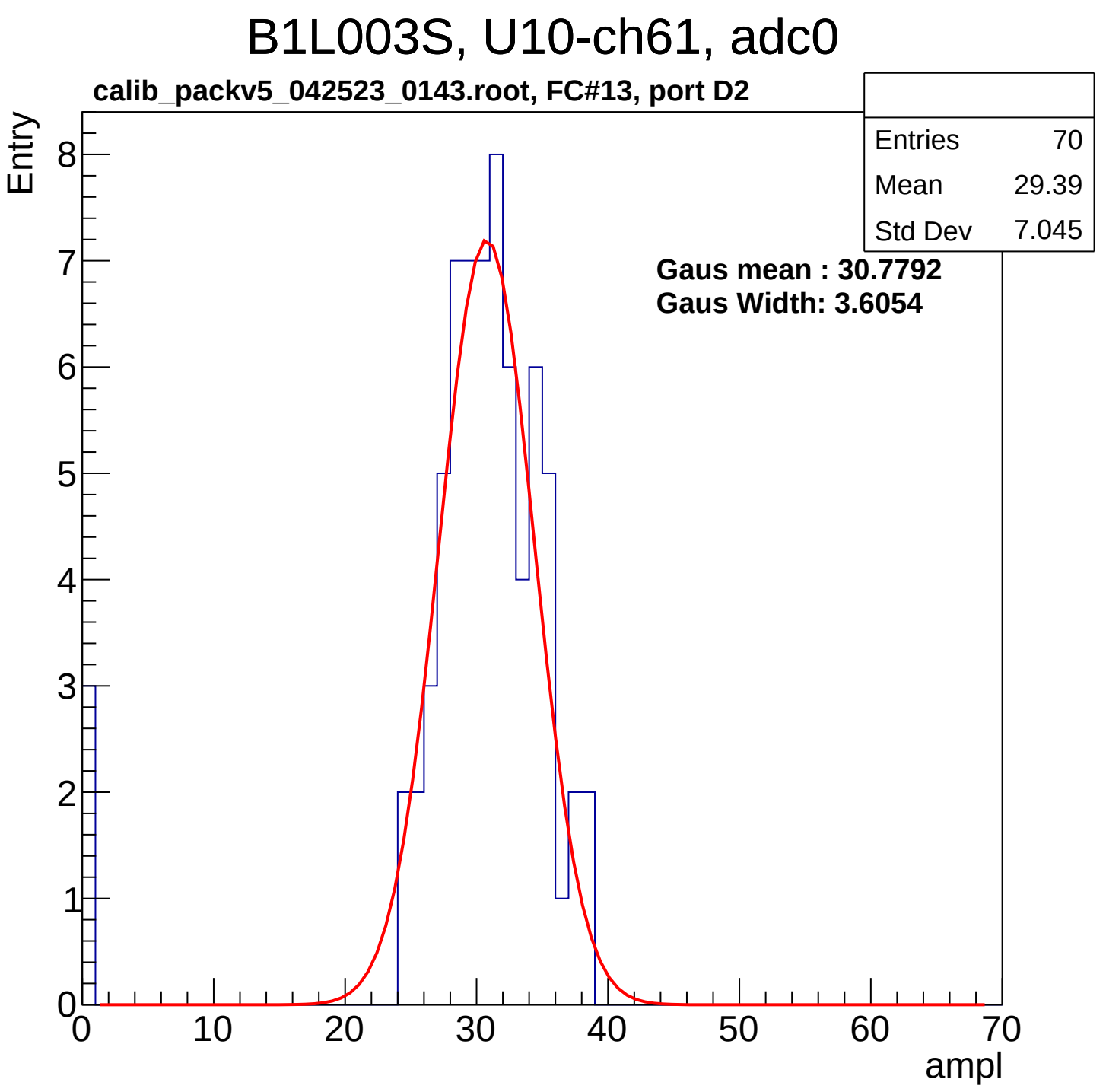
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch61, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 29.39
Std Dev 7.045
Gaus mean : 30.7792
Gaus Width: 3.6054
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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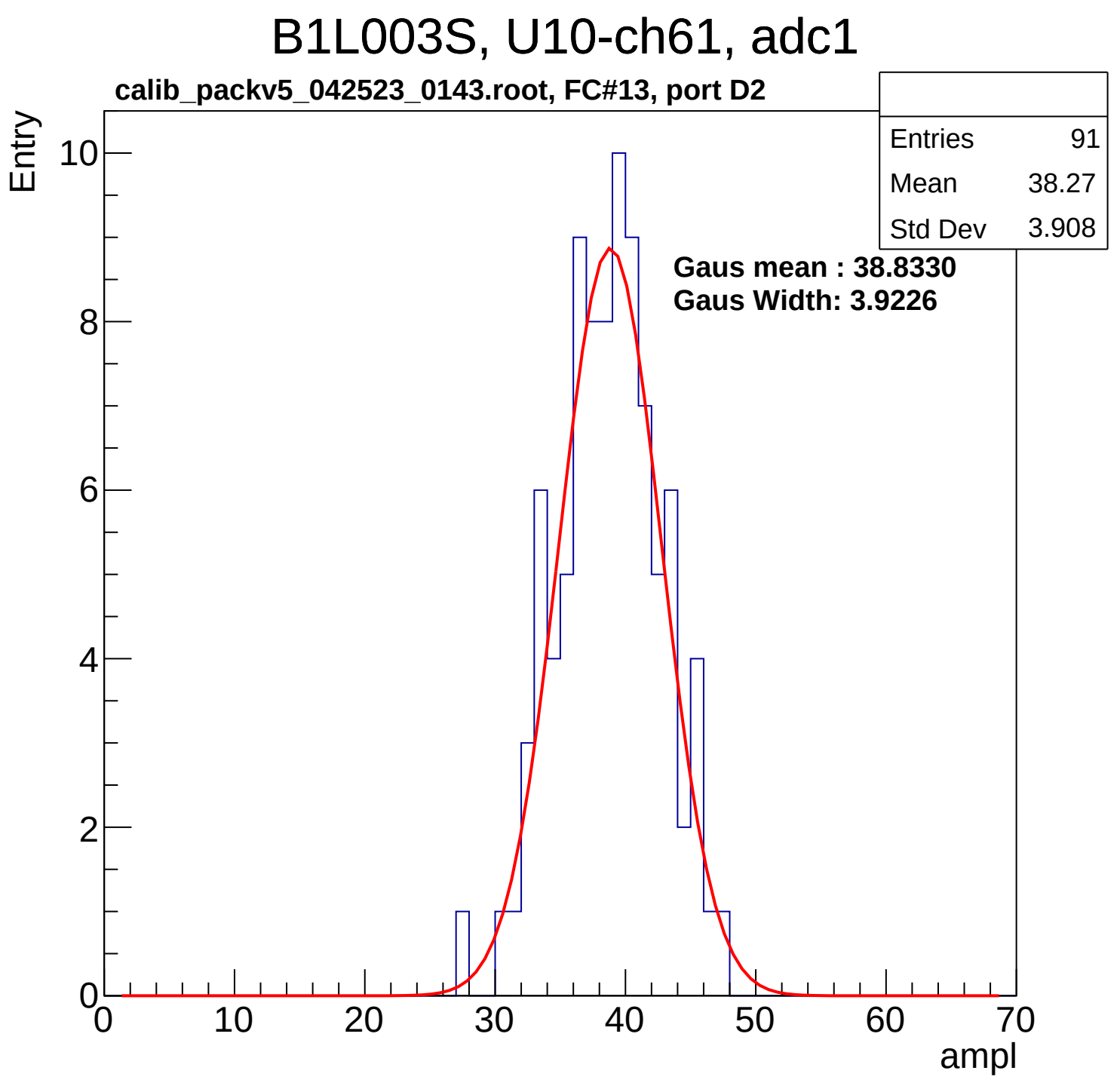
B1L003S, U10-ch61, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 91
Mean 38.27
Std Dev 3.908
Gaus mean : 38.8330
Gaus Width: 3.9226
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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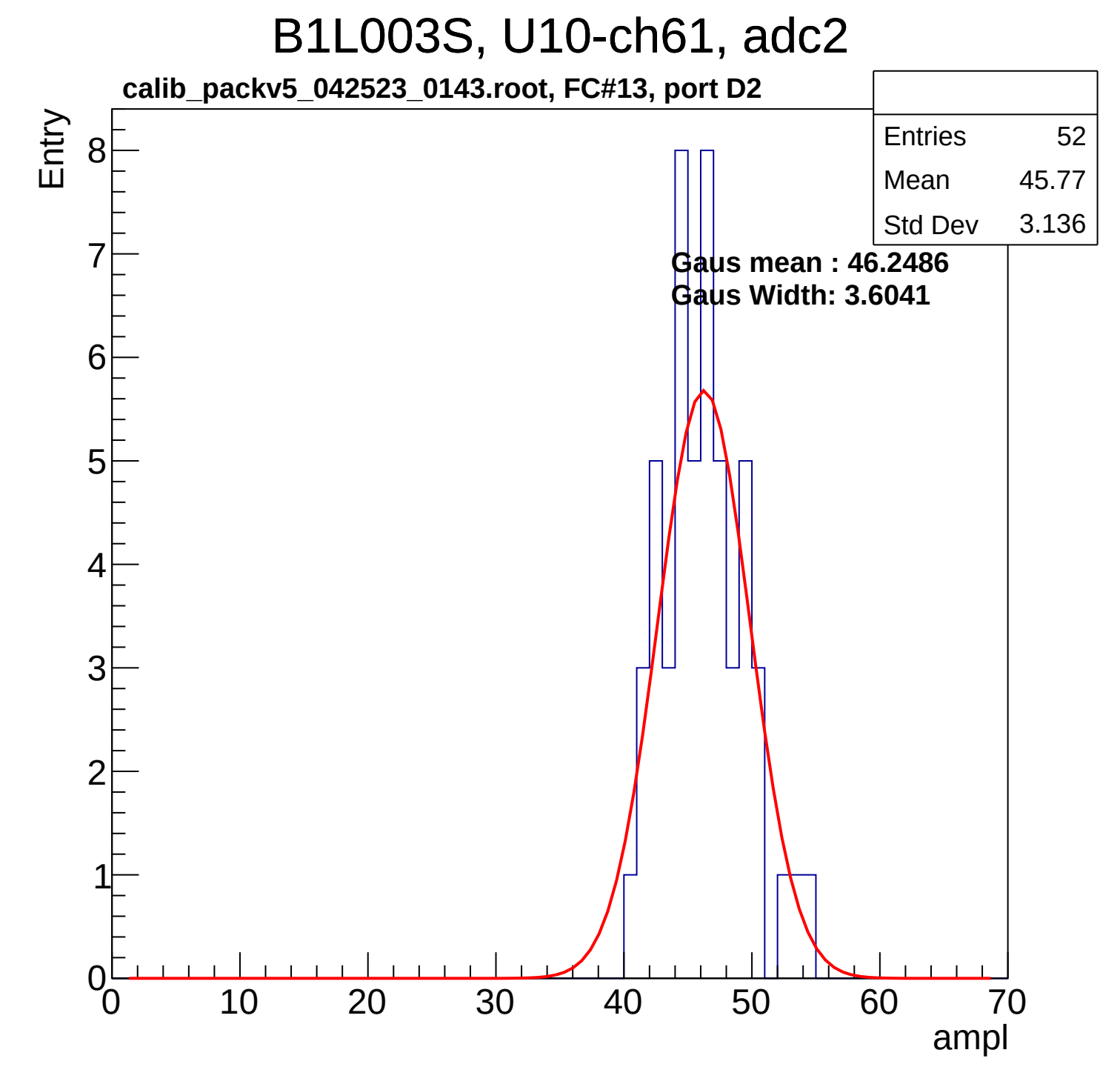
B1L003S, U10-ch61, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 45.77
Std Dev 3.136
Gaus mean : 46.2486
Gaus Width: 3.6041
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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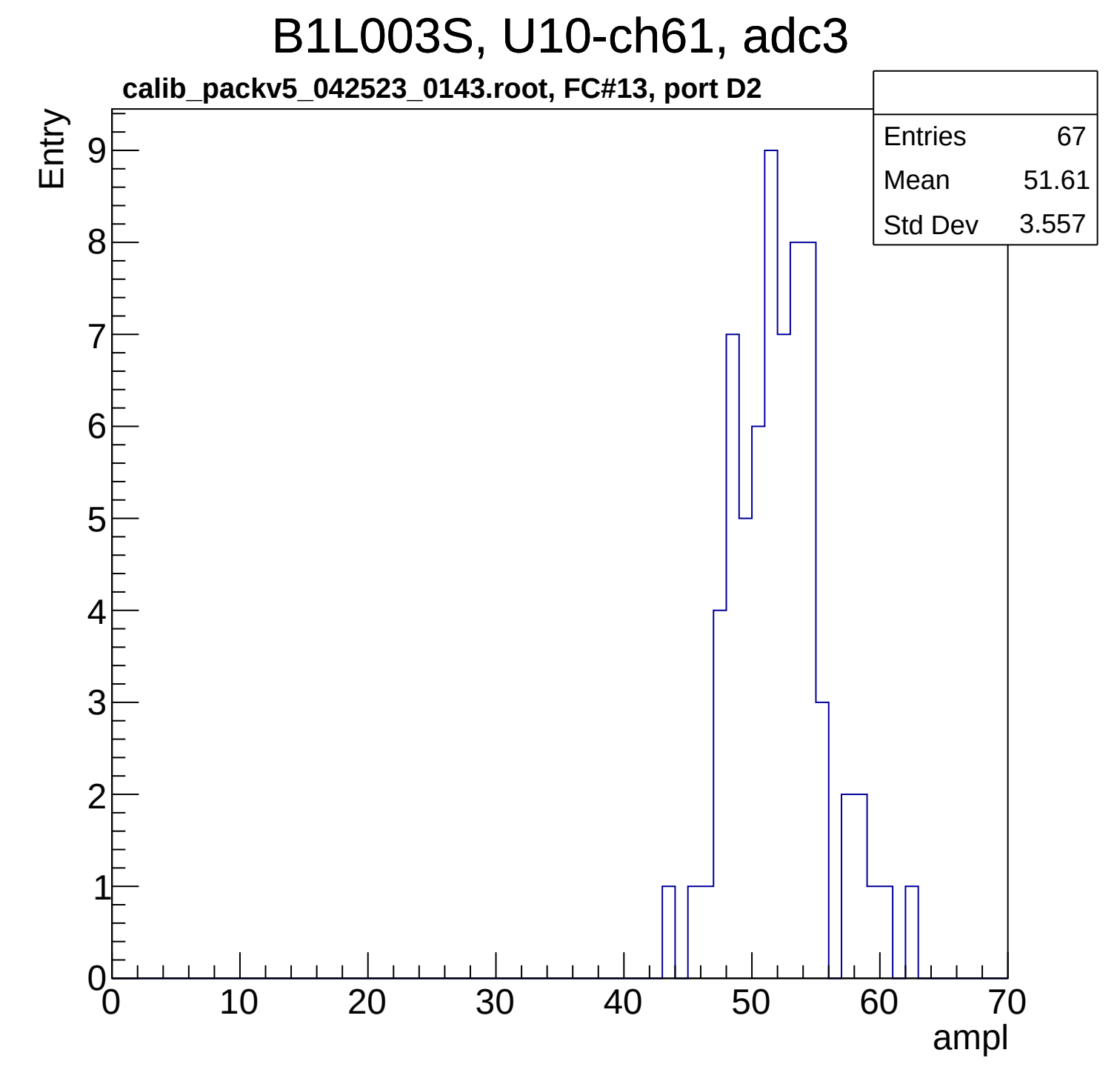
B1L003S, U10-ch61, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 51.61
Std Dev 3.557
Entry
ampl
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2
3
4
5
6
7
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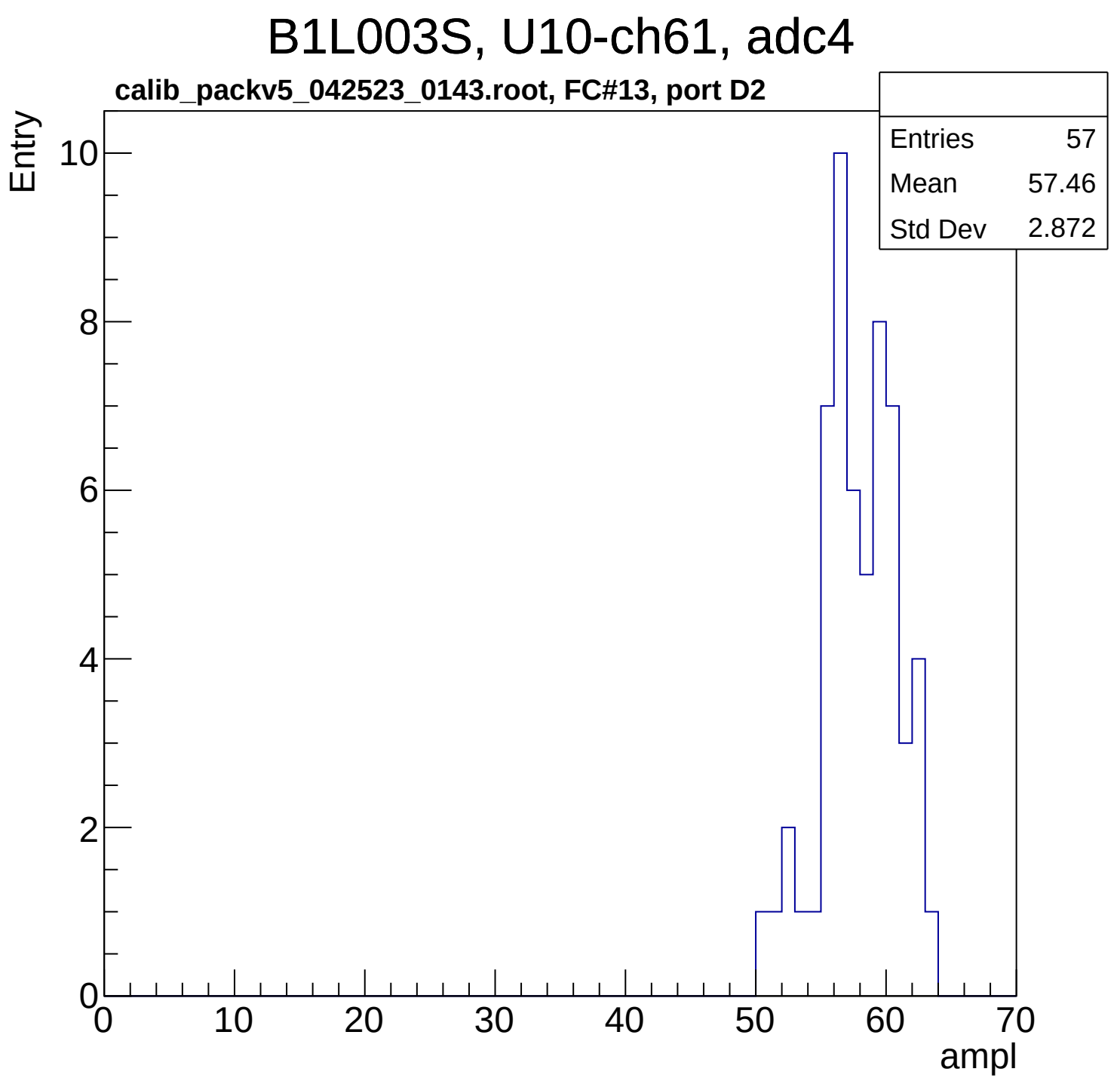
B1L003S, U10-ch61, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 57.46
Std Dev 2.872
Entry
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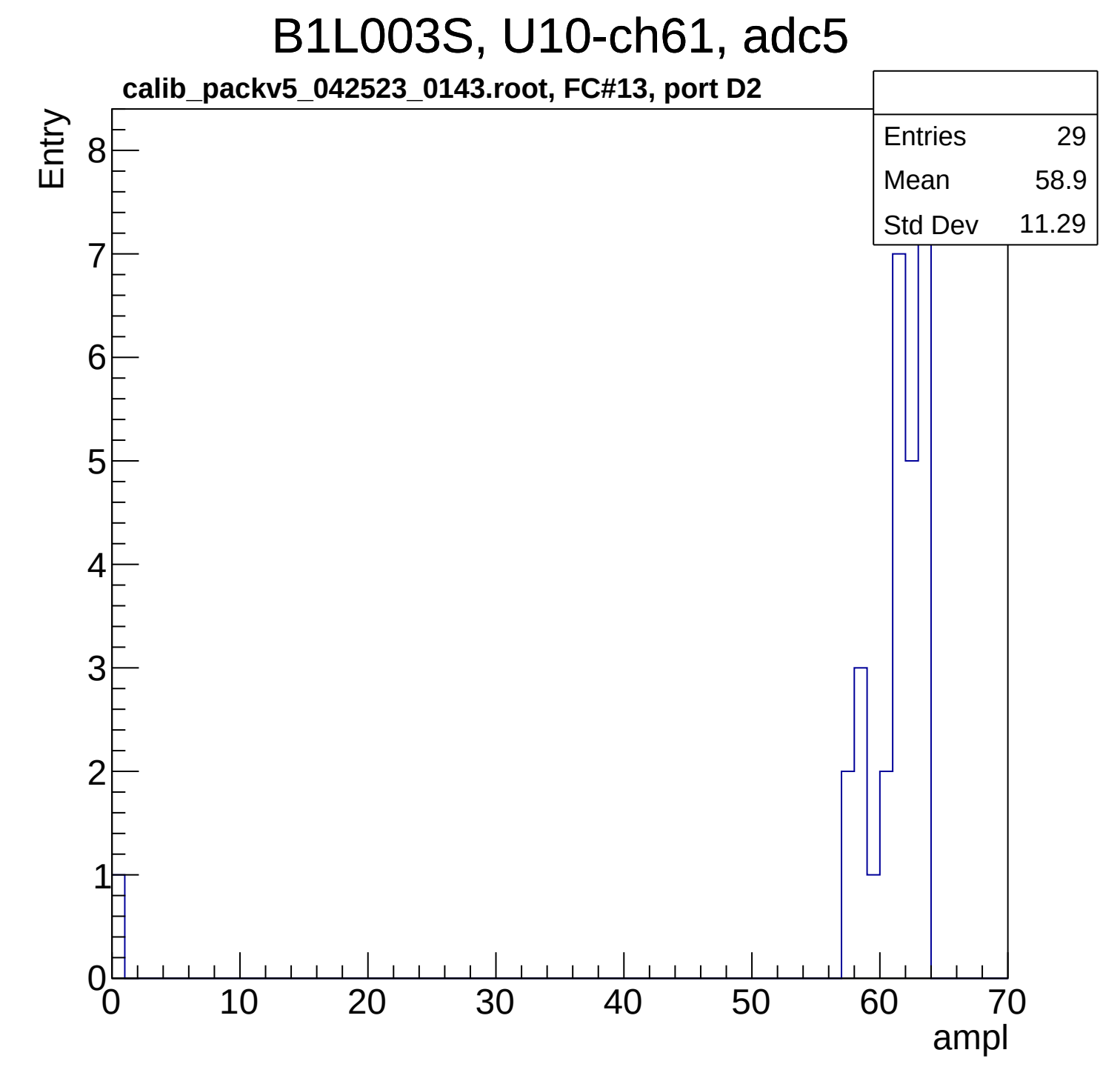
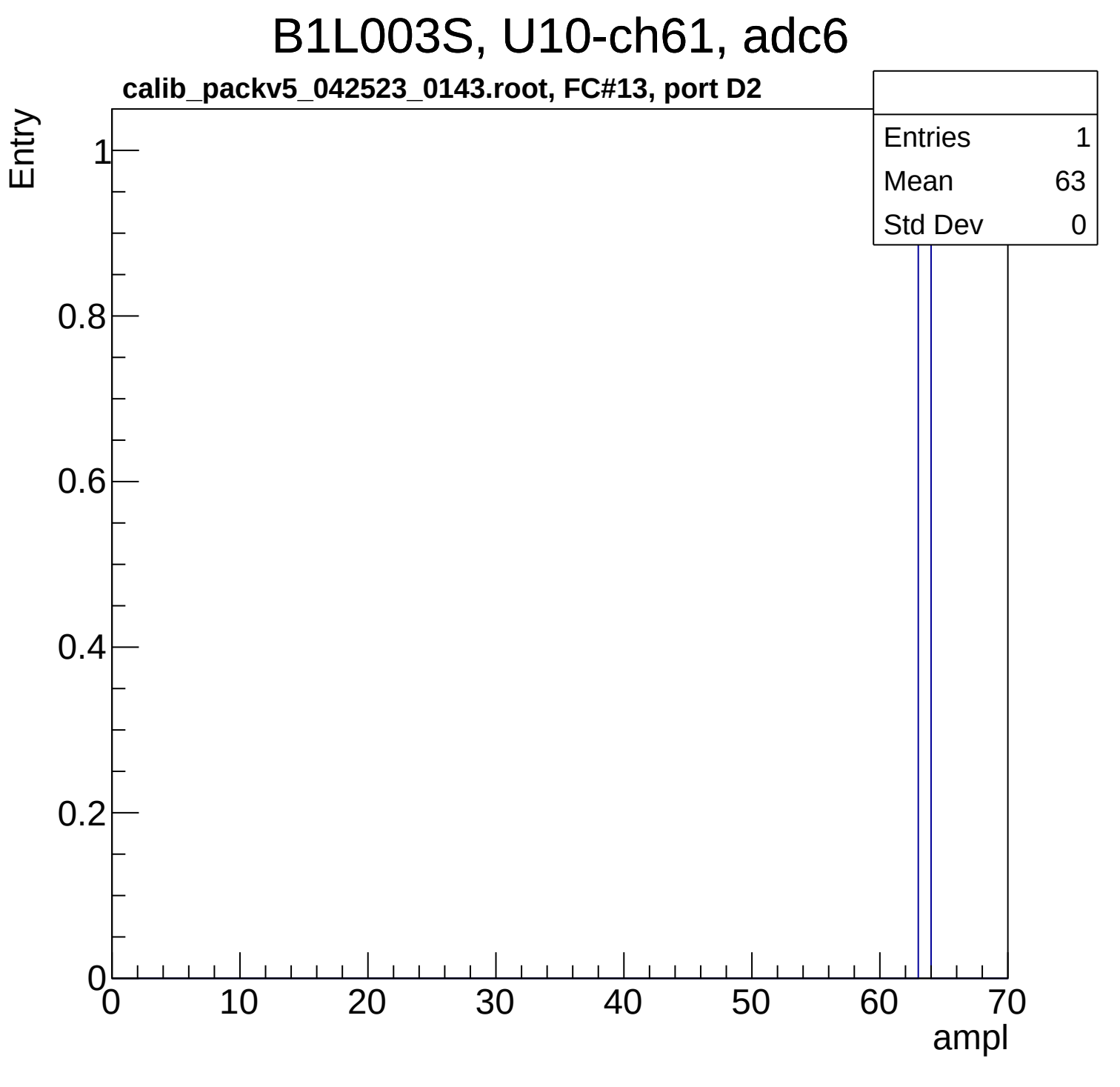
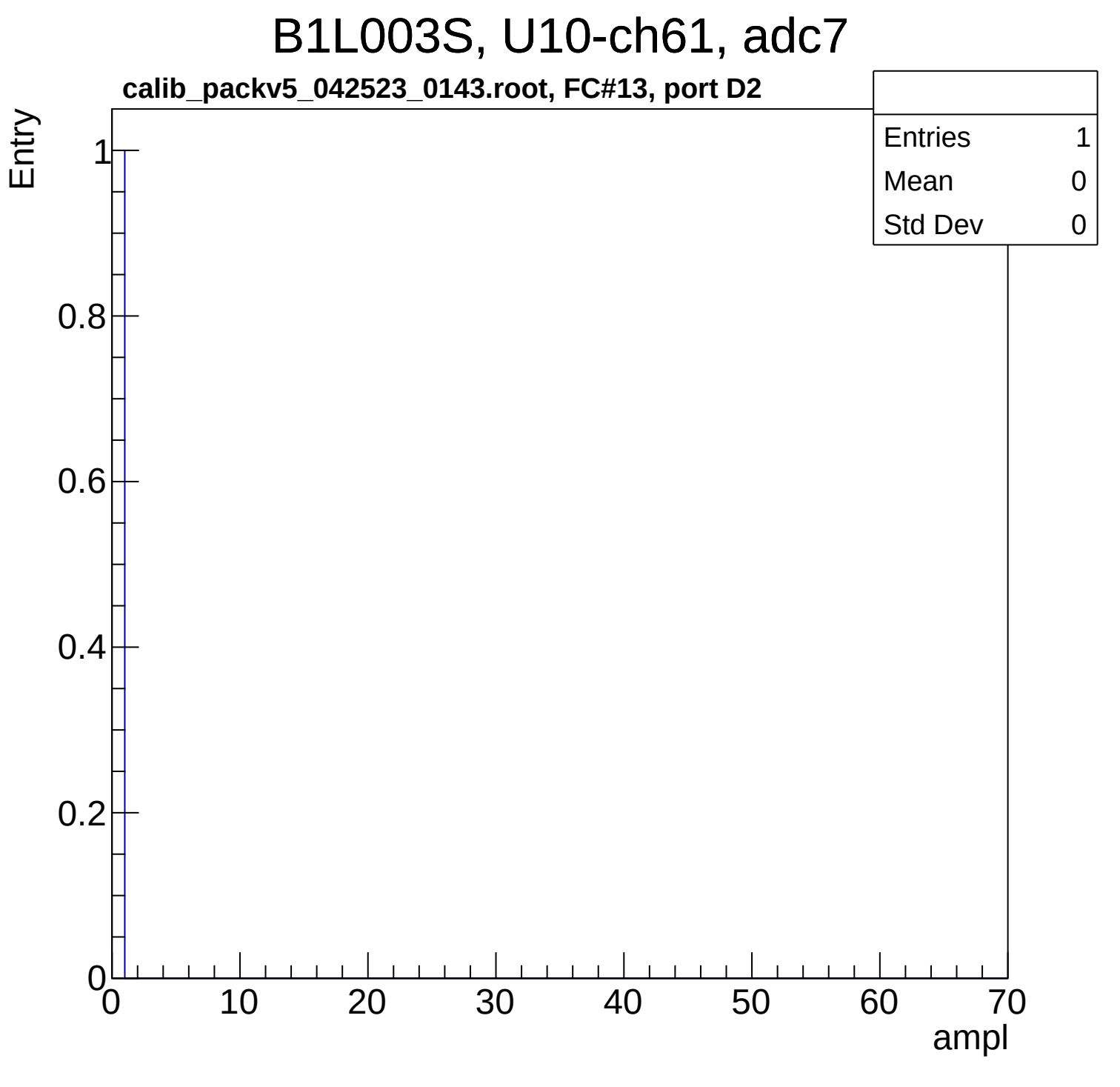
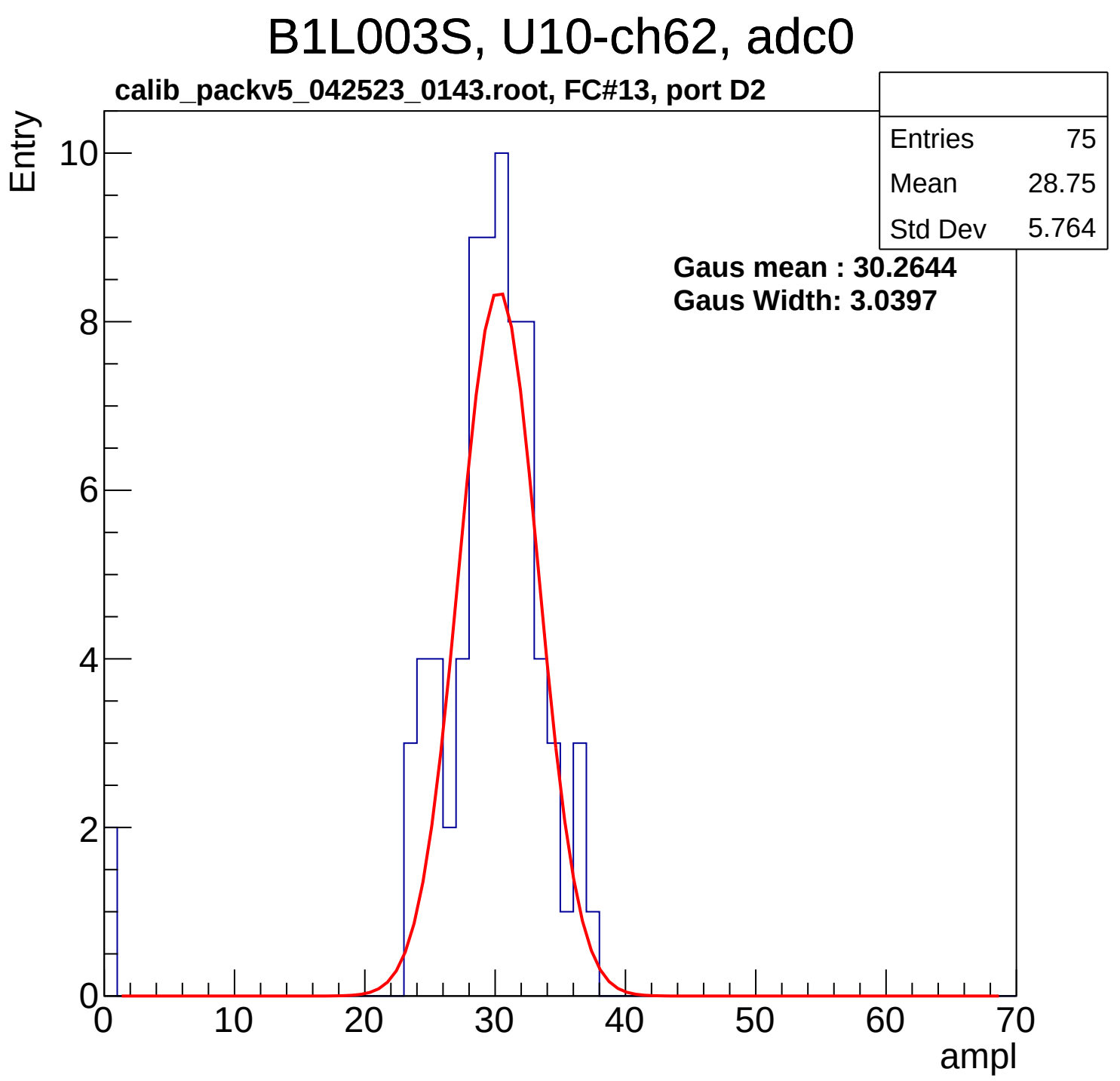
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch61, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 29
Mean 58.9
Std Dev 11.29
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch61, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch61, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch62, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.75
Std Dev 5.764
Gaus mean : 30.2644
Gaus Width: 3.0397
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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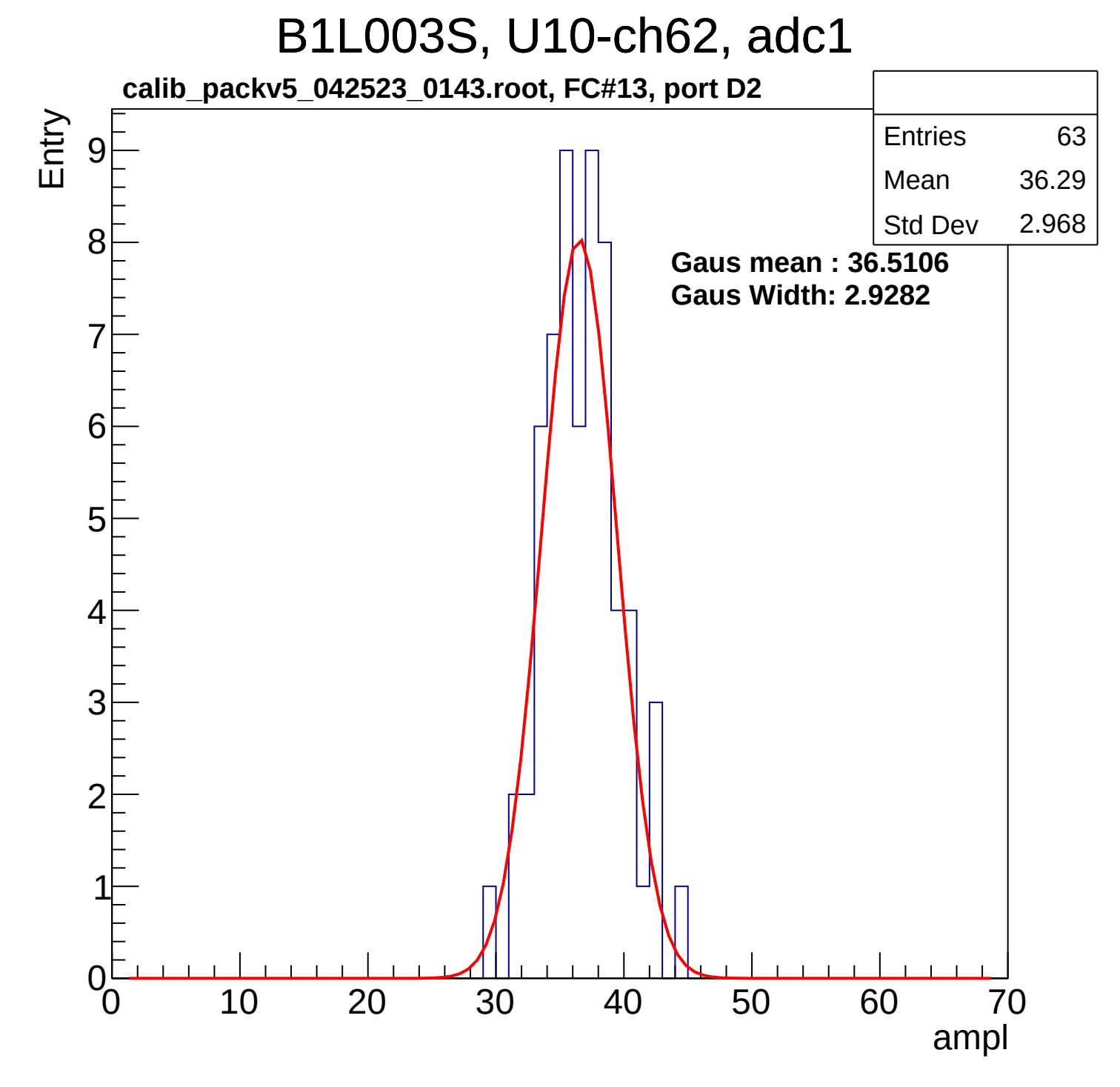
B1L003S, U10-ch62, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.29
Std Dev 2.968
Gaus mean : 36.5106
Gaus Width: 2.9282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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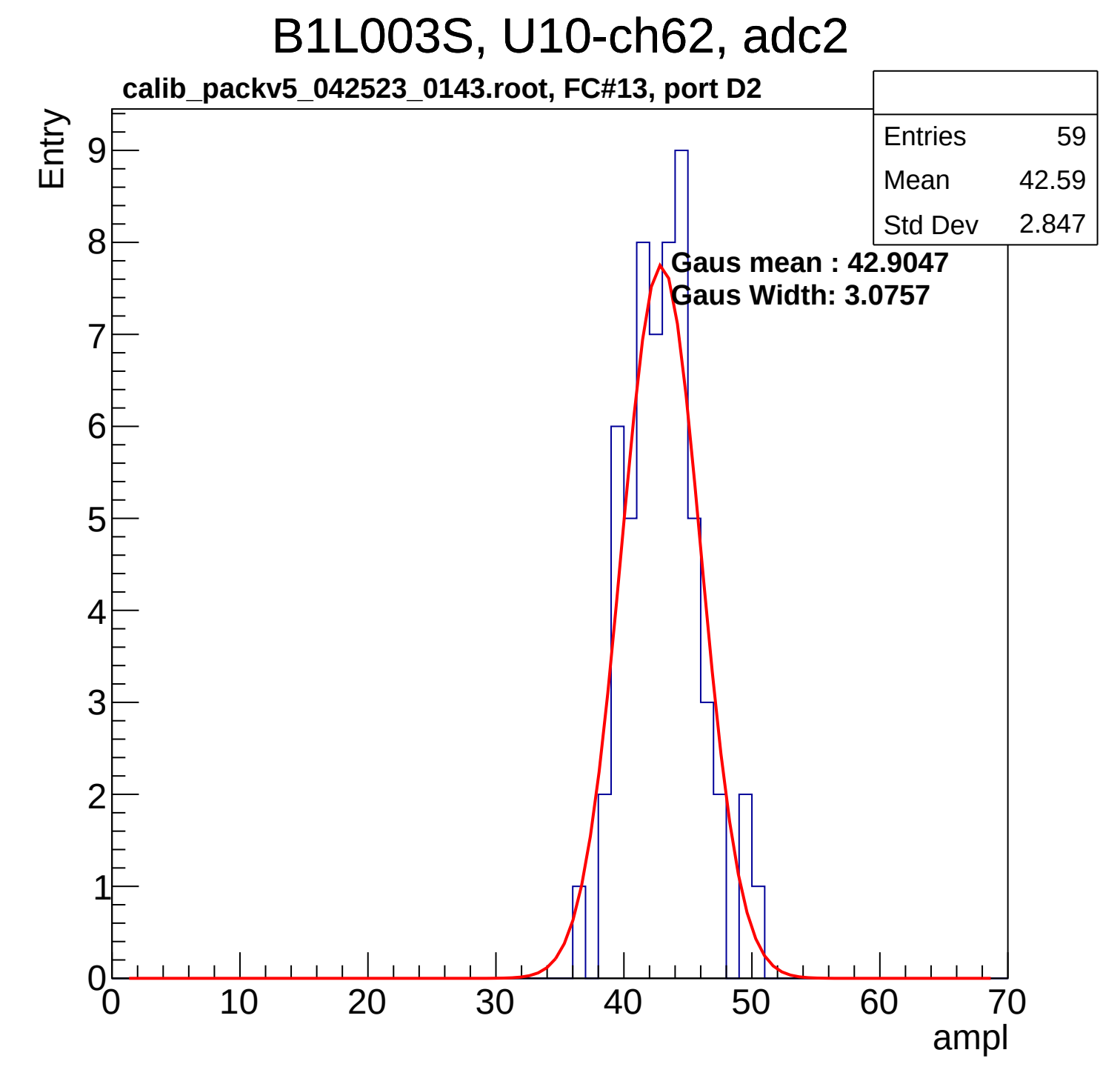
B1L003S, U10-ch62, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.59
Std Dev 2.847
Gaus mean : 42.9047
Gaus Width: 3.0757
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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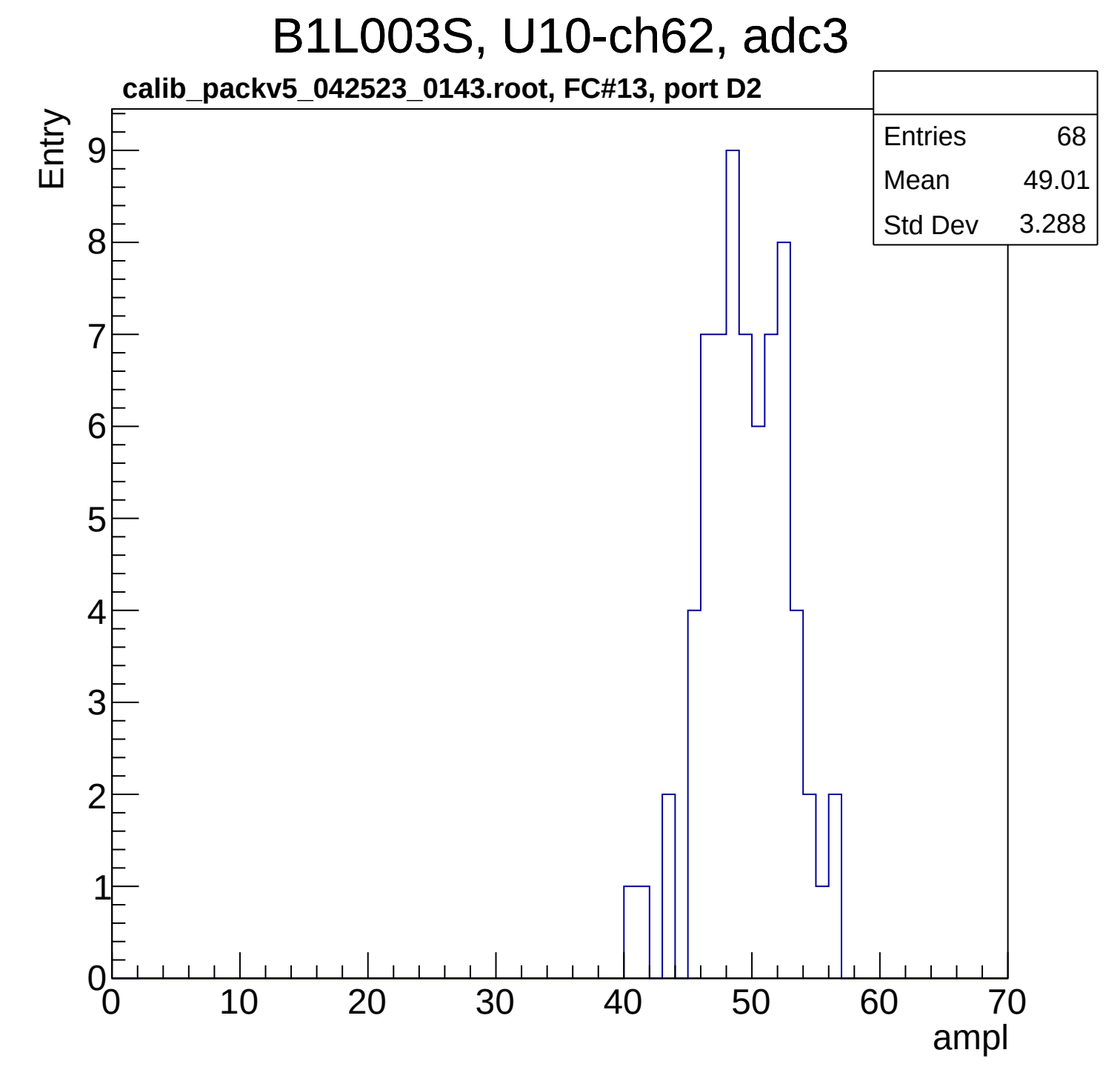
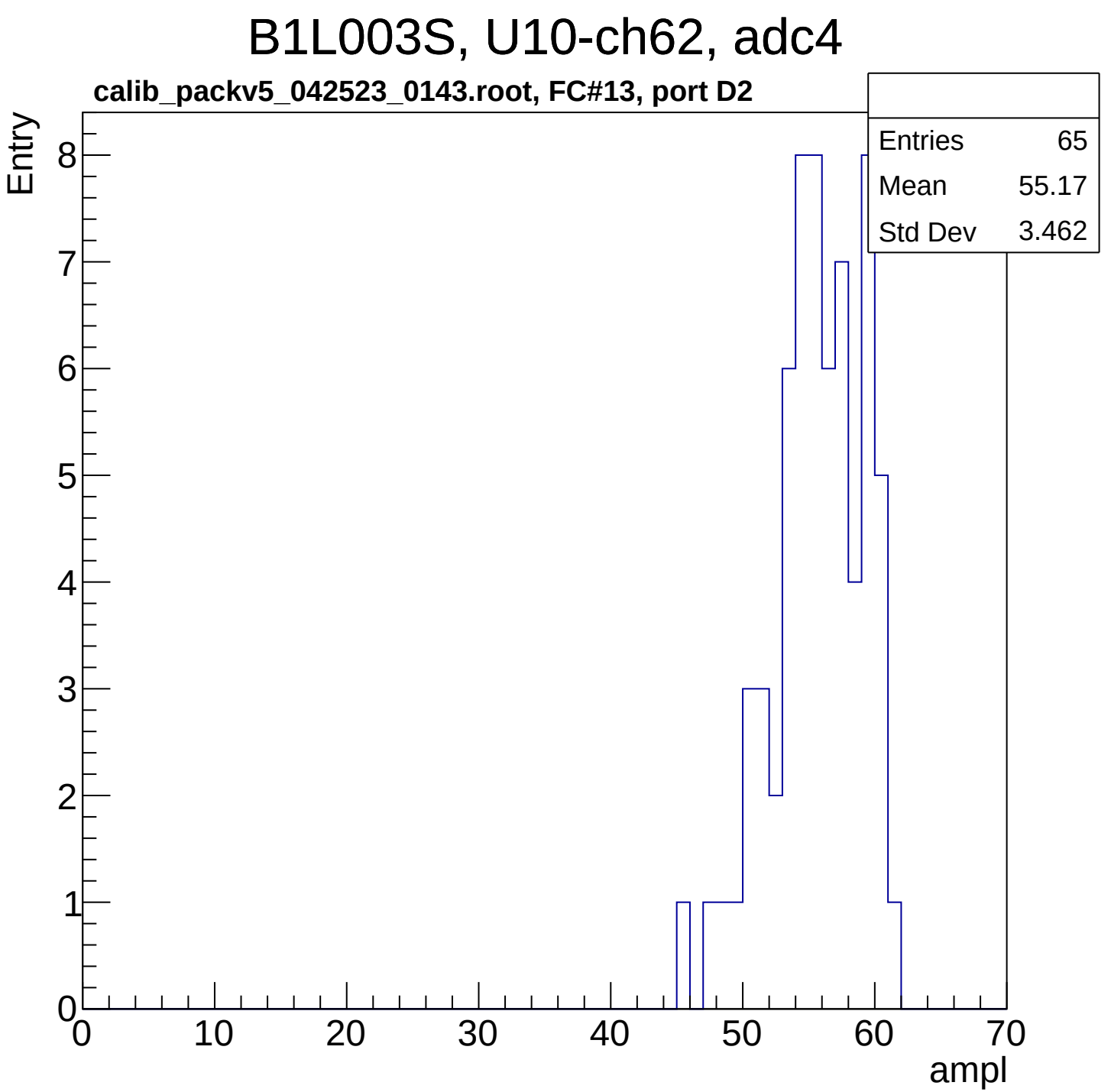
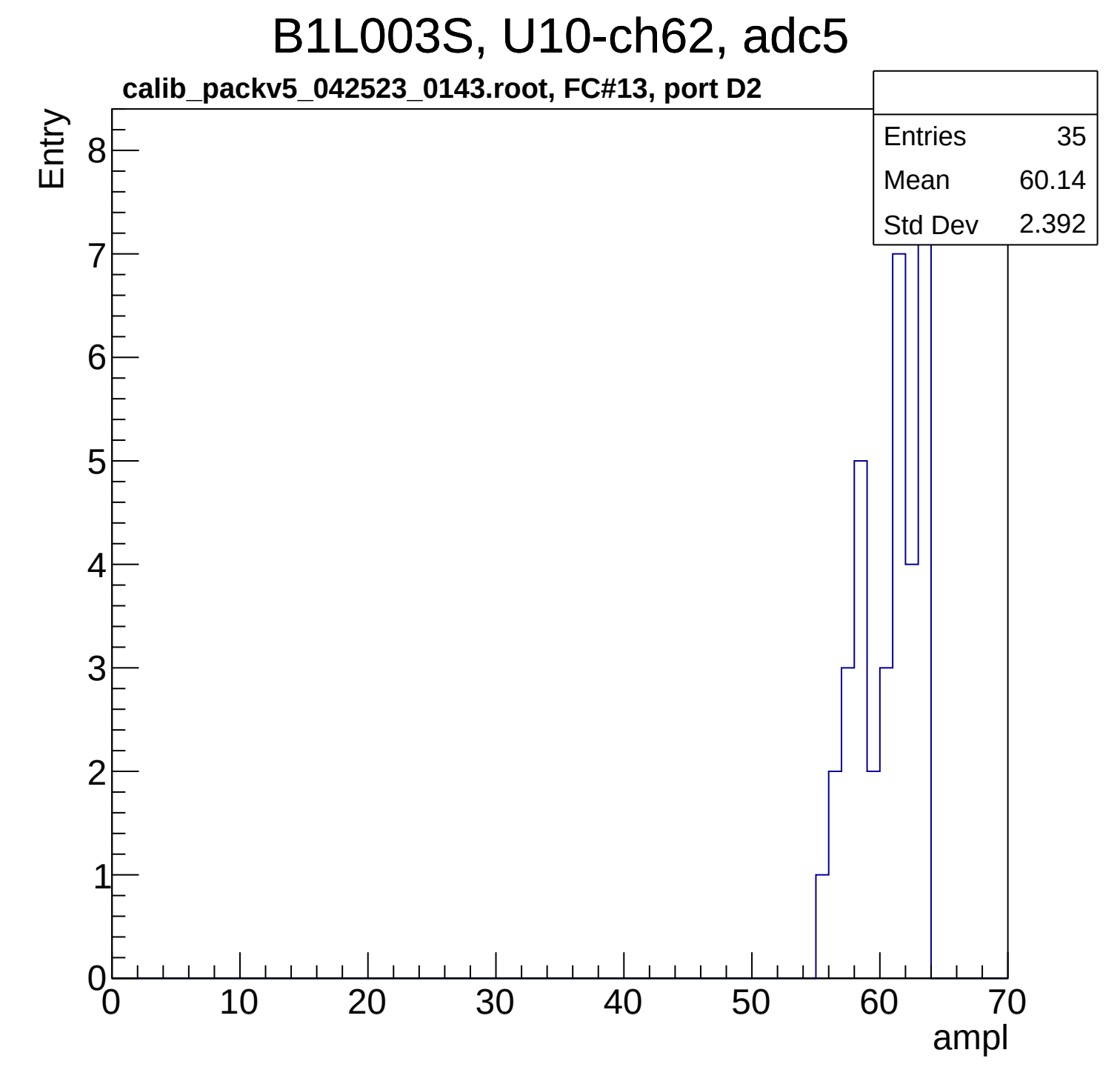

B1L003S, U10-ch62, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.01
Std Dev 3.288
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch62, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 55.17
Std Dev 3.462
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch62, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 35
Mean 60.14
Std Dev 2.392
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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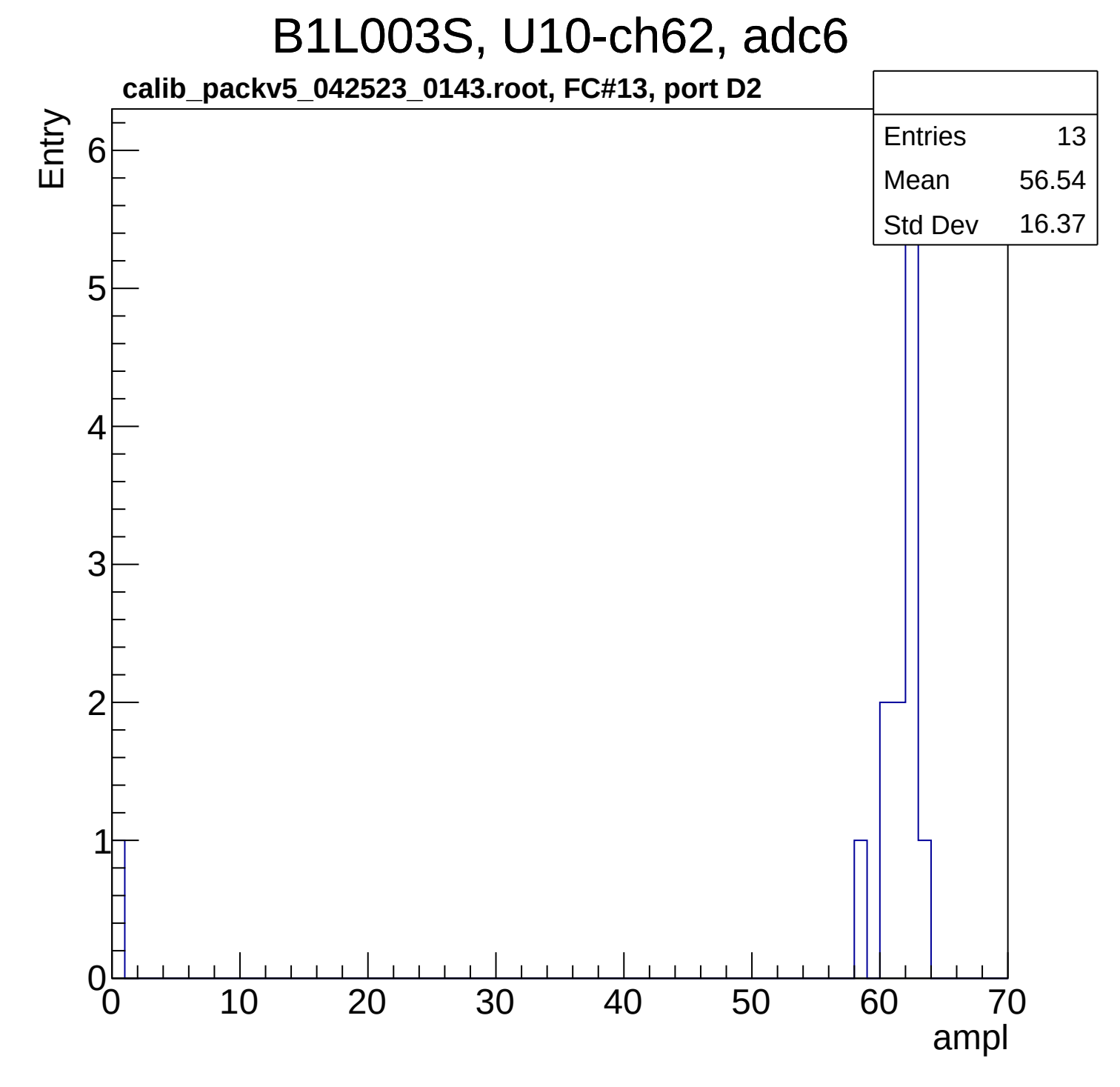
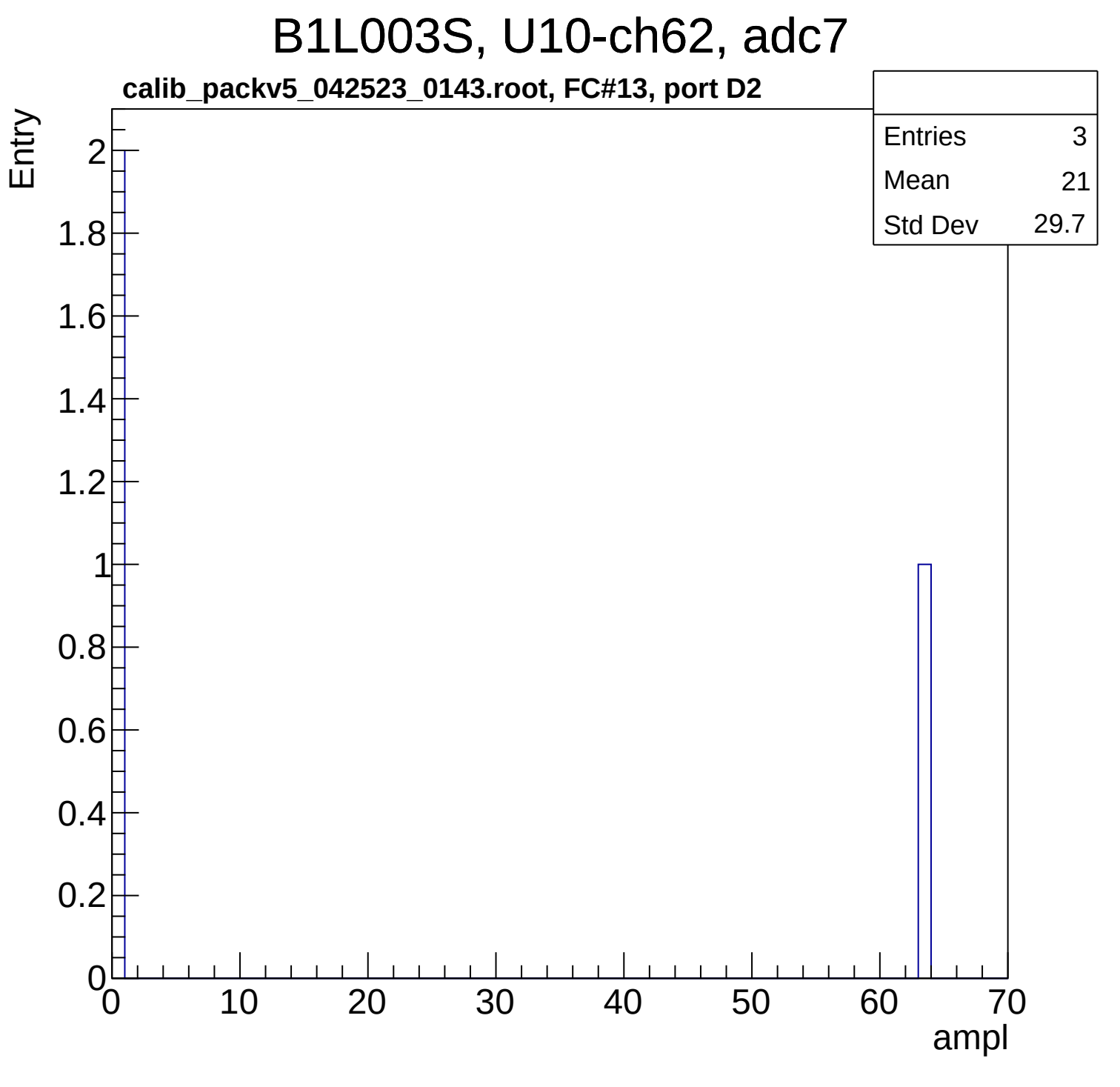
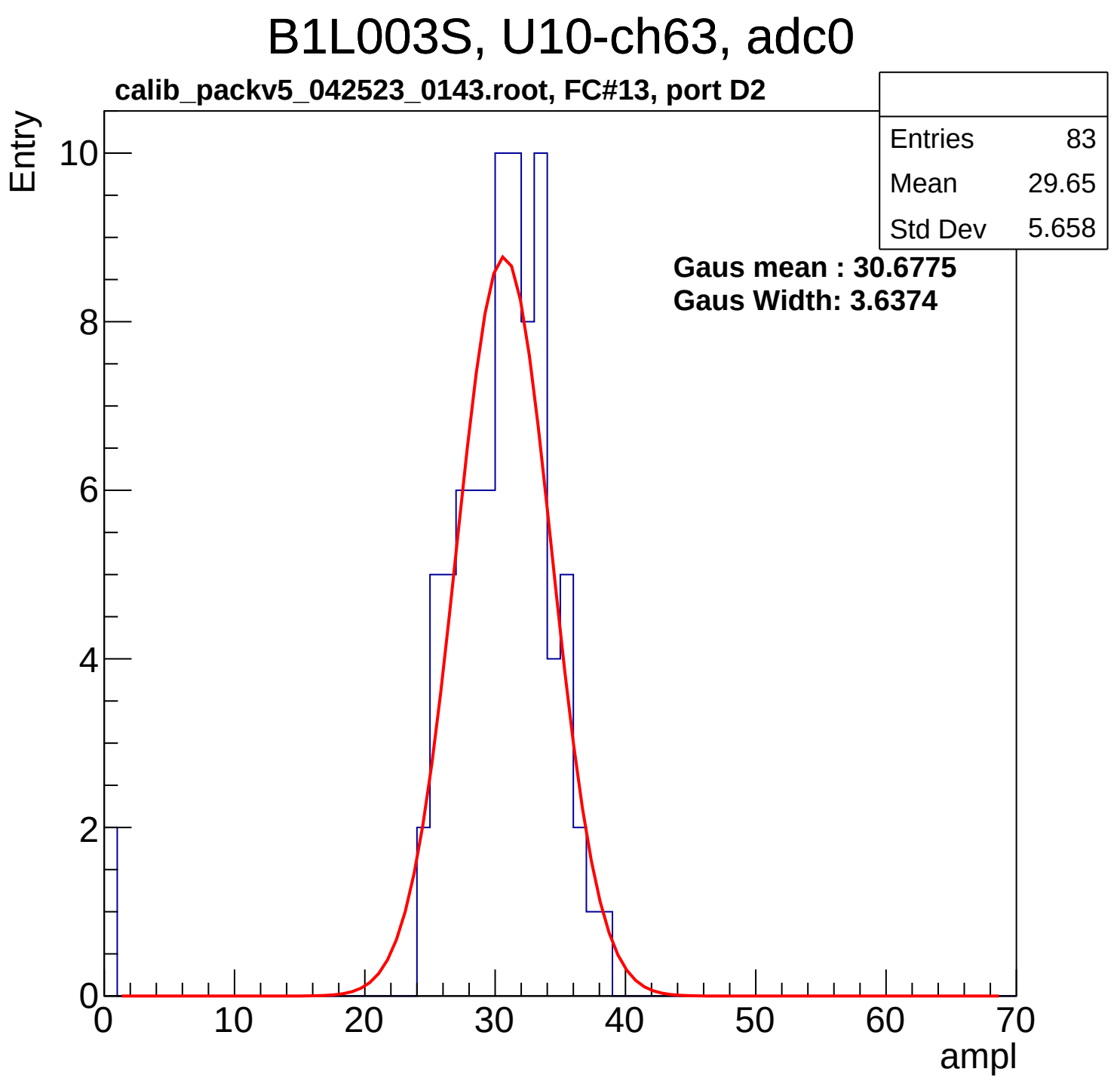
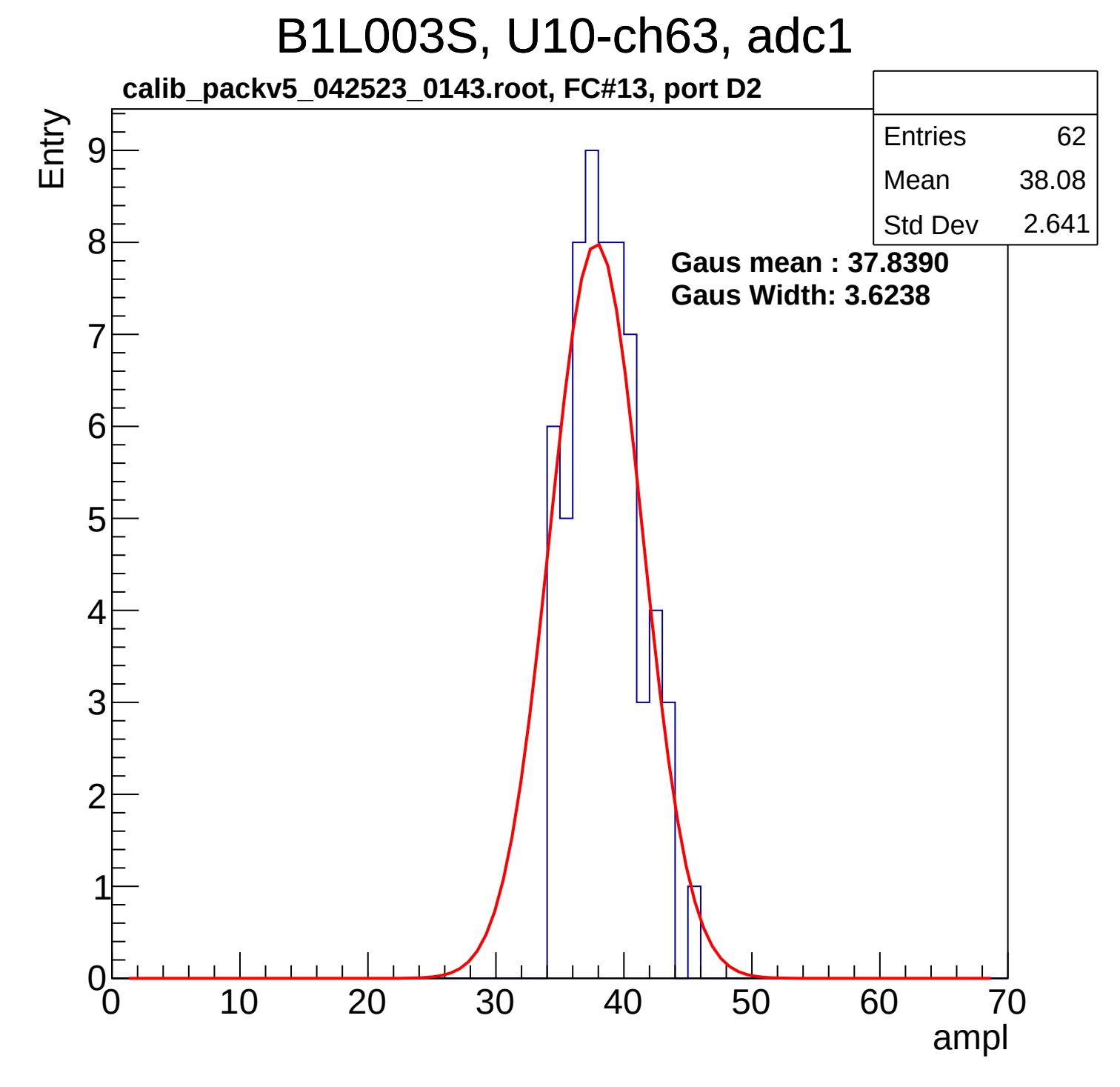
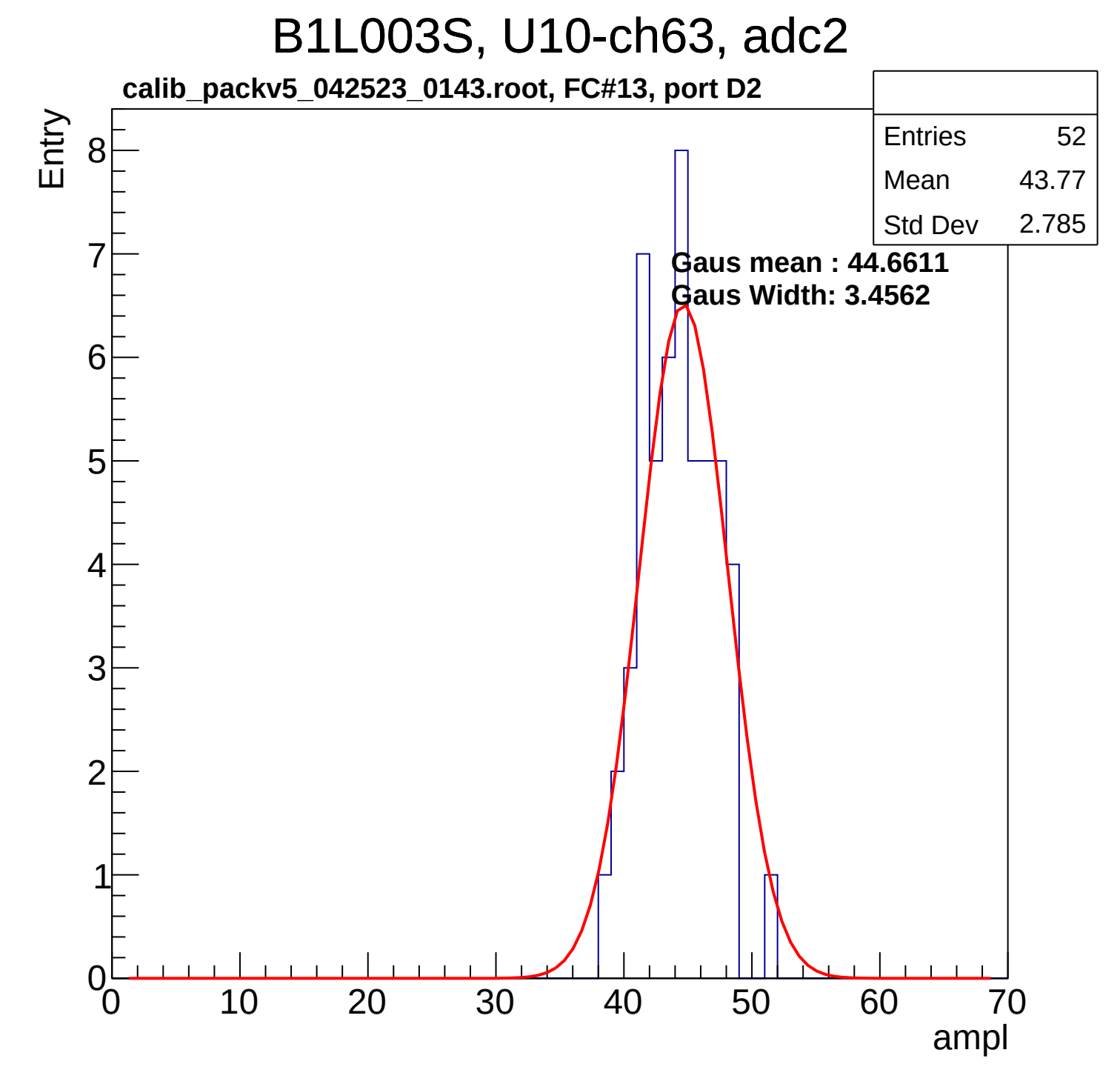
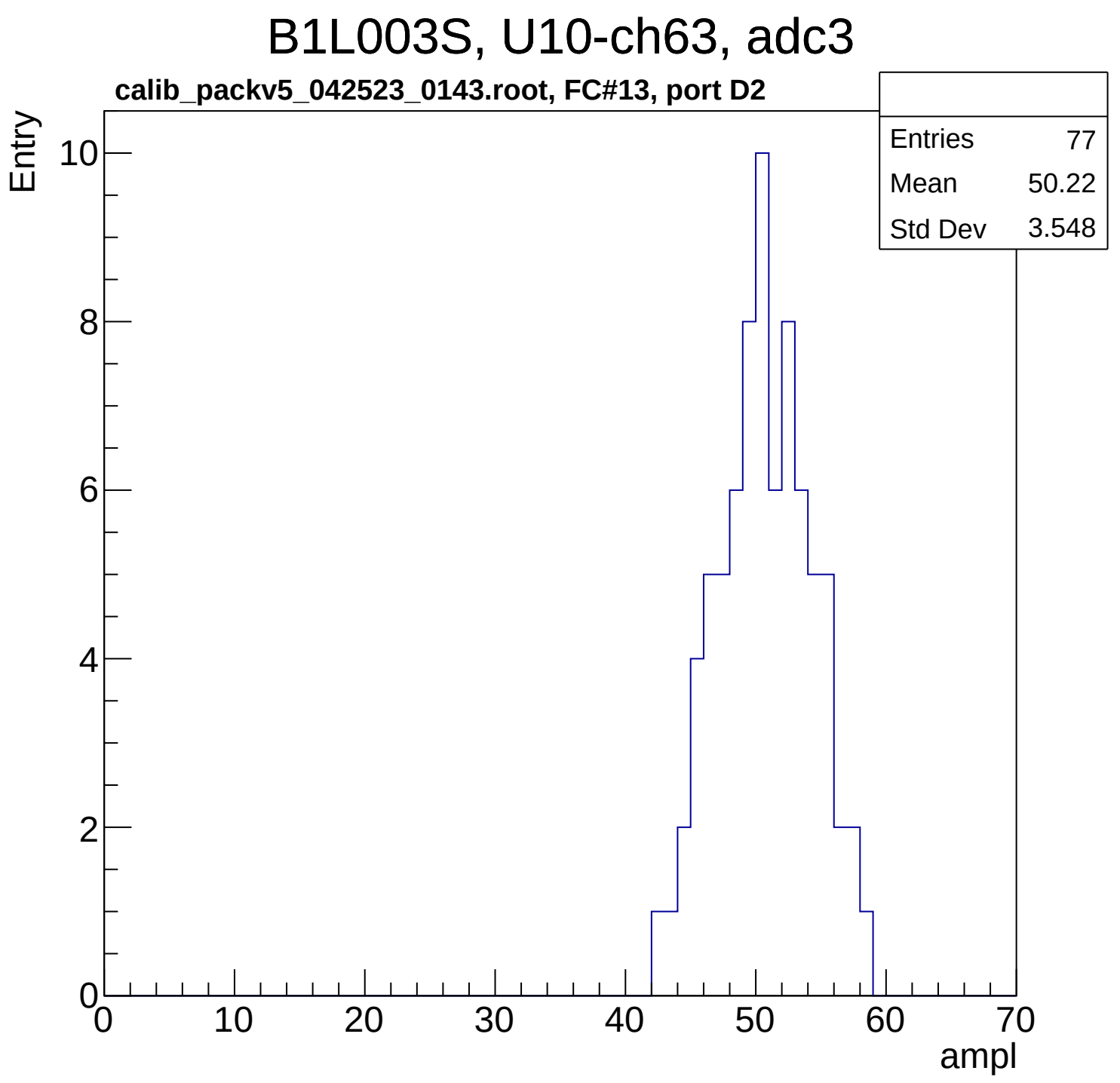

B1L003S, U10-ch62, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.54
Std Dev 16.37
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

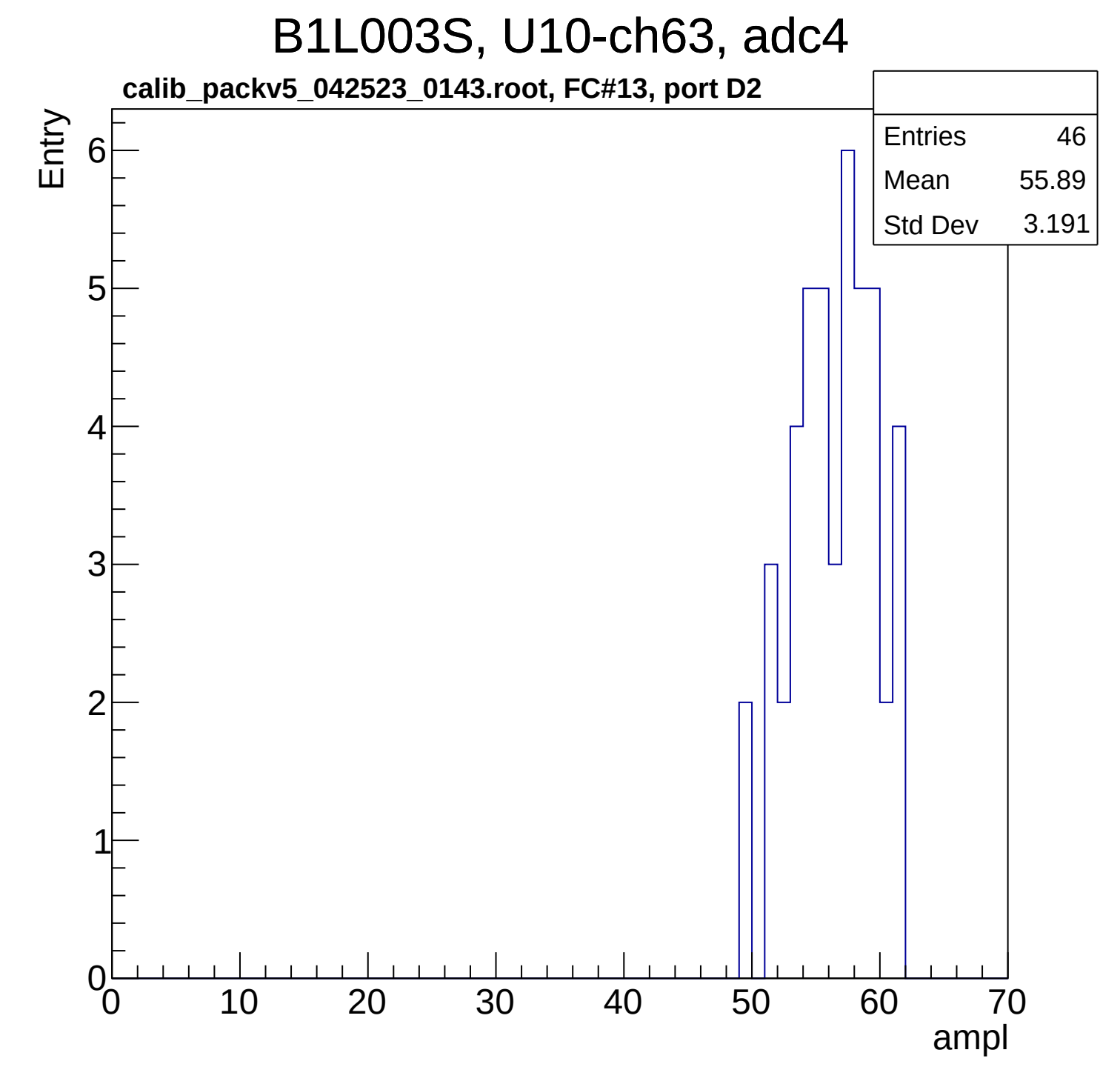

B1L003S, U10-ch62, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

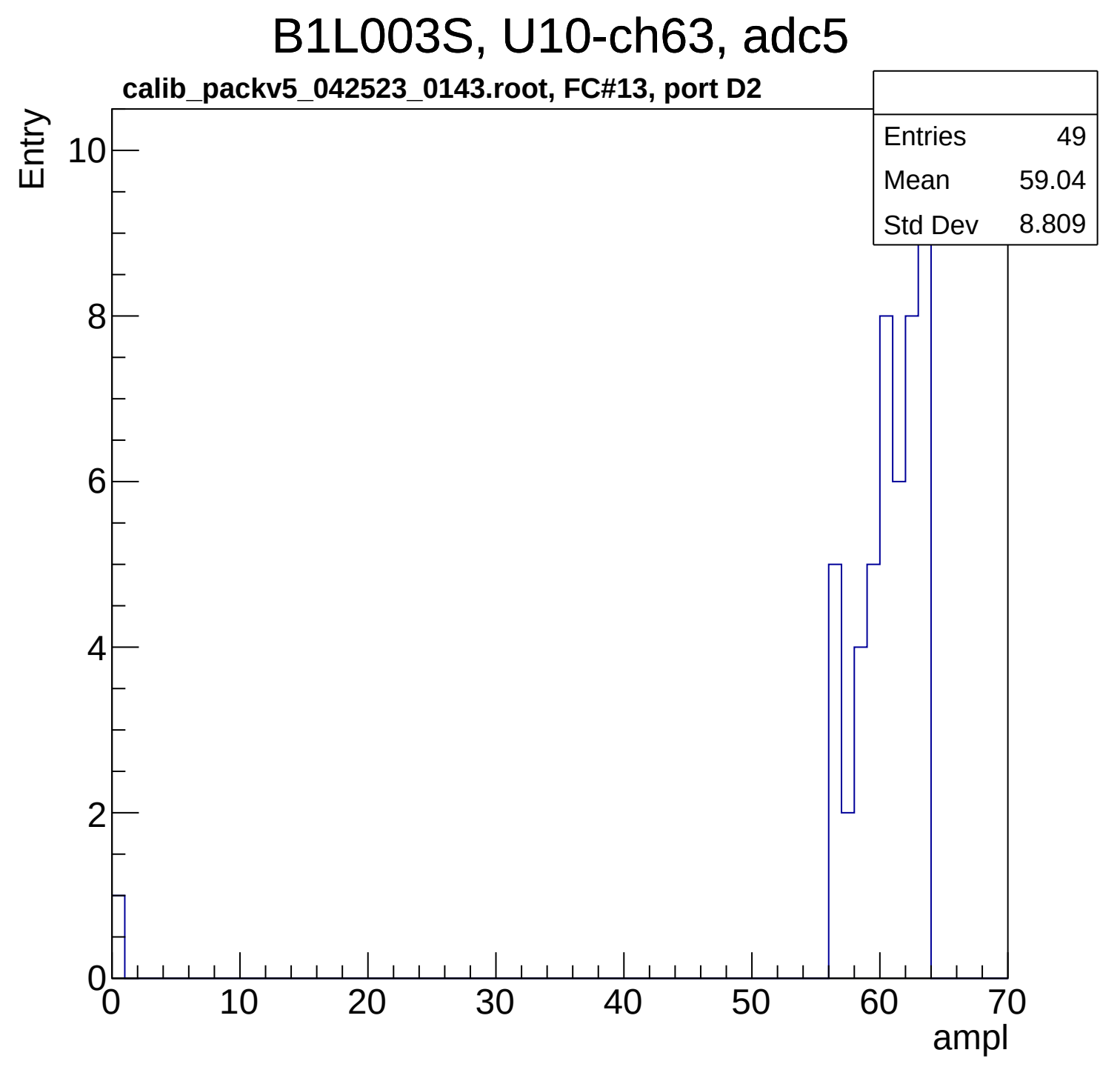

B1L003S, U10-ch63, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 29.65
Std Dev 5.658
Gaus mean : 30.6775
Gaus Width: 3.6374
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

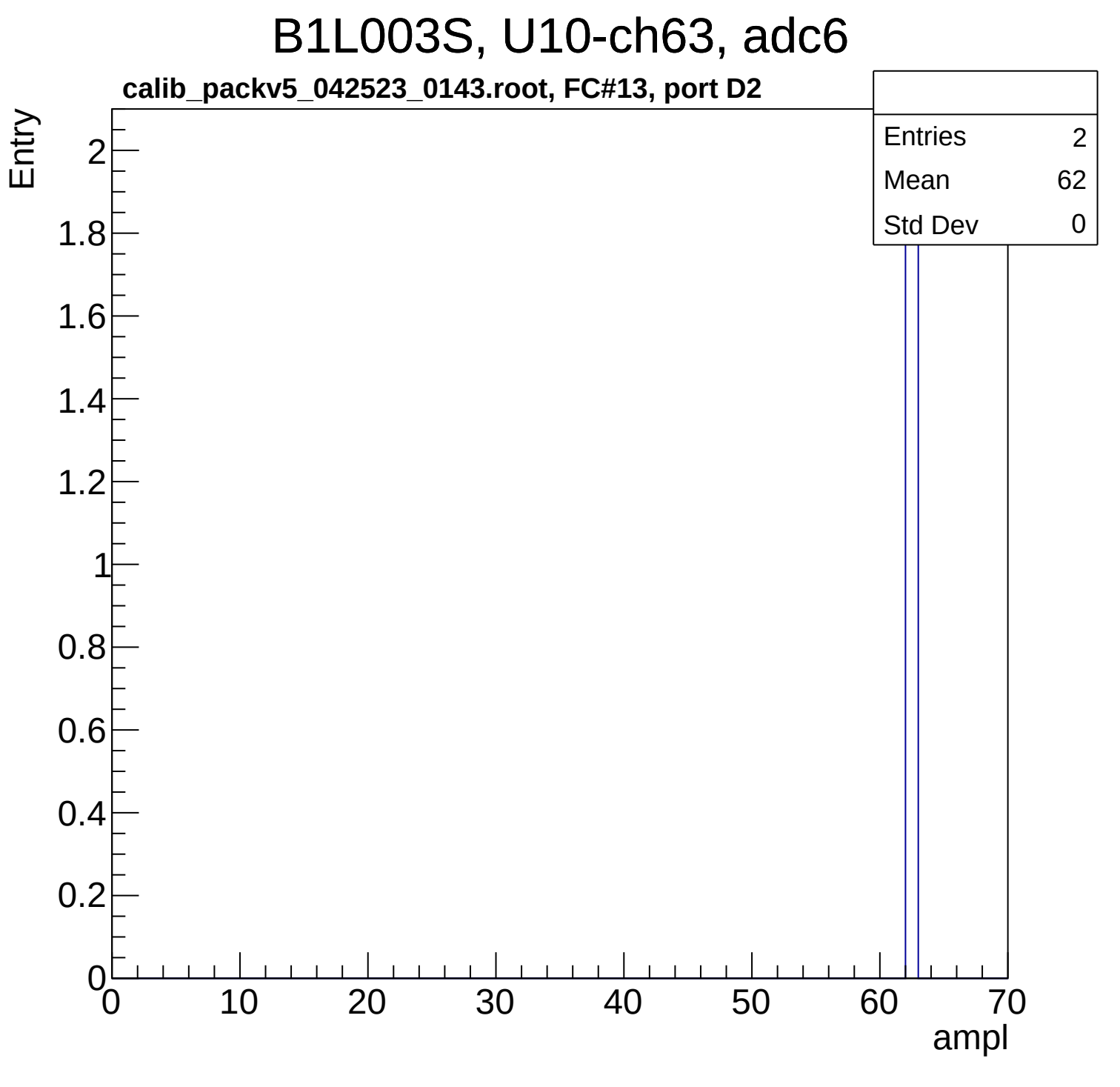

B1L003S, U10-ch63, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 38.08
Std Dev 2.641
Gaus mean : 37.8390
Gaus Width: 3.6238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

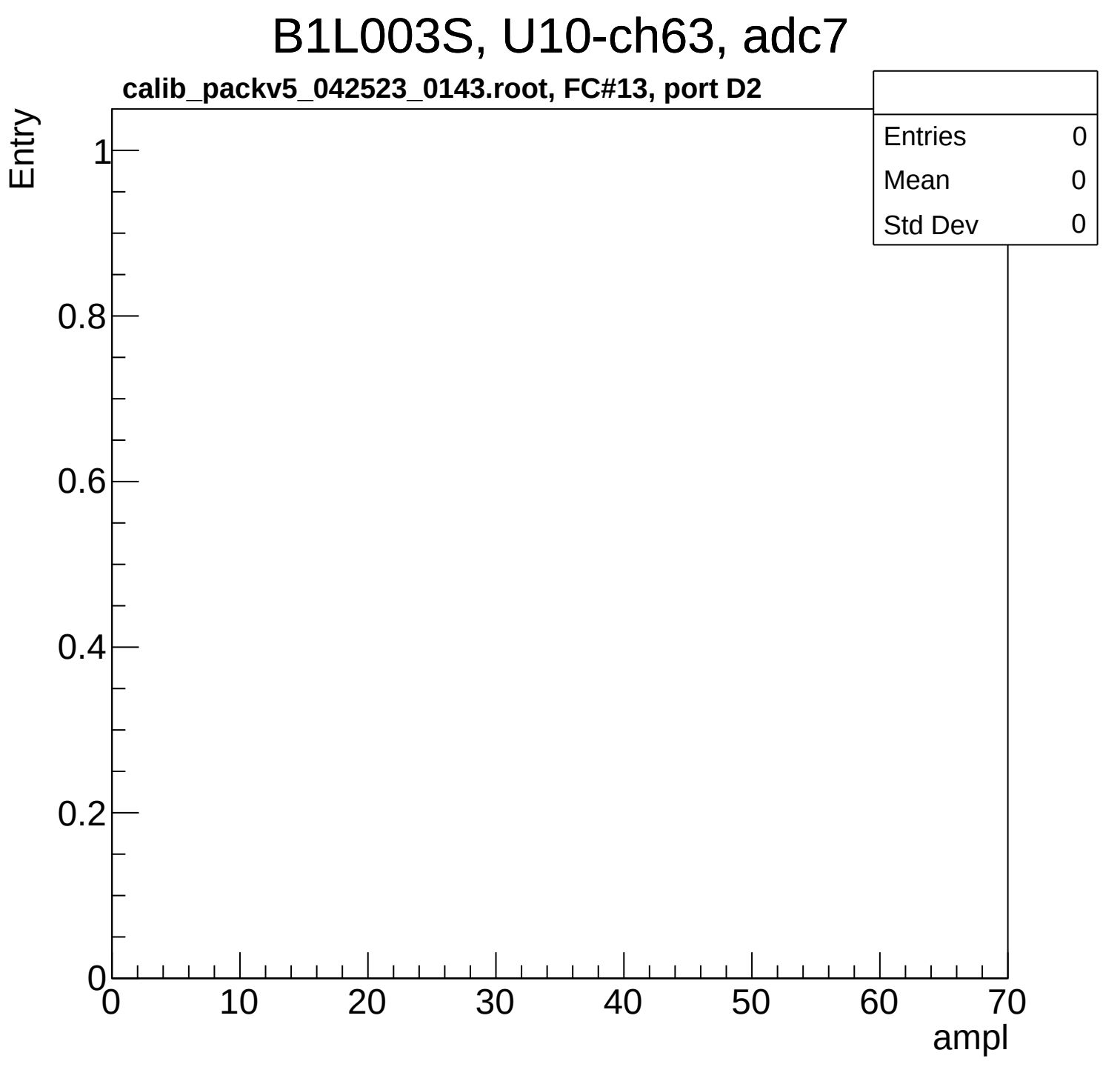

B1L003S, U10-ch63, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 43.77
Std Dev 2.785
Gaus mean : 44.6611
Gaus Width: 3.4562
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

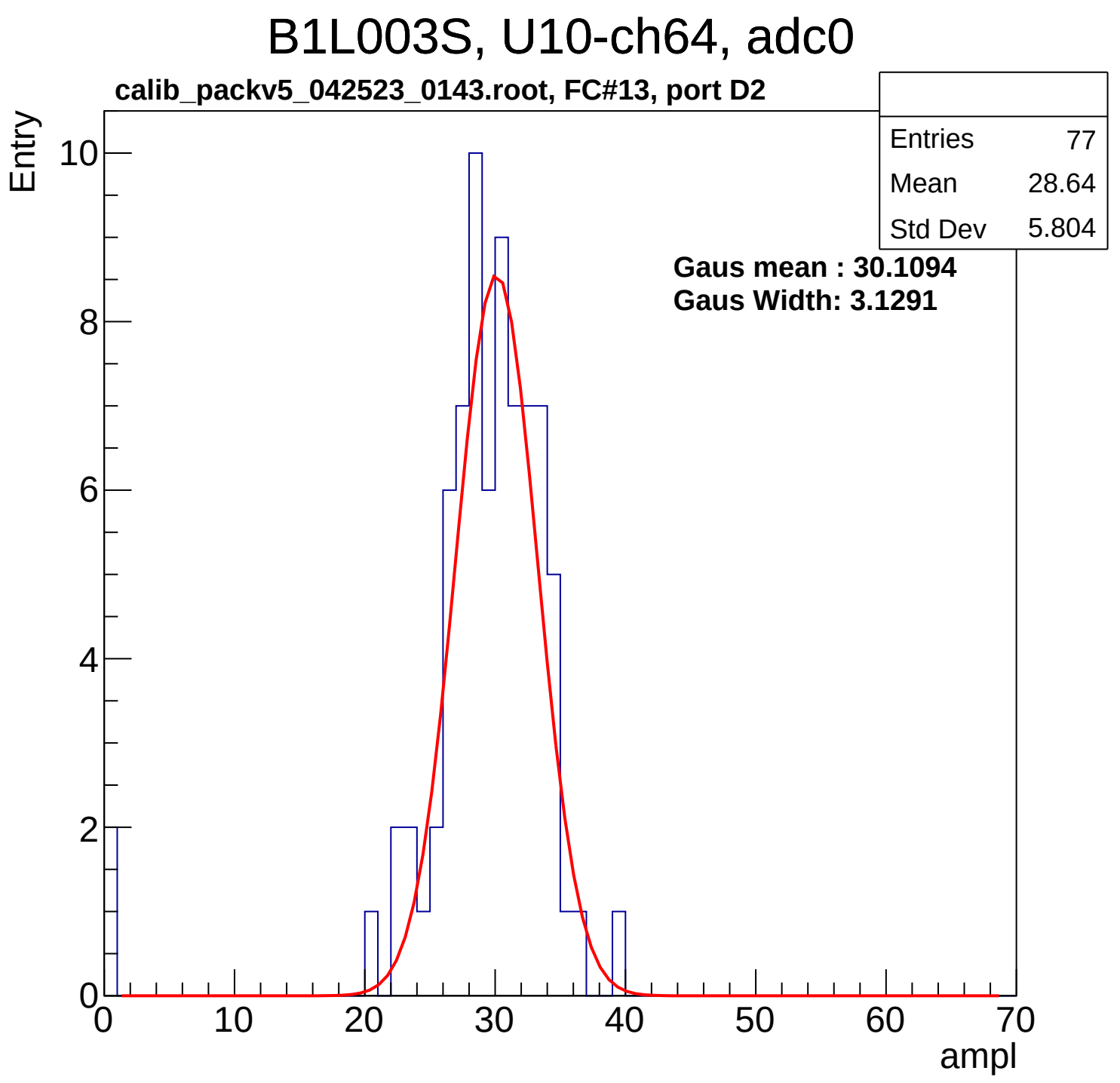

B1L003S, U10-ch63, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 50.22
Std Dev 3.548
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

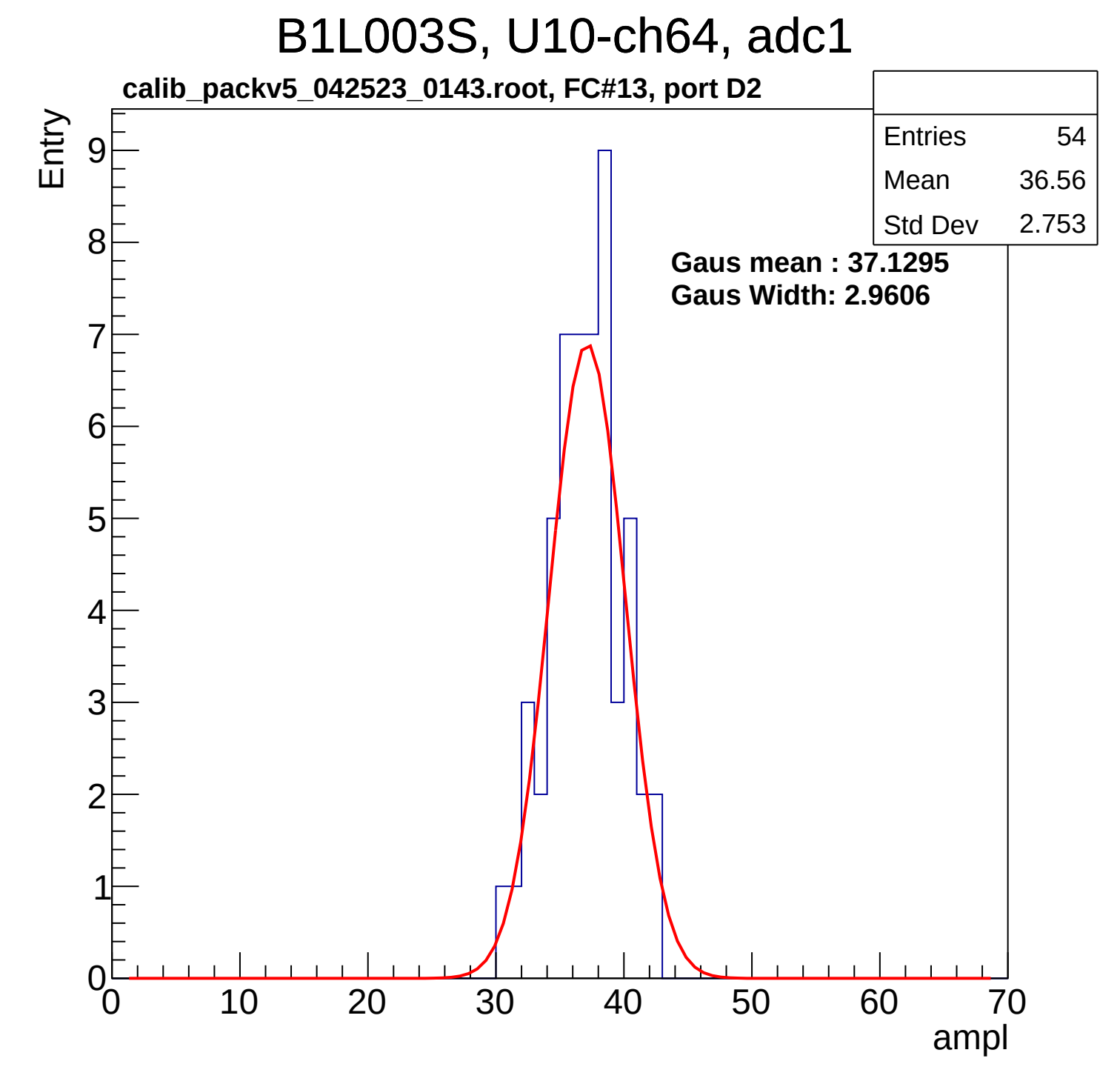

B1L003S, U10-ch63, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 55.89
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U10-ch63, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 59.04
Std Dev 8.809
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch63, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch63, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch64, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.64
Std Dev 5.804
Gaus mean : 30.1094
Gaus Width: 3.1291
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch64, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 36.56
Std Dev 2.753
Gaus mean : 37.1295
Gaus Width: 2.9606
Entry
ampl
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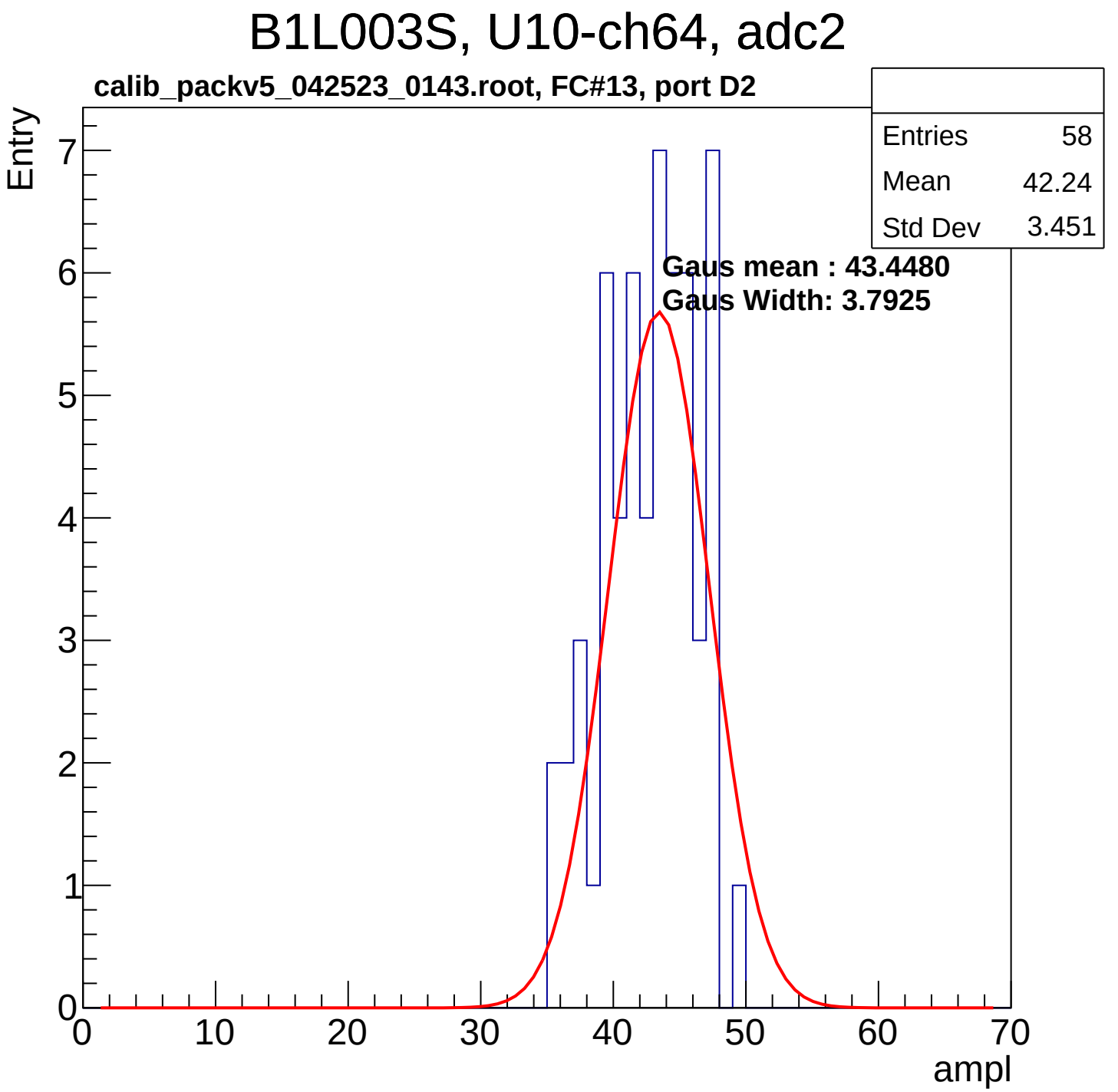
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch64, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.24
Std Dev 3.451
Gaus mean : 43.4480
Gaus Width: 3.7925
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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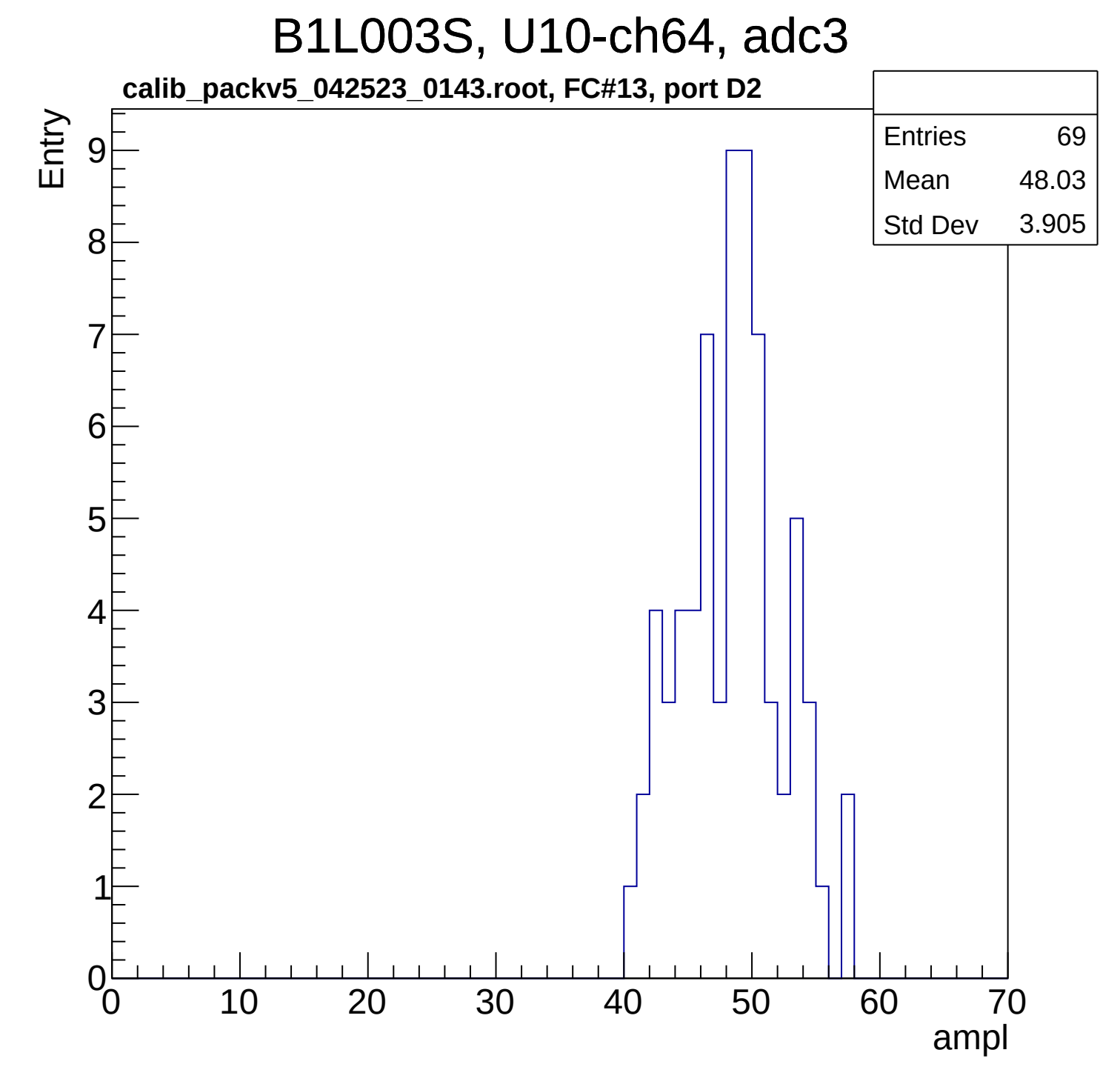
B1L003S, U10-ch64, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.03
Std Dev 3.905
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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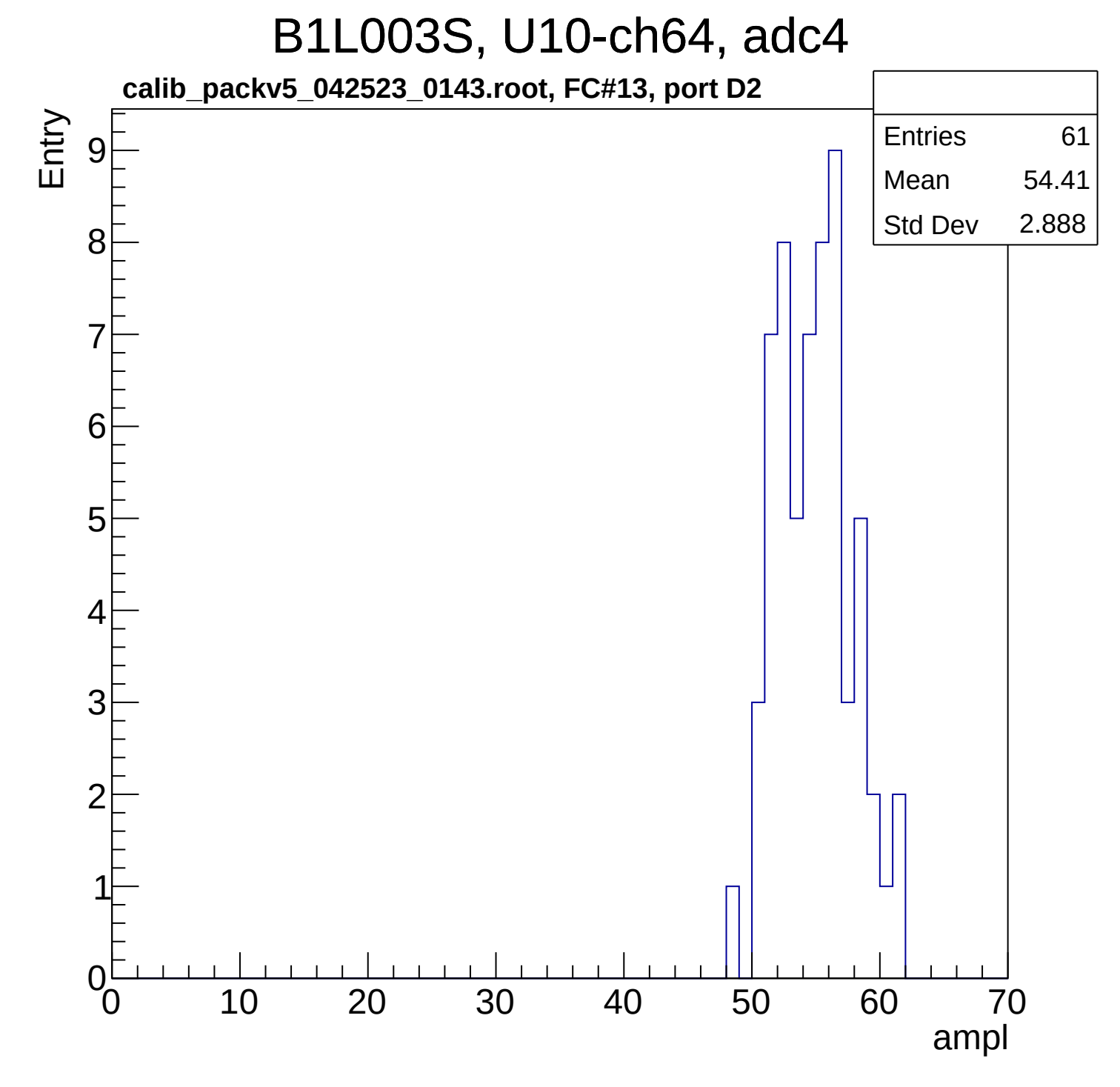
B1L003S, U10-ch64, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.41
Std Dev 2.888
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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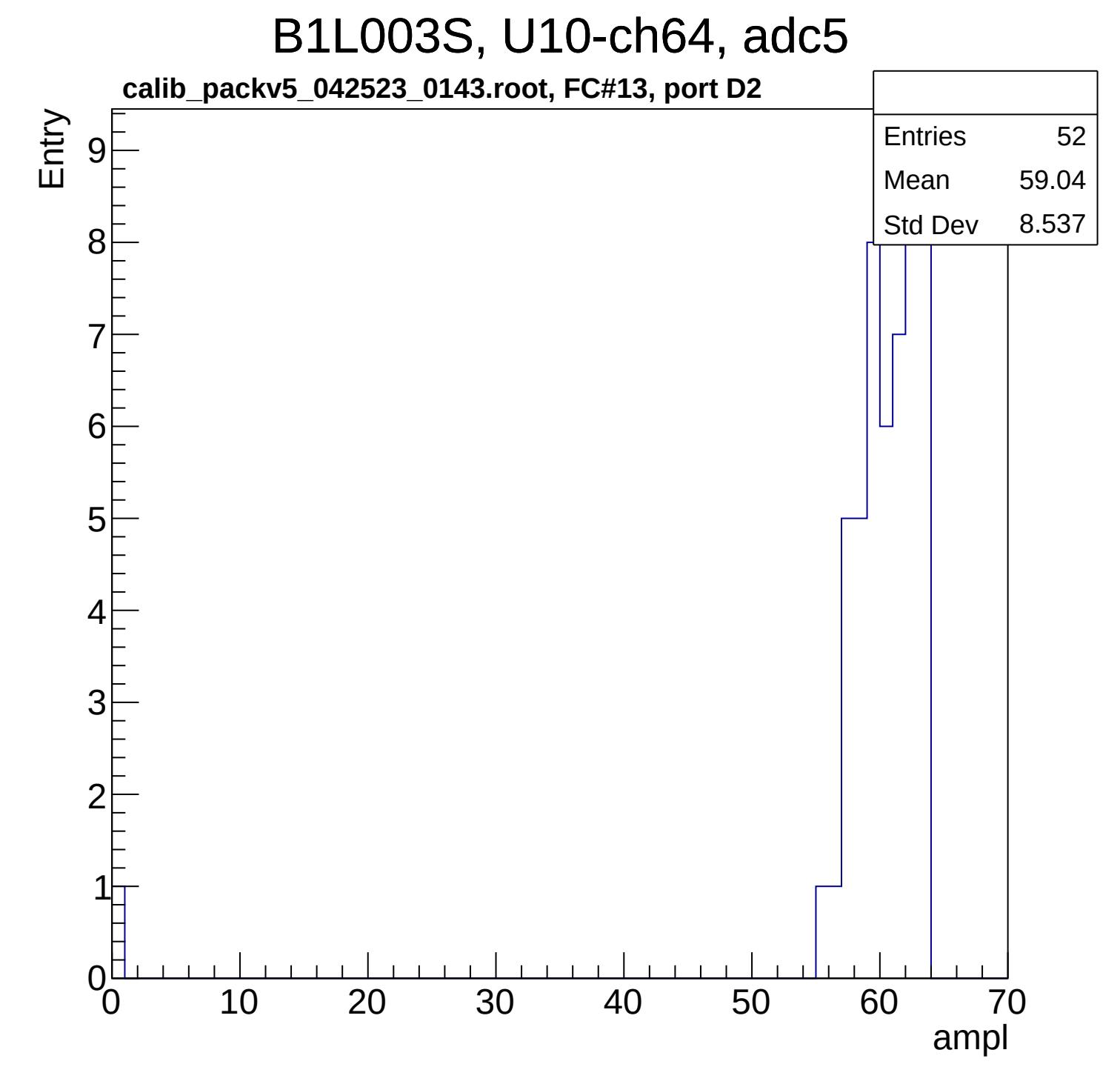
B1L003S, U10-ch64, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.04
Std Dev 8.537
Entry
ampl
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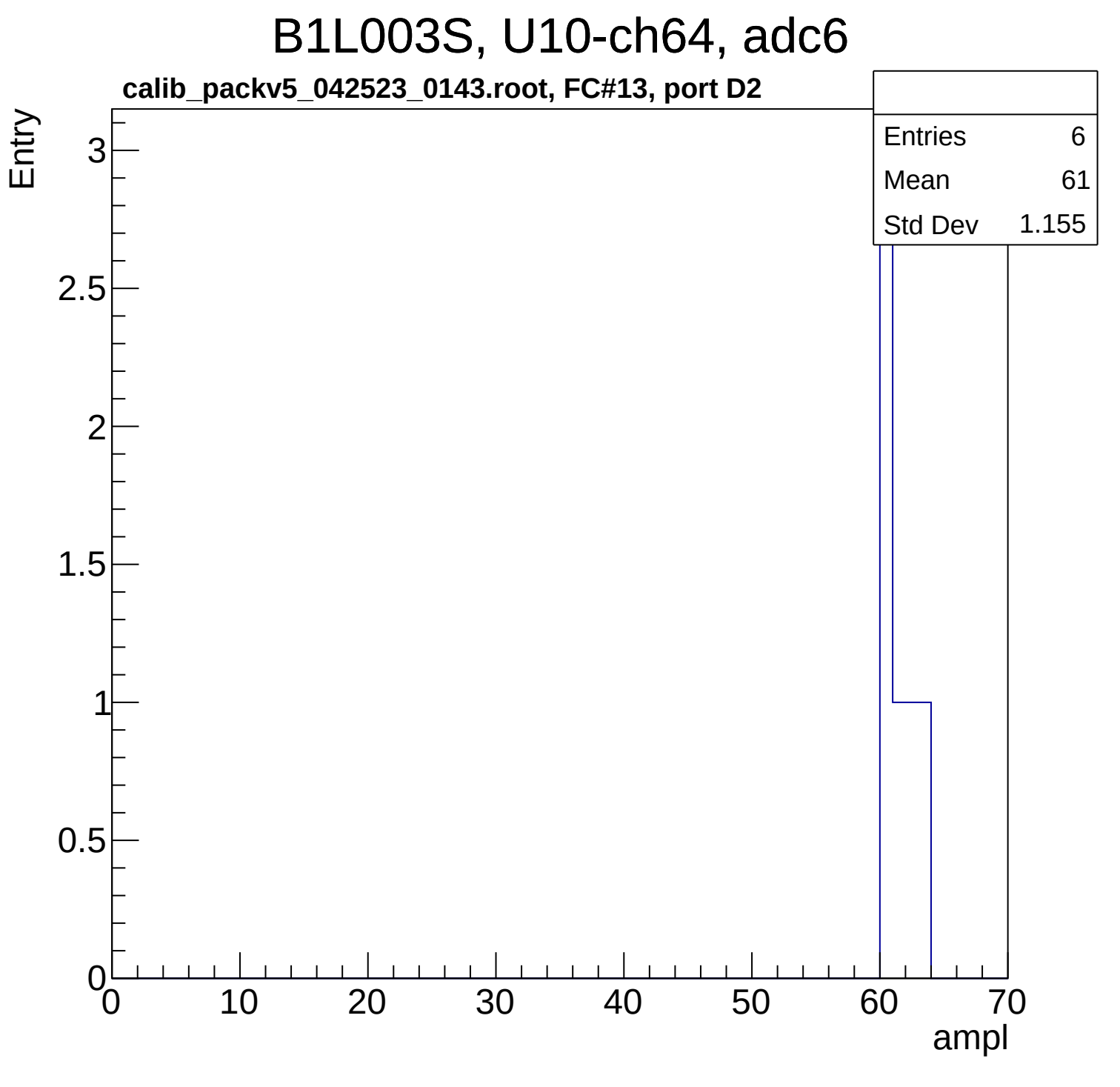
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch64, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61
Std Dev 1.155
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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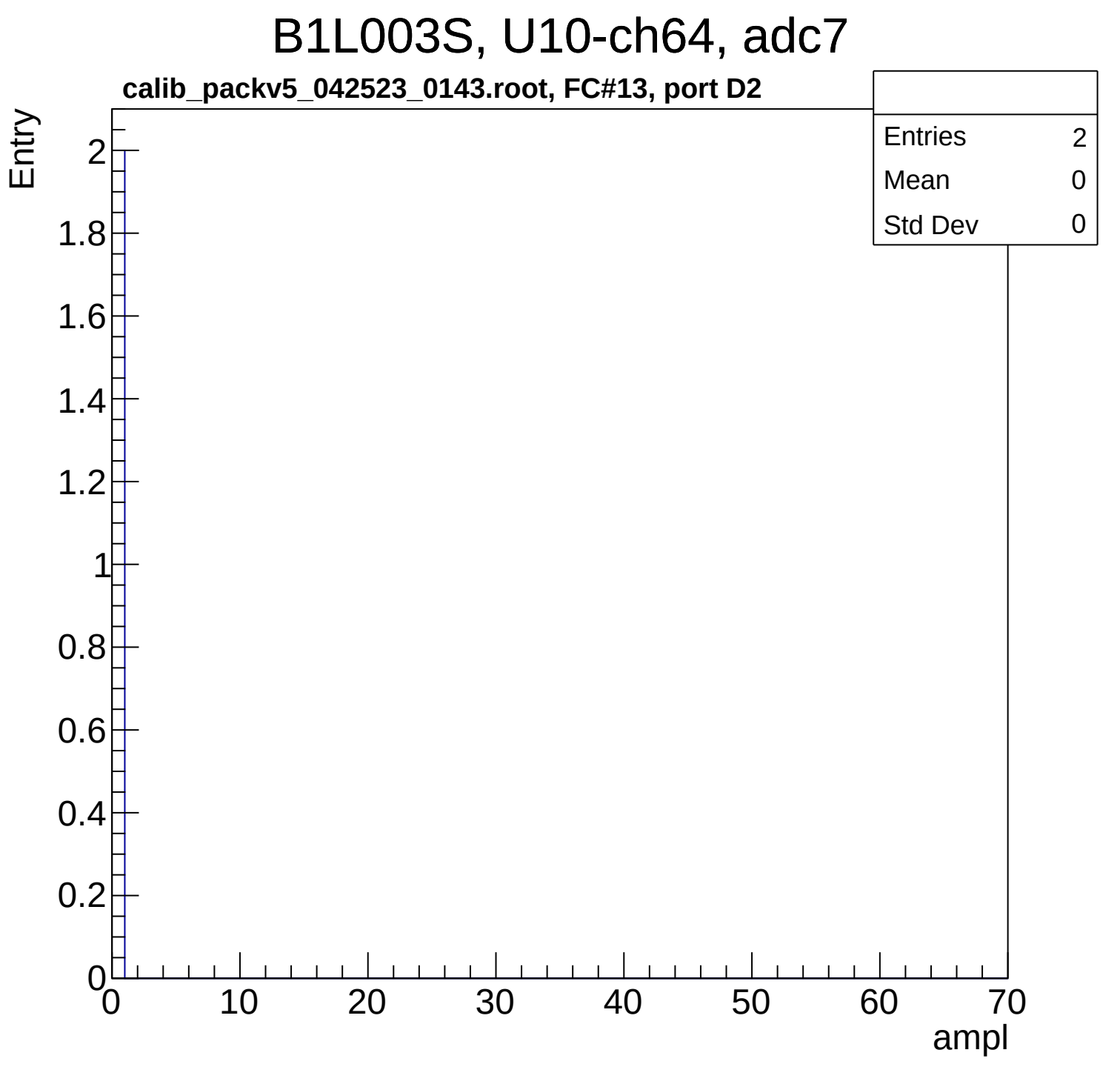
50
60
70


B1L003S, U10-ch64, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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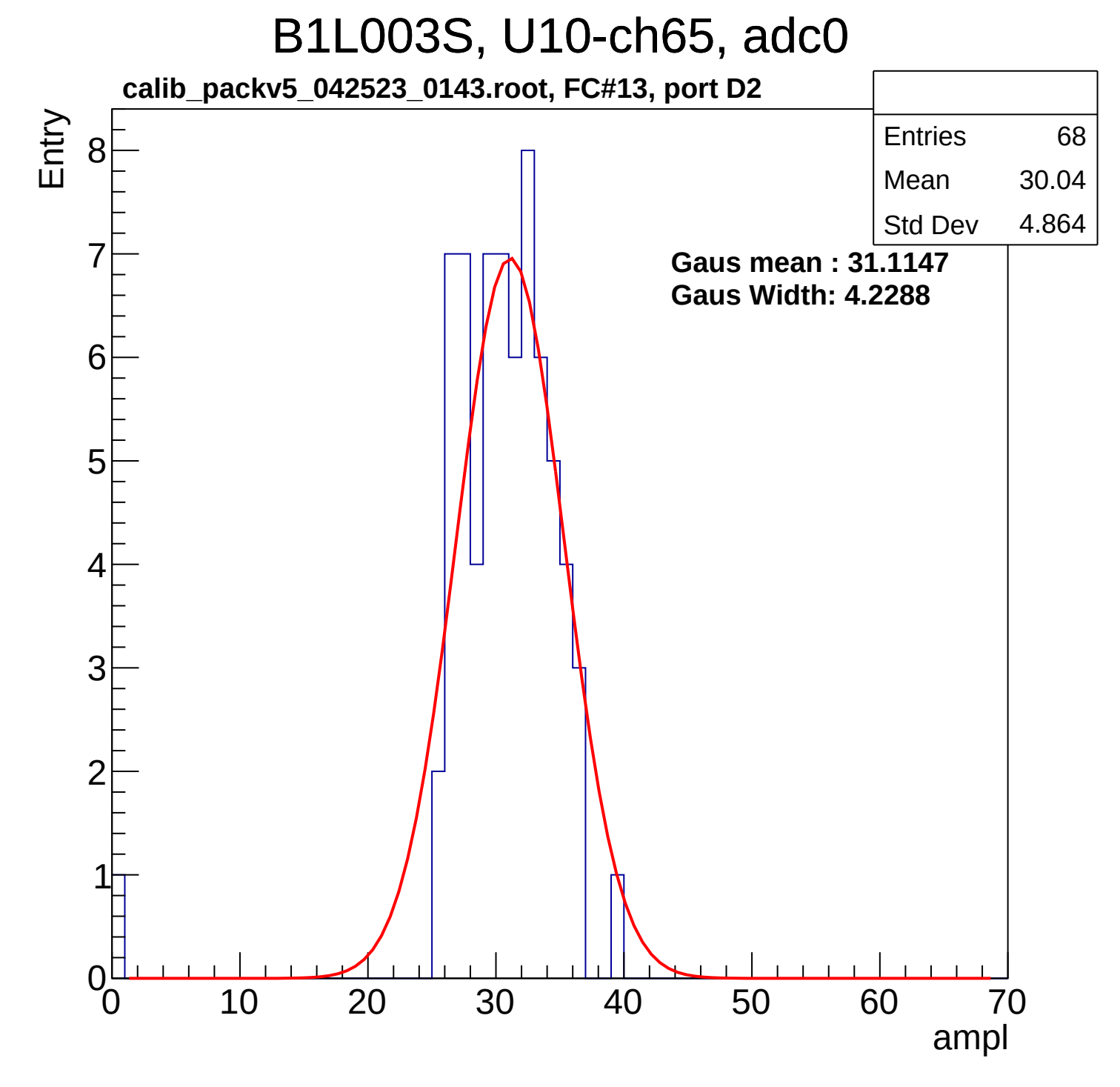
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch65, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.04
Std Dev 4.864
Gaus mean : 31.1147
Gaus Width: 4.2288
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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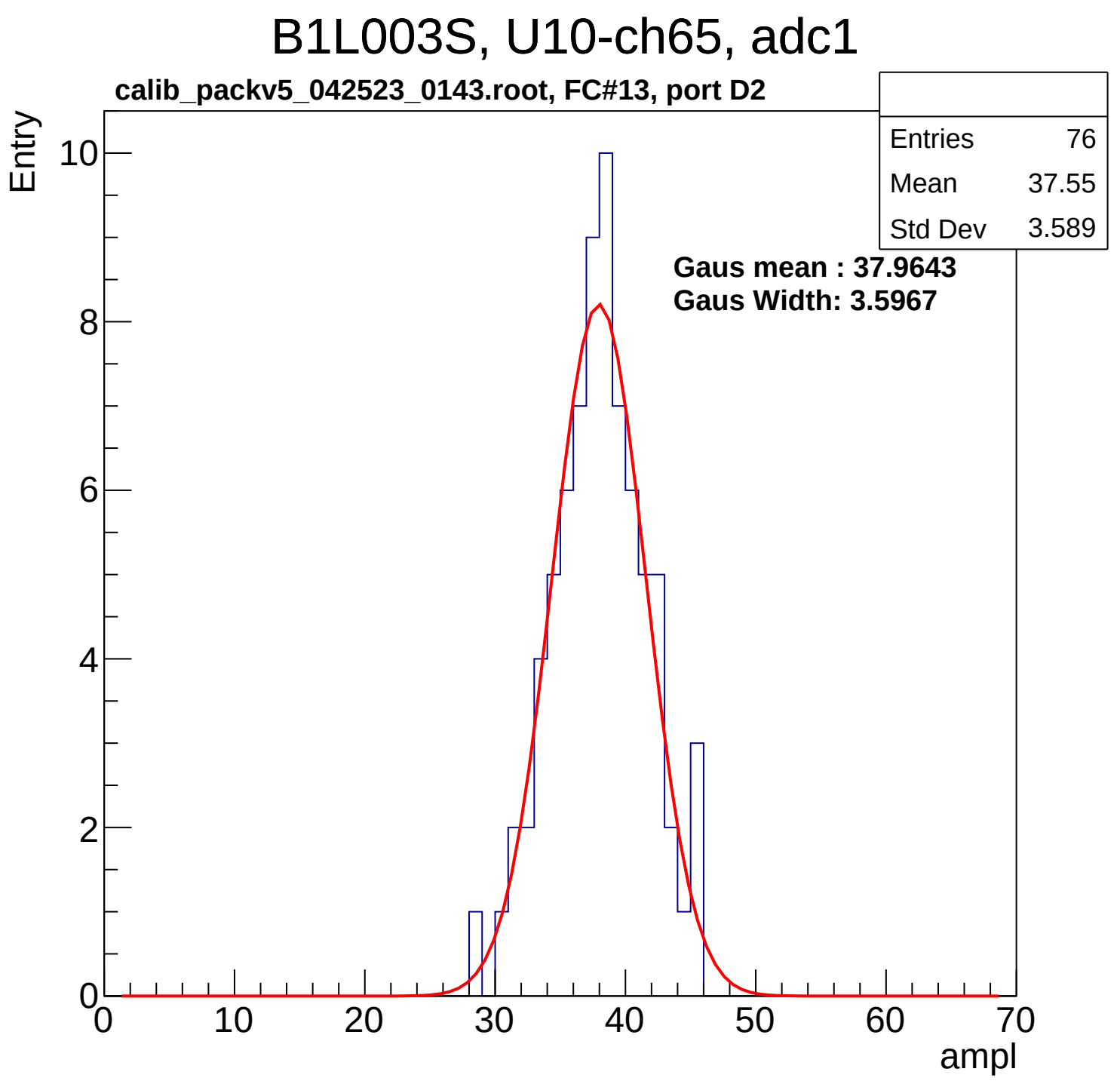
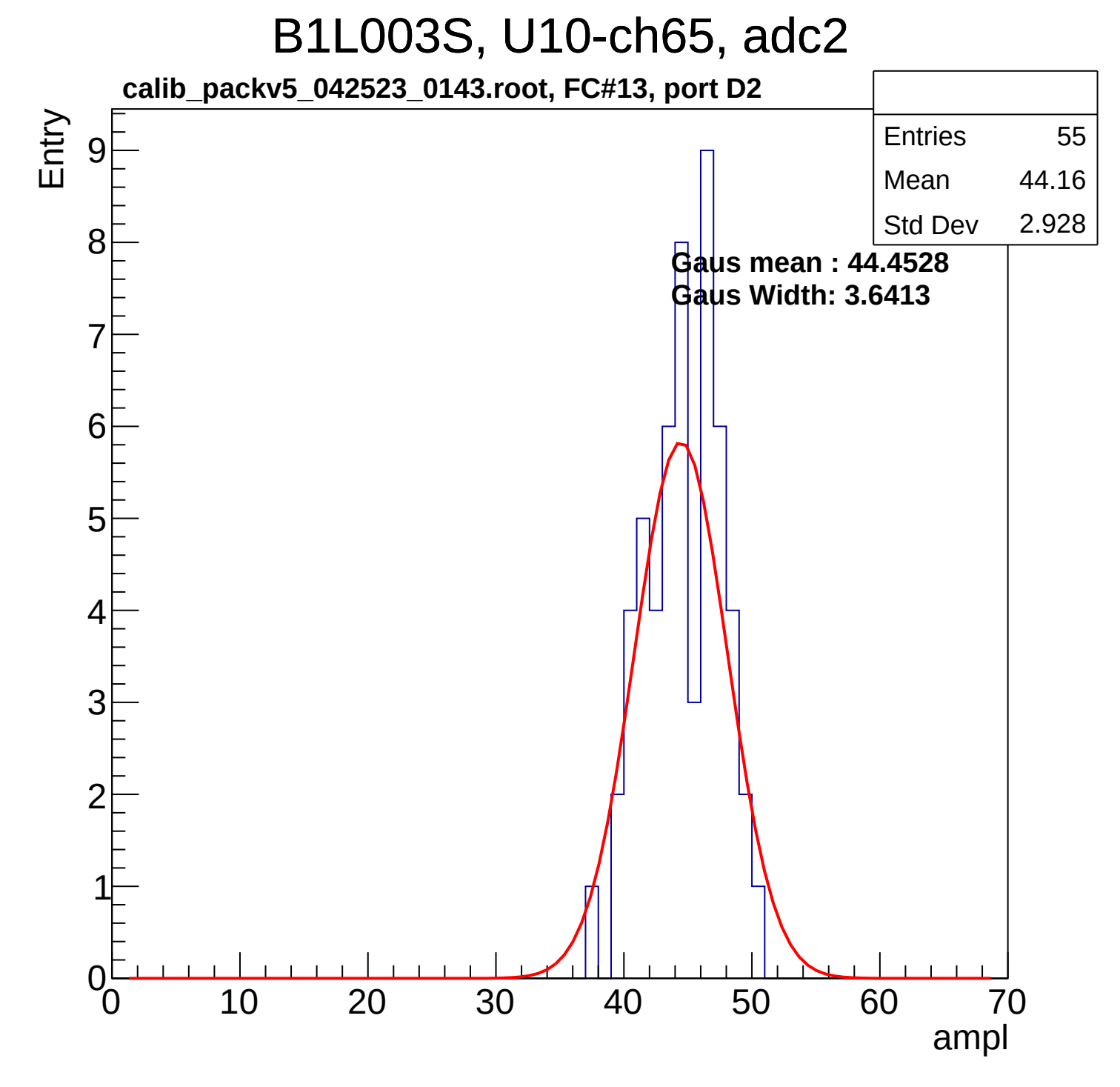

B1L003S, U10-ch65, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 37.55
Std Dev 3.589
Gaus mean : 37.9643
Gaus Width: 3.5967
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch65, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 44.16
Std Dev 2.928
Gaus mean : 44.4528
Gaus Width: 3.6413
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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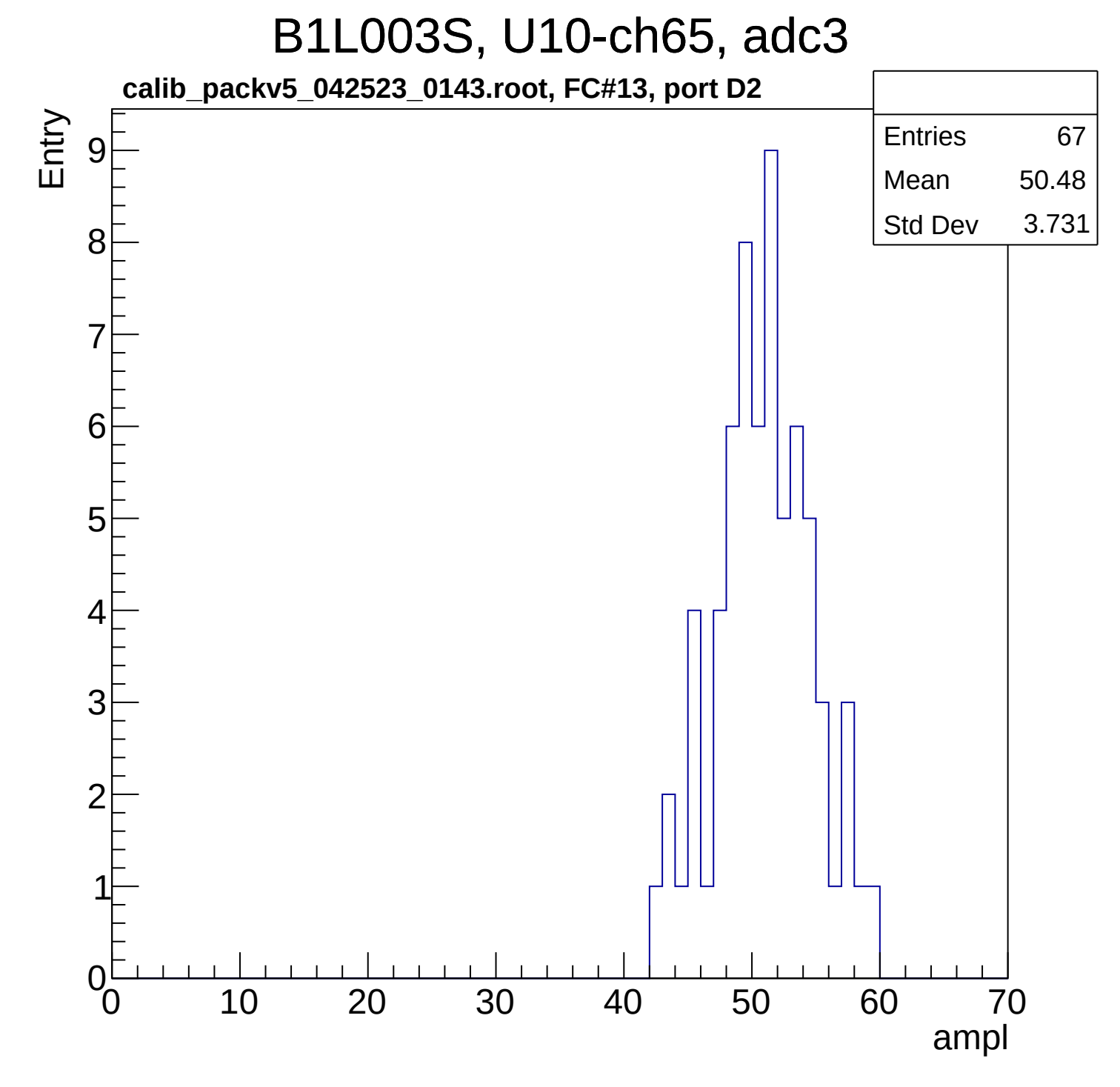
B1L003S, U10-ch65, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 50.48
Std Dev 3.731
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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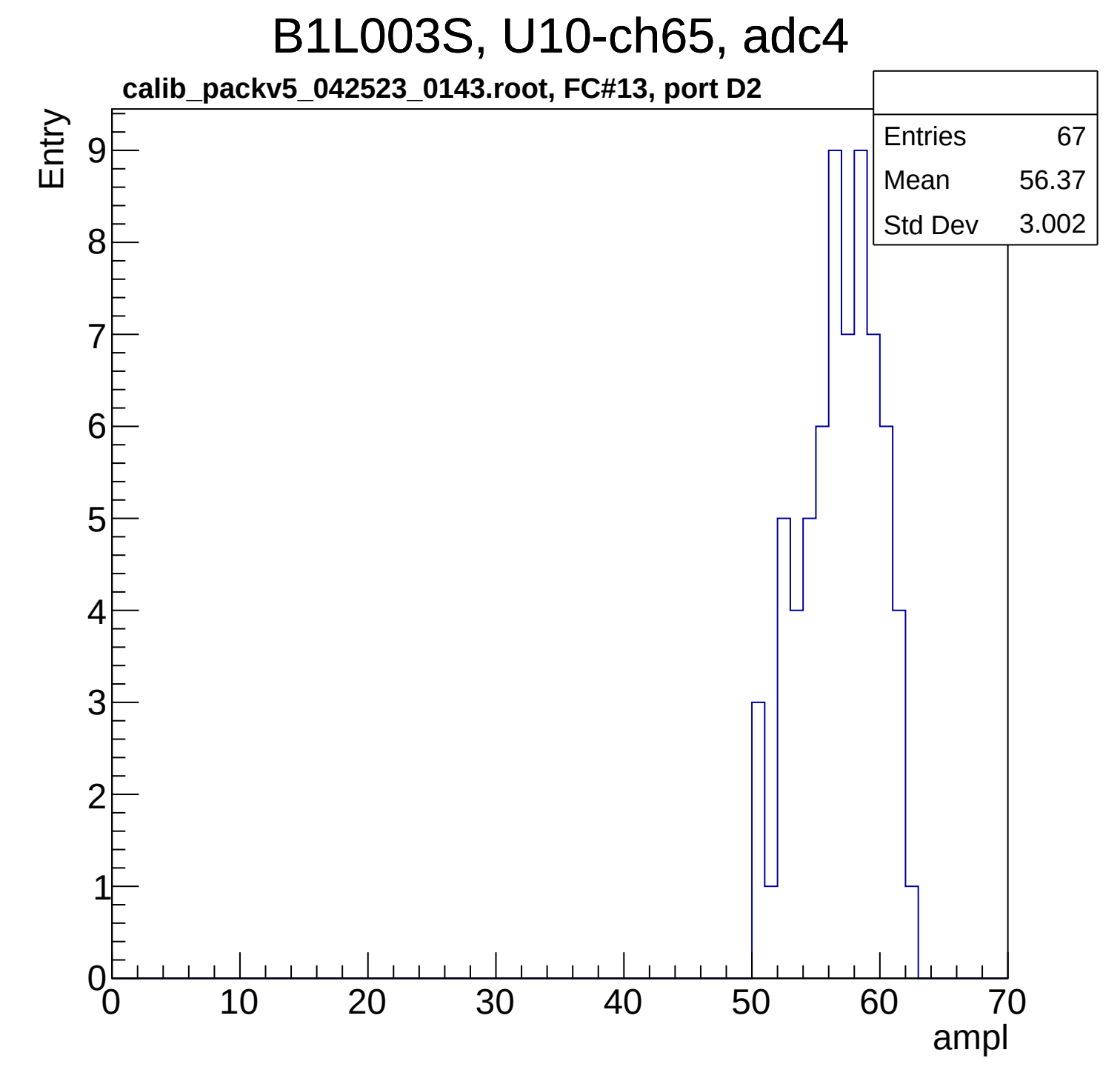
B1L003S, U10-ch65, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 56.37
Std Dev 3.002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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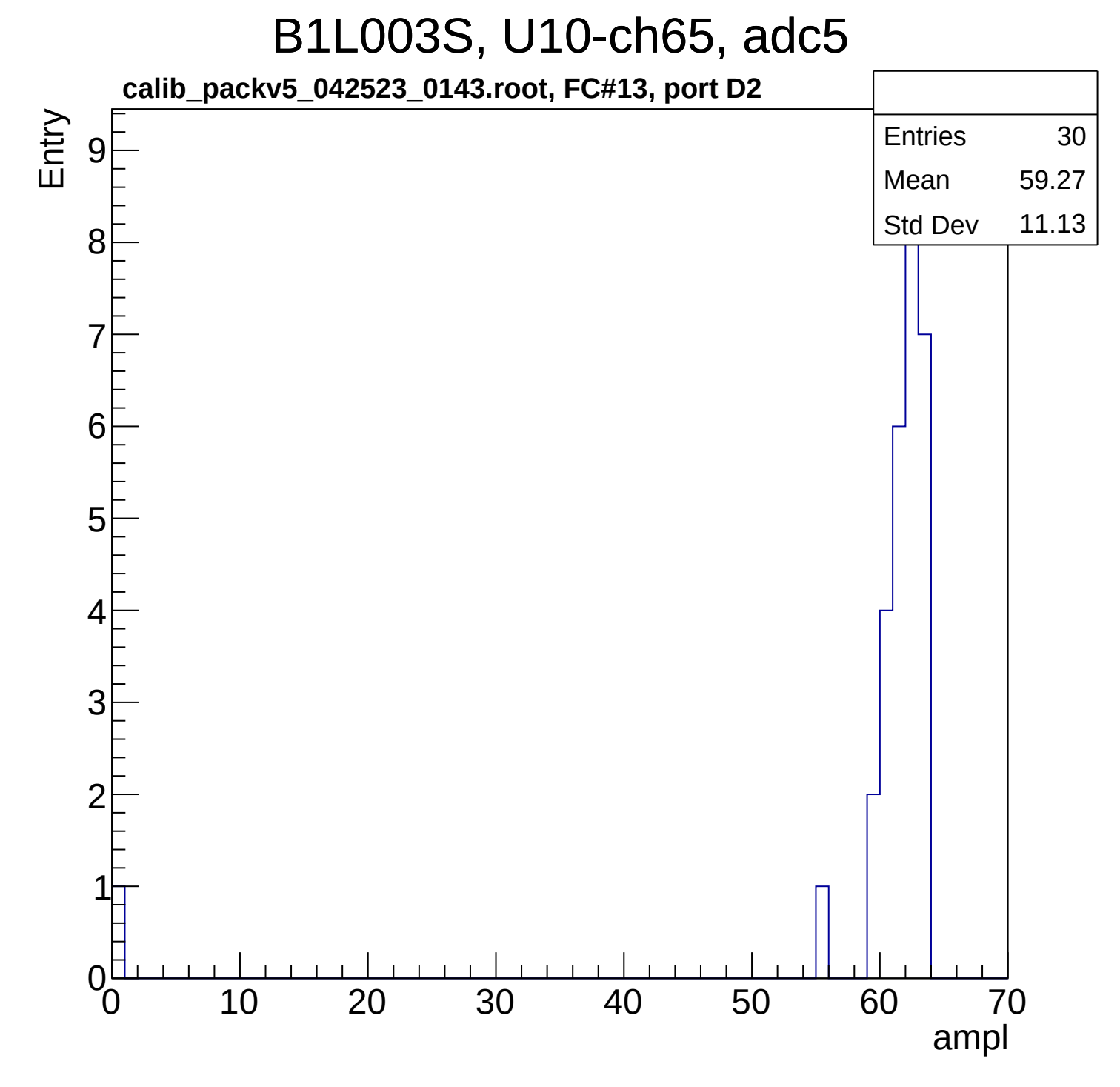
B1L003S, U10-ch65, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 59.27
Std Dev 11.13
Entry
ampl
0
1
2
3
4
5
6
7
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0
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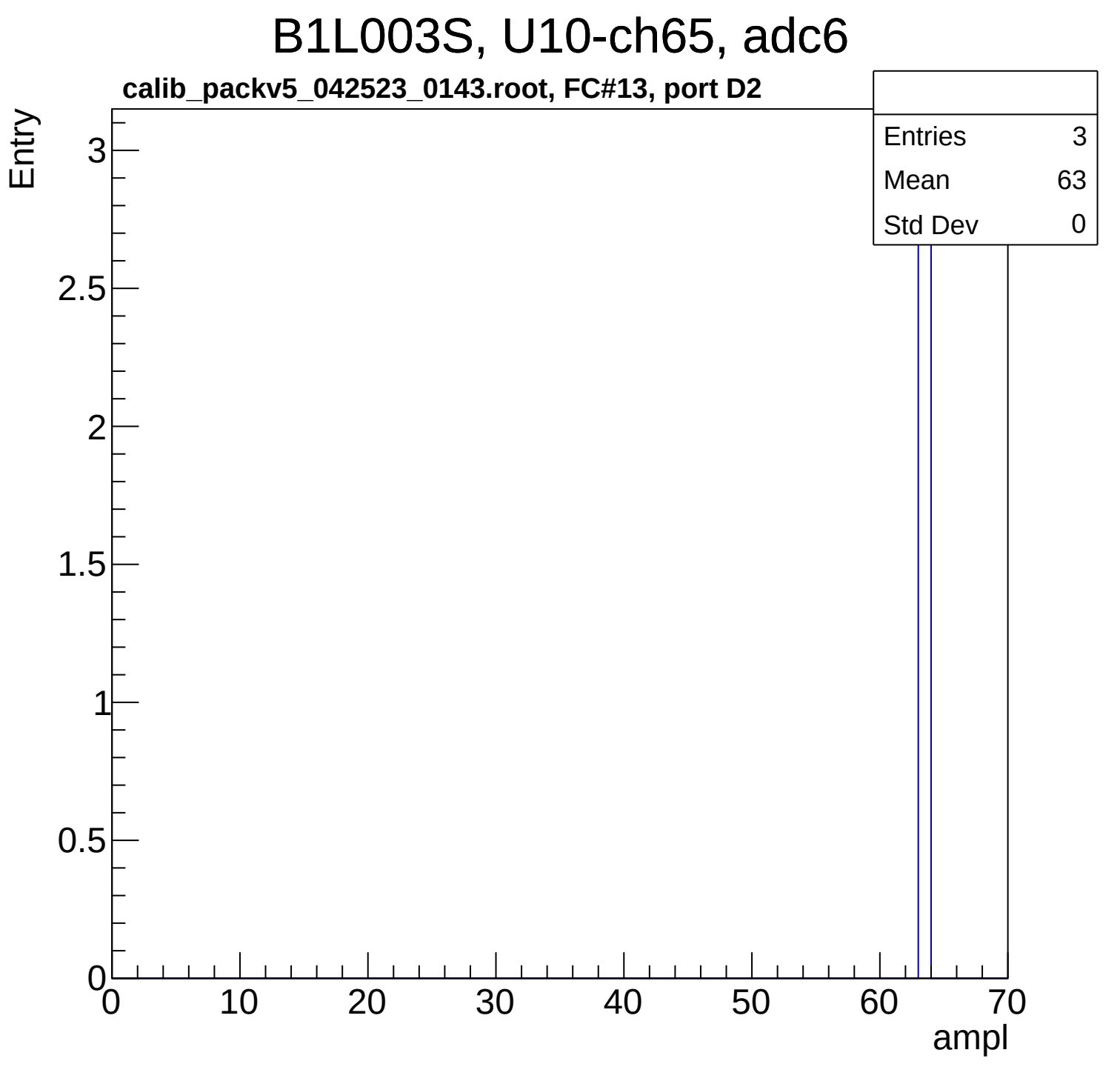
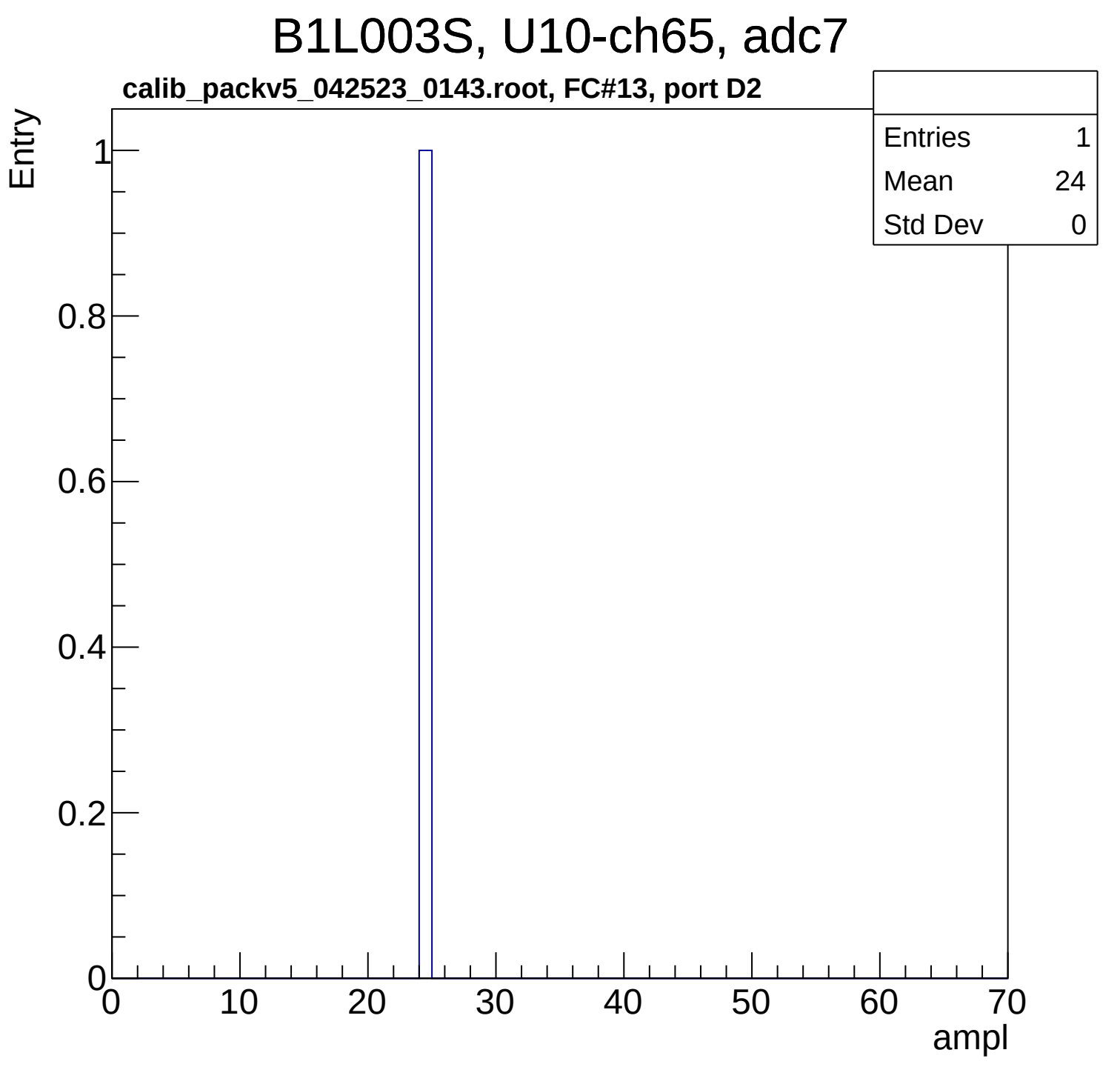
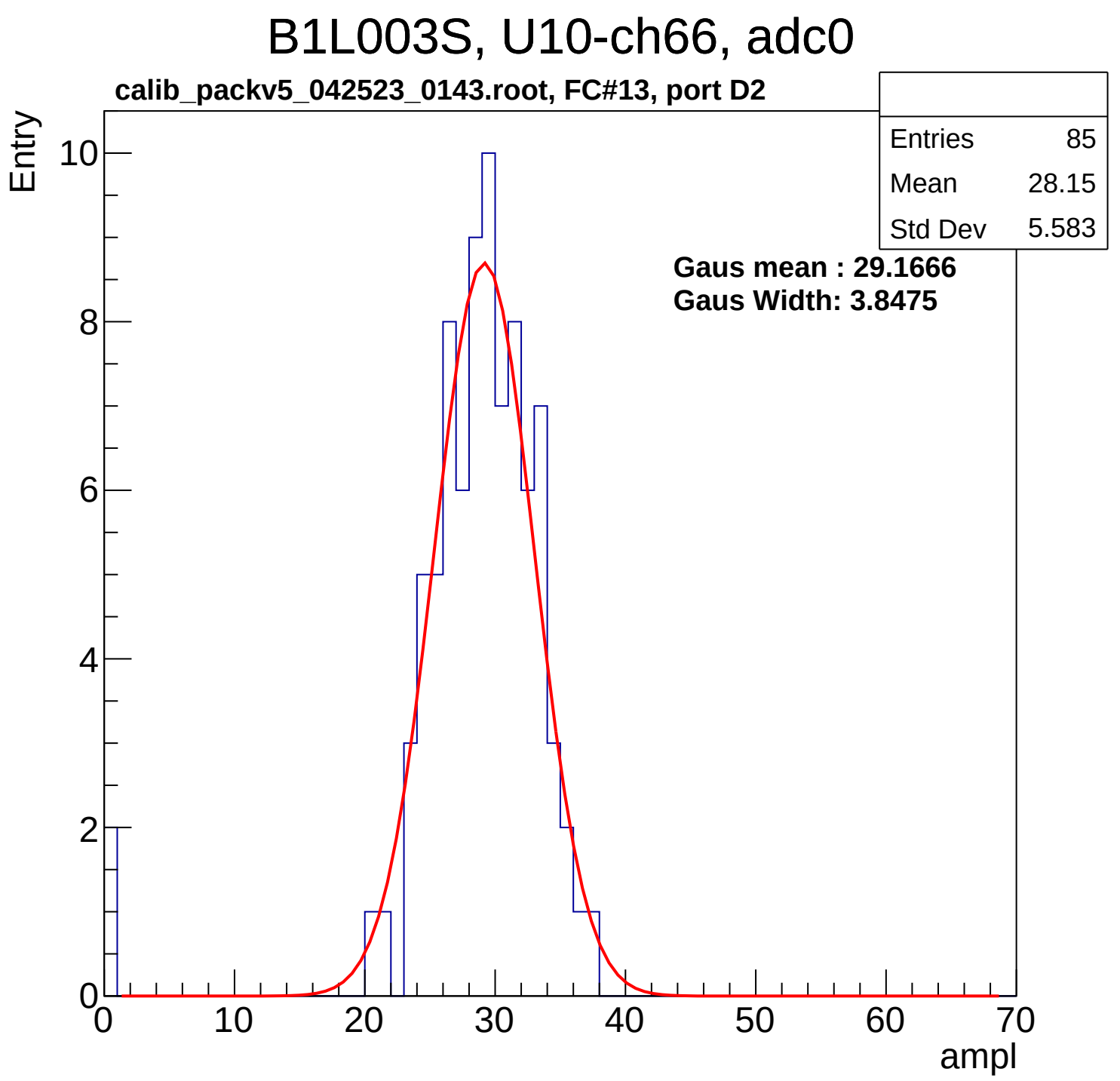
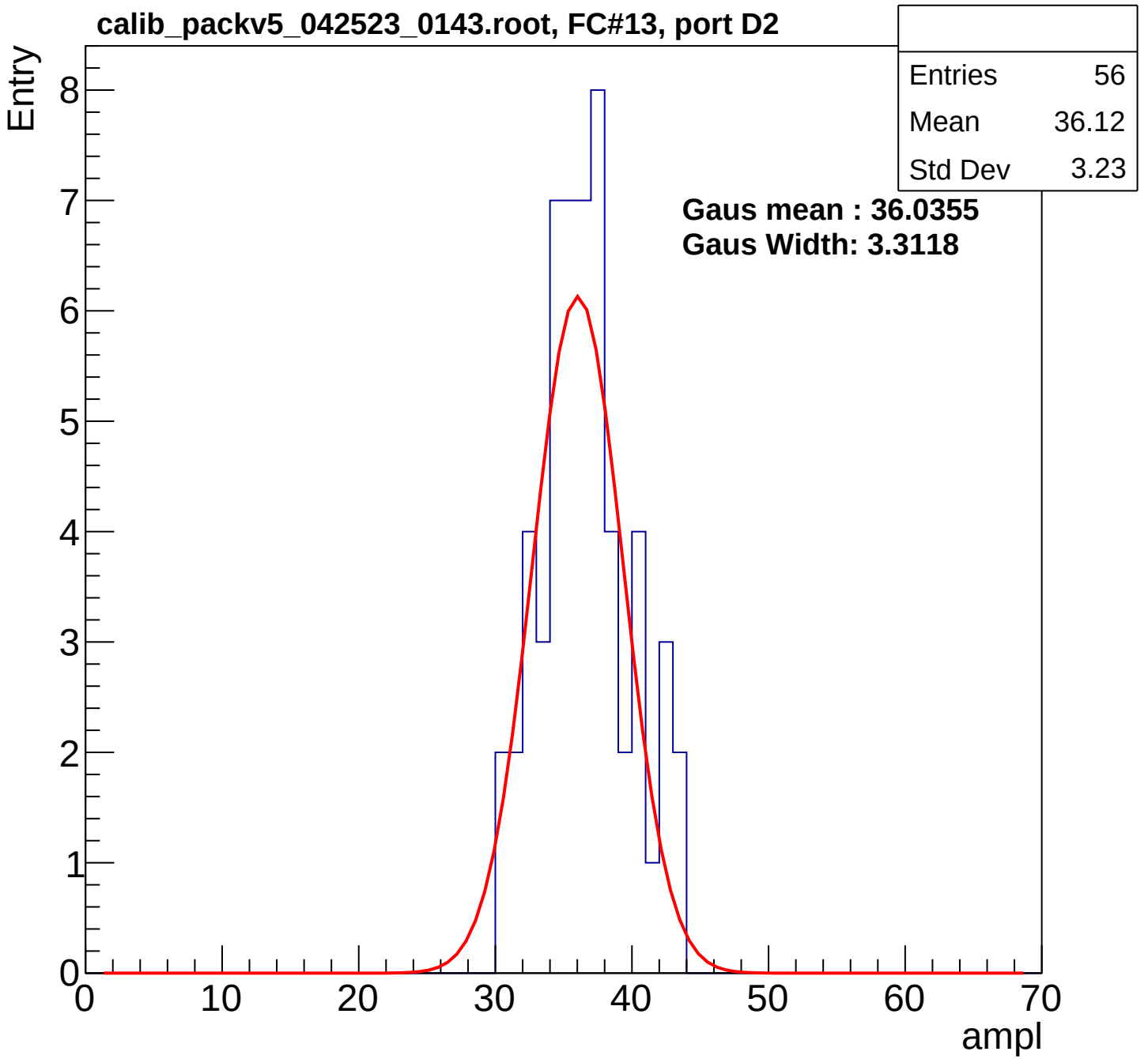

B1L003S, U10-ch65, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch65, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch66, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 28.15
Std Dev 5.583
Gaus mean : 29.1666
Gaus Width: 3.8475
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch66, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 36.12
Std Dev 3.23
Gaus mean : 36.0355
Gaus Width: 3.3118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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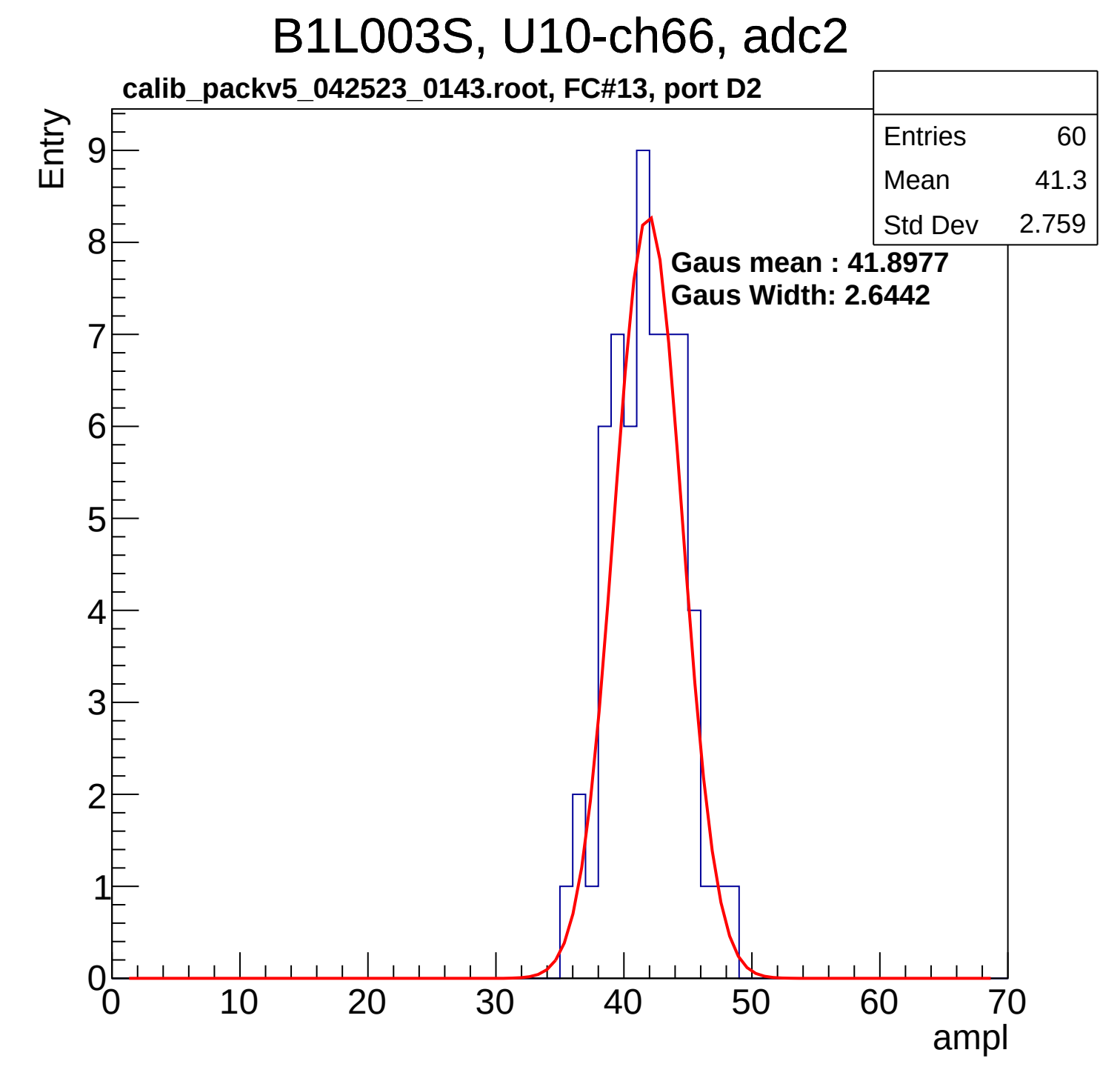
B1L003S, U10-ch66, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.3
Std Dev 2.759
Gaus mean : 41.8977
Gaus Width: 2.6442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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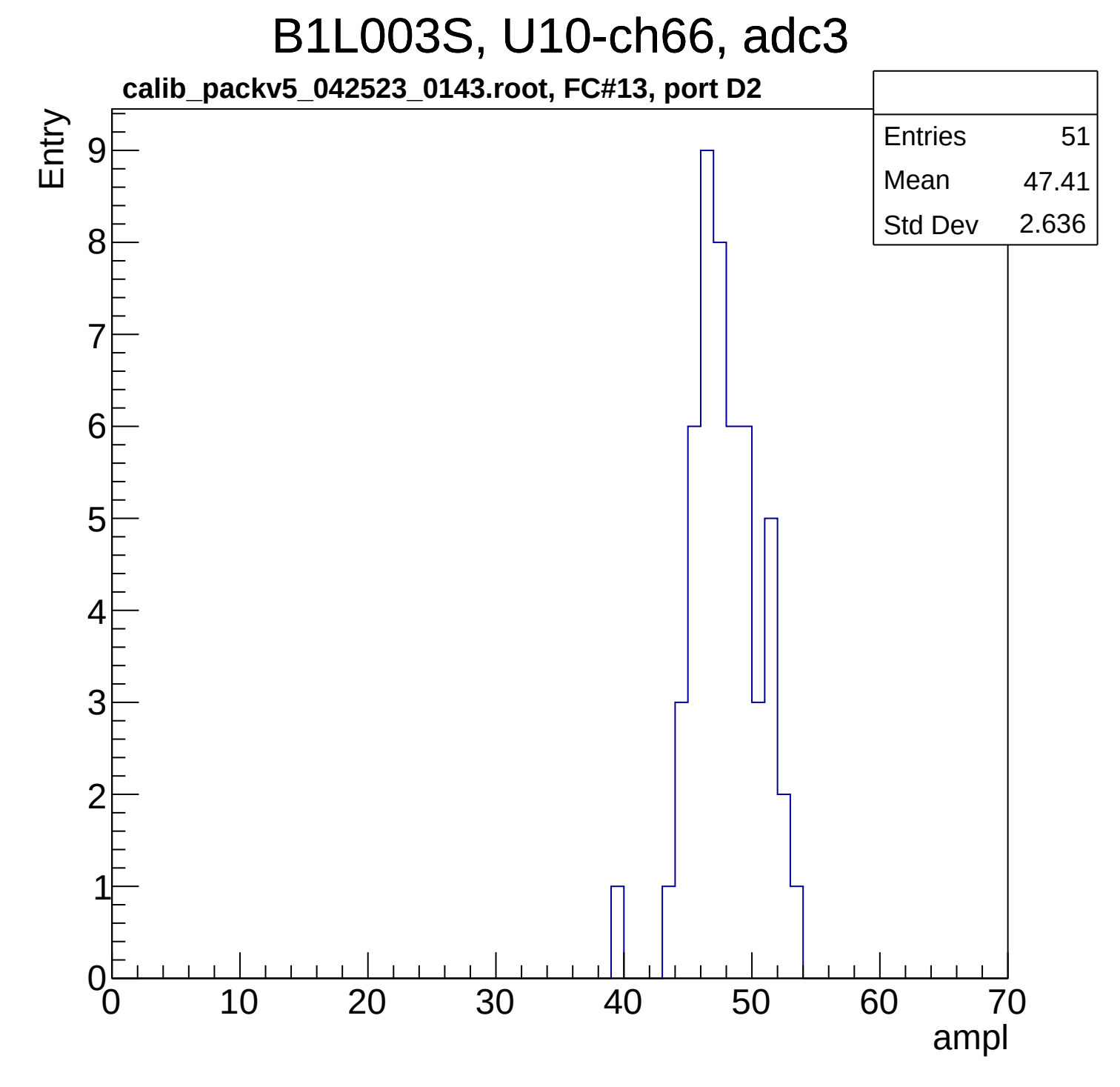
B1L003S, U10-ch66, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 47.41
Std Dev 2.636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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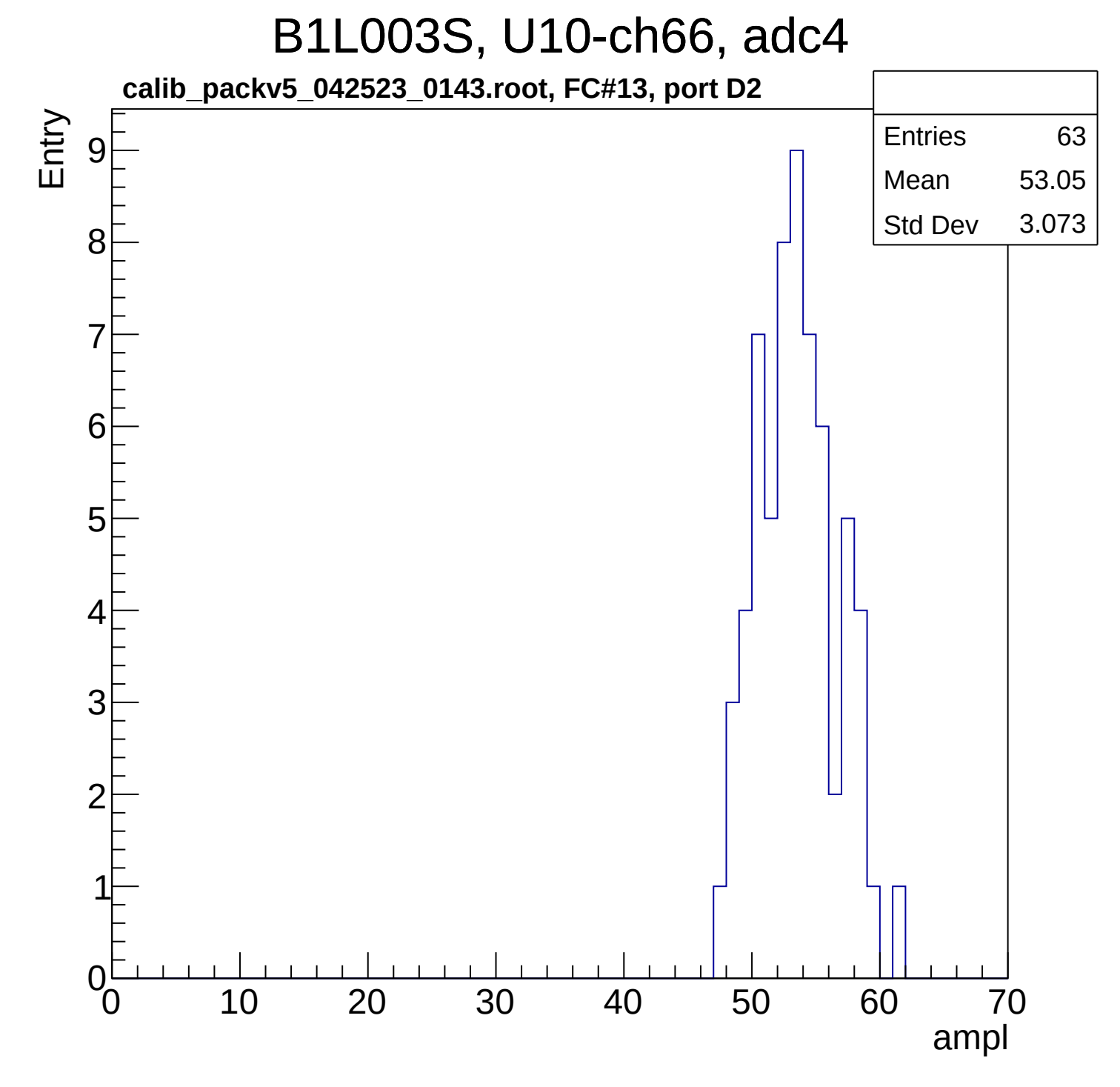
B1L003S, U10-ch66, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 53.05
Std Dev 3.073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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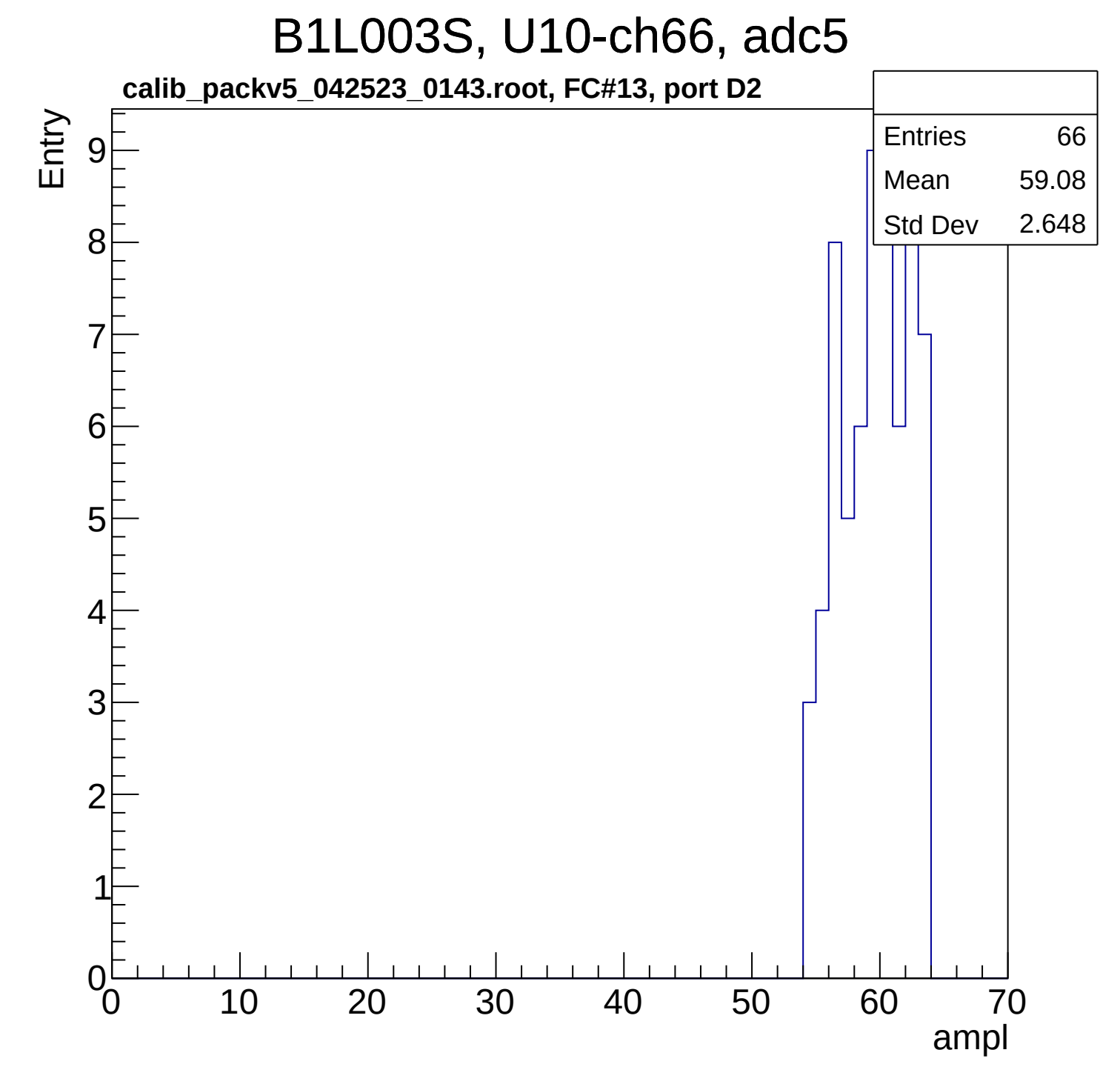
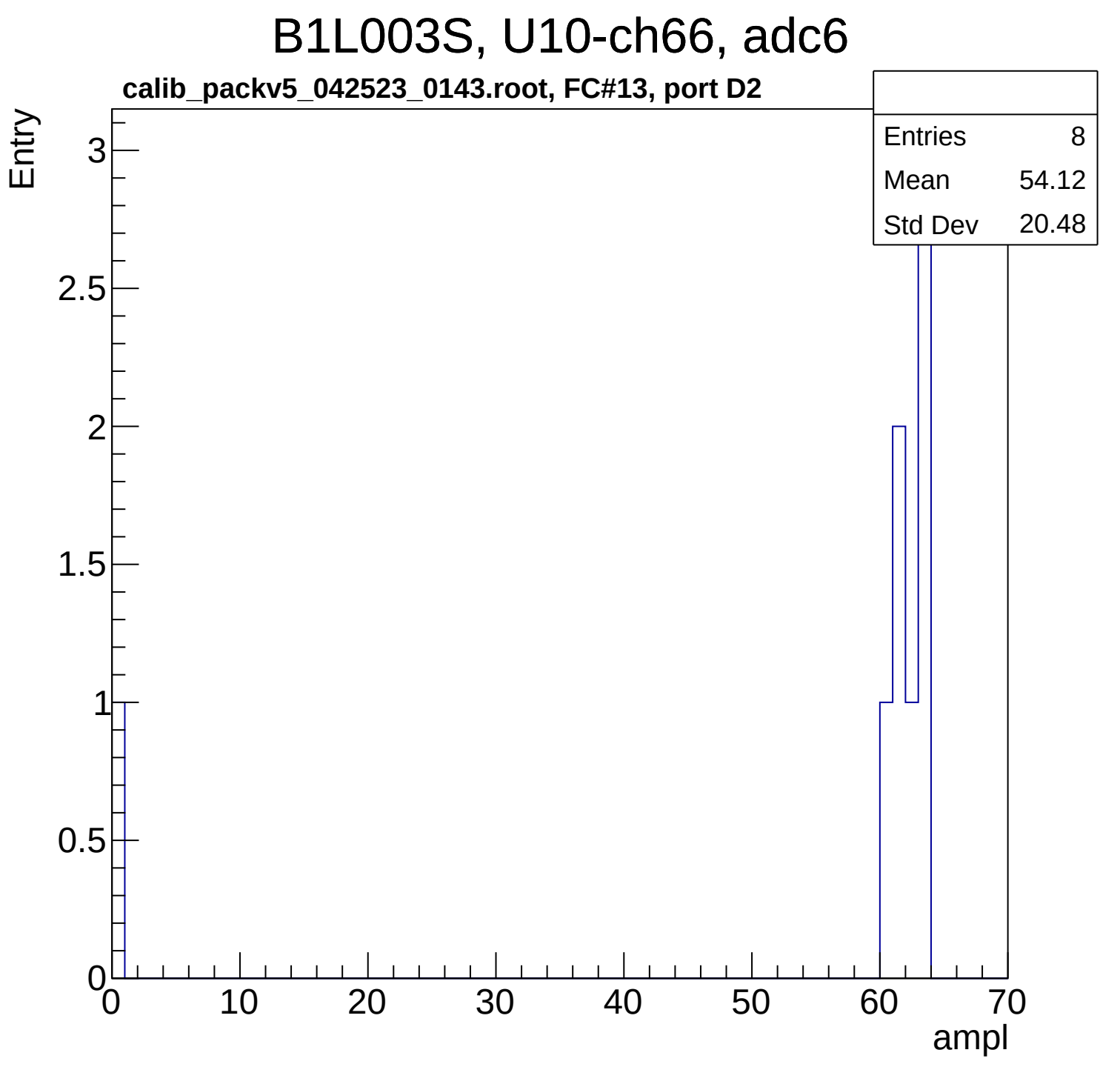
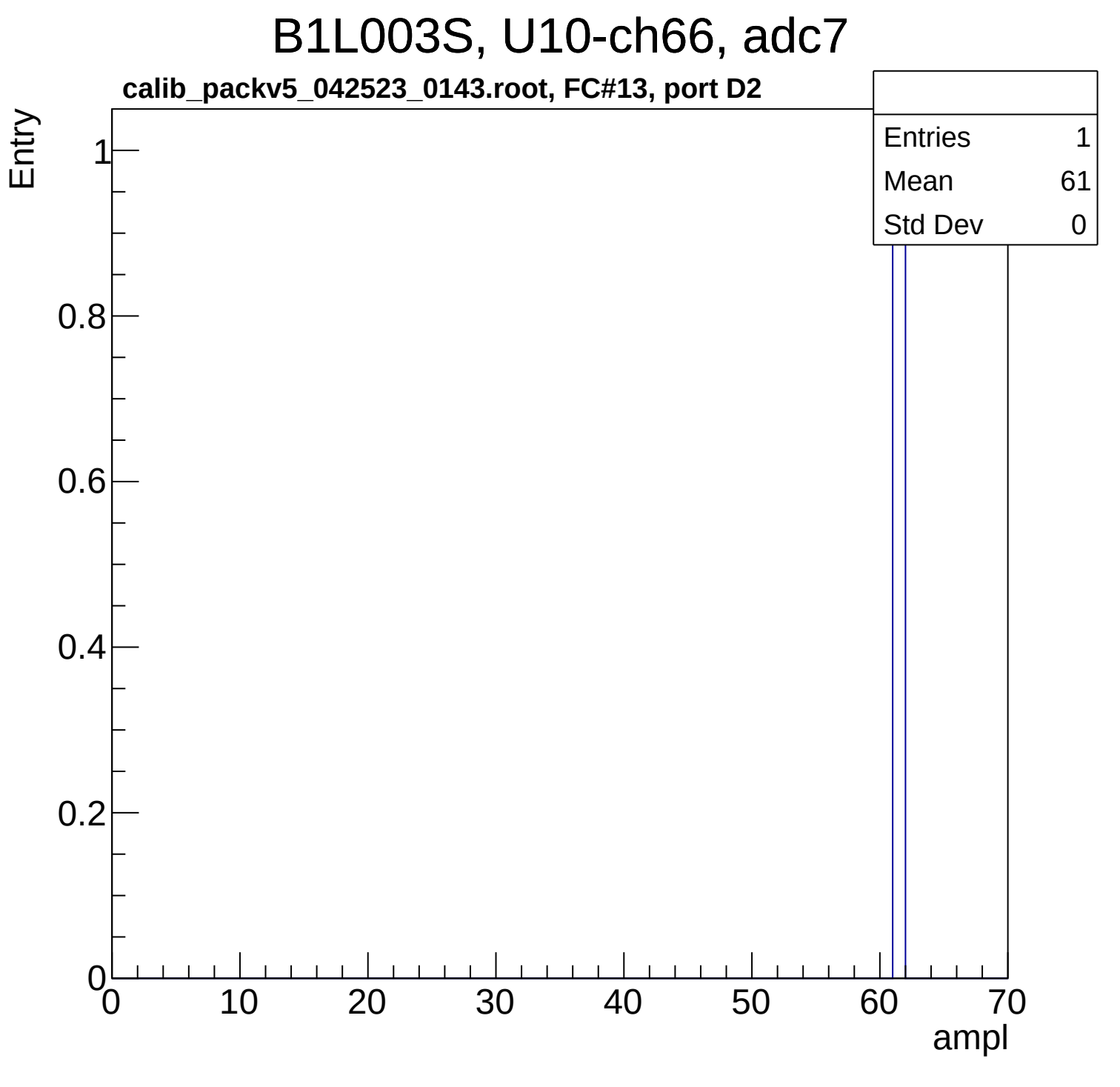
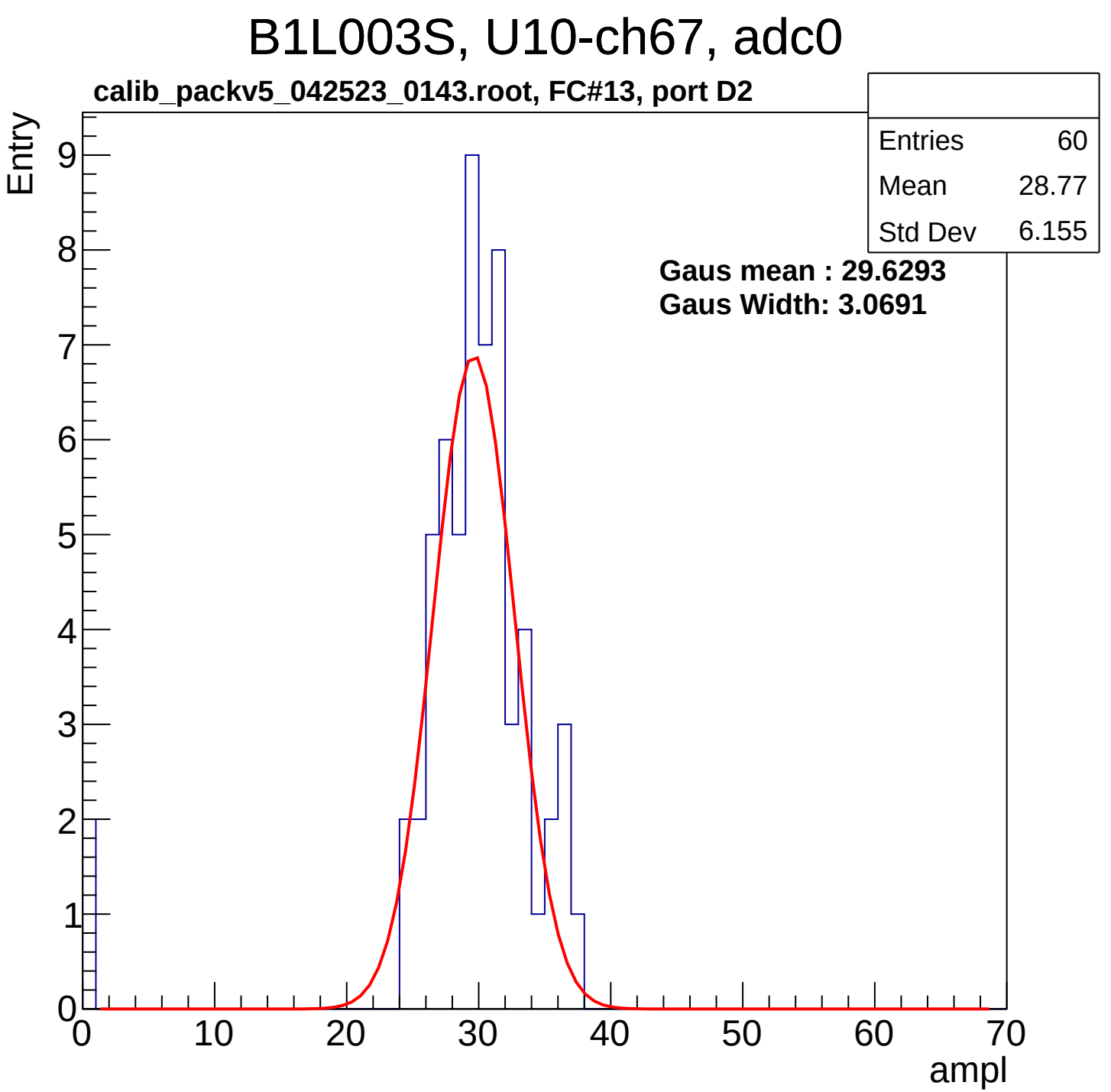

B1L003S, U10-ch66, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 59.08
Std Dev 2.648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch66, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.12
Std Dev 20.48
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch66, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch67, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.77
Std Dev 6.155
Gaus mean : 29.6293
Gaus Width: 3.0691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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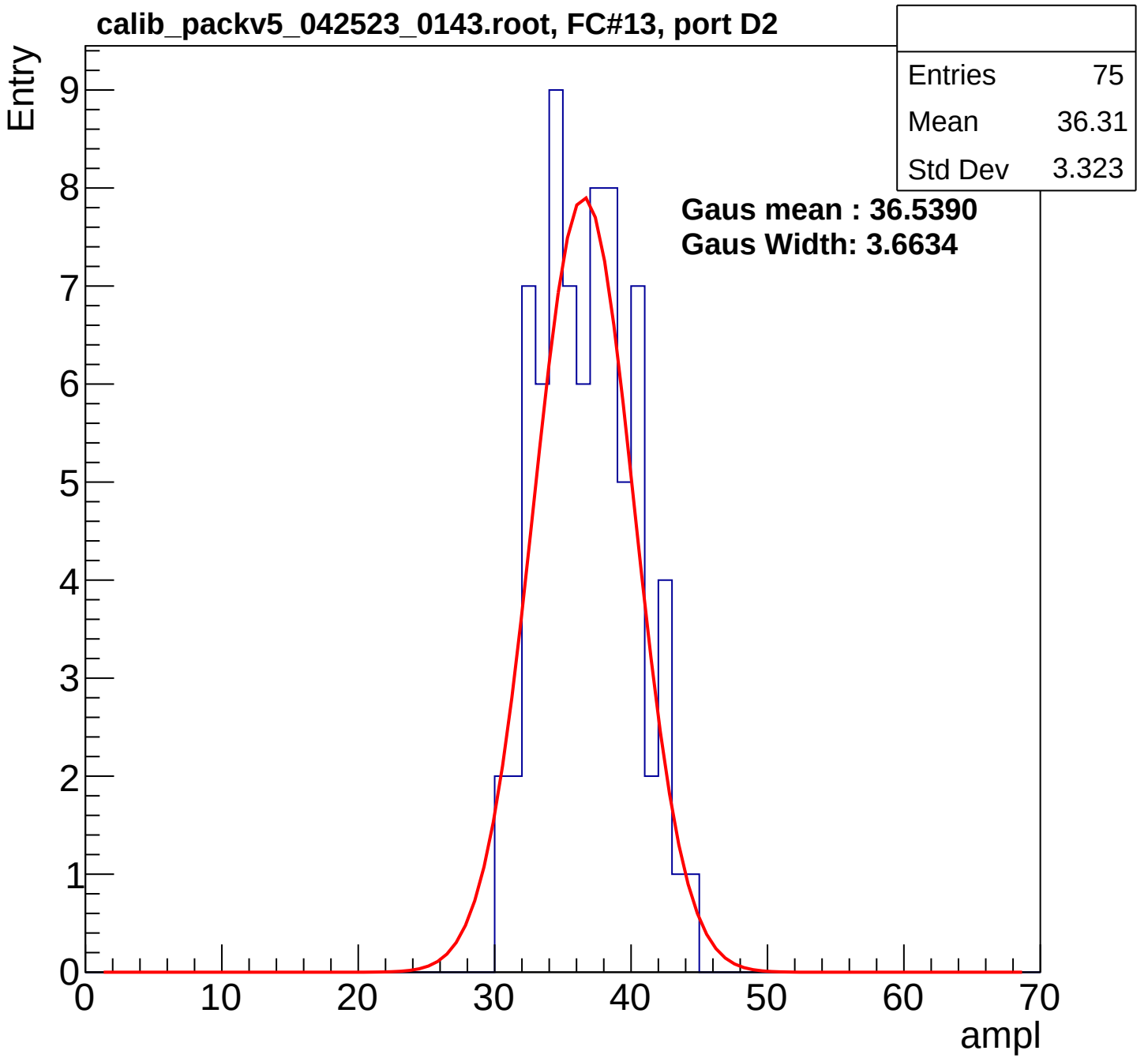
B1L003S, U10-ch67, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 36.31
Std Dev 3.323
Gaus mean : 36.5390
Gaus Width: 3.6634
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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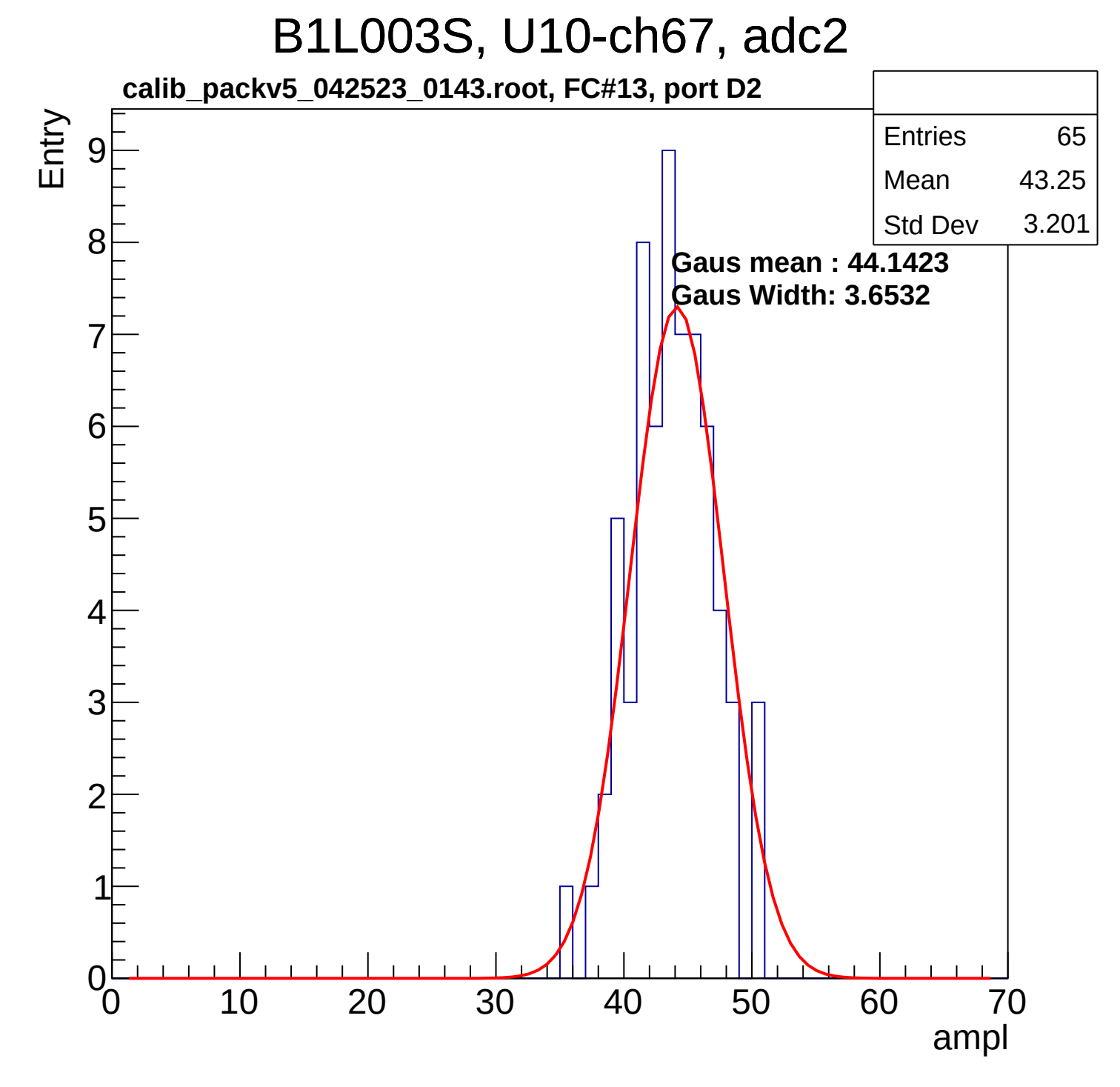
B1L003S, U10-ch67, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.25
Std Dev 3.201
Gaus mean : 44.1423
Gaus Width: 3.6532
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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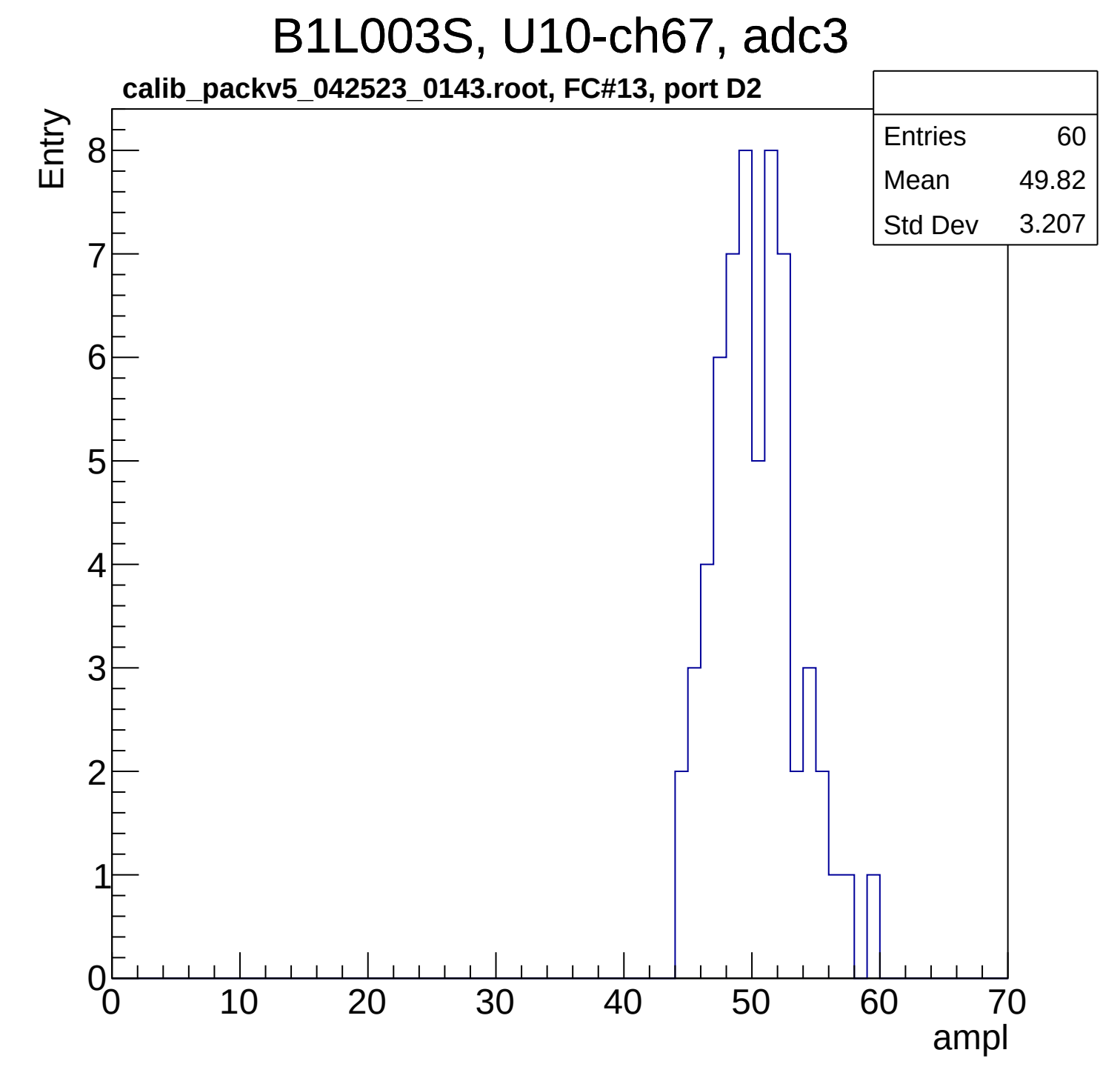
B1L003S, U10-ch67, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.82
Std Dev 3.207
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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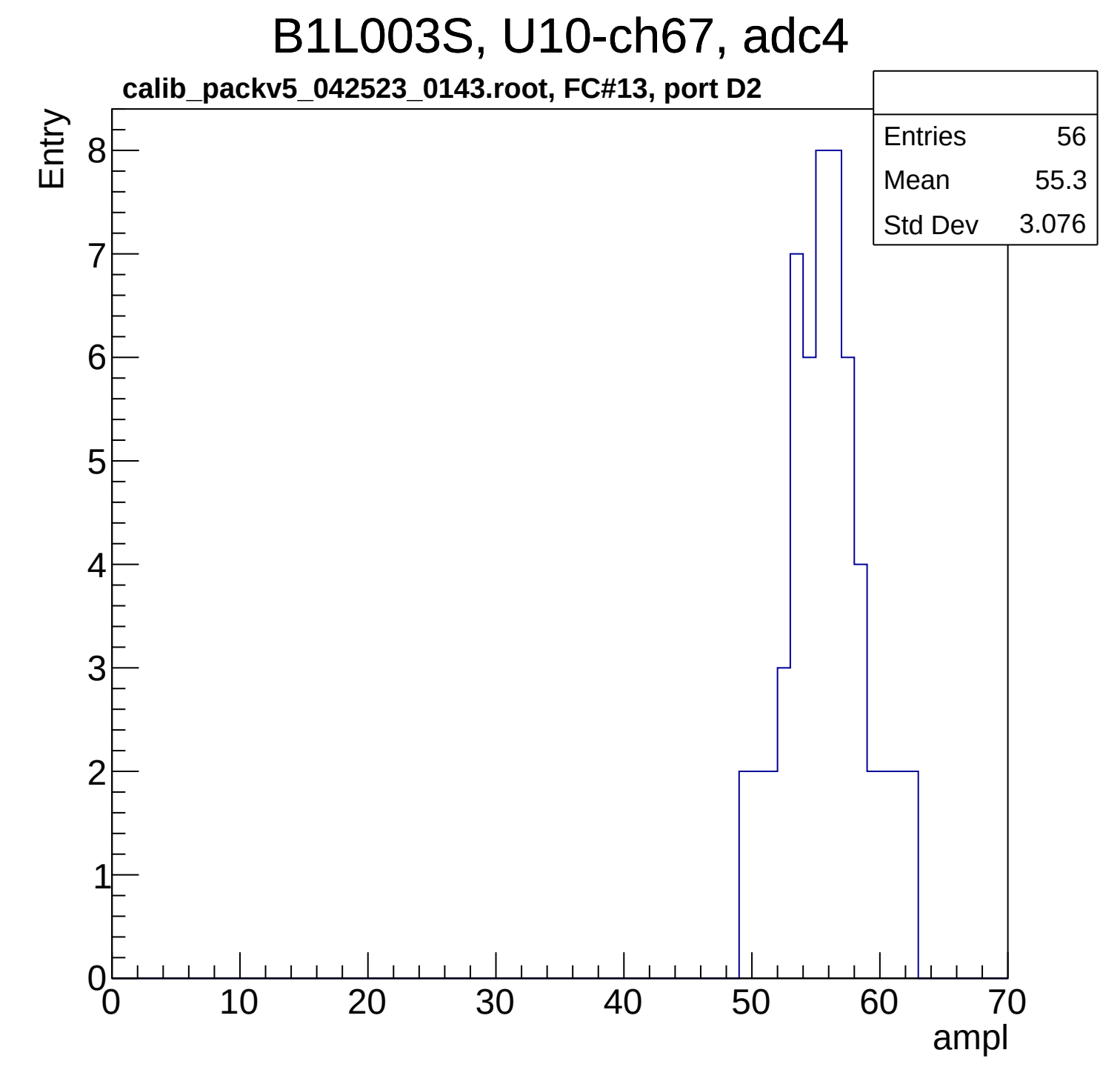
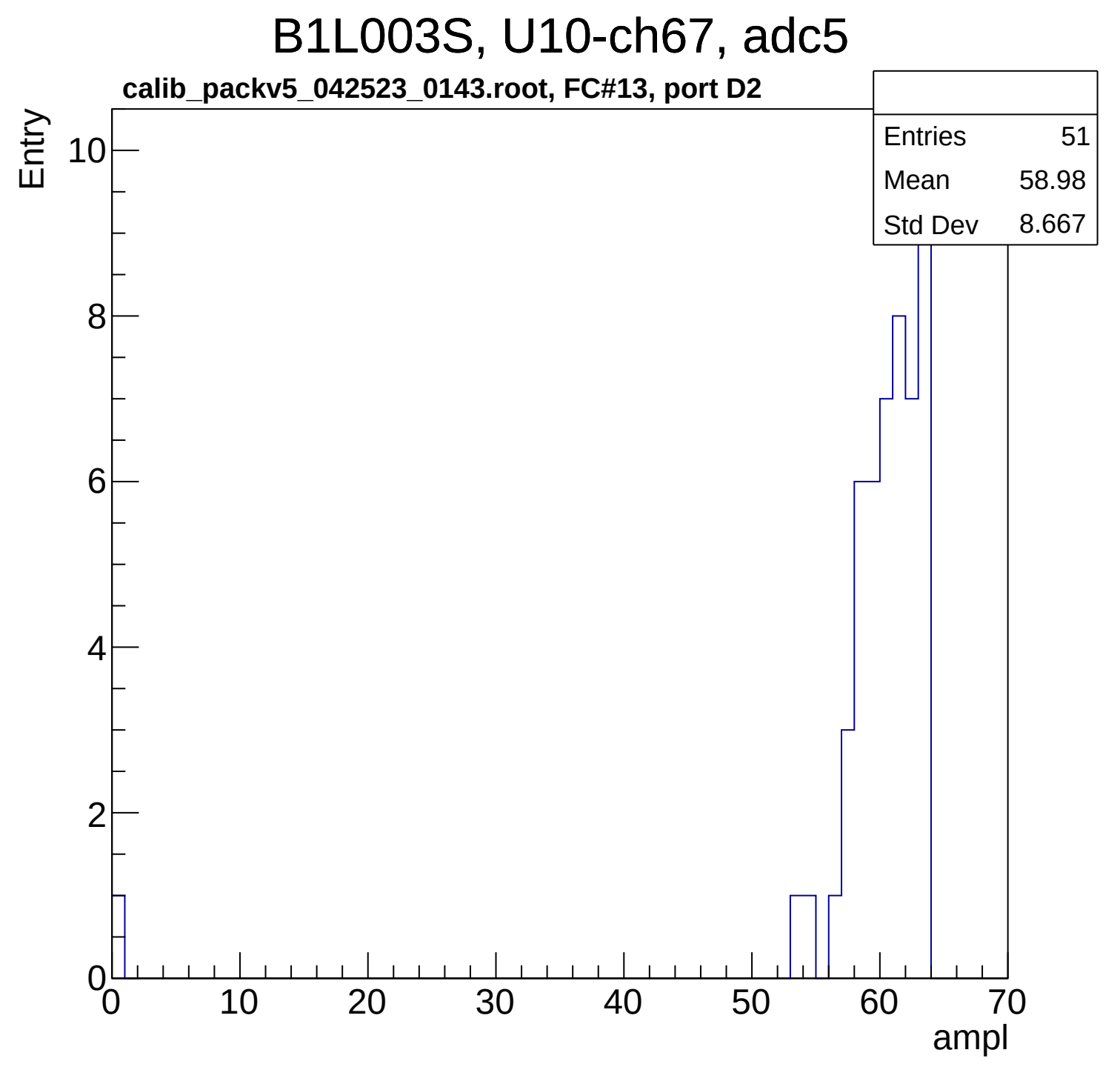

B1L003S, U10-ch67, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.3
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch67, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.98
Std Dev 8.667
Entry
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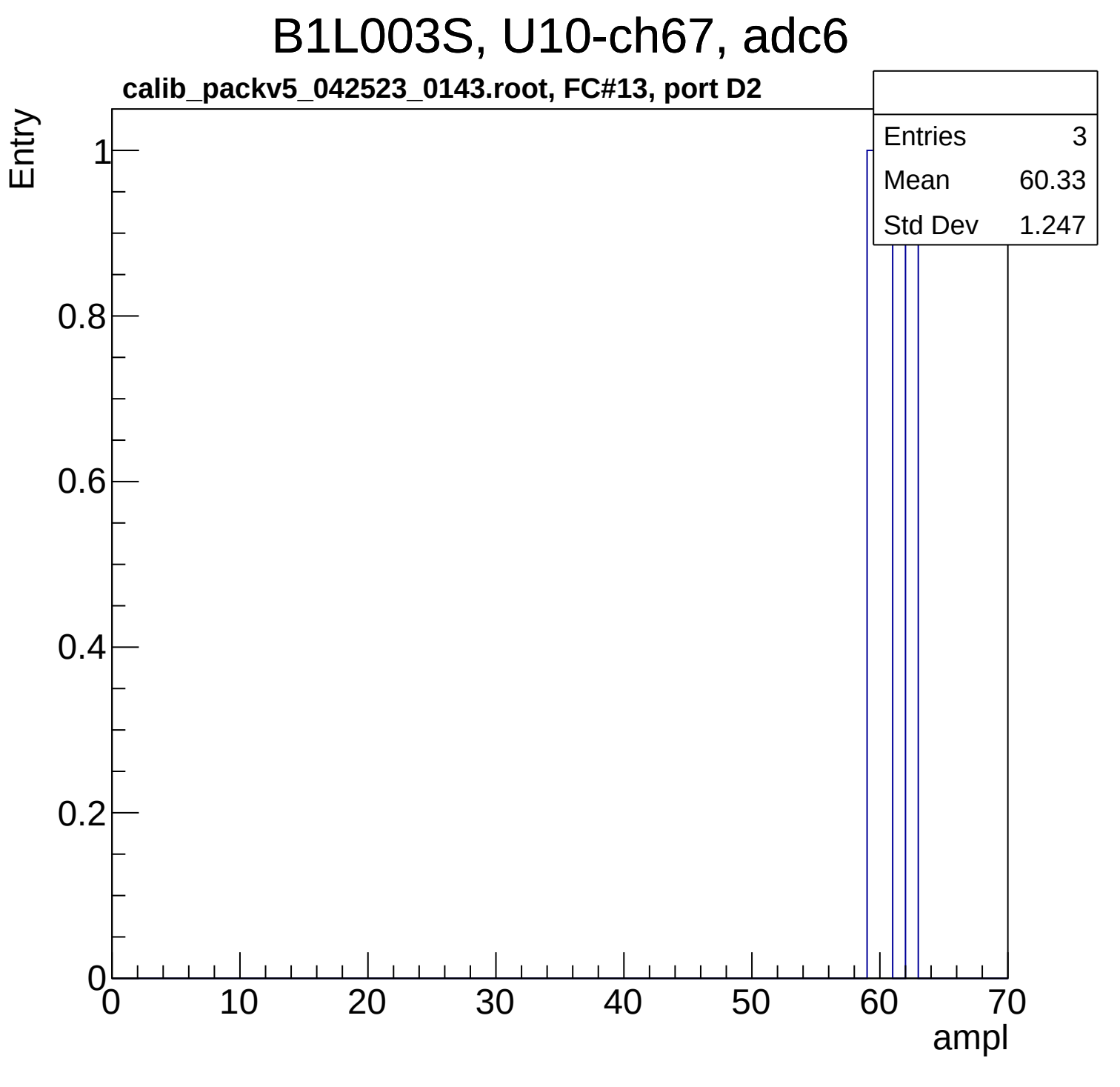
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch67, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 60.33
Std Dev 1.247
Entry
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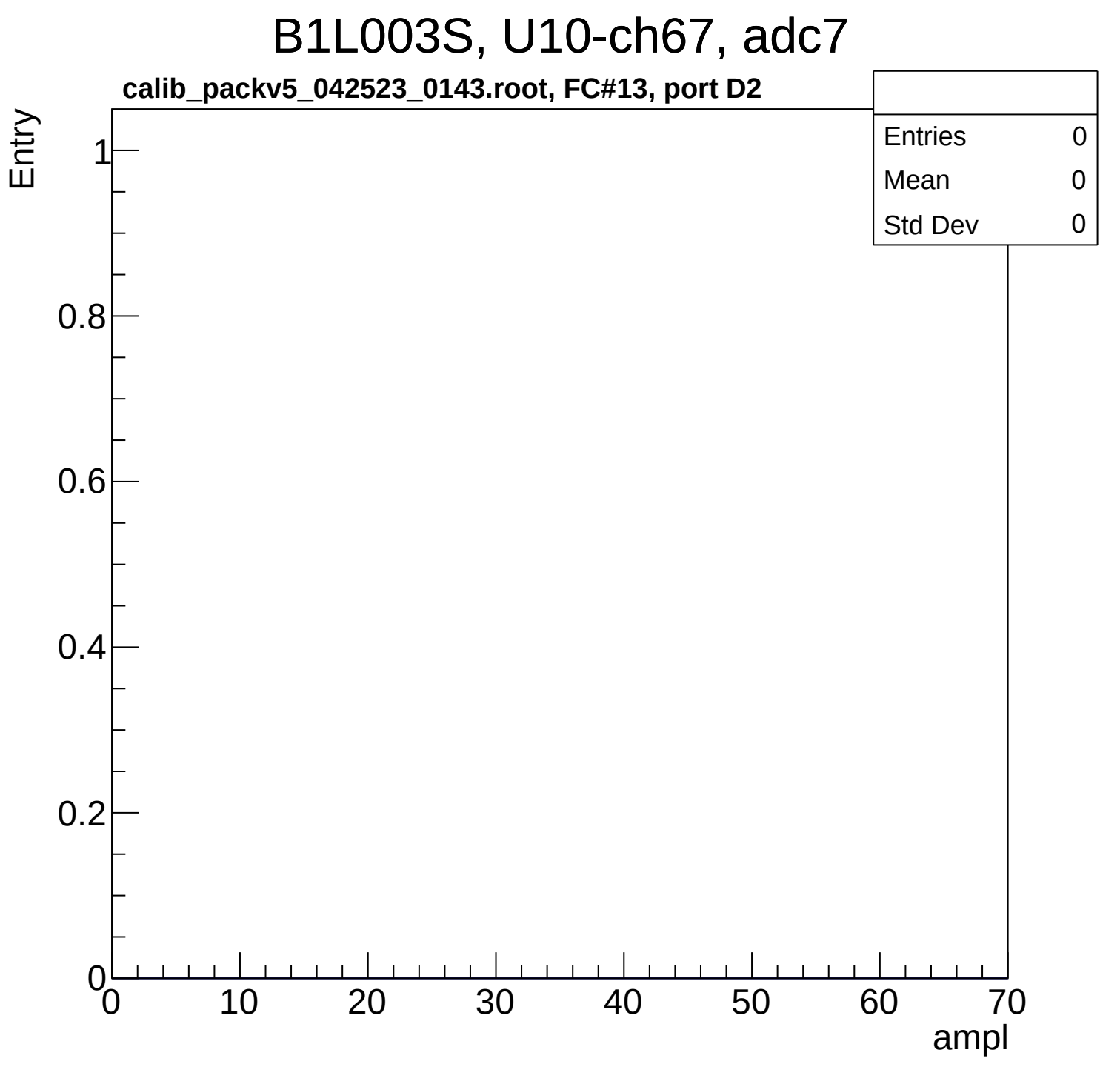
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch67, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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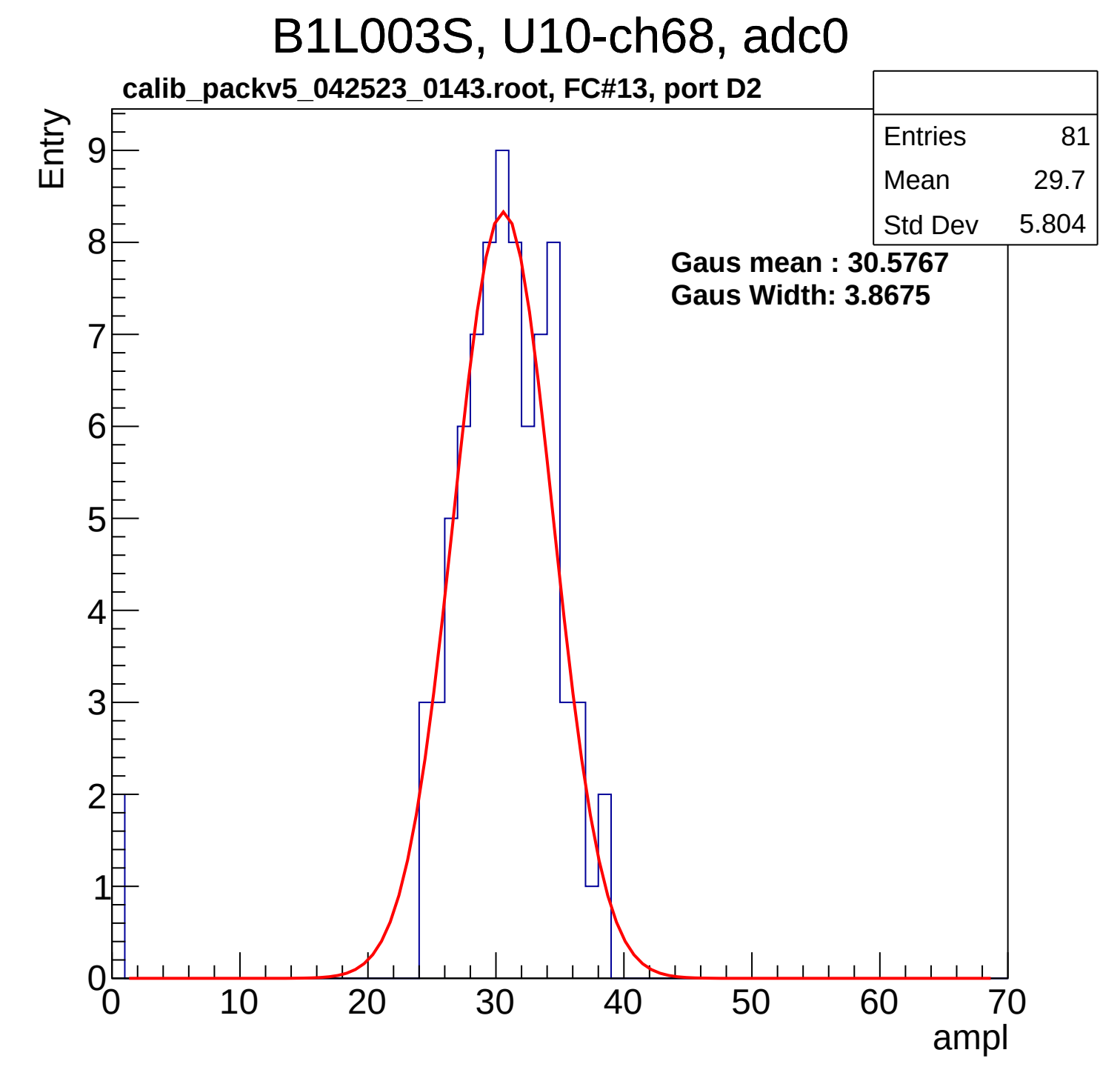
50
60
70
ampl


B1L003S, U10-ch68, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 29.7
Std Dev 5.804
Gaus mean : 30.5767
Gaus Width: 3.8675
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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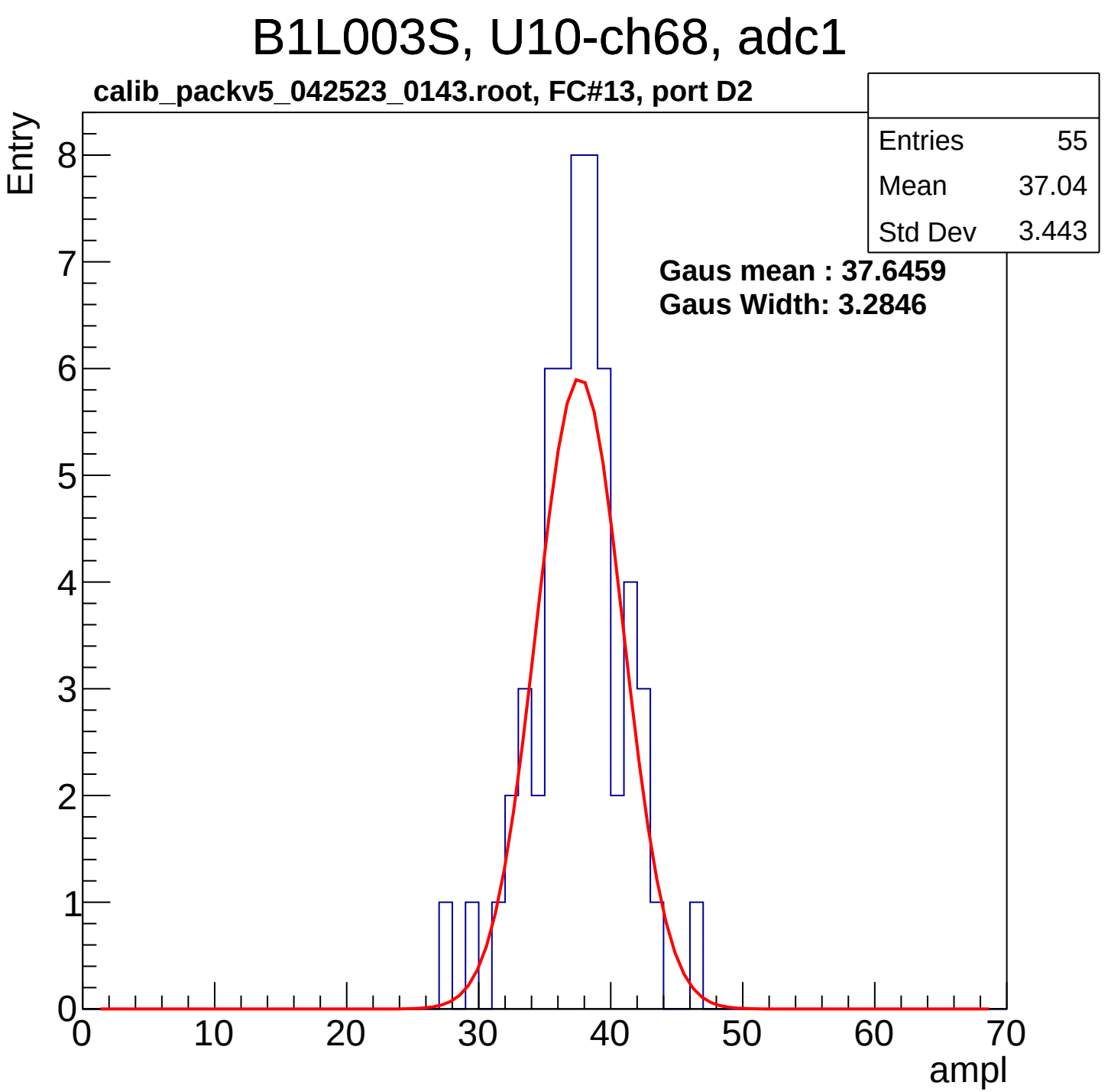
B1L003S, U10-ch68, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 37.04
Std Dev 3.443
Gaus mean : 37.6459
Gaus Width: 3.2846
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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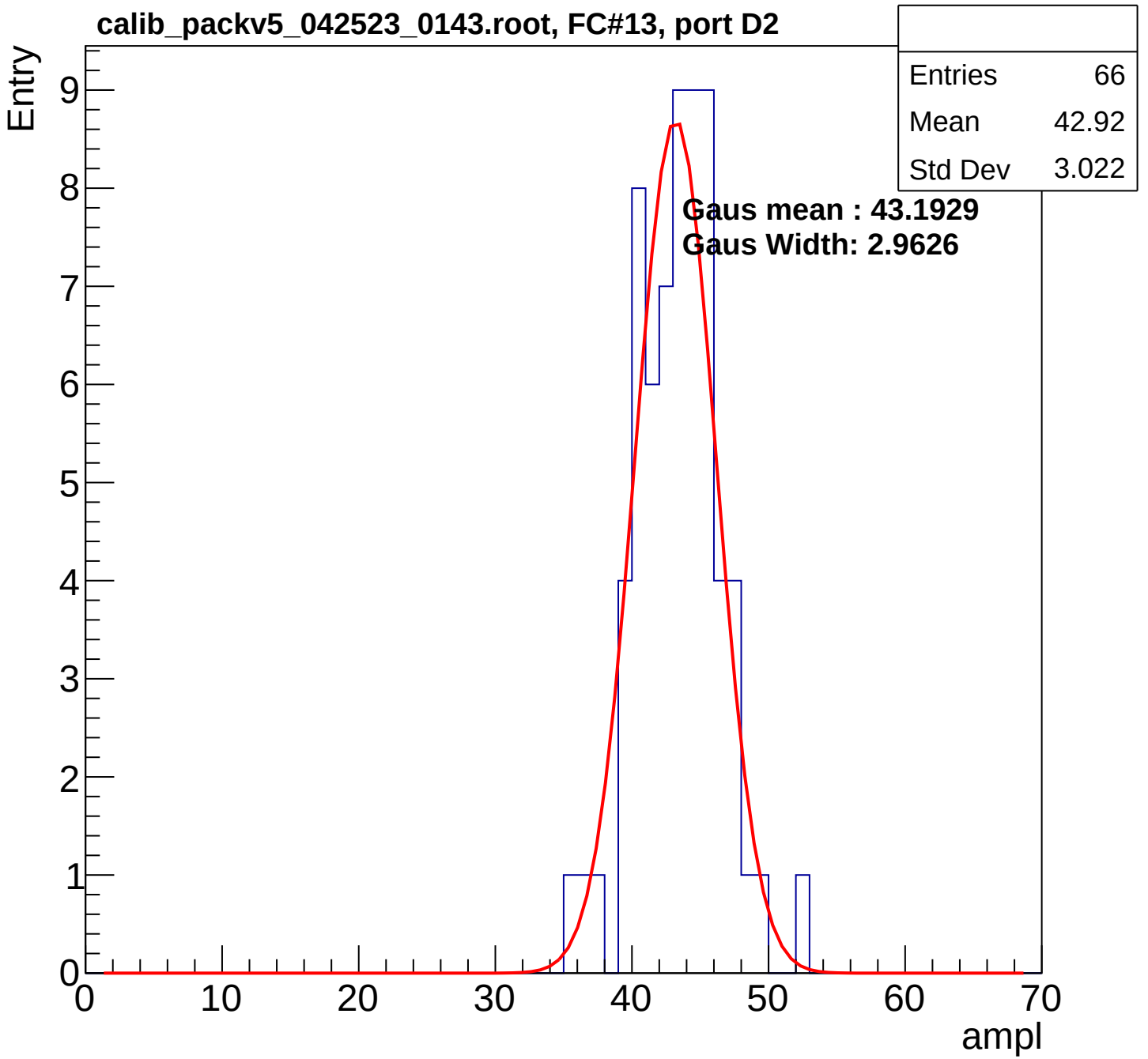
B1L003S, U10-ch68, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.92
Std Dev 3.022
Gaus mean : 43.1929
Gaus Width: 2.9626
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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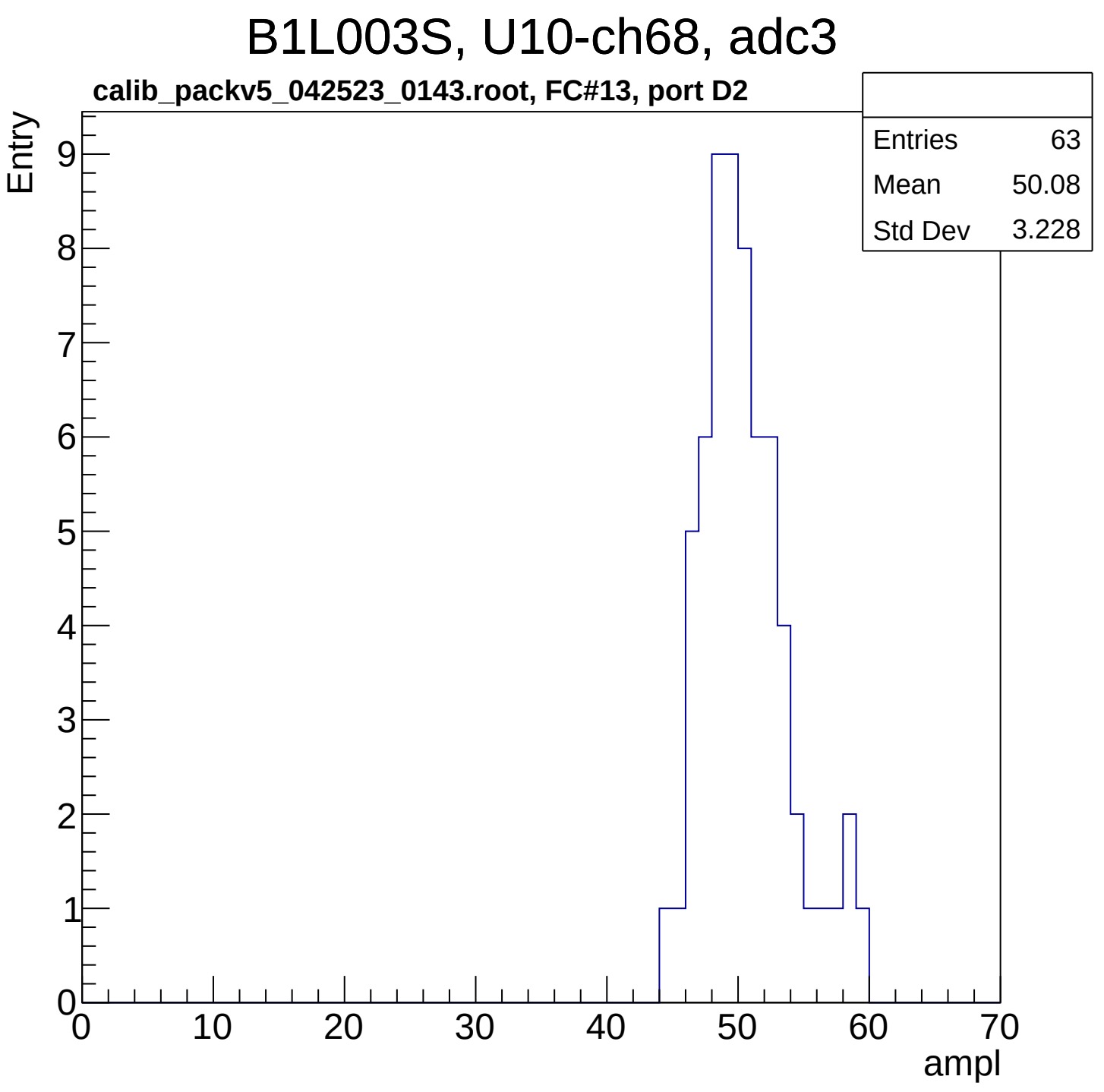
B1L003S, U10-ch68, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 50.08
Std Dev 3.228
Entry
ampl
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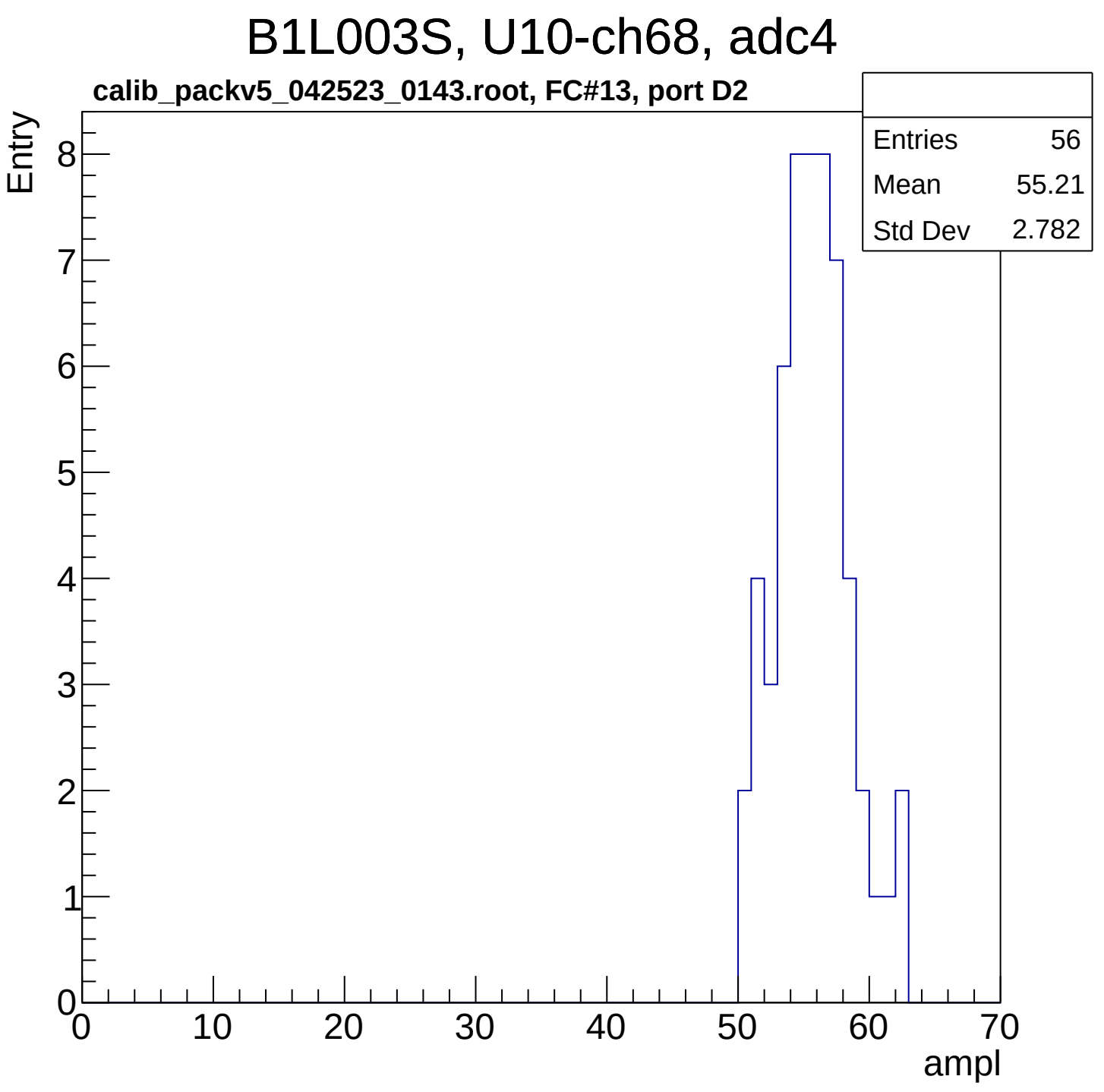
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch68, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.21
Std Dev 2.782
Entry
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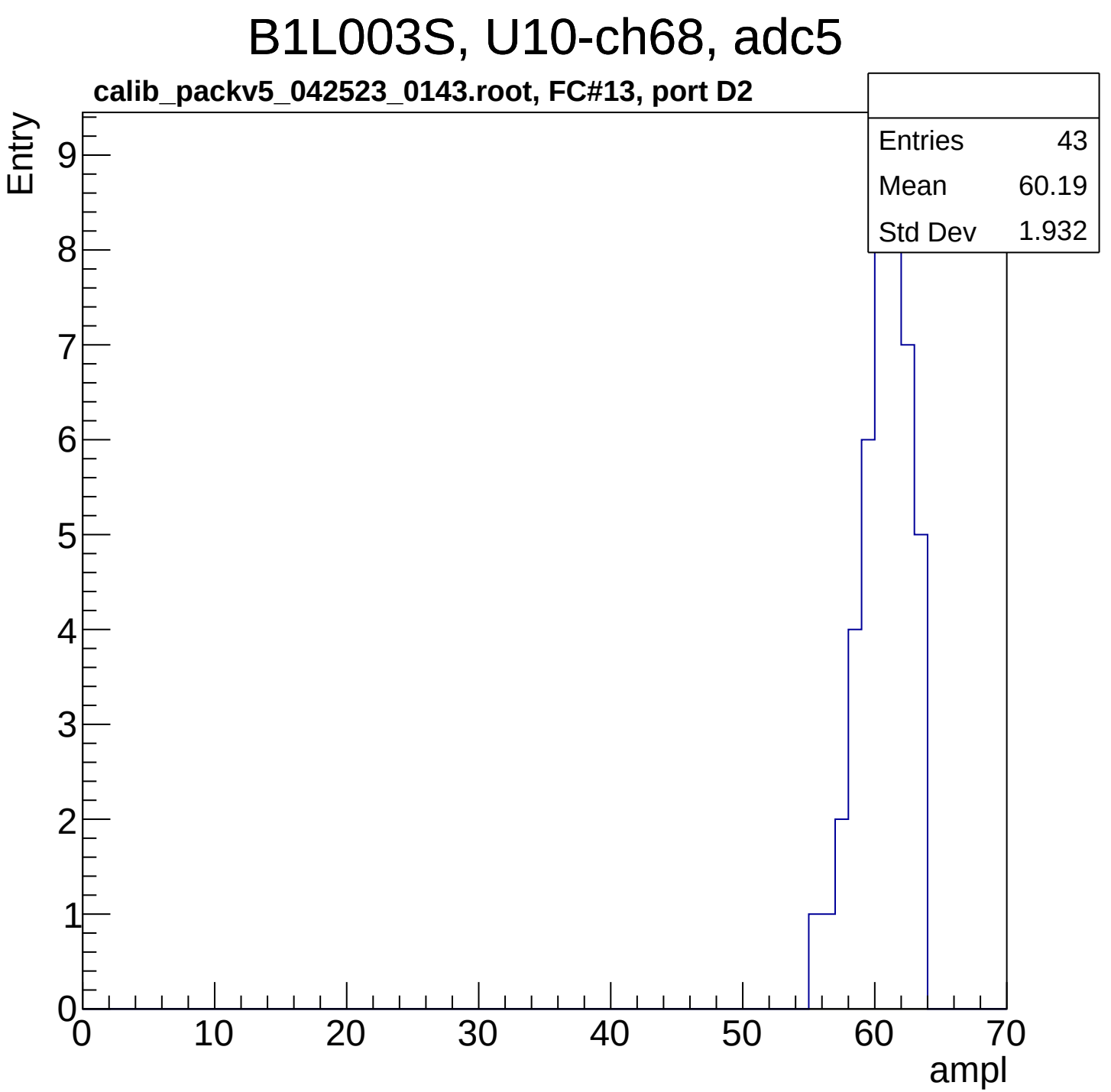
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch68, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 60.19
Std Dev 1.932
Entry
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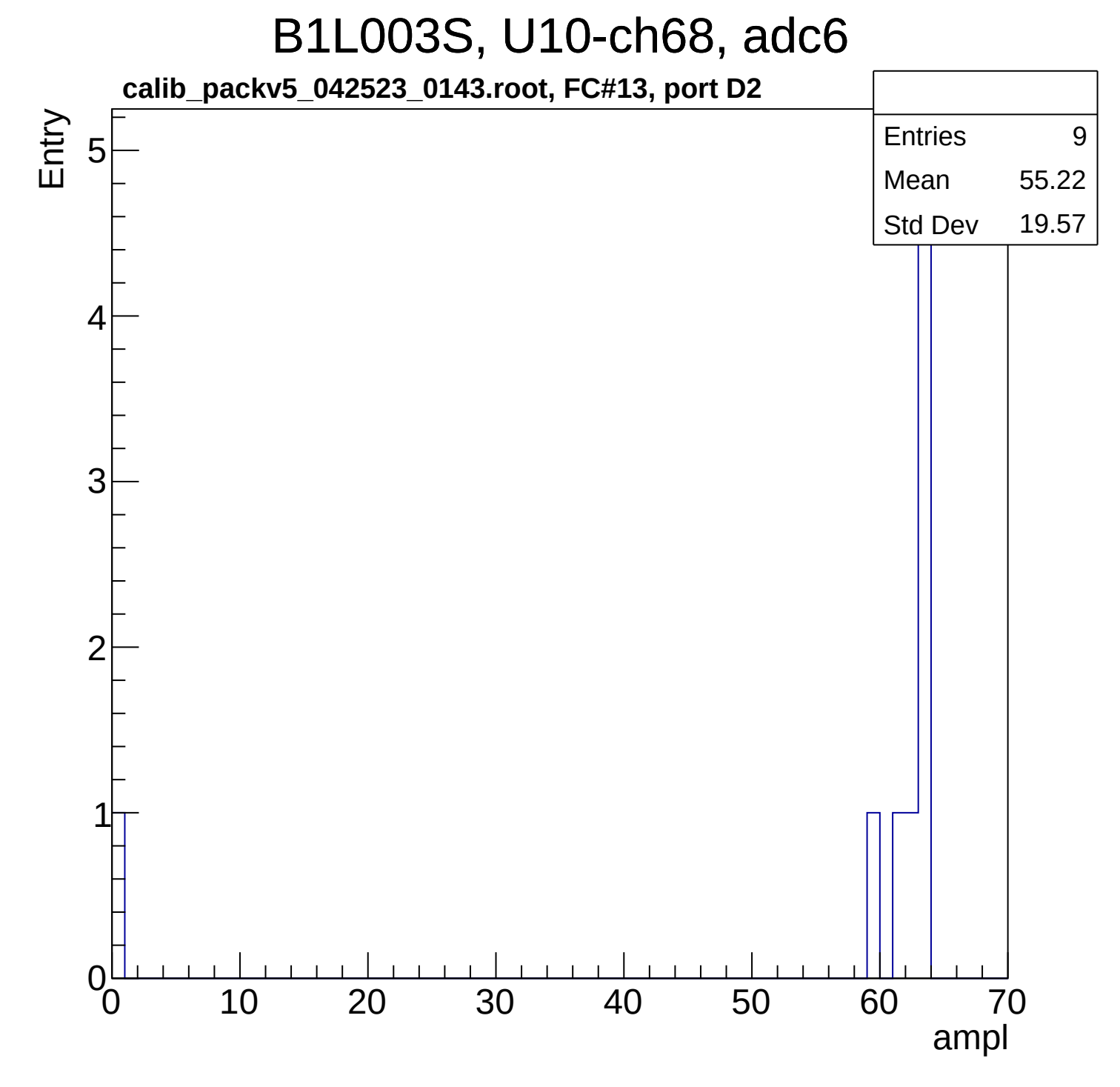
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch68, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55.22
Std Dev 19.57
Entry
0
1
2
3
4
5
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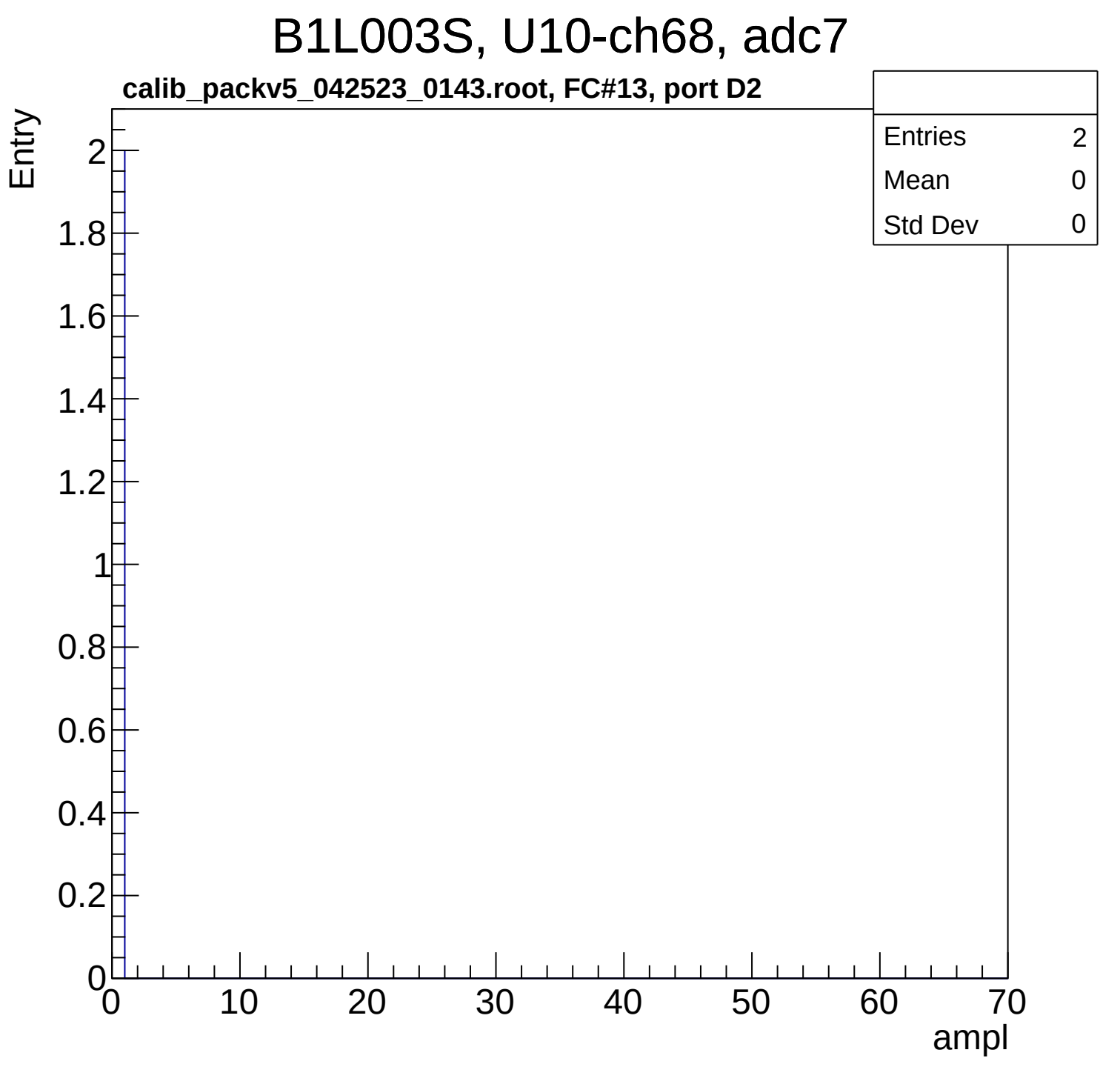
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch68, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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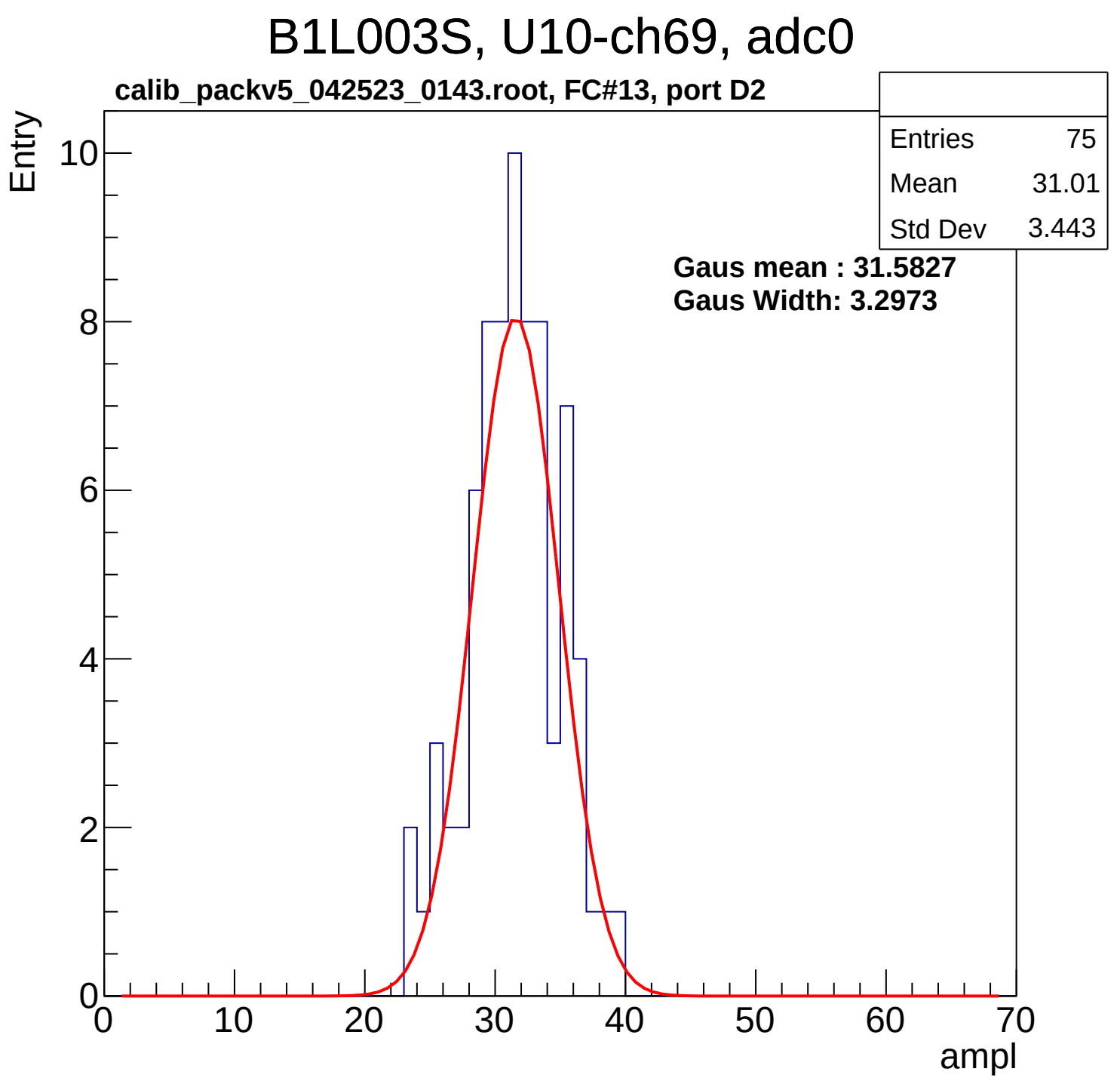
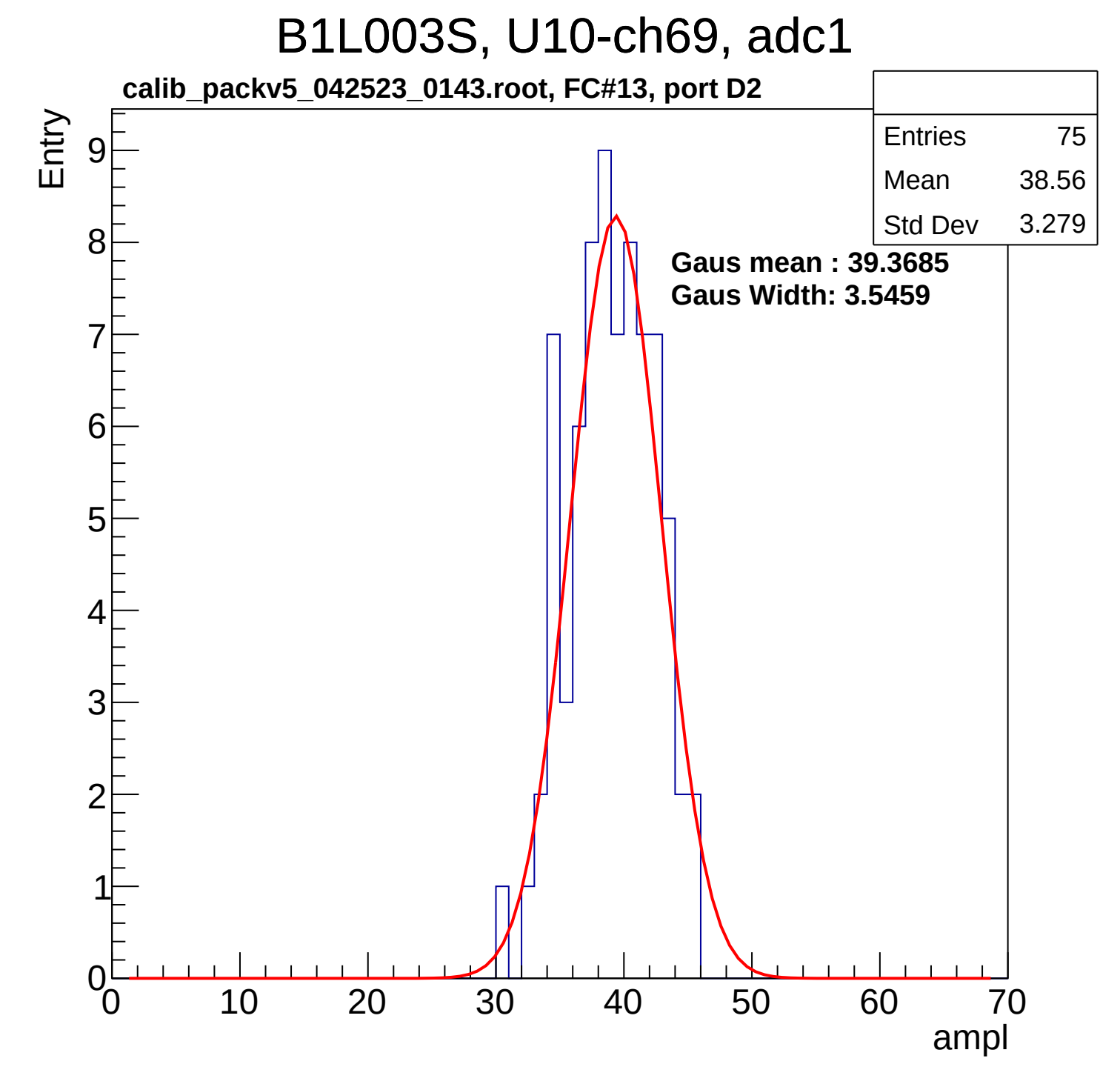

B1L003S, U10-ch69, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 31.01
Std Dev 3.443
Gaus mean : 31.5827
Gaus Width: 3.2973
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch69, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 38.56
Std Dev 3.279
Gaus mean : 39.3685
Gaus Width: 3.5459
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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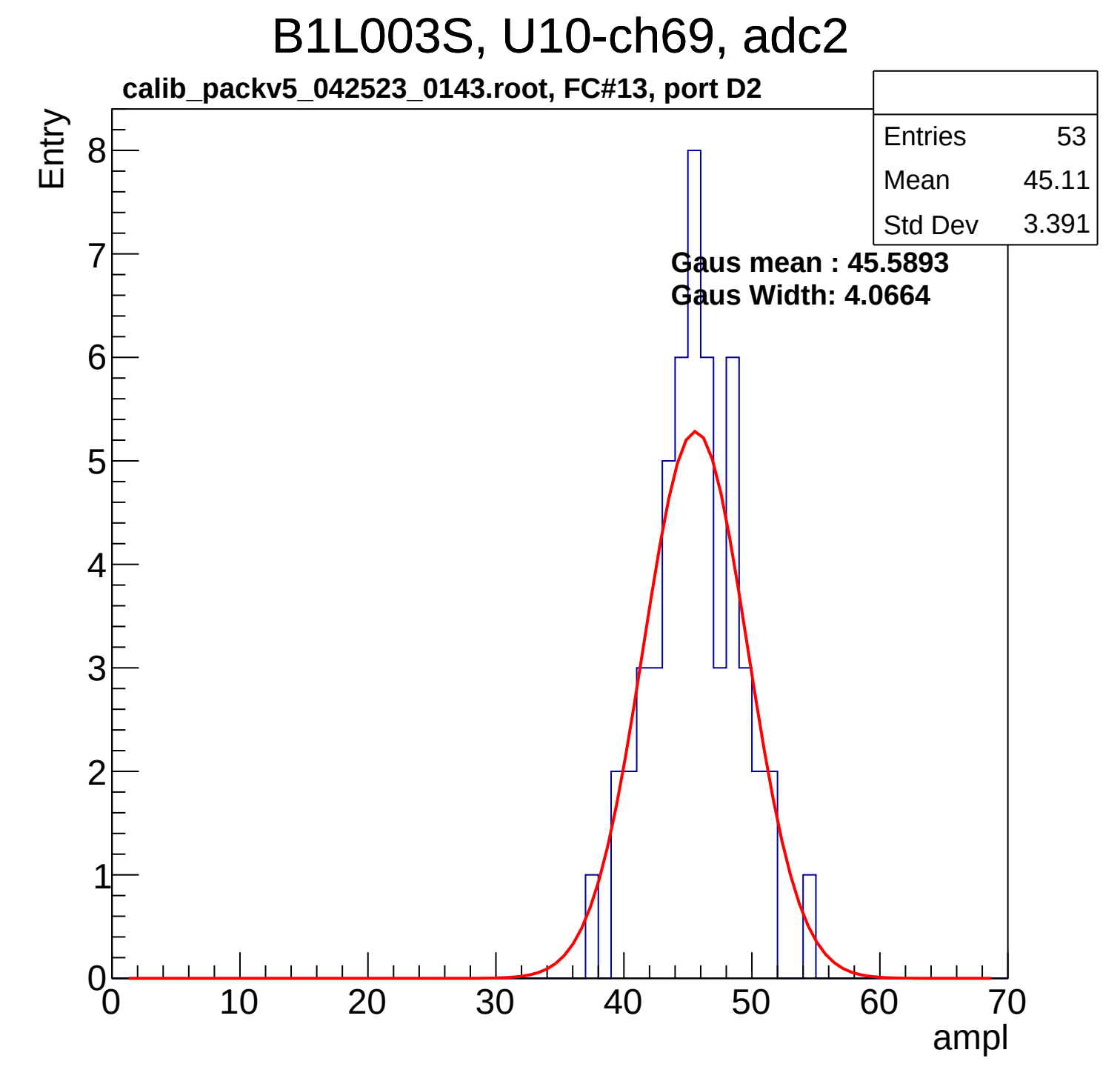
B1L003S, U10-ch69, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 45.11
Std Dev 3.391
Gaus mean : 45.5893
Gaus Width: 4.0664
Entry
ampl
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1
2
3
4
5
6
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0
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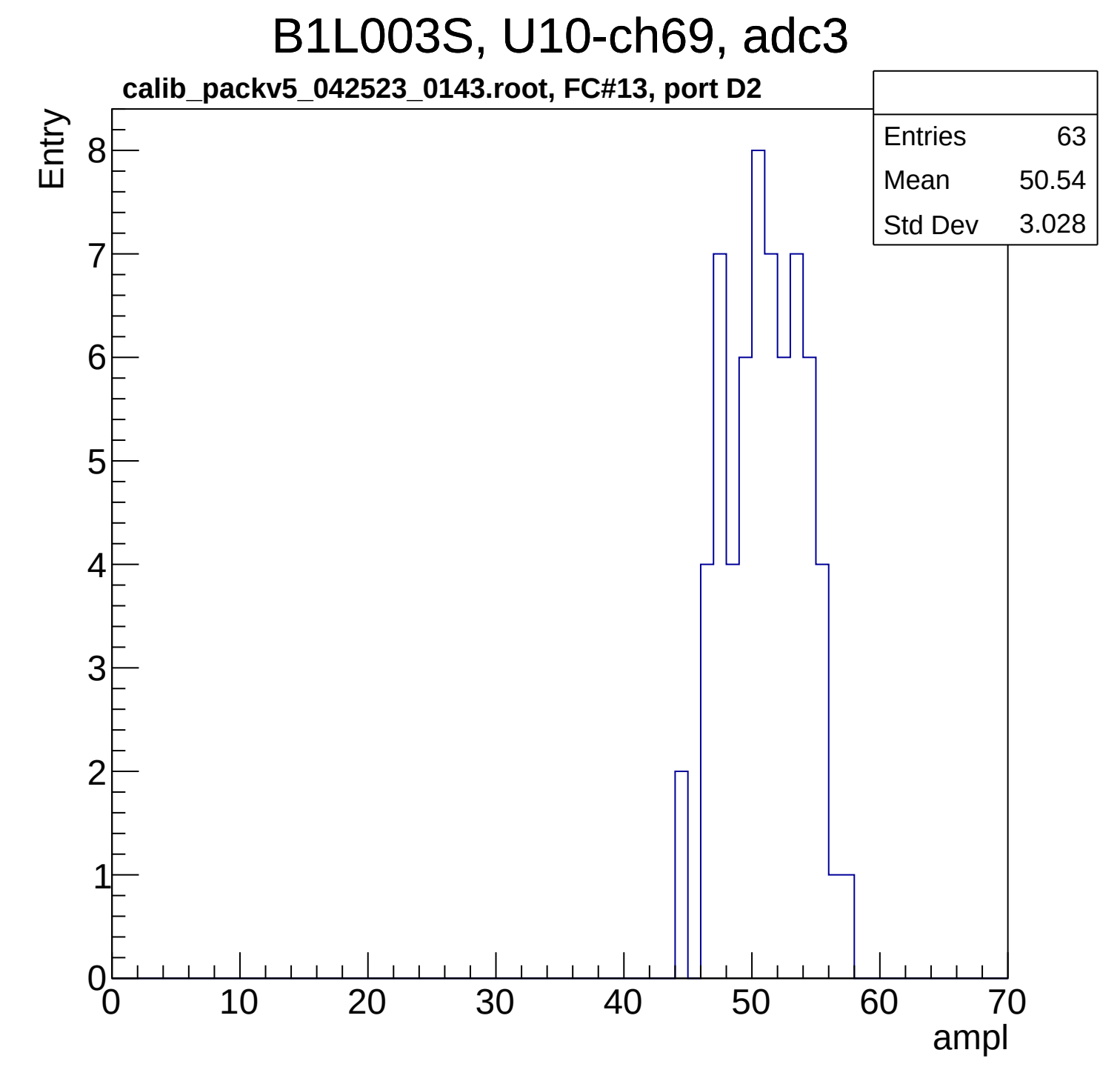
B1L003S, U10-ch69, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 50.54
Std Dev 3.028
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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60
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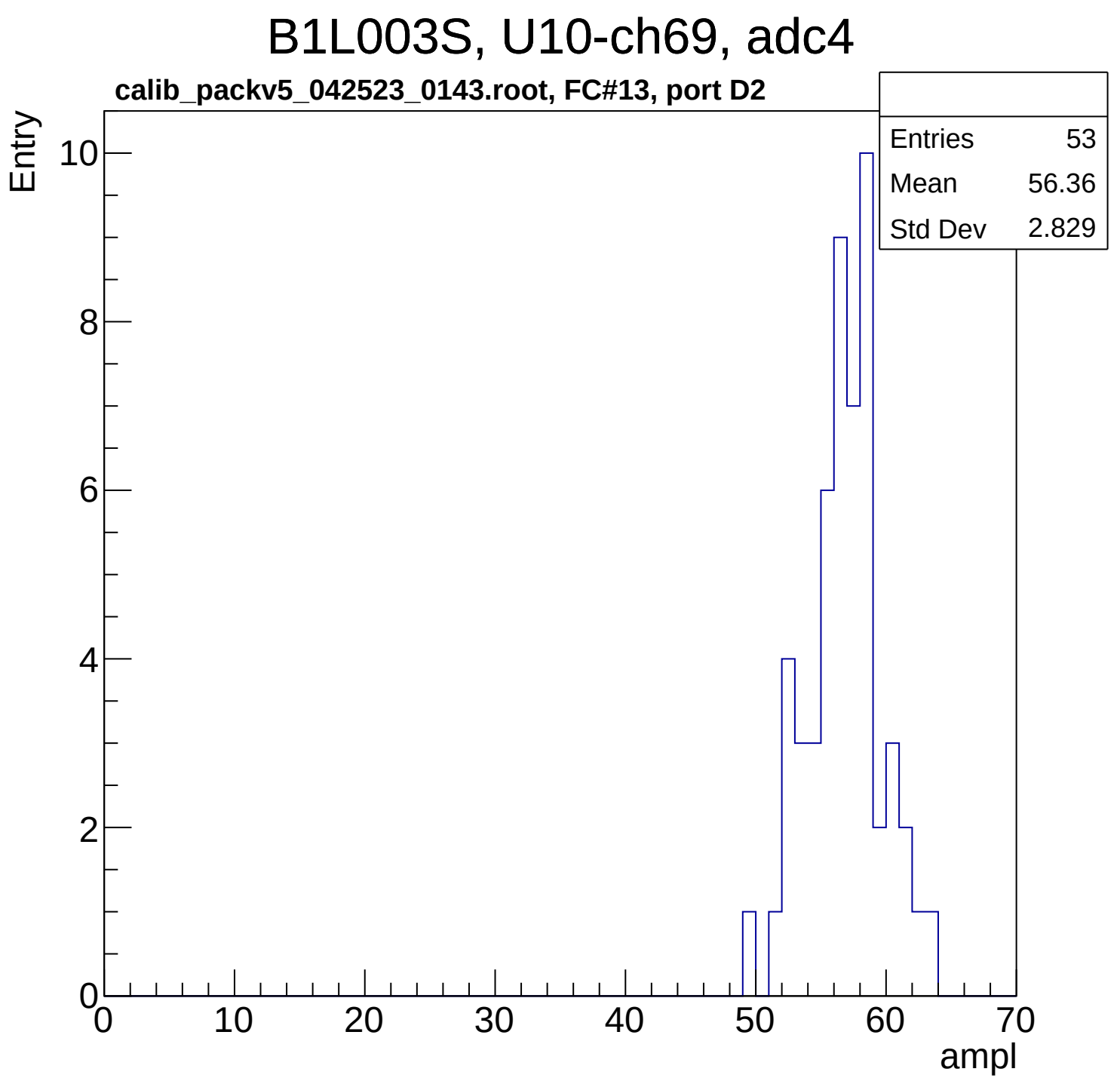
B1L003S, U10-ch69, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 56.36
Std Dev 2.829
Entry
0
2
4
6
8
10
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50
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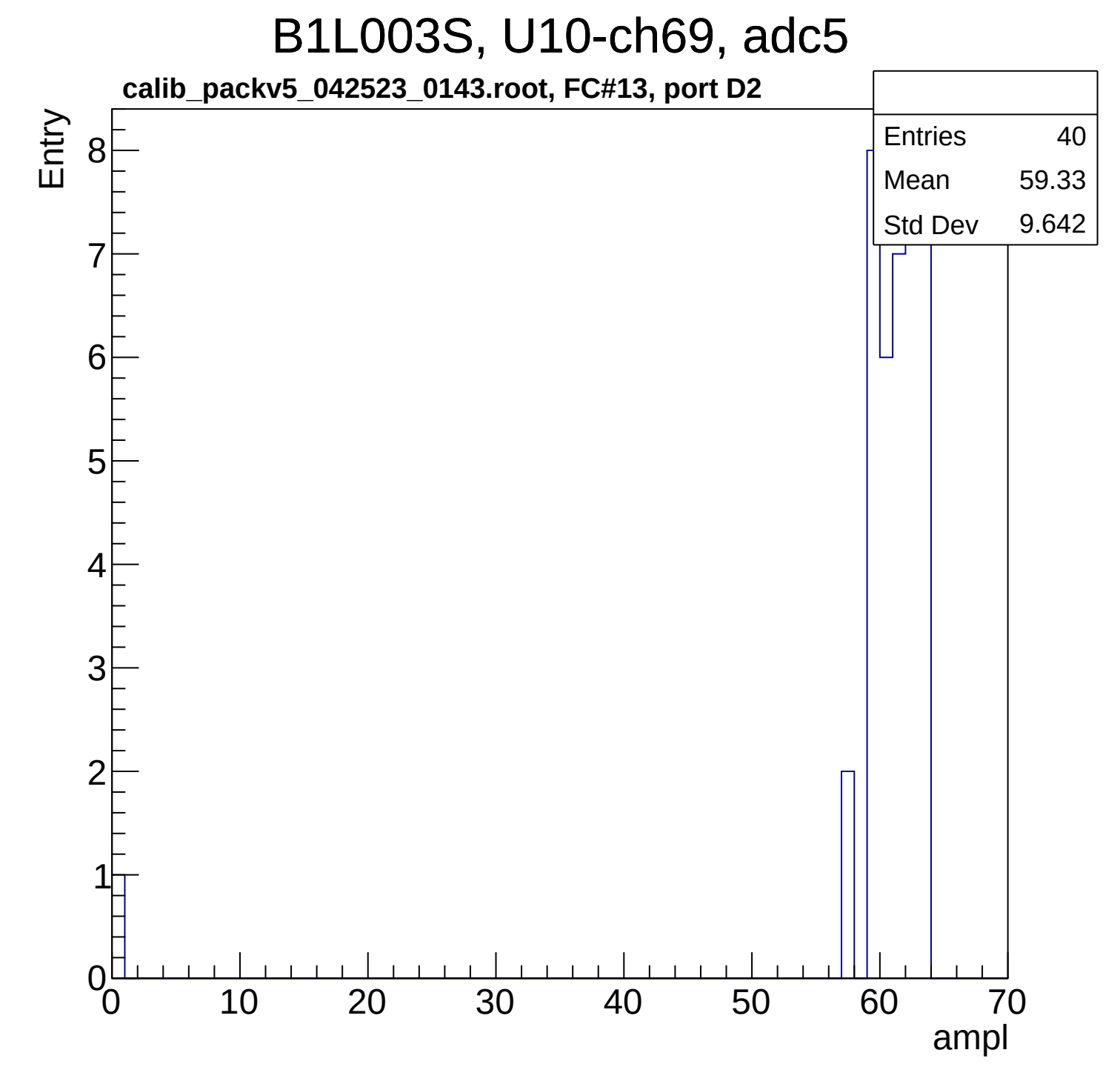
70
ampl


B1L003S, U10-ch69, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.33
Std Dev 9.642
Entry
ampl
0
1
2
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4
5
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60
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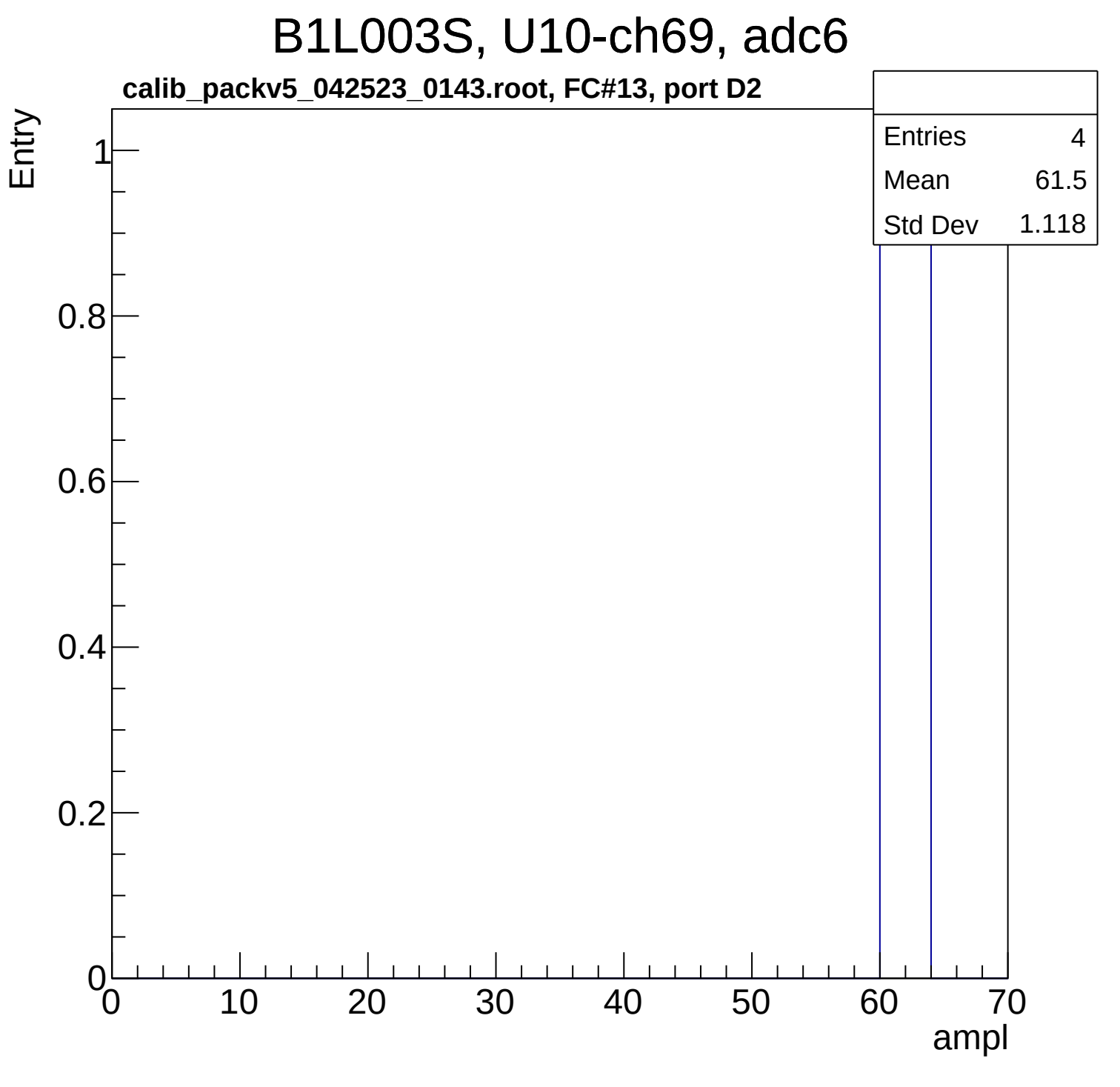
B1L003S, U10-ch69, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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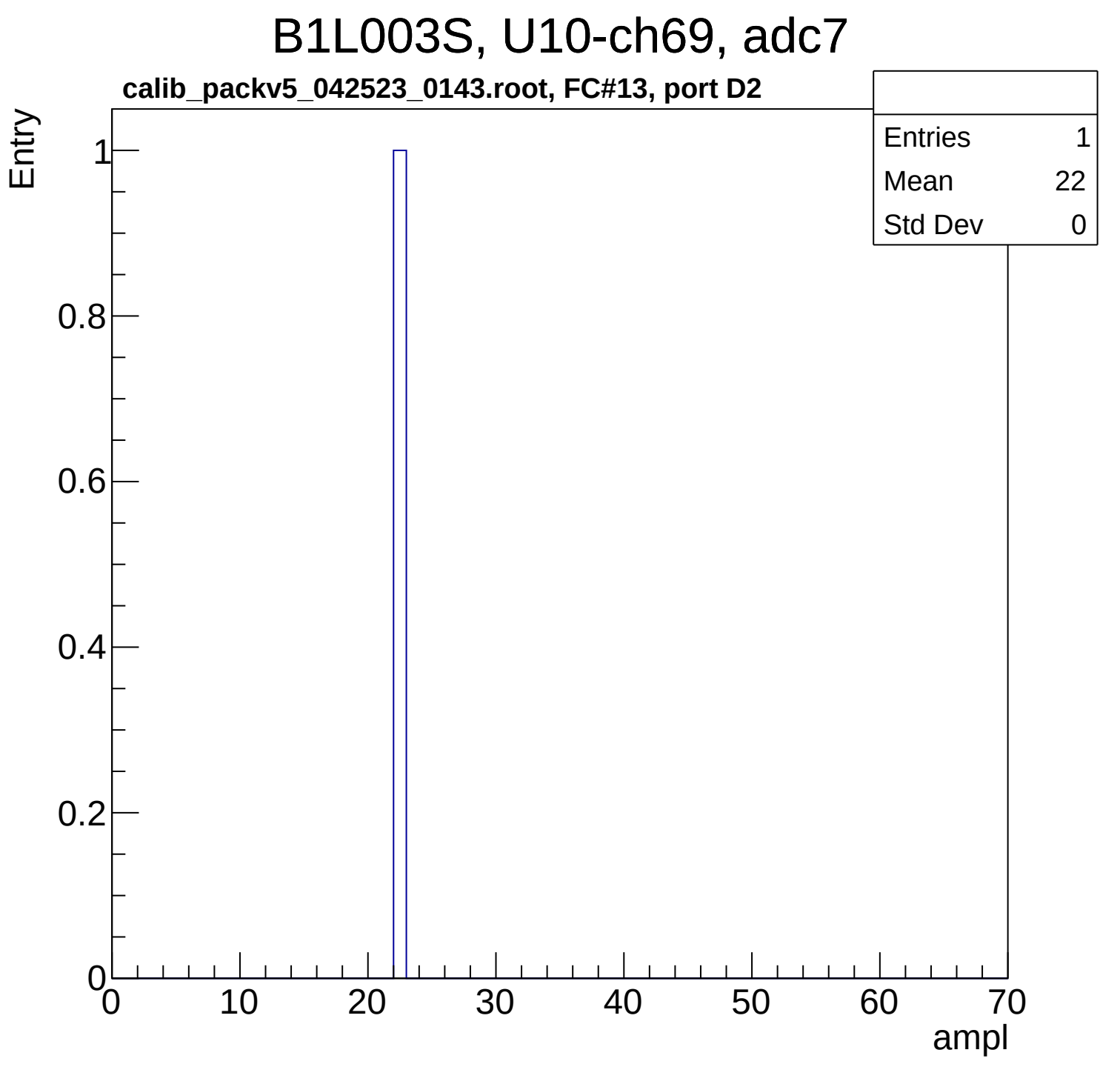
50
60
70
ampl


B1L003S, U10-ch69, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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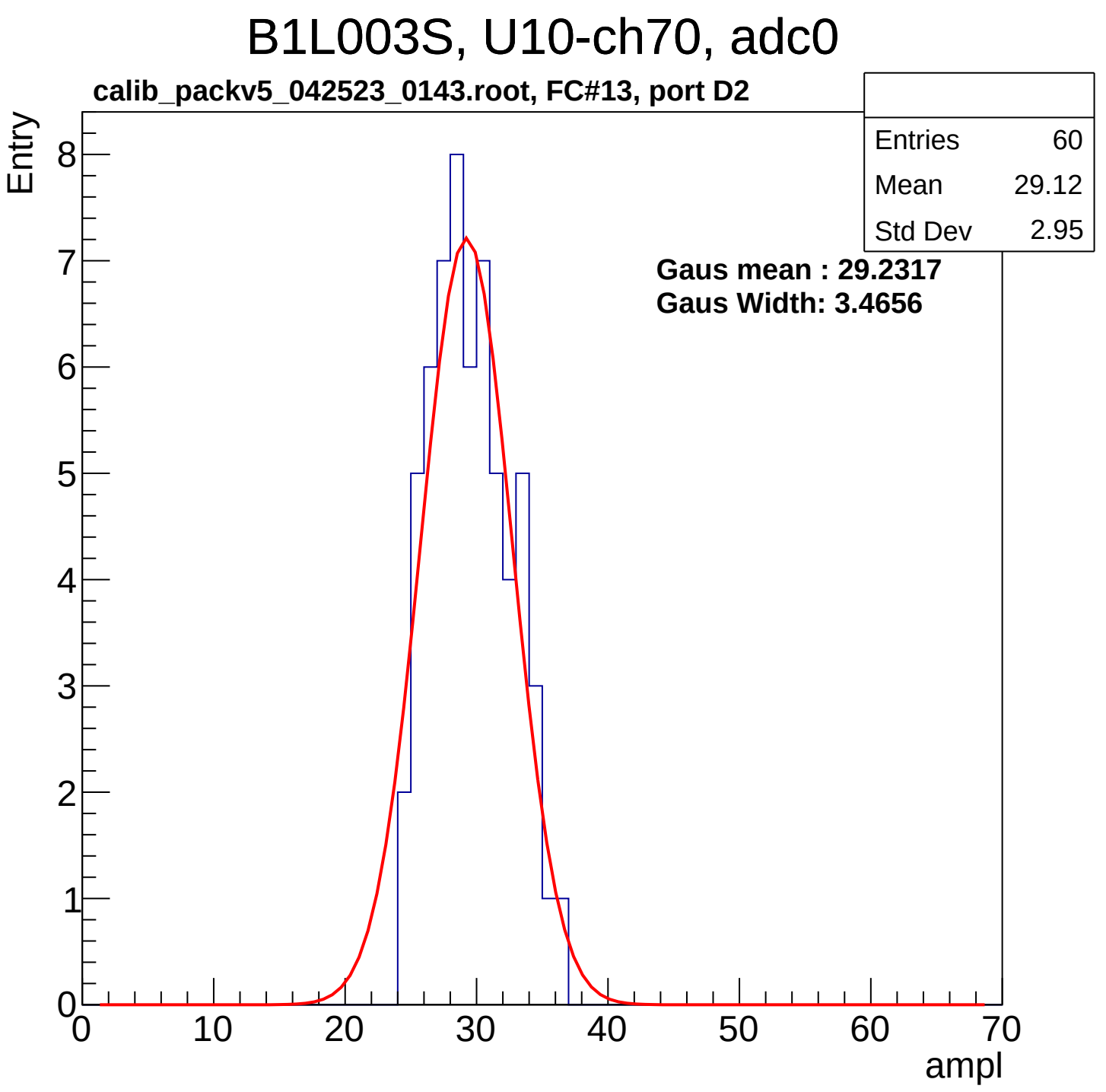
40
50
60
70
ampl


B1L003S, U10-ch70, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 29.12
Std Dev 2.95
Gaus mean : 29.2317
Gaus Width: 3.4656
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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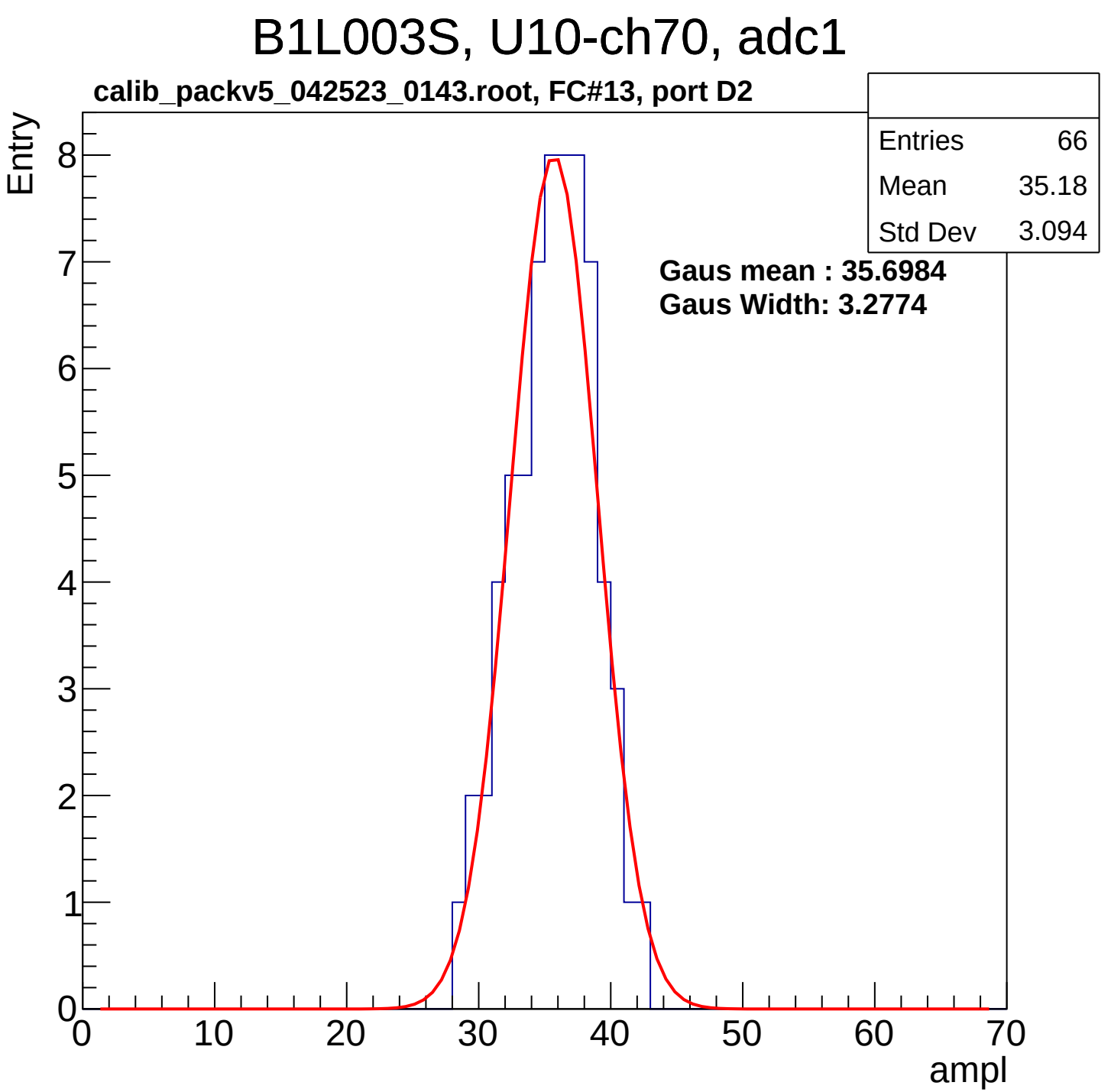
B1L003S, U10-ch70, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.18
Std Dev 3.094
Gaus mean : 35.6984
Gaus Width: 3.2774
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
50
60
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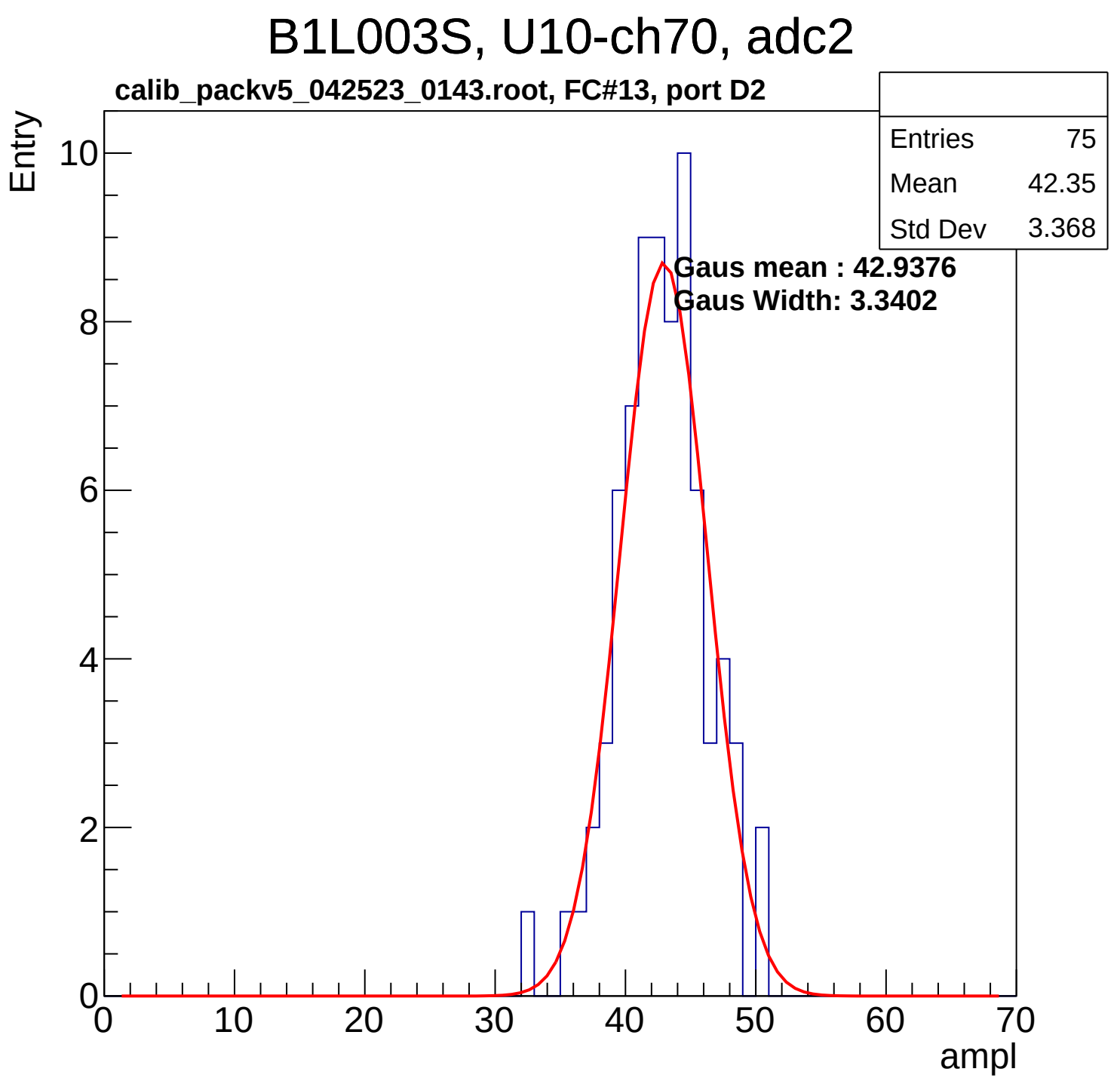
B1L003S, U10-ch70, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 42.35
Std Dev 3.368
Gaus mean : 42.9376
Gaus Width: 3.3402
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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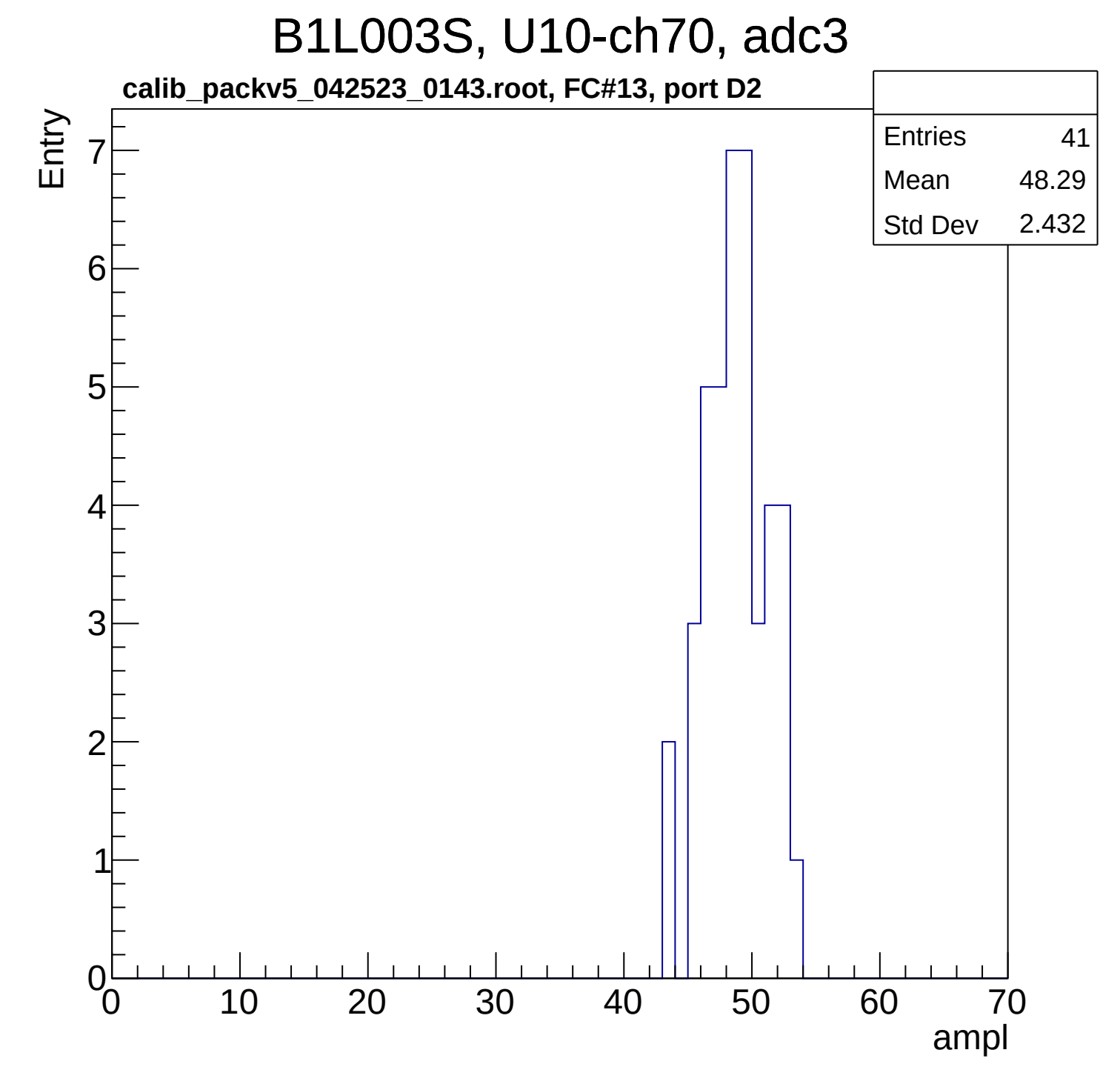
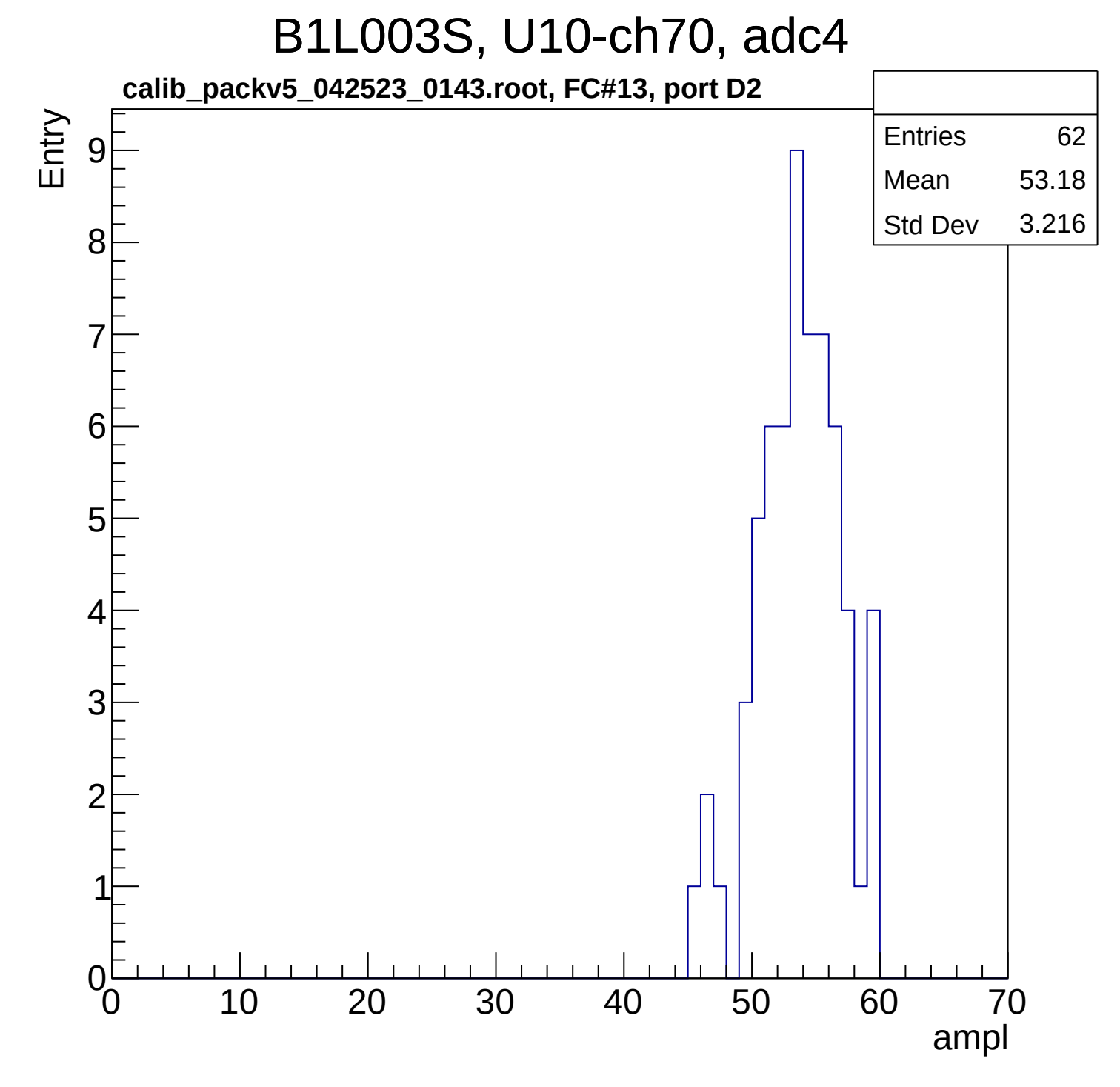
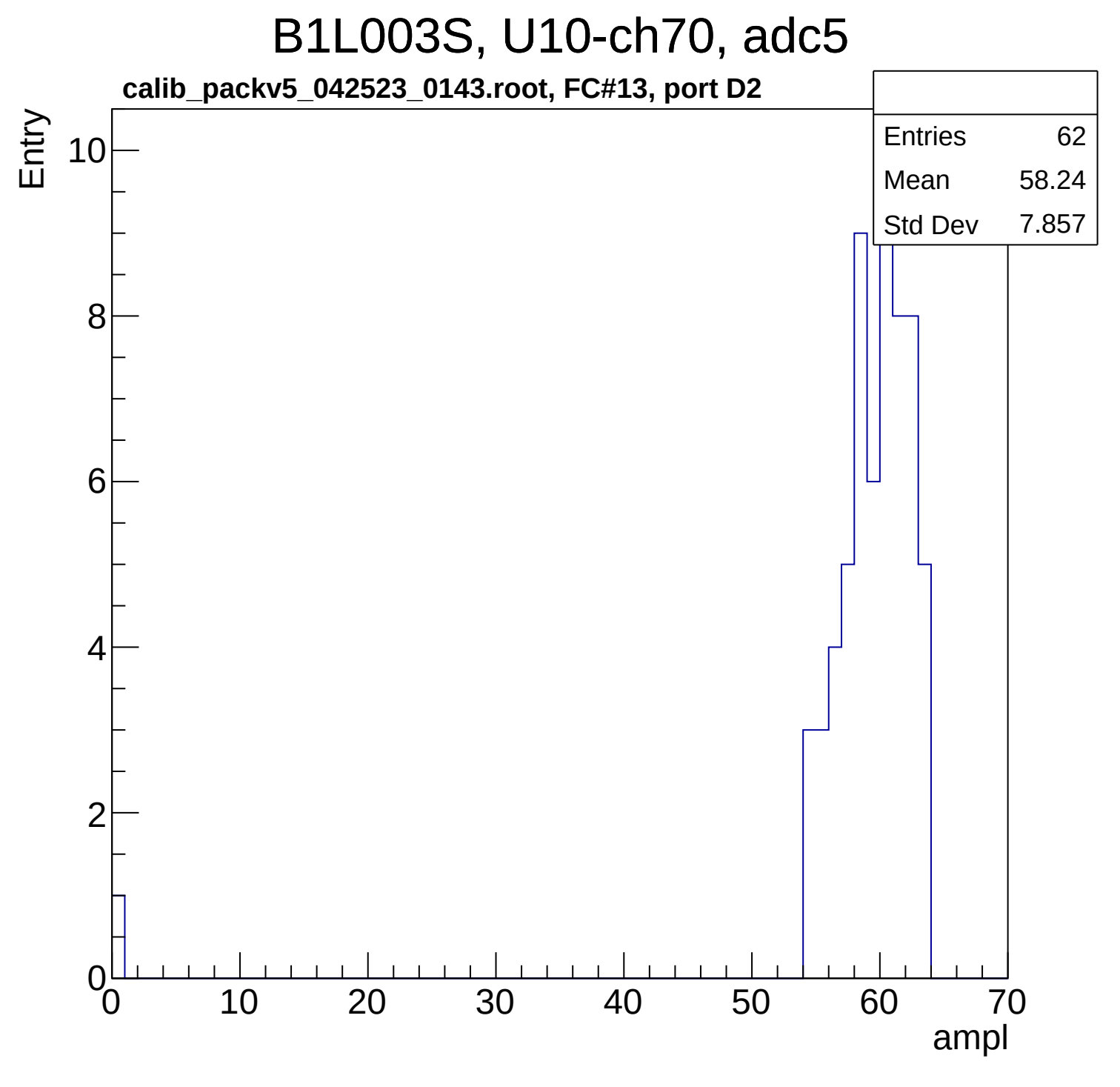
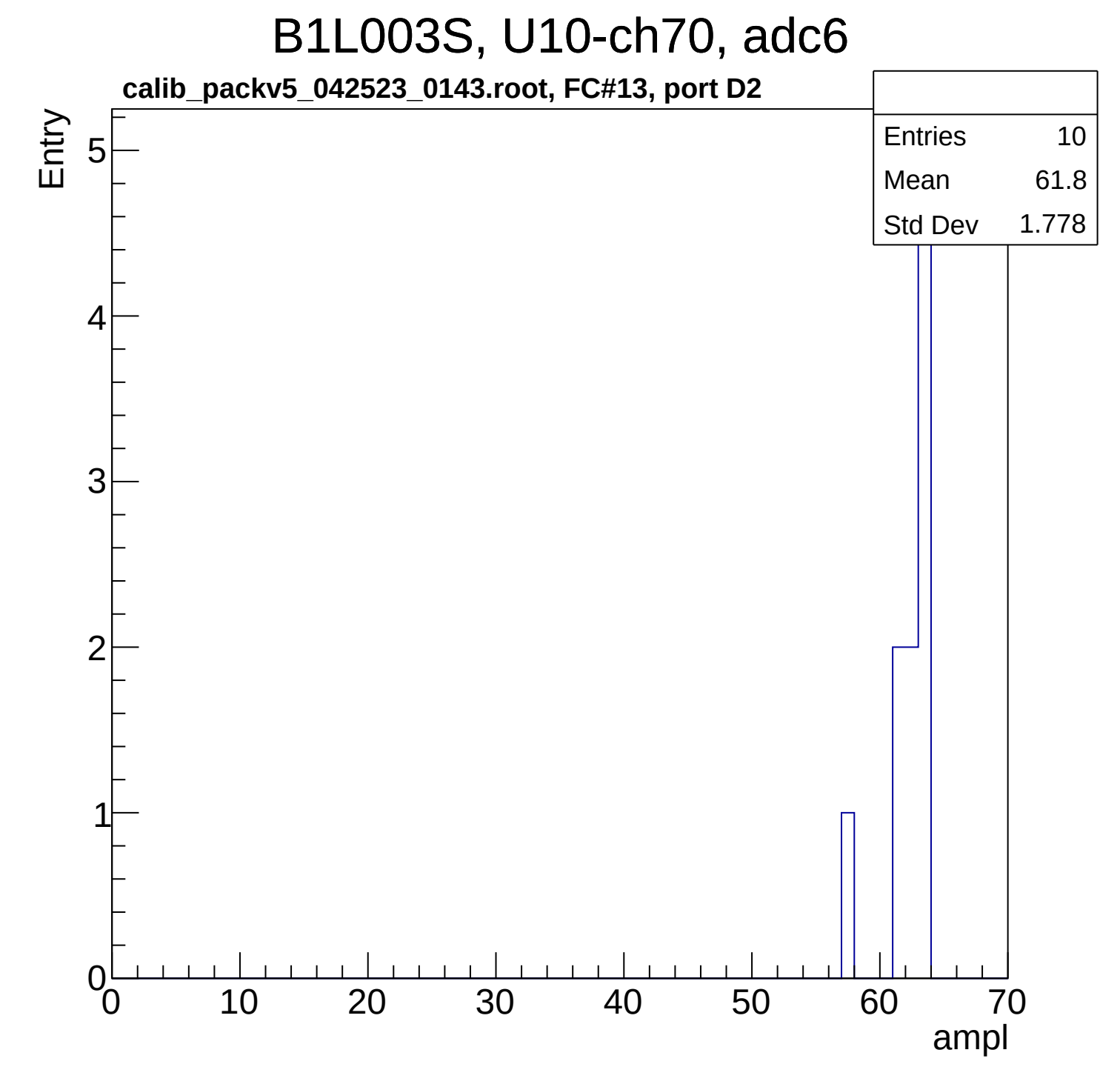
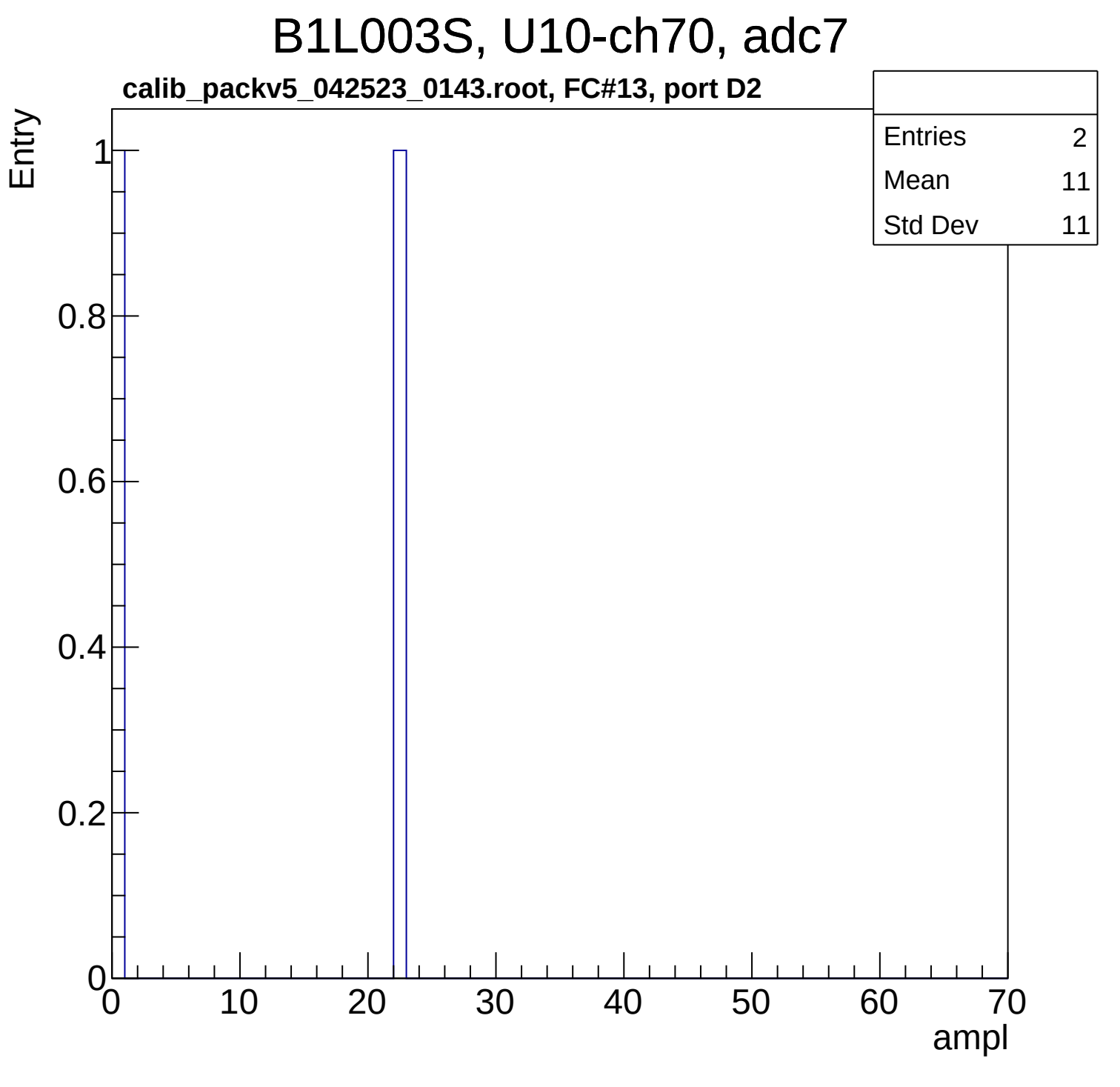
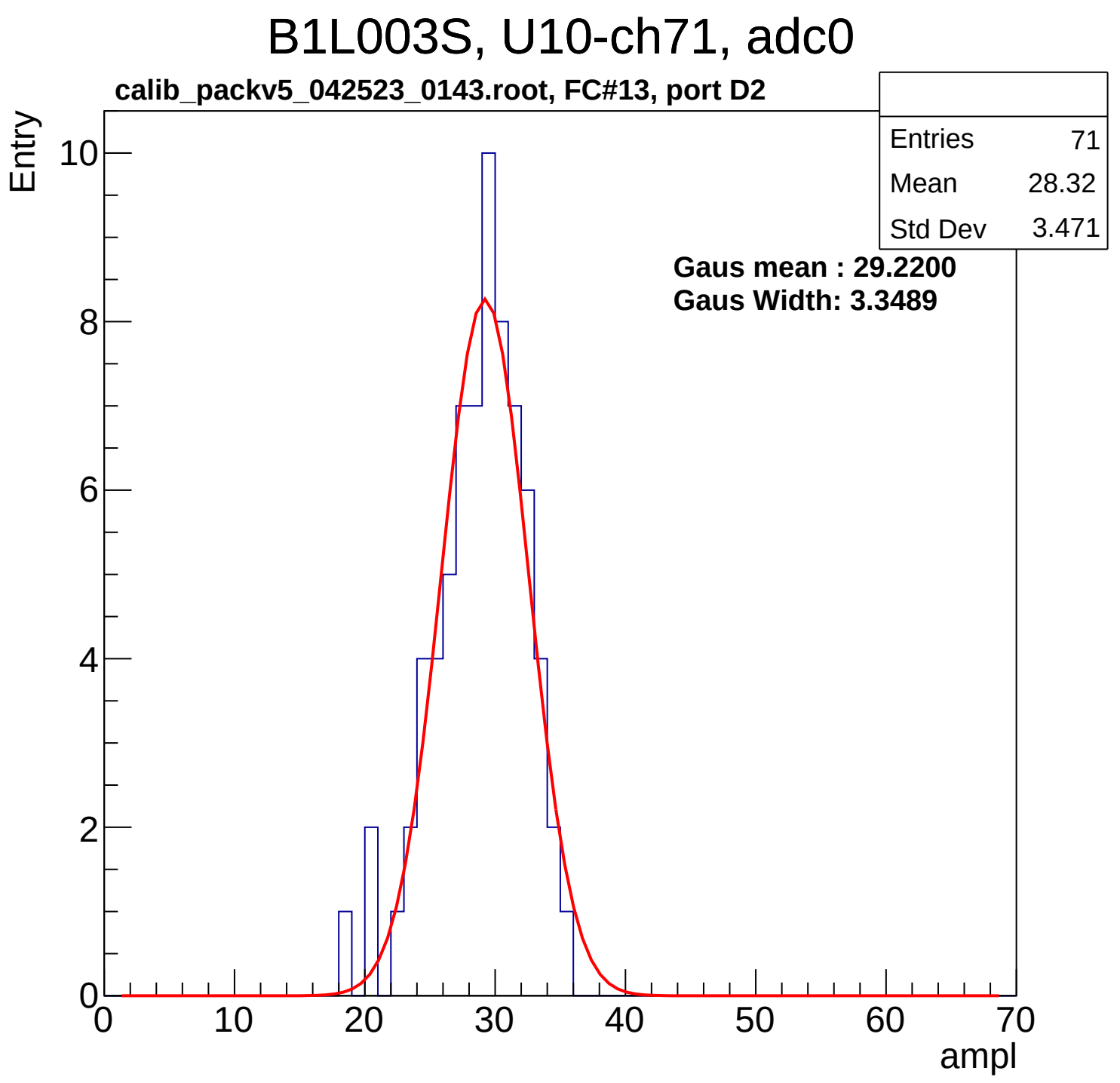

B1L003S, U10-ch70, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 48.29
Std Dev 2.432
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

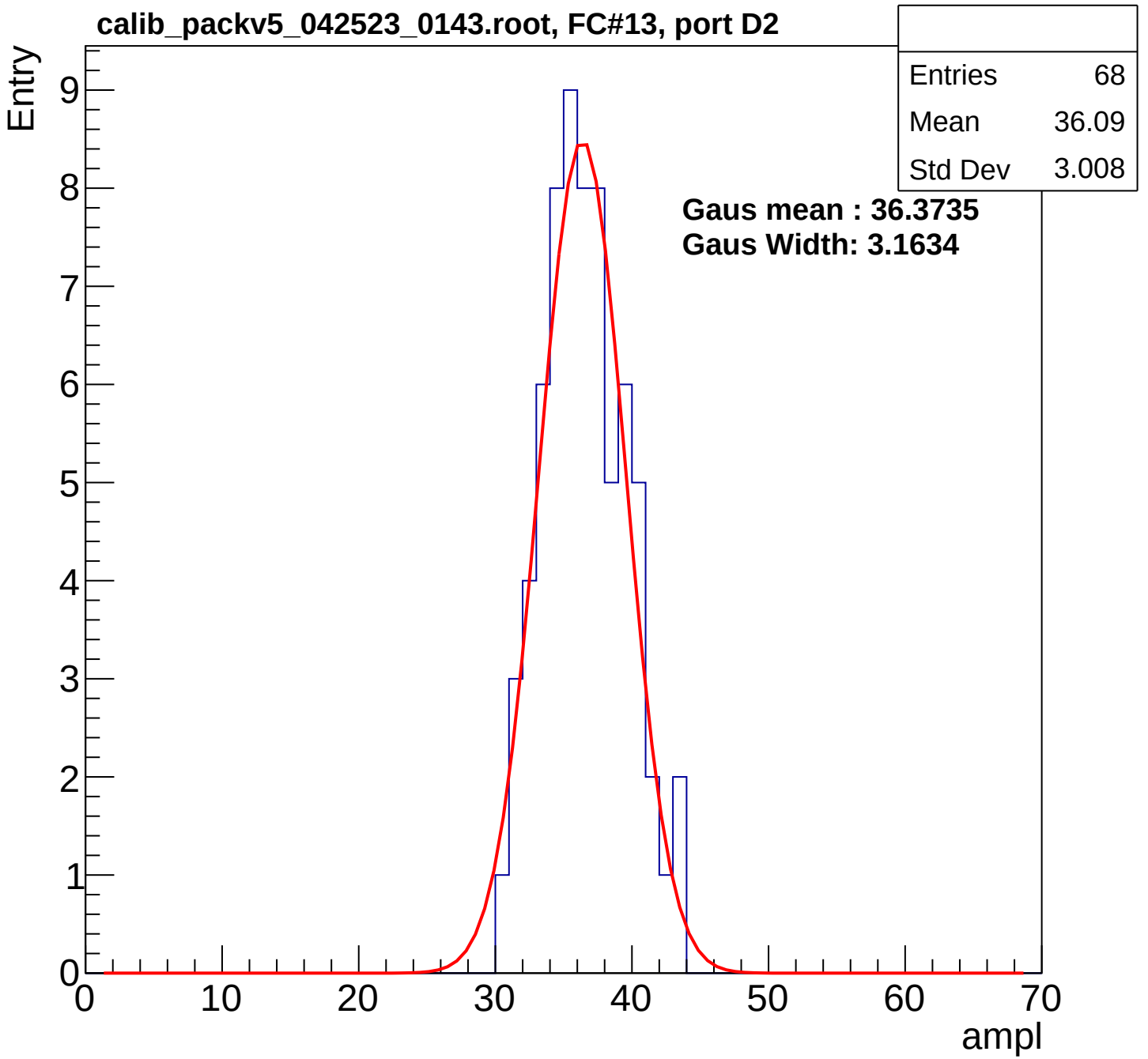

B1L003S, U10-ch70, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 53.18
Std Dev 3.216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

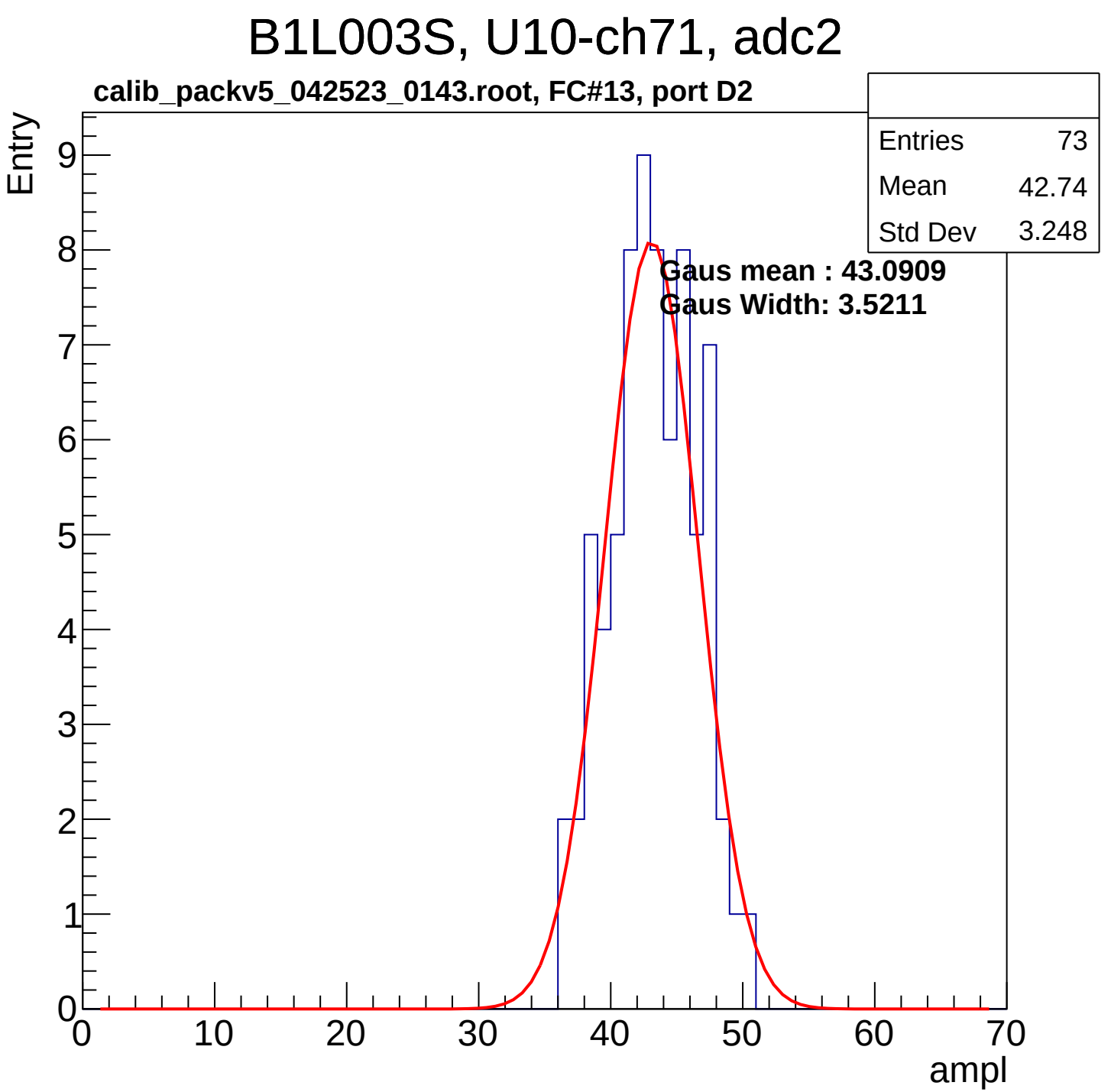

B1L003S, U10-ch70, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.24
Std Dev 7.857
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch70, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.8
Std Dev 1.778
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U10-ch70, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch71, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 28.32
Std Dev 3.471
Gaus mean : 29.2200
Gaus Width: 3.3489
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch71, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.09
Std Dev 3.008
Gaus mean : 36.3735
Gaus Width: 3.1634
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch71, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 42.74
Std Dev 3.248
Gaus mean : 43.0909
Gaus Width: 3.5211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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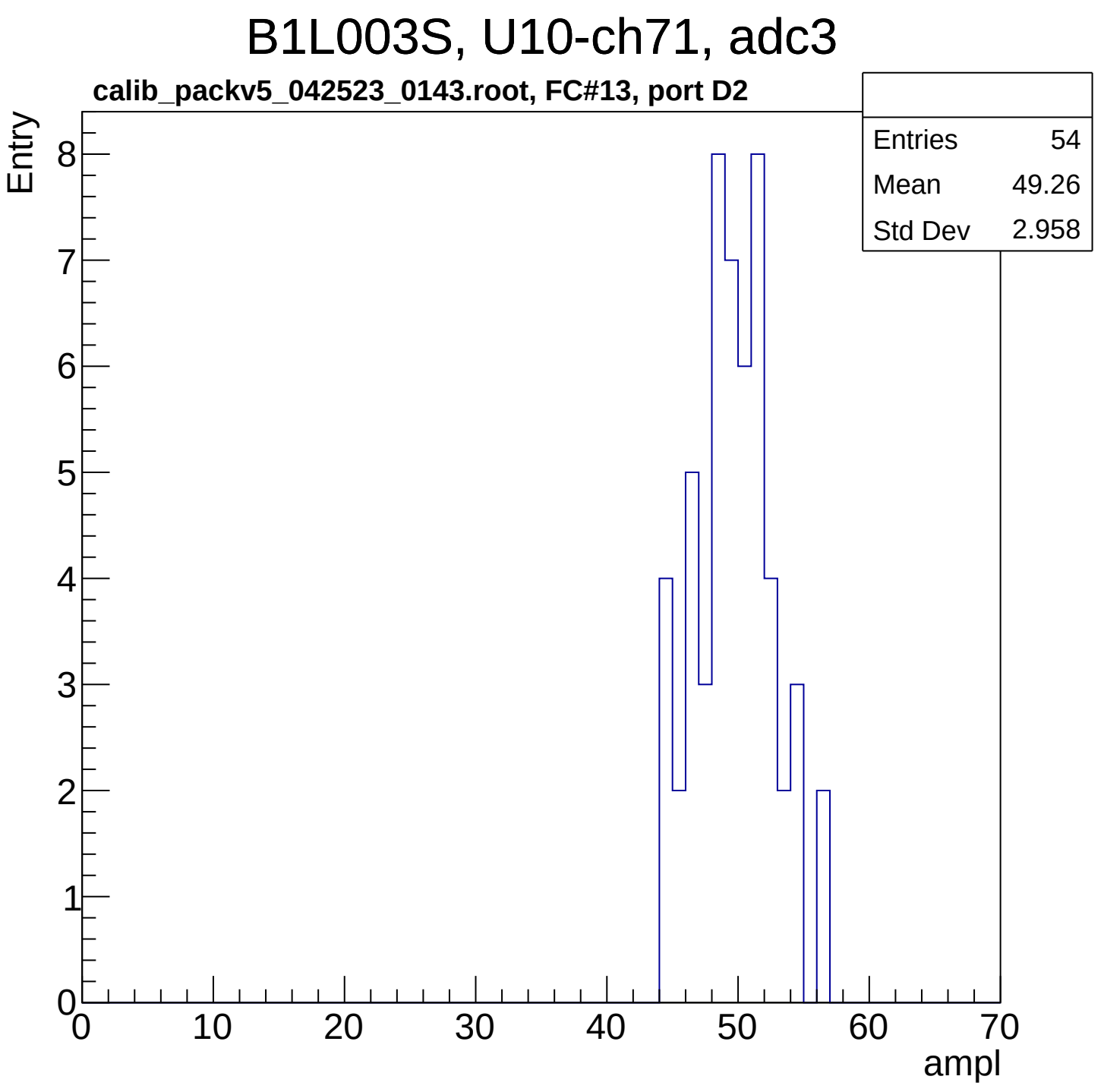
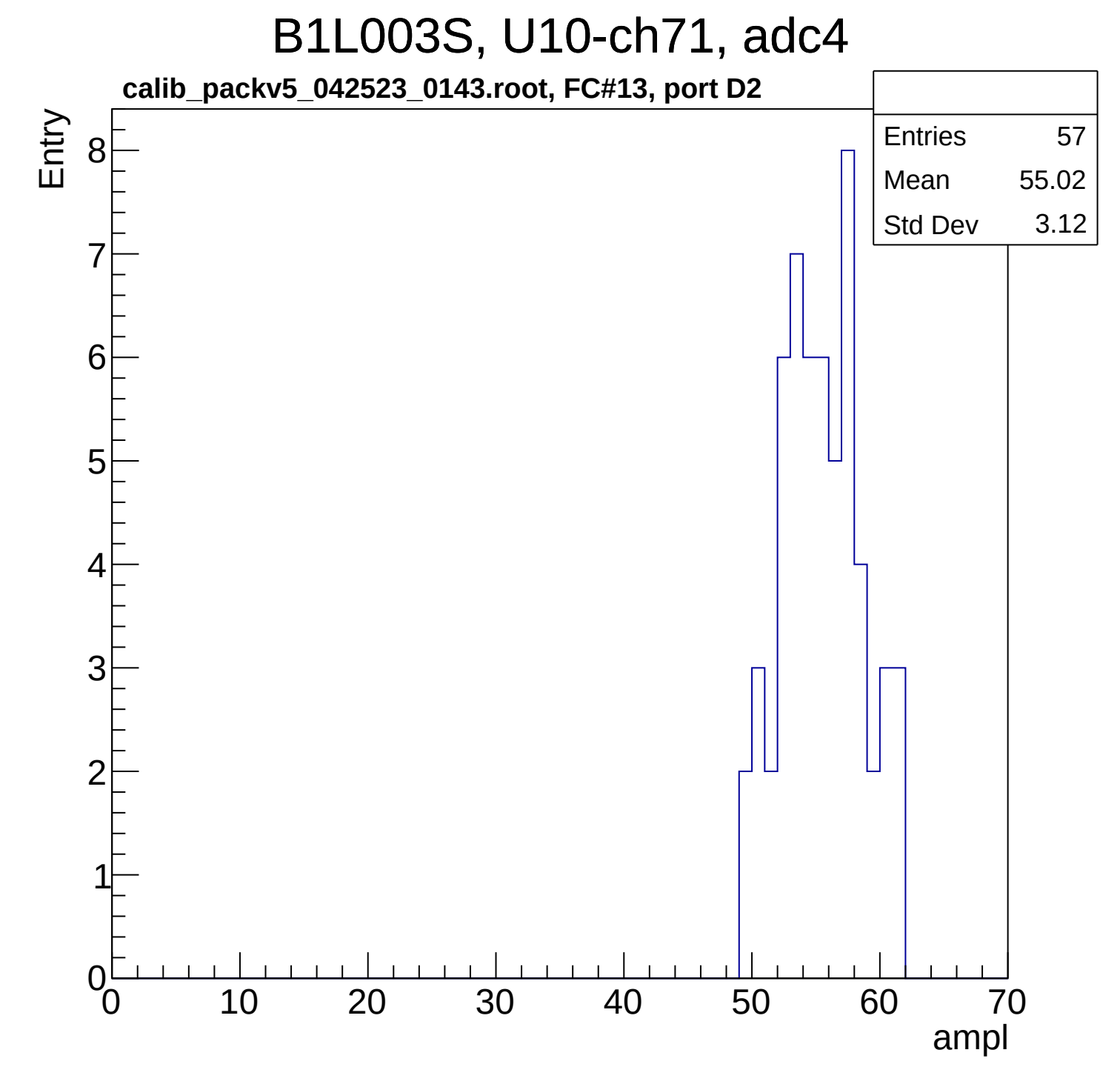
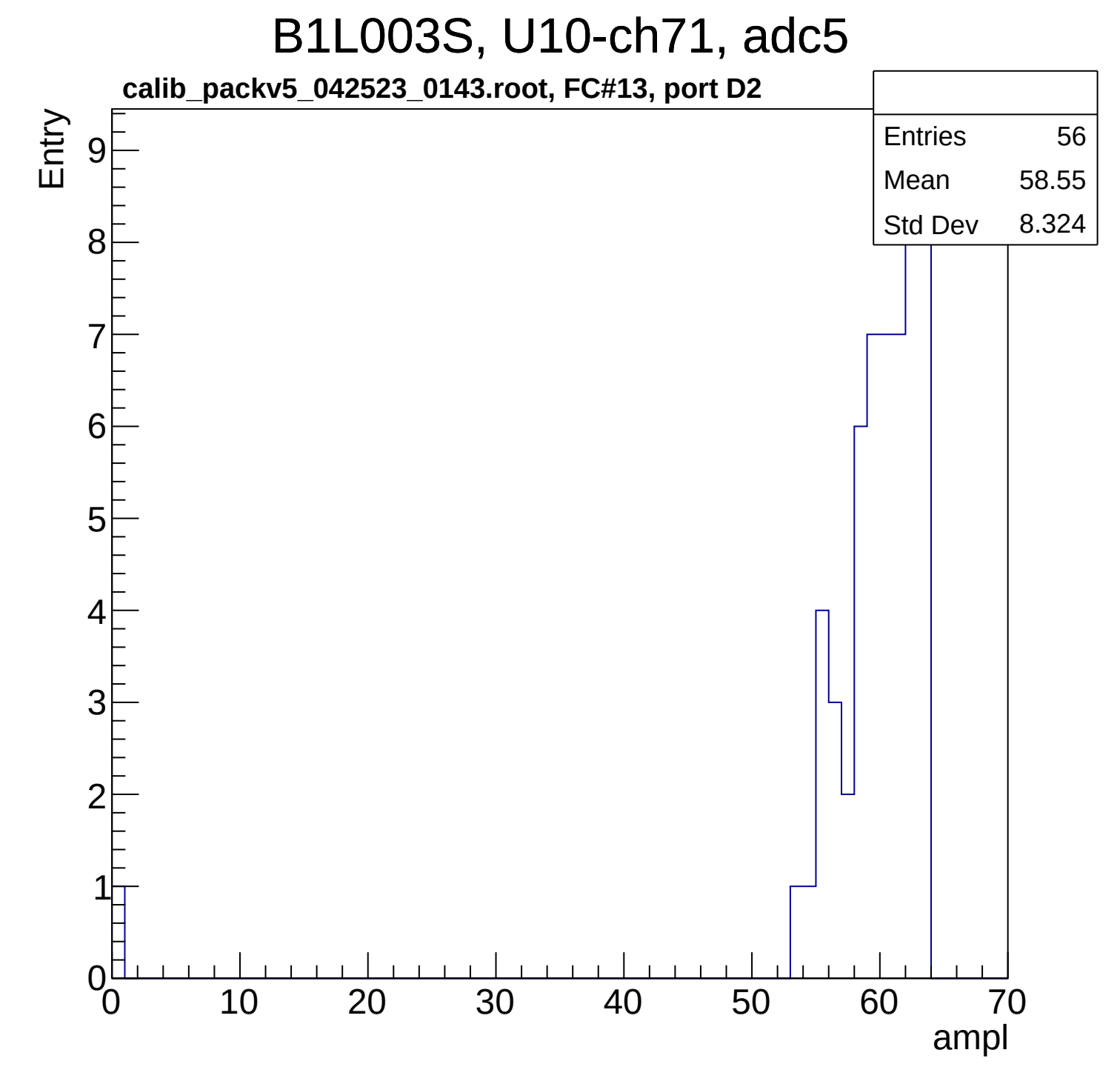

B1L003S, U10-ch71, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.26
Std Dev 2.958
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch71, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.02
Std Dev 3.12
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch71, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.55
Std Dev 8.324
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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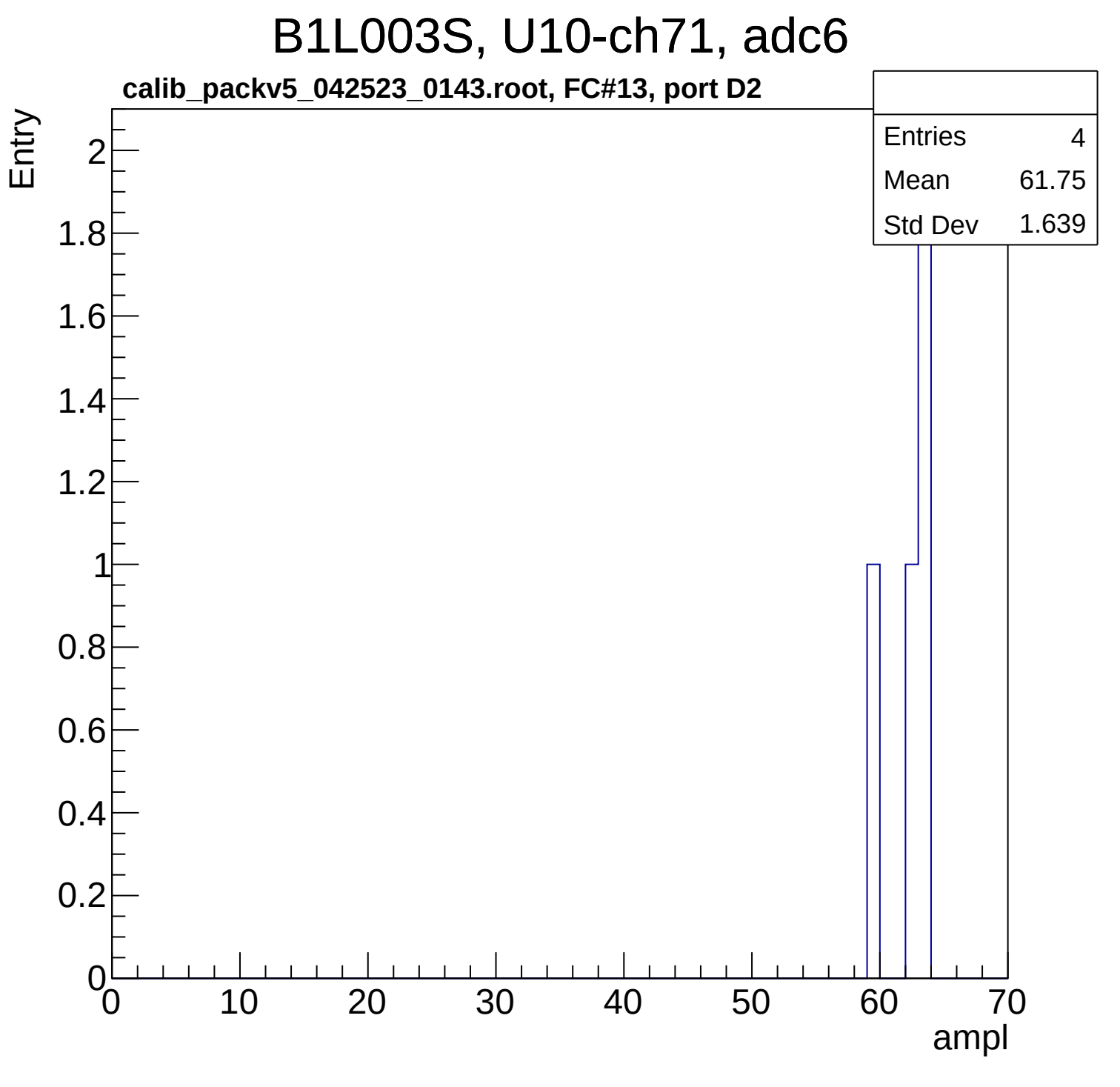
40
50
60
70


B1L003S, U10-ch71, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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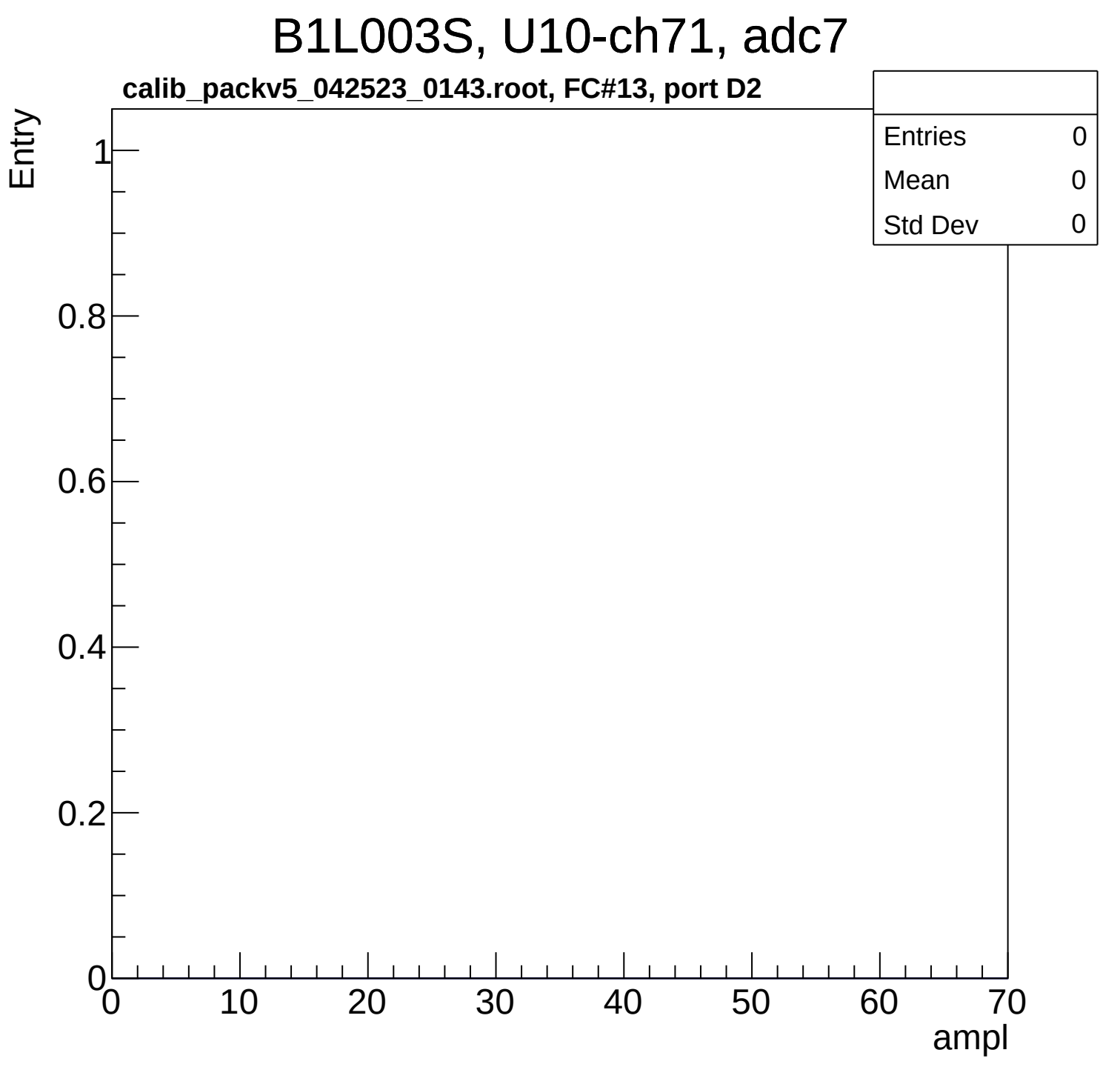
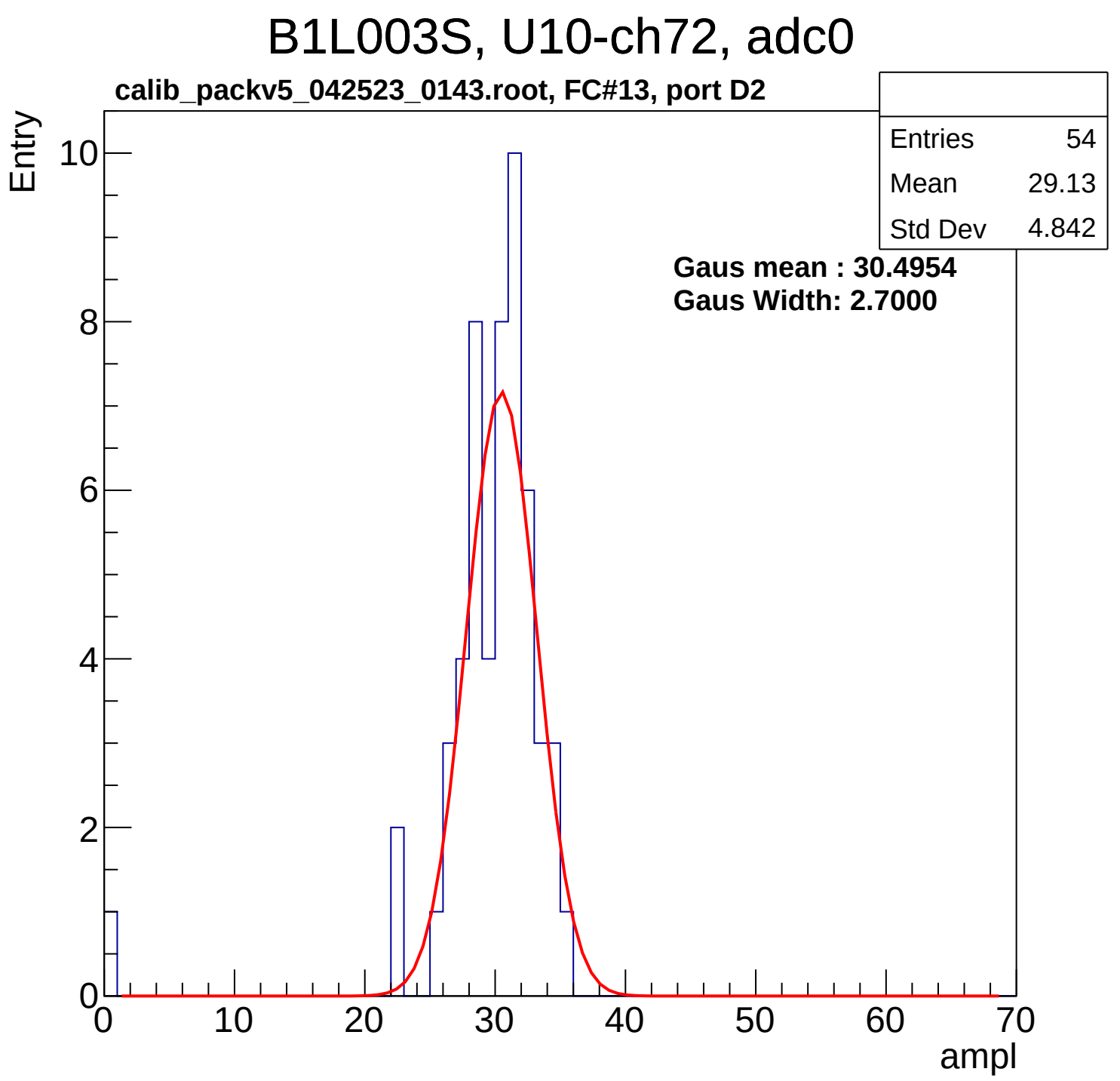
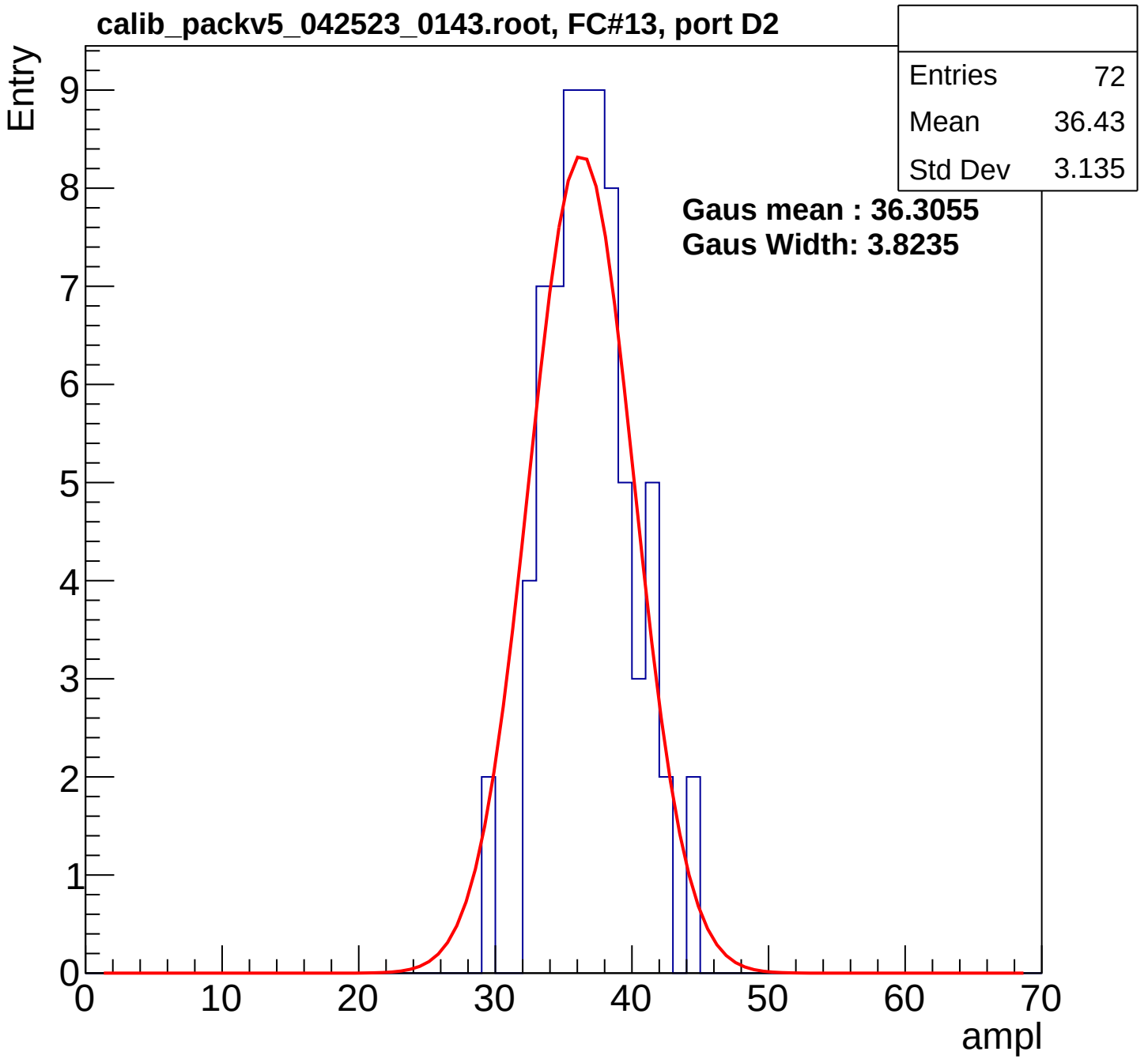
20
30
40
50
60
70


B1L003S, U10-ch71, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch72, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.13
Std Dev 4.842
Gaus mean : 30.4954
Gaus Width: 2.7000
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch72, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.43
Std Dev 3.135
Gaus mean : 36.3055
Gaus Width: 3.8235
Entry
ampl
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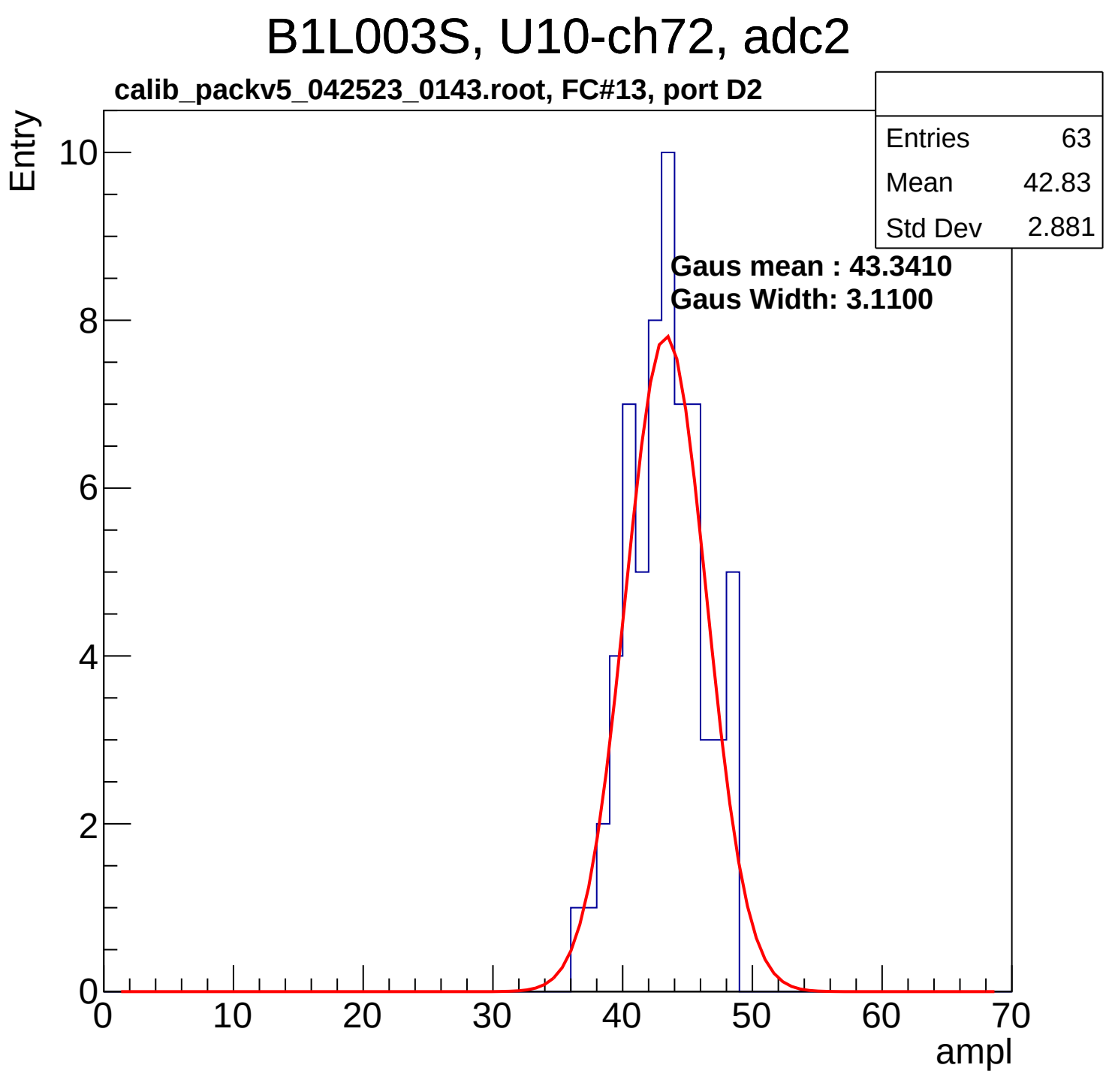
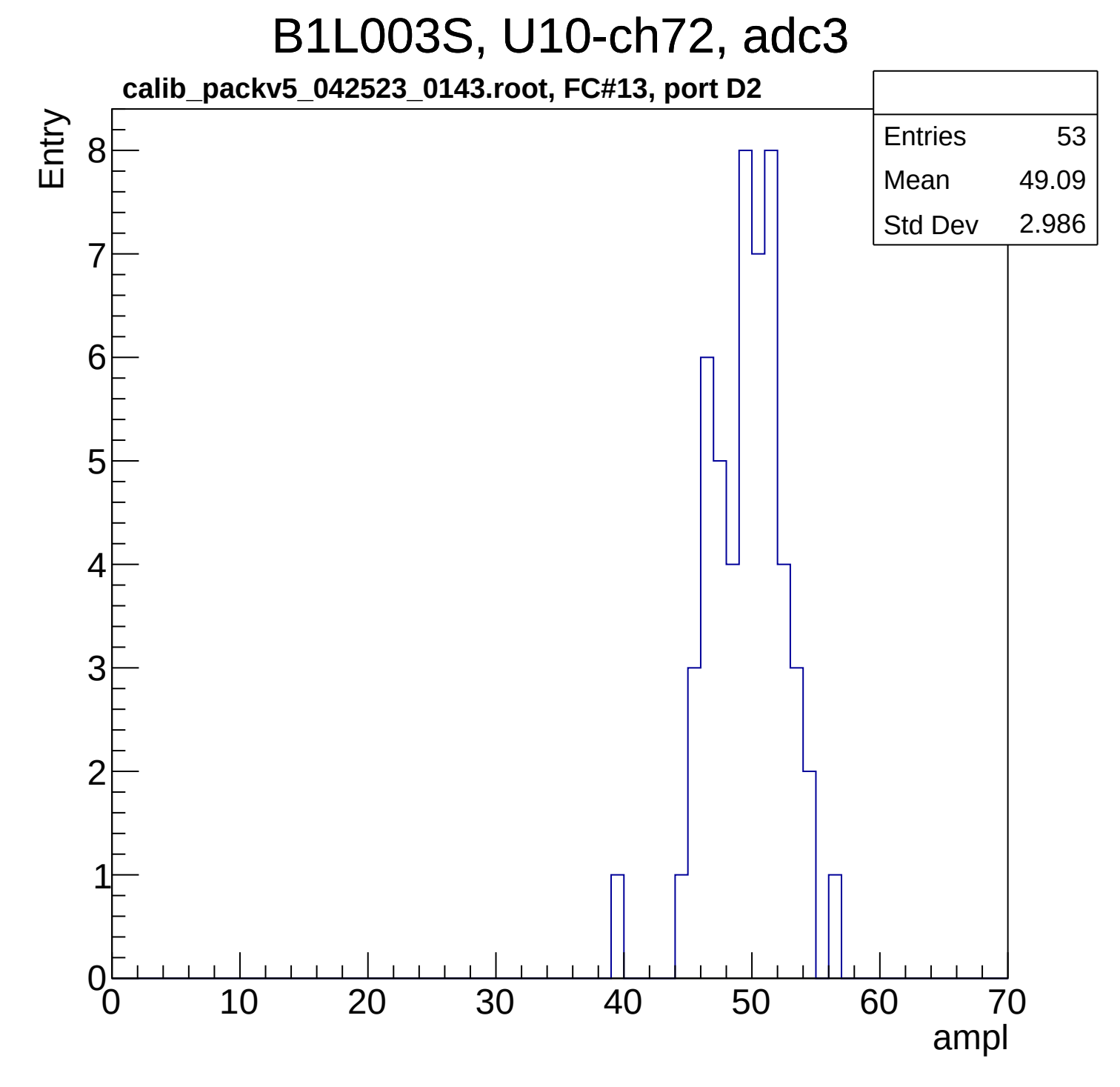
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch72, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 42.83
Std Dev 2.881
Gaus mean : 43.3410
Gaus Width: 3.1100
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch72, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 49.09
Std Dev 2.986
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
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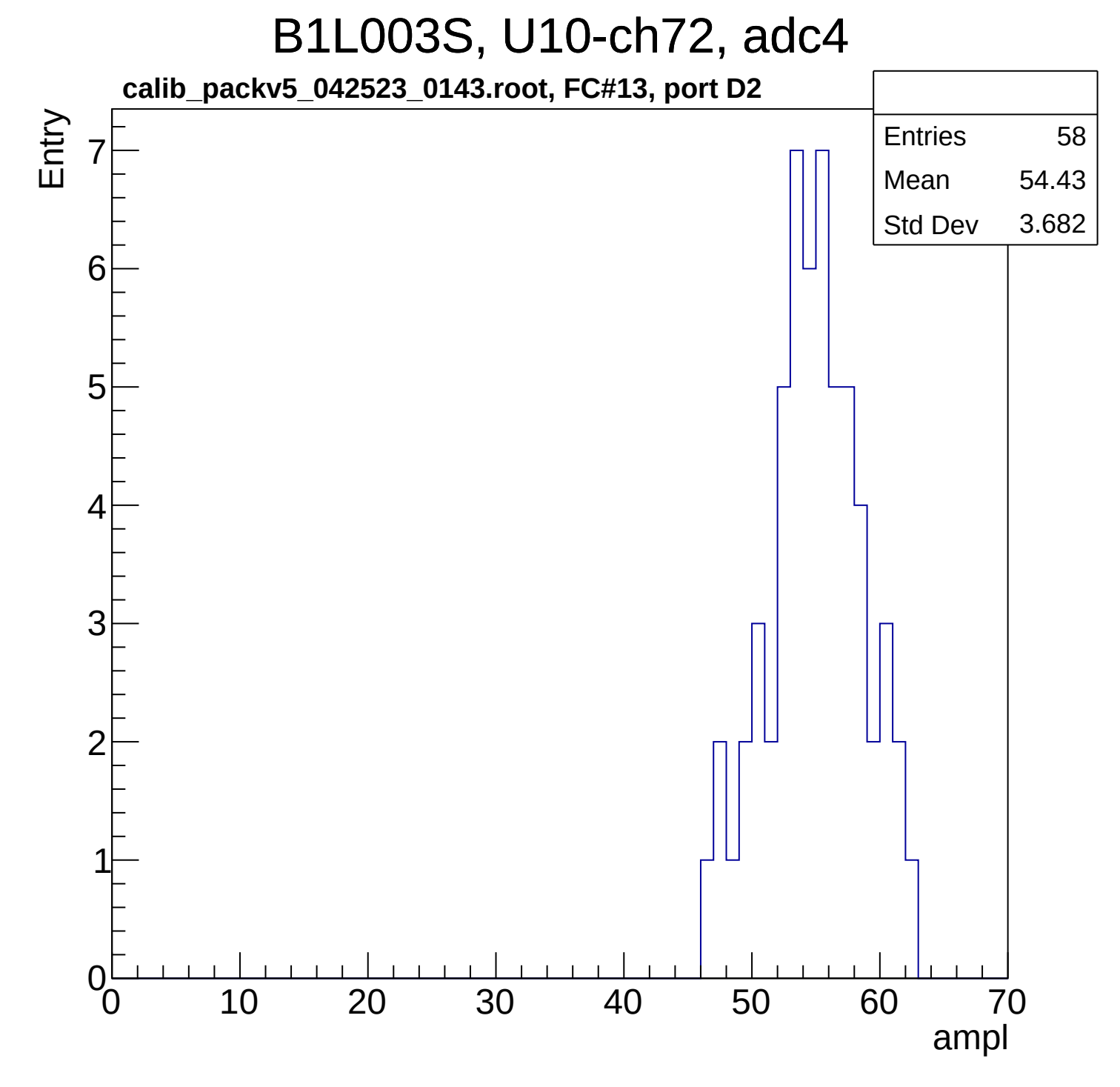
60
70


B1L003S, U10-ch72, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.43
Std Dev 3.682
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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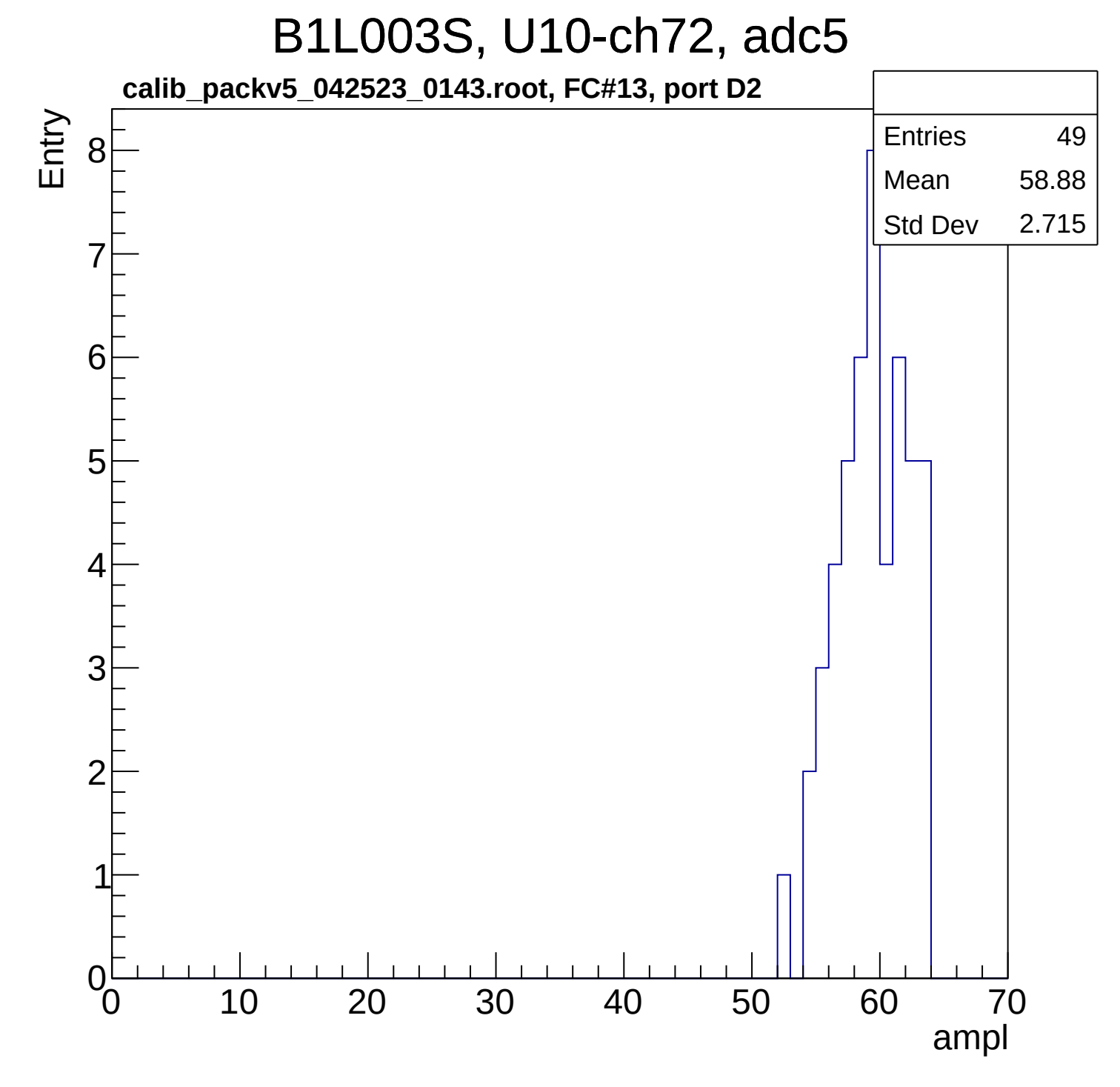
40
50
60
70


B1L003S, U10-ch72, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.88
Std Dev 2.715
Entry
ampl
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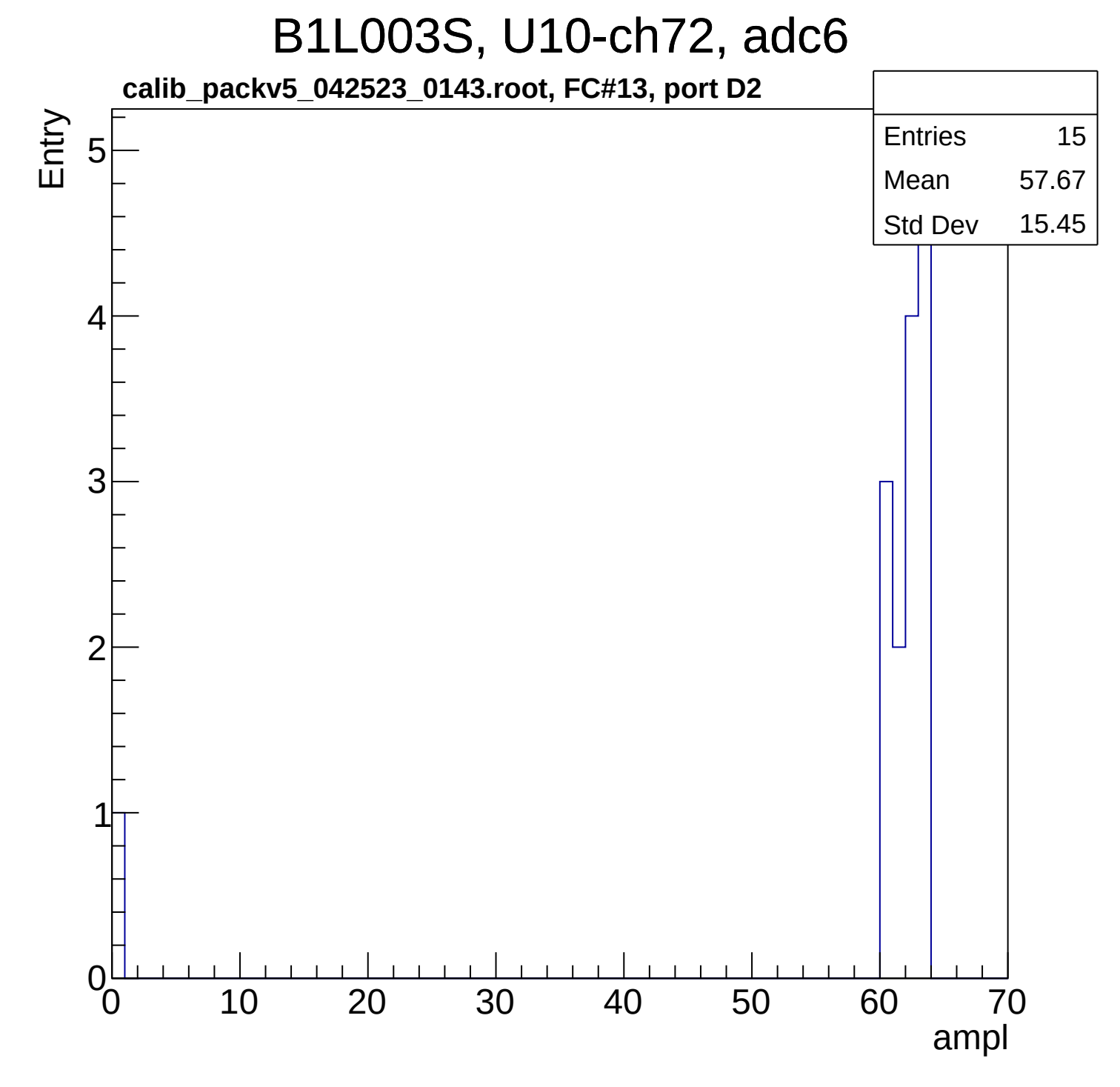
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch72, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.67
Std Dev 15.45
Entry
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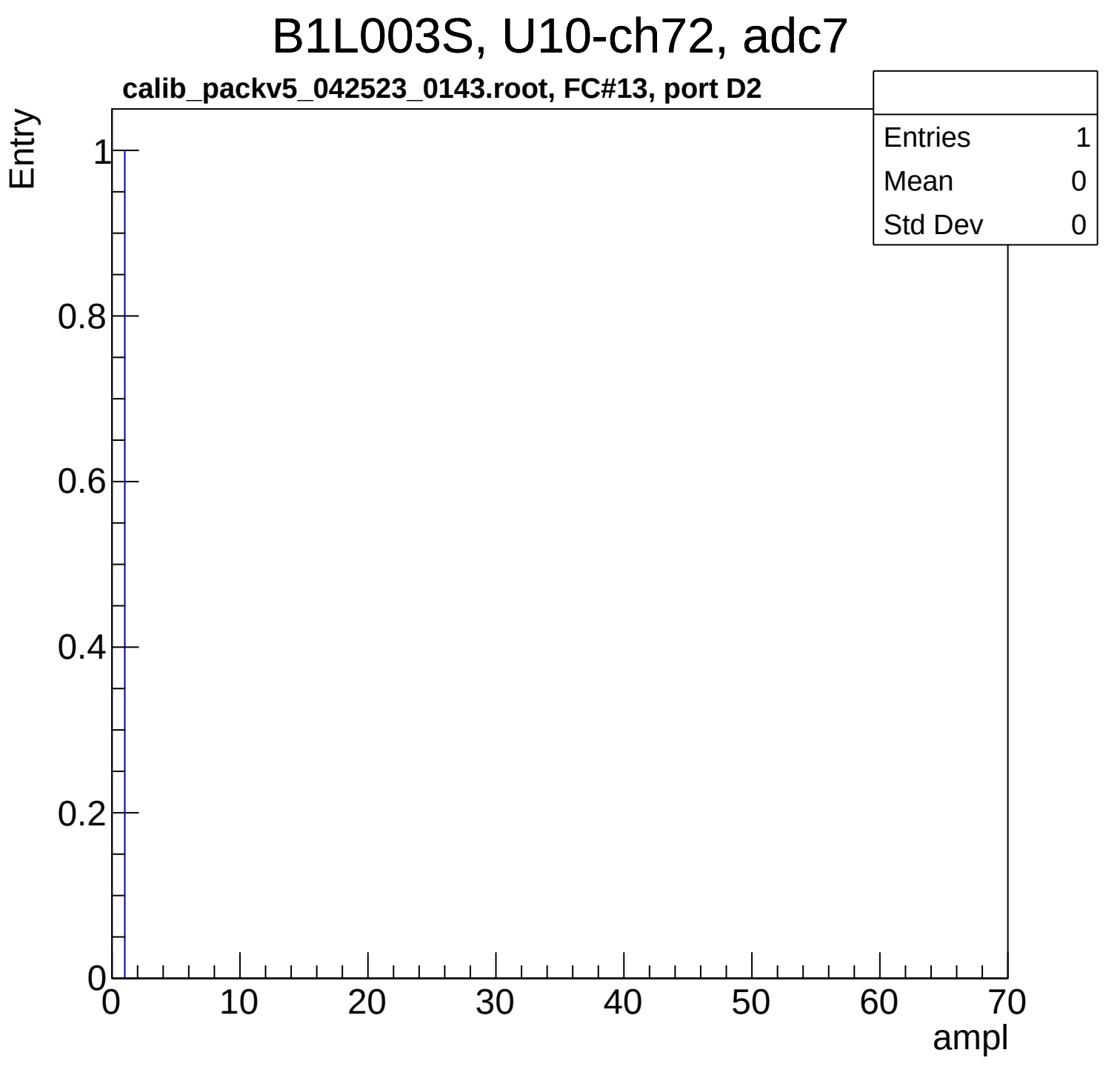
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch72, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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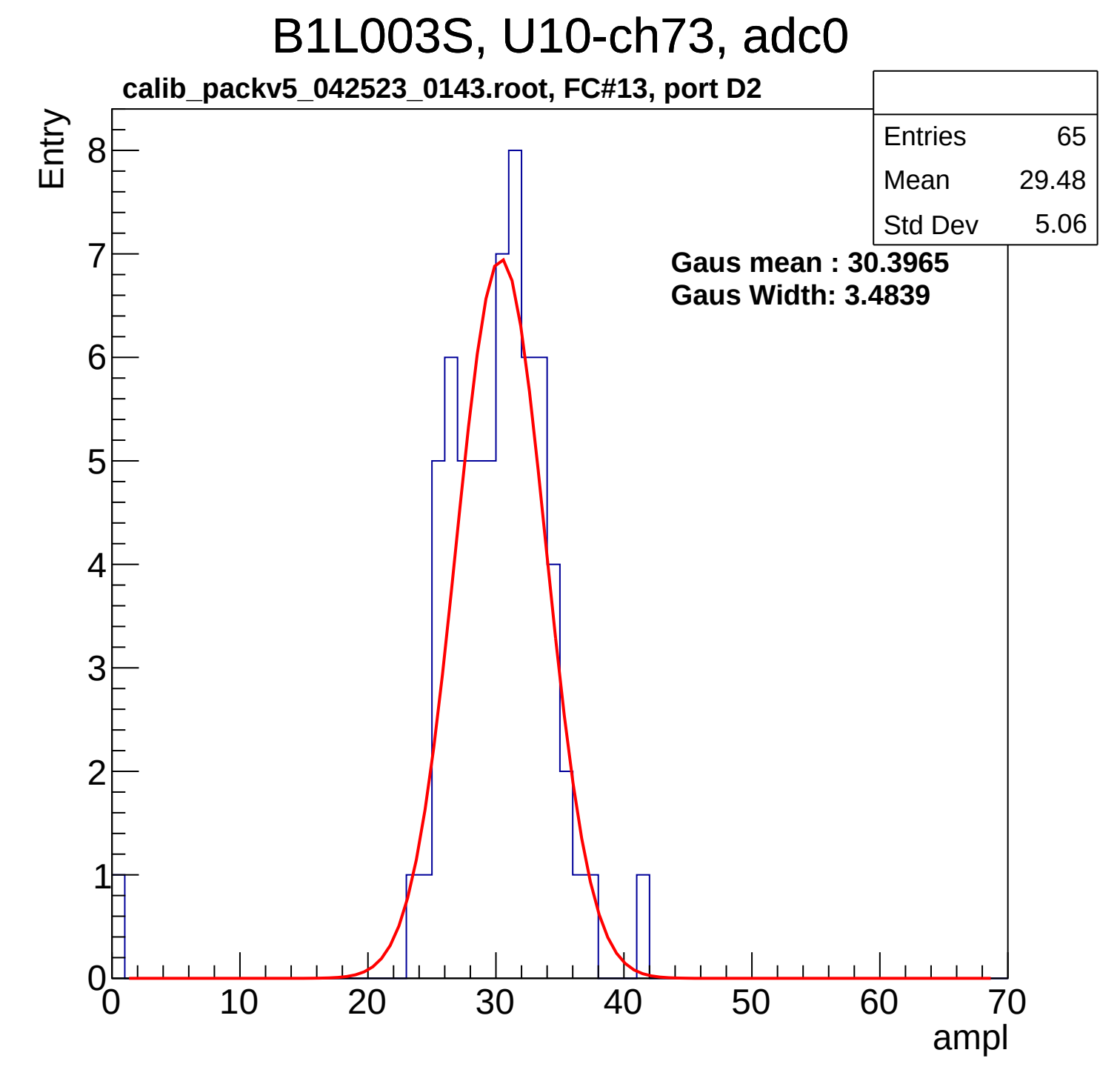
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch73, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.48
Std Dev 5.06
Gaus mean : 30.3965
Gaus Width: 3.4839
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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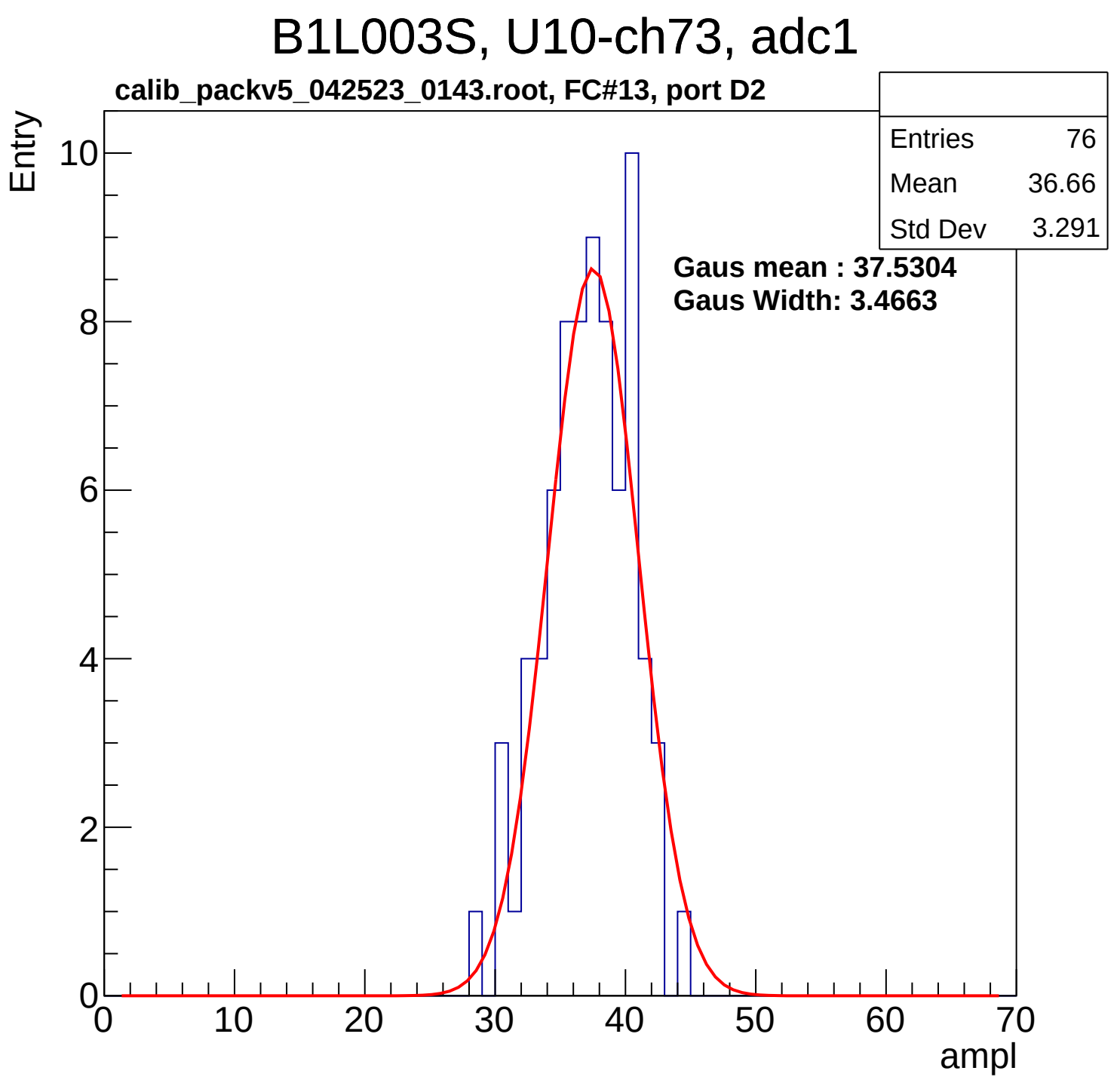
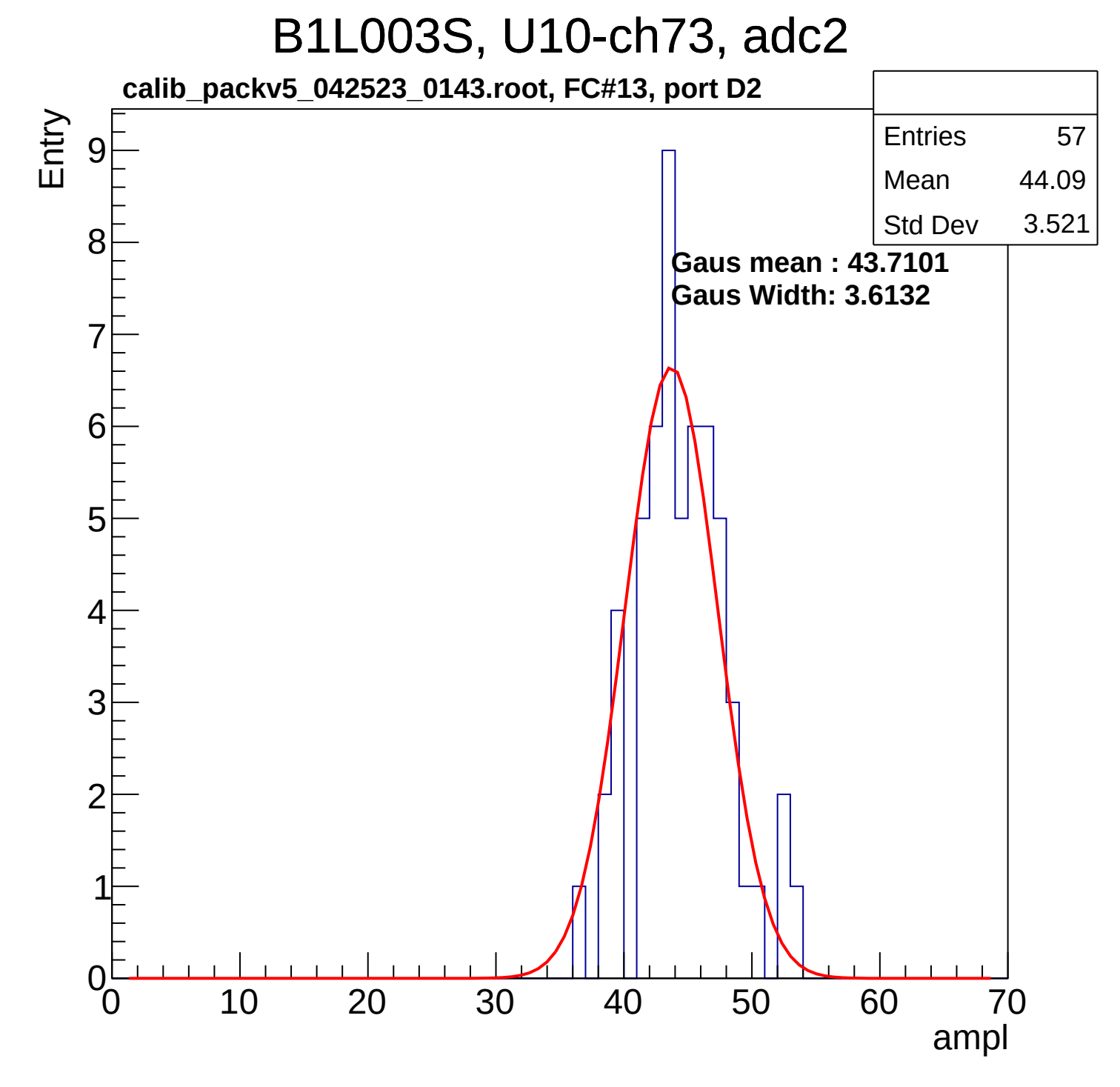

B1L003S, U10-ch73, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.66
Std Dev 3.291
Gaus mean : 37.5304
Gaus Width: 3.4663
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch73, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 44.09
Std Dev 3.521
Gaus mean : 43.7101
Gaus Width: 3.6132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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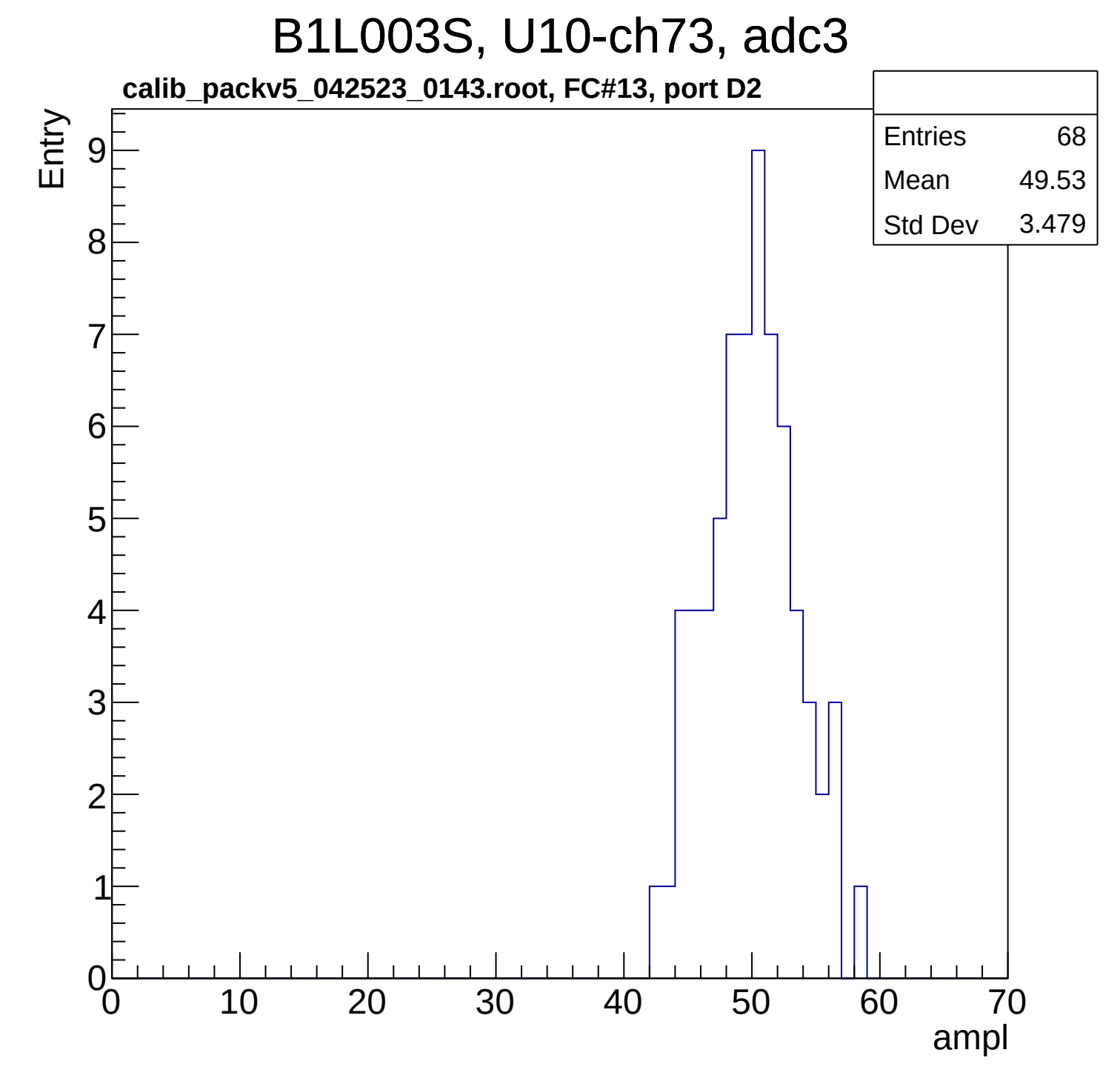
B1L003S, U10-ch73, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.53
Std Dev 3.479
Entry
ampl
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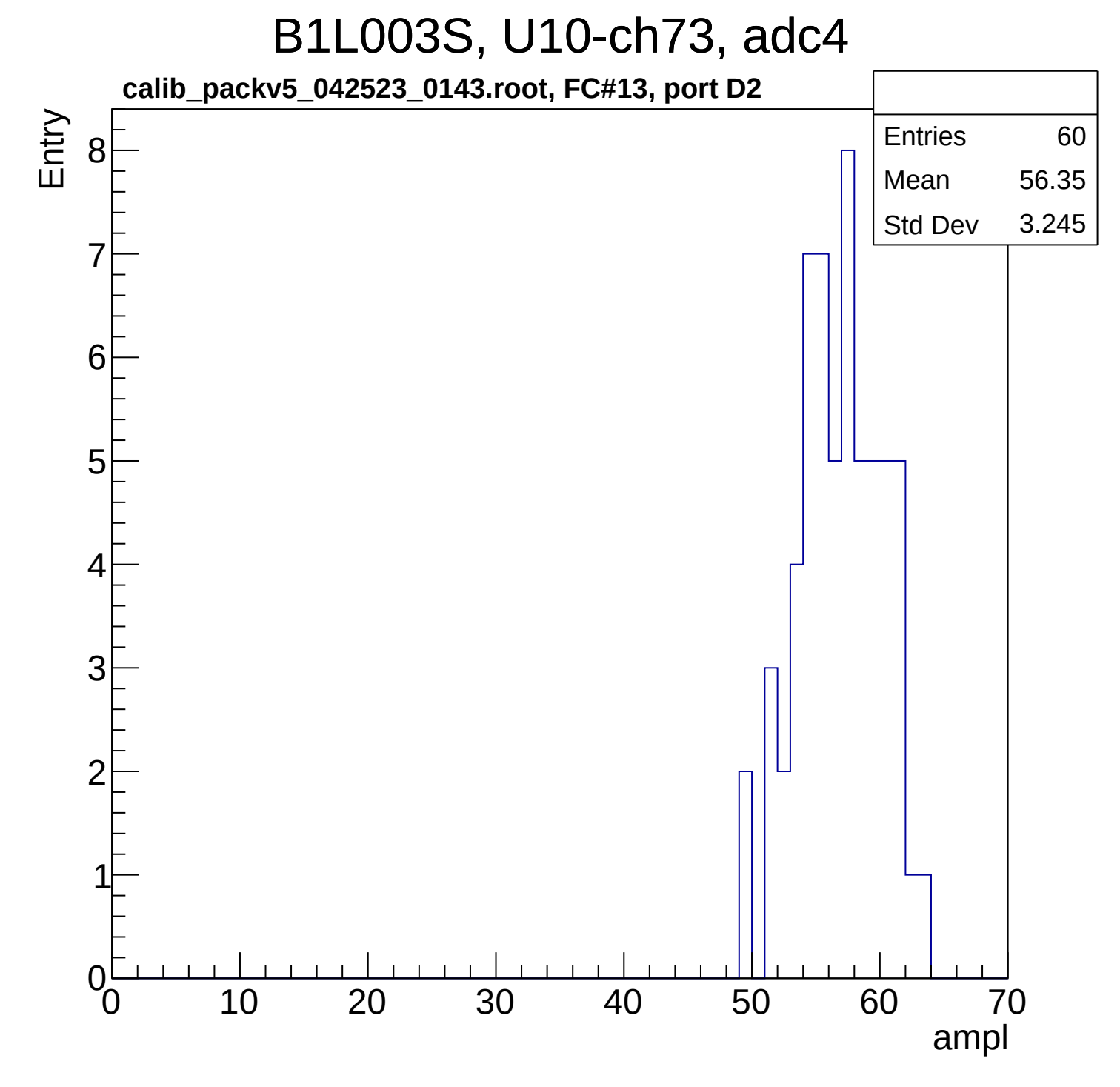
B1L003S, U10-ch73, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 56.35
Std Dev 3.245
Entry
ampl
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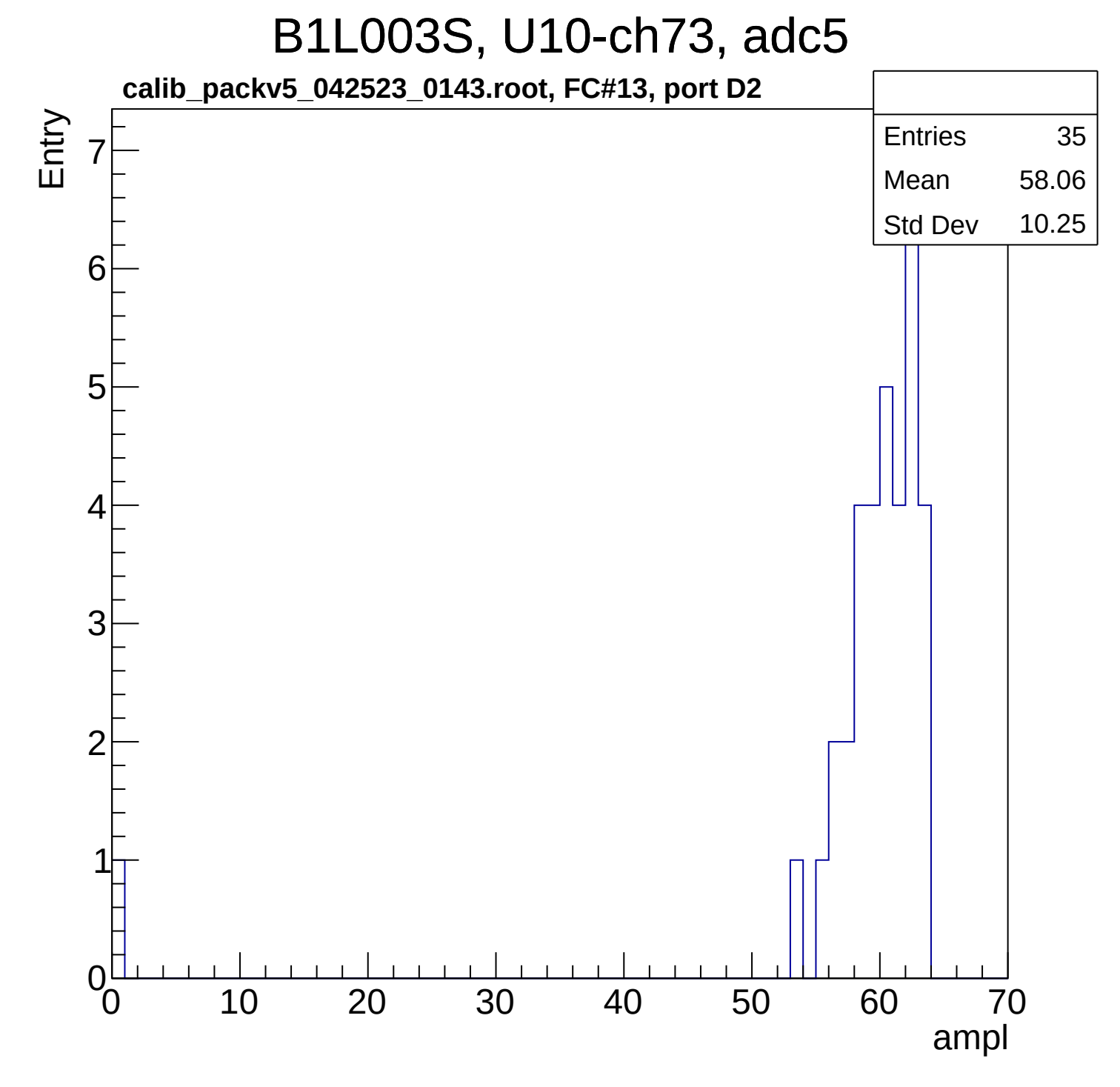
B1L003S, U10-ch73, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 35
Mean 58.06
Std Dev 10.25
Entry
ampl
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0
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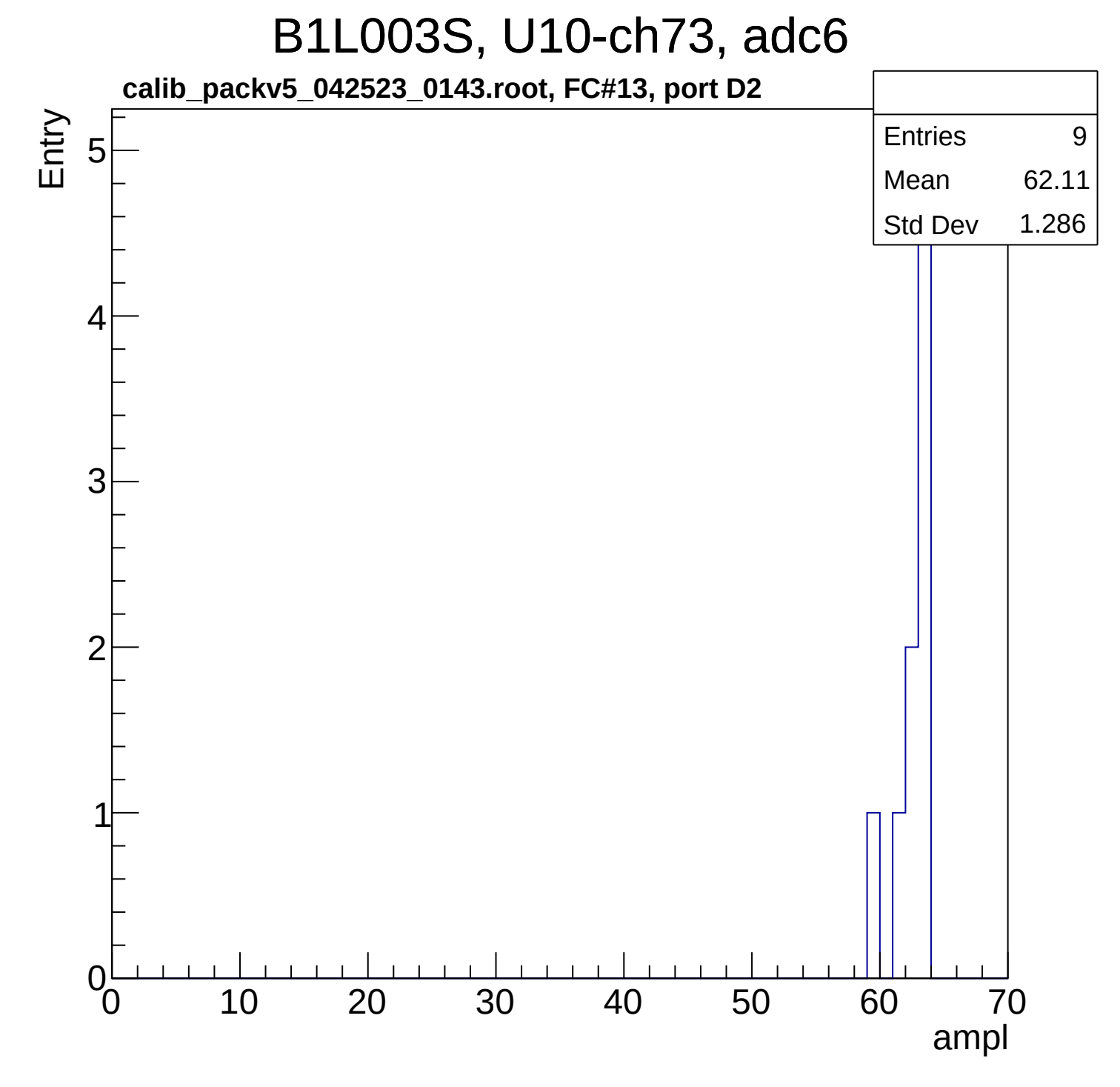
B1L003S, U10-ch73, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 1.286
Entry
0
1
2
3
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5
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10
20
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50
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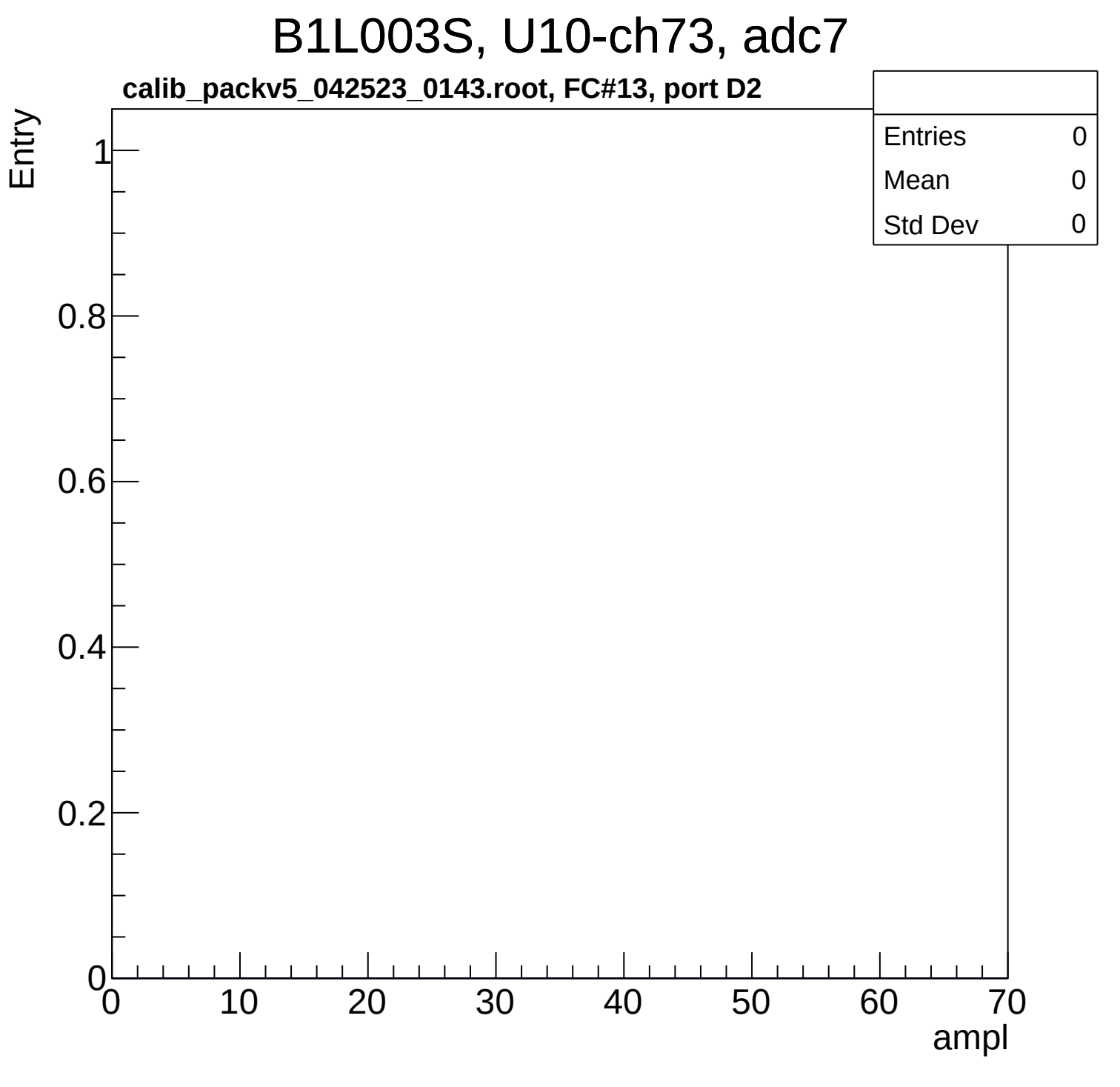
70
ampl


B1L003S, U10-ch73, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
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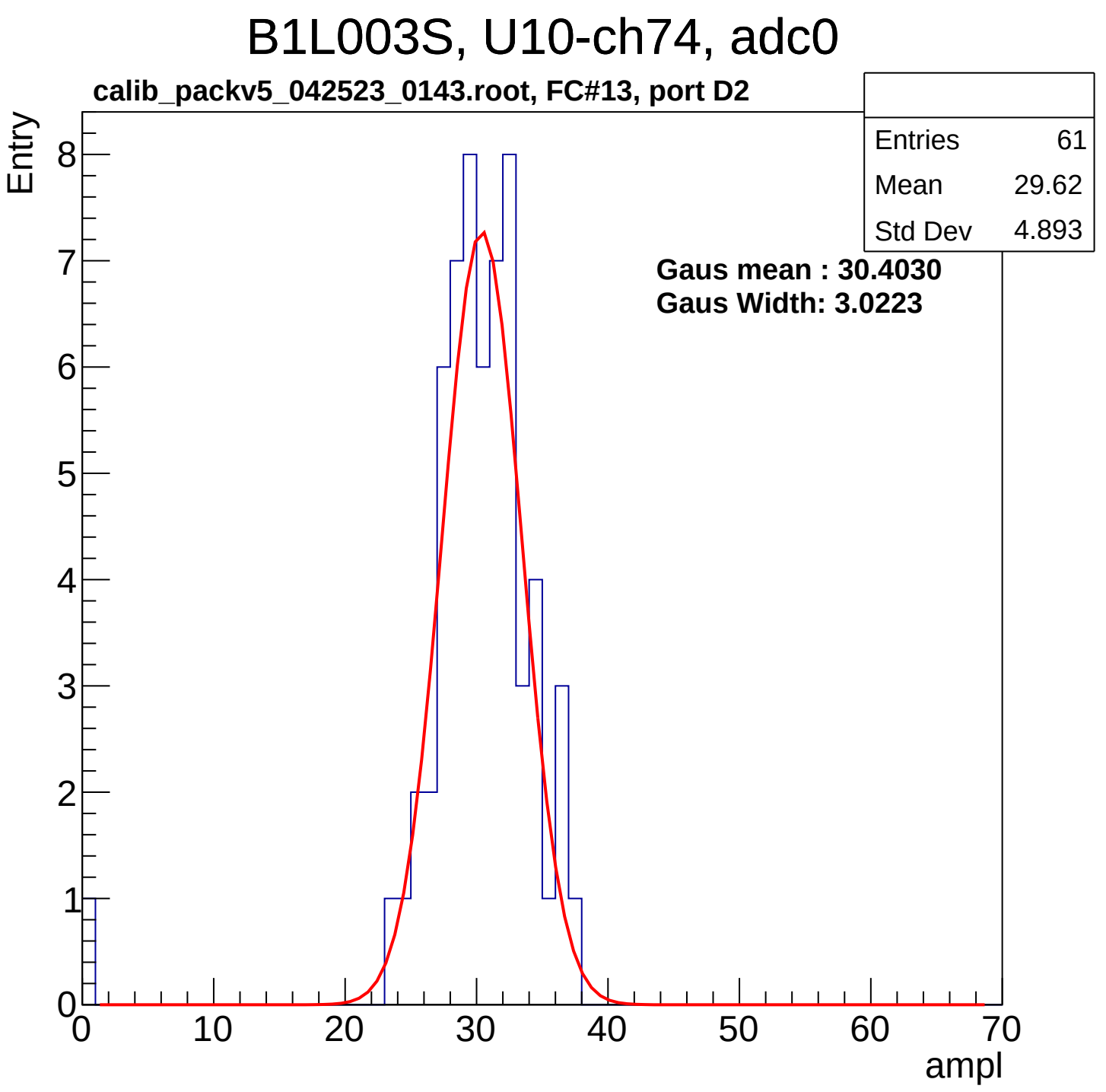
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch74, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 29.62
Std Dev 4.893
Gaus mean : 30.4030
Gaus Width: 3.0223
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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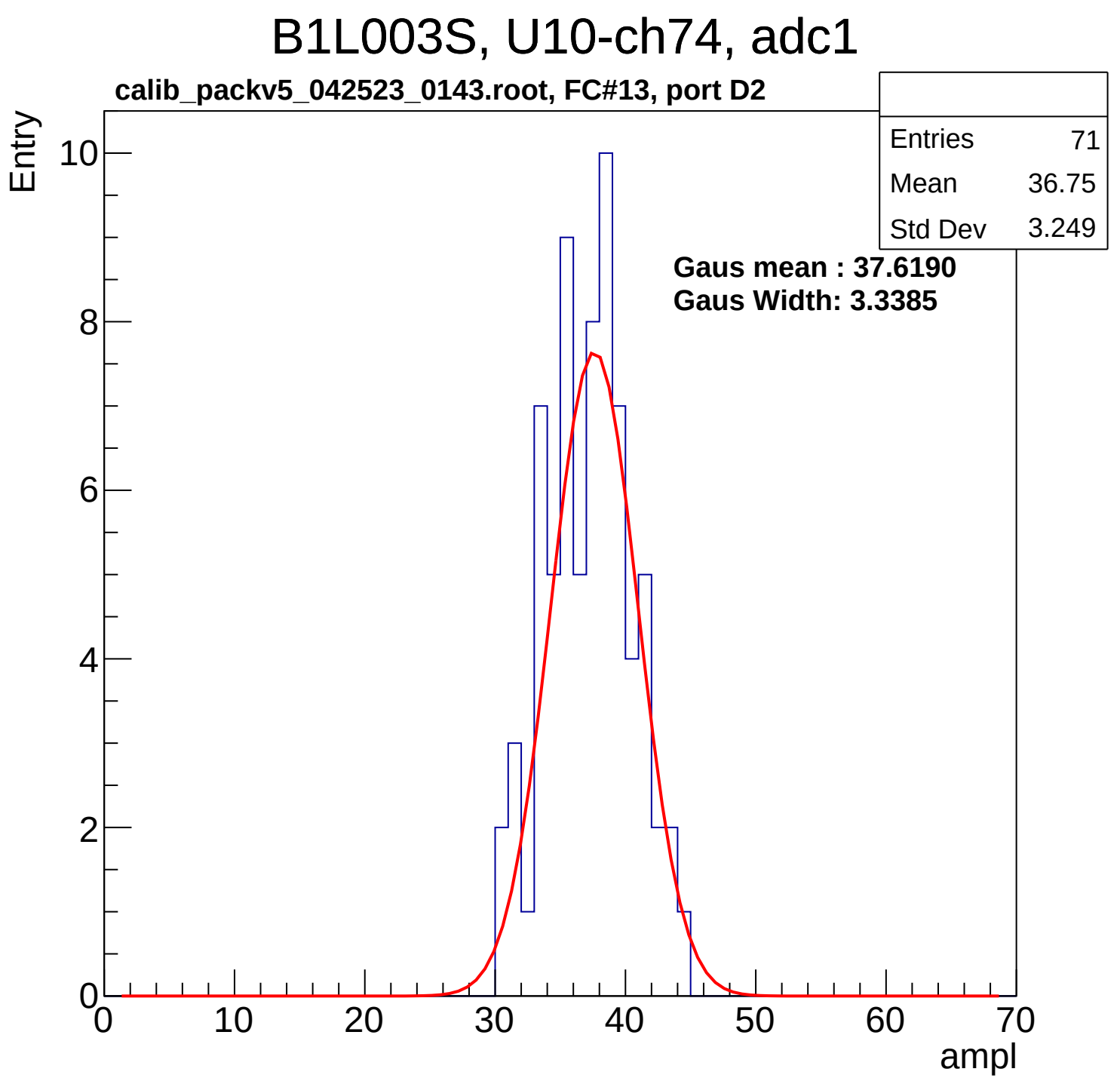
B1L003S, U10-ch74, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.75
Std Dev 3.249
Gaus mean : 37.6190
Gaus Width: 3.3385
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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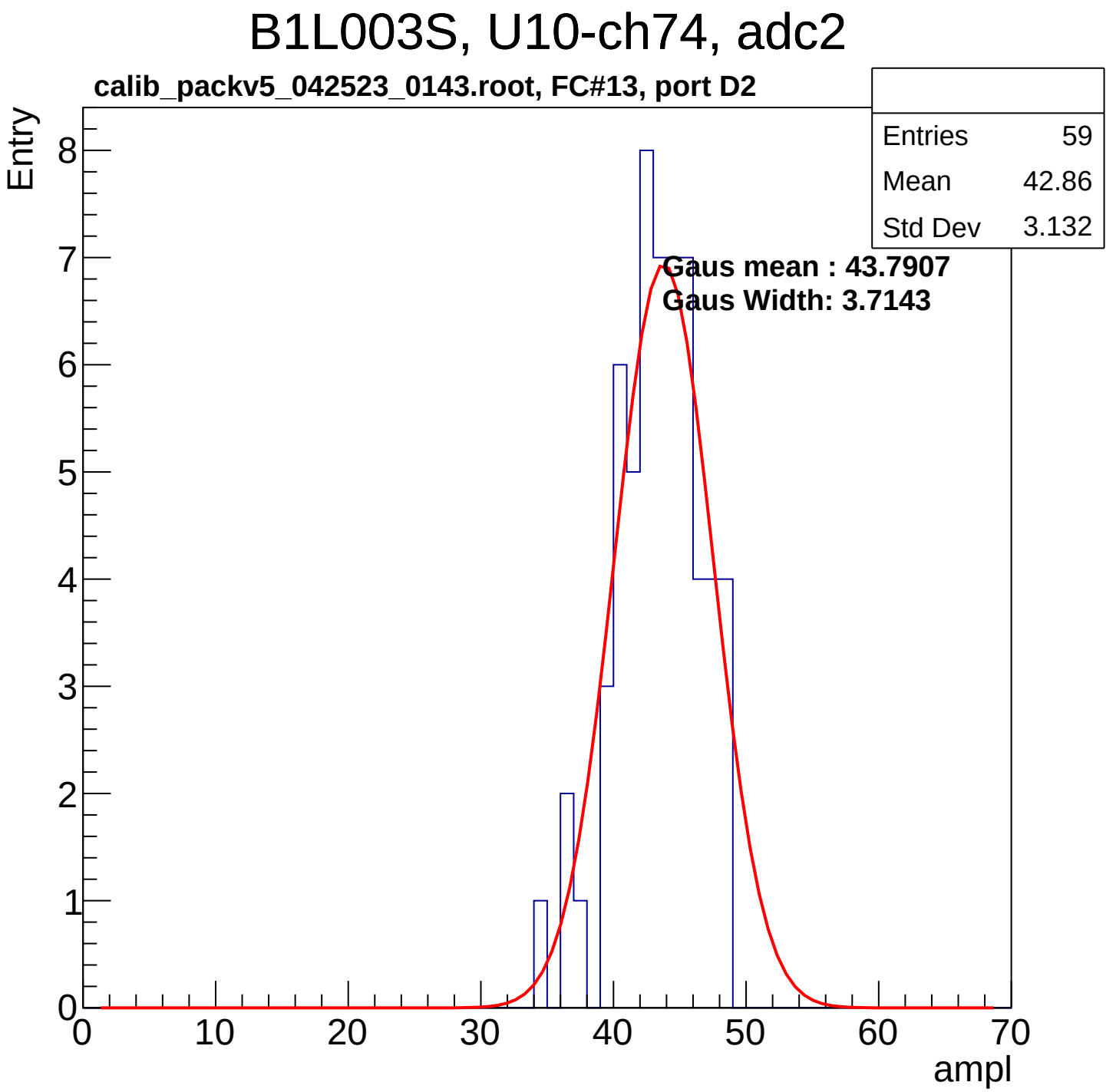
B1L003S, U10-ch74, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.86
Std Dev 3.132
Gaus mean : 43.7907
Gaus Width: 3.7143
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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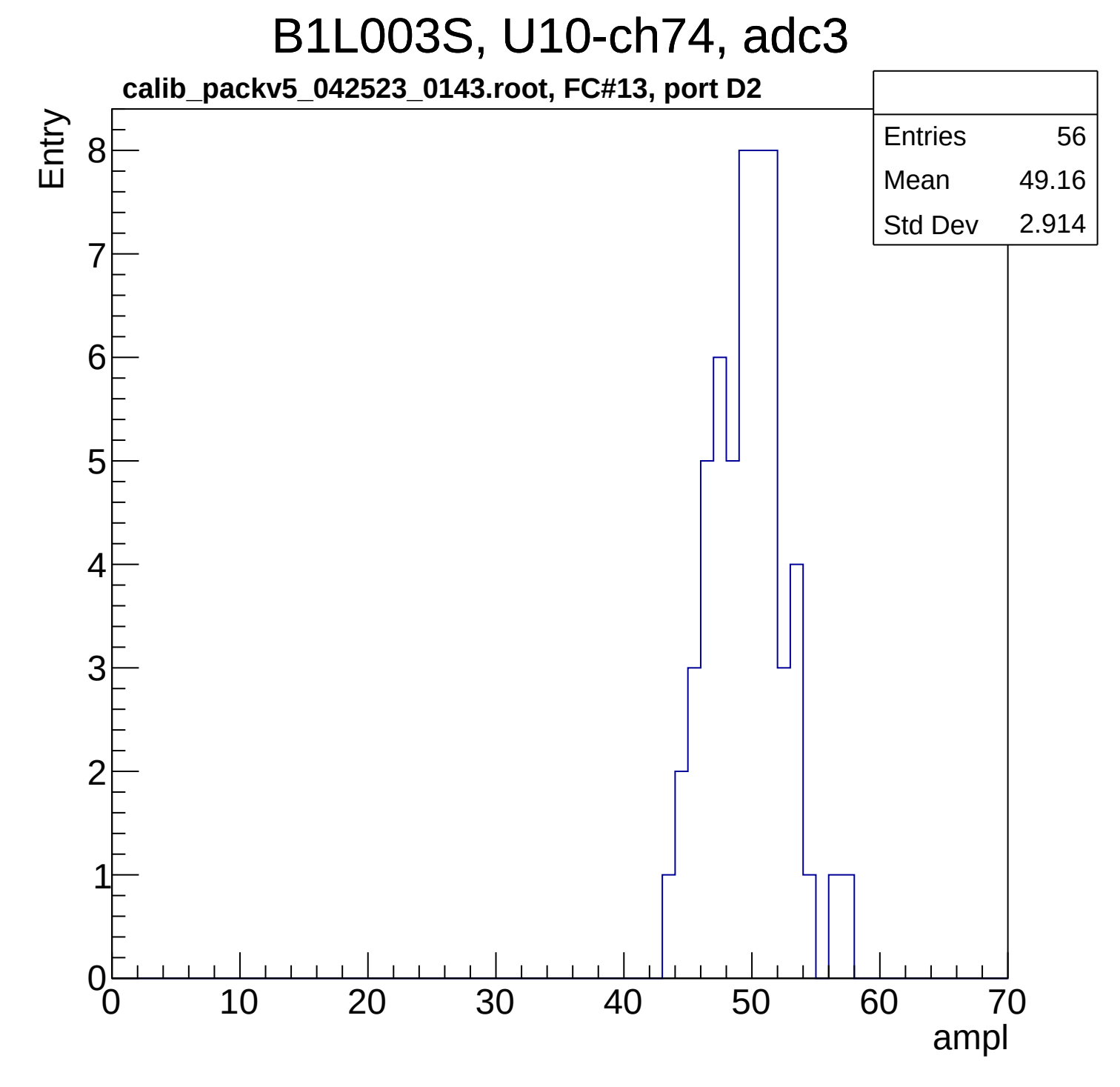
B1L003S, U10-ch74, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 49.16
Std Dev 2.914
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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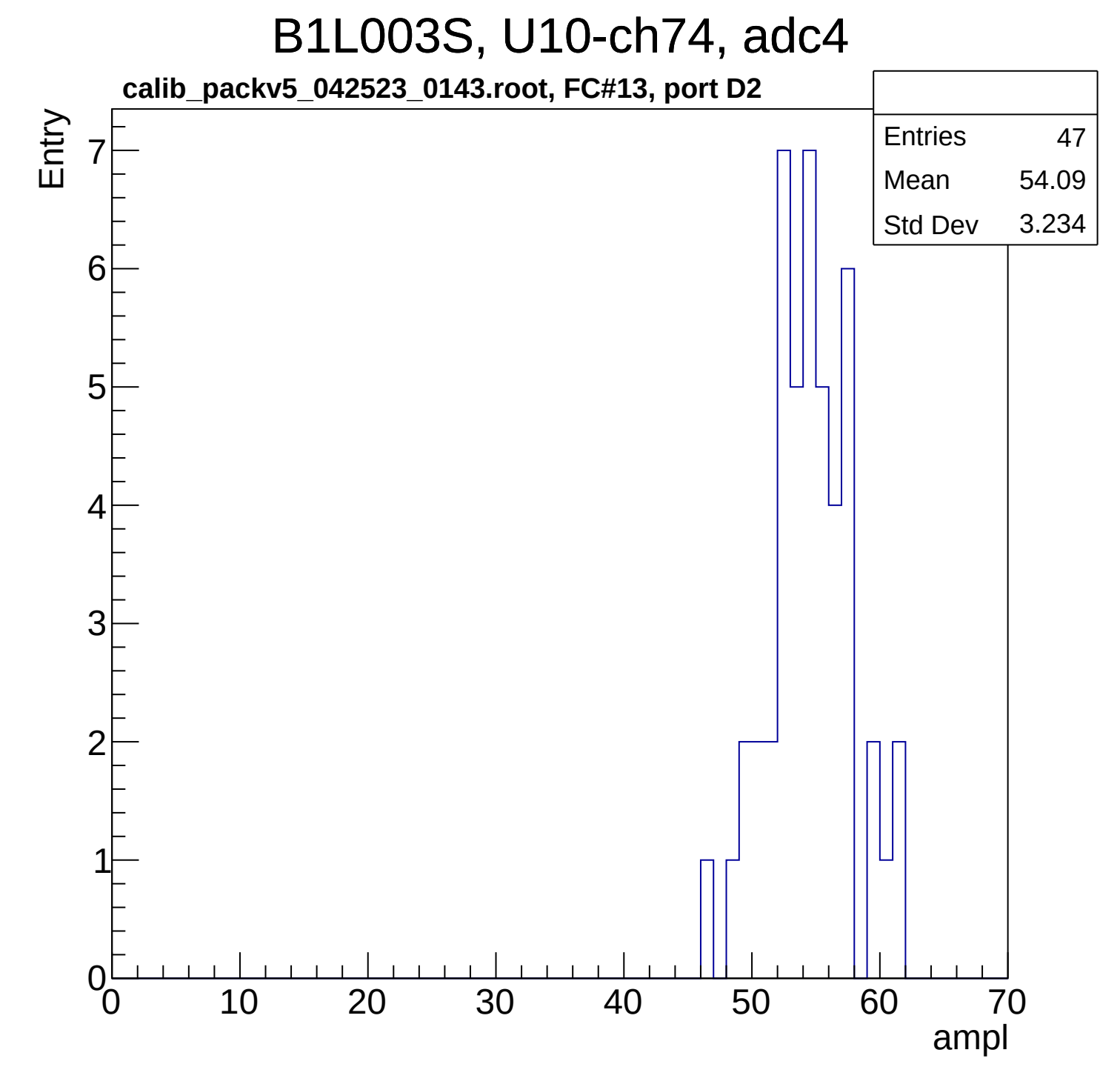
B1L003S, U10-ch74, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.09
Std Dev 3.234
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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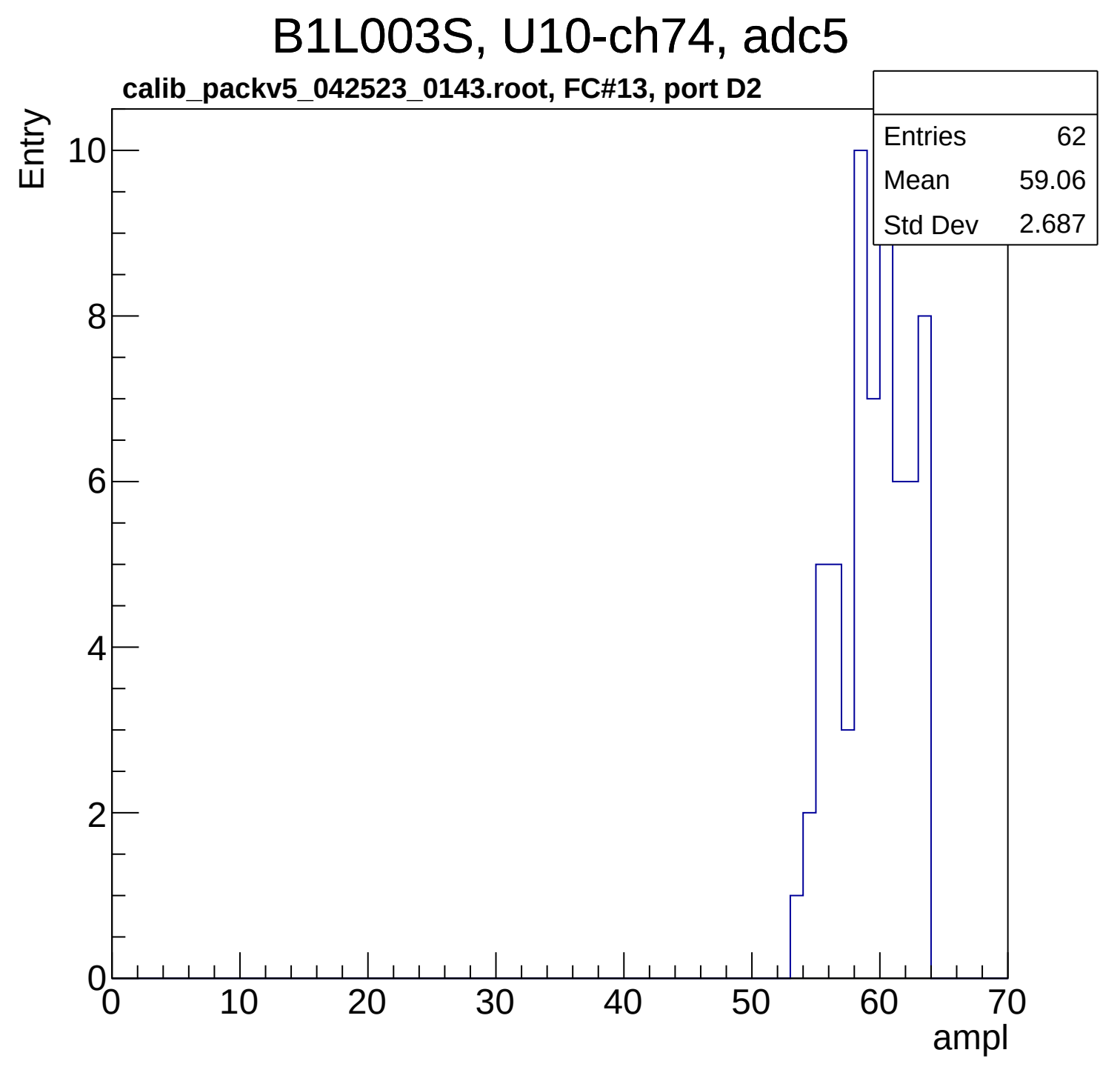
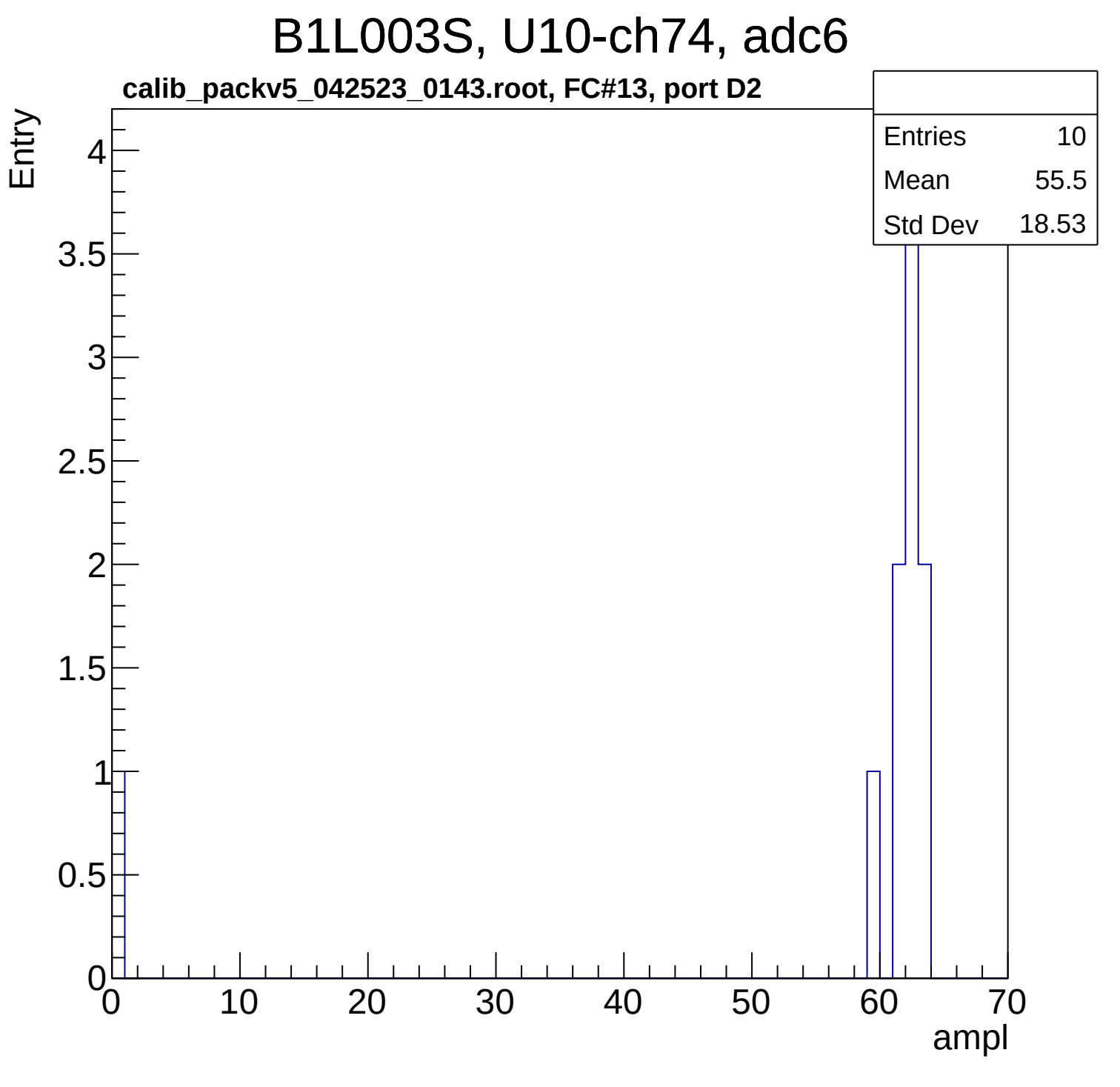
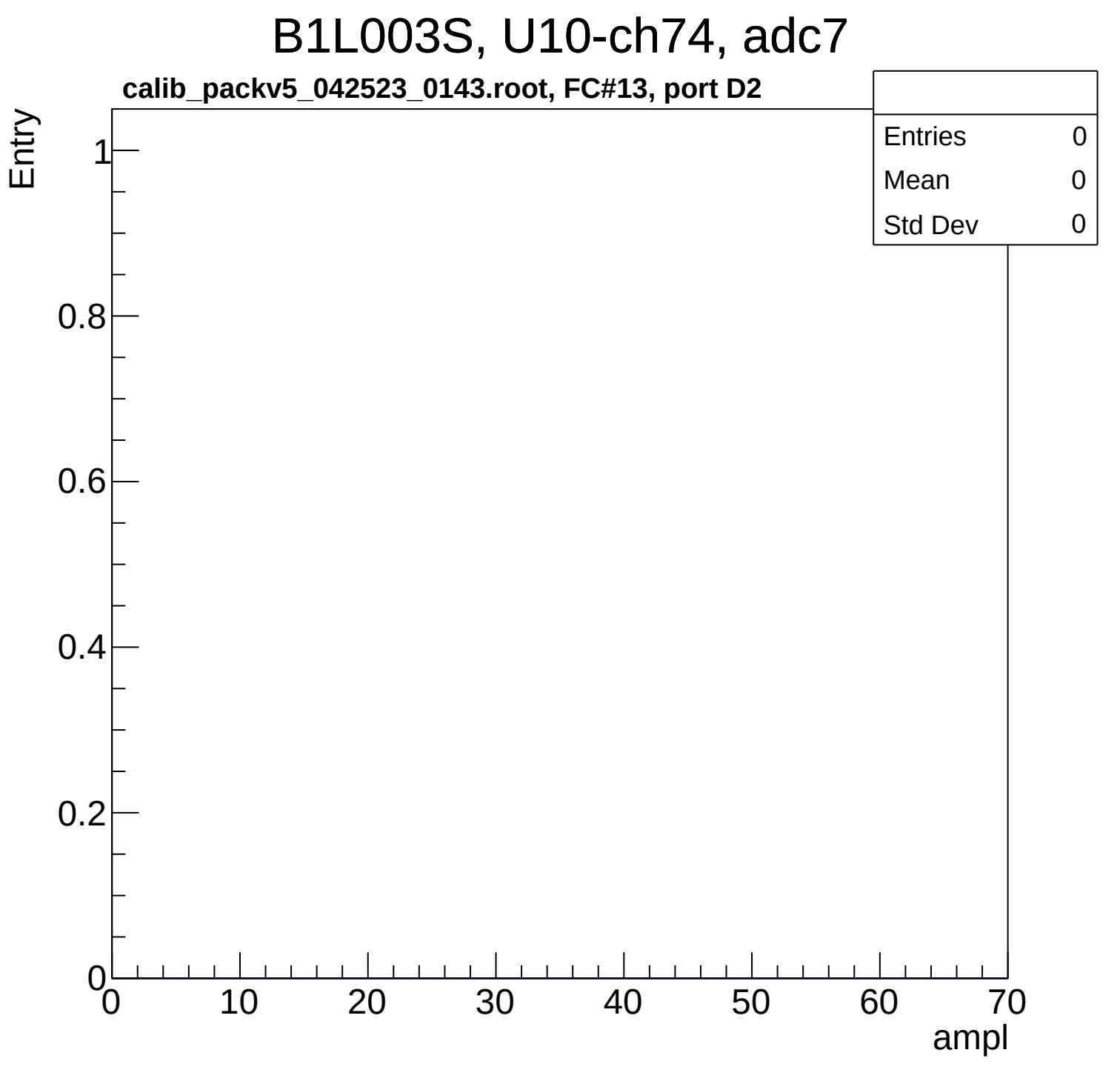
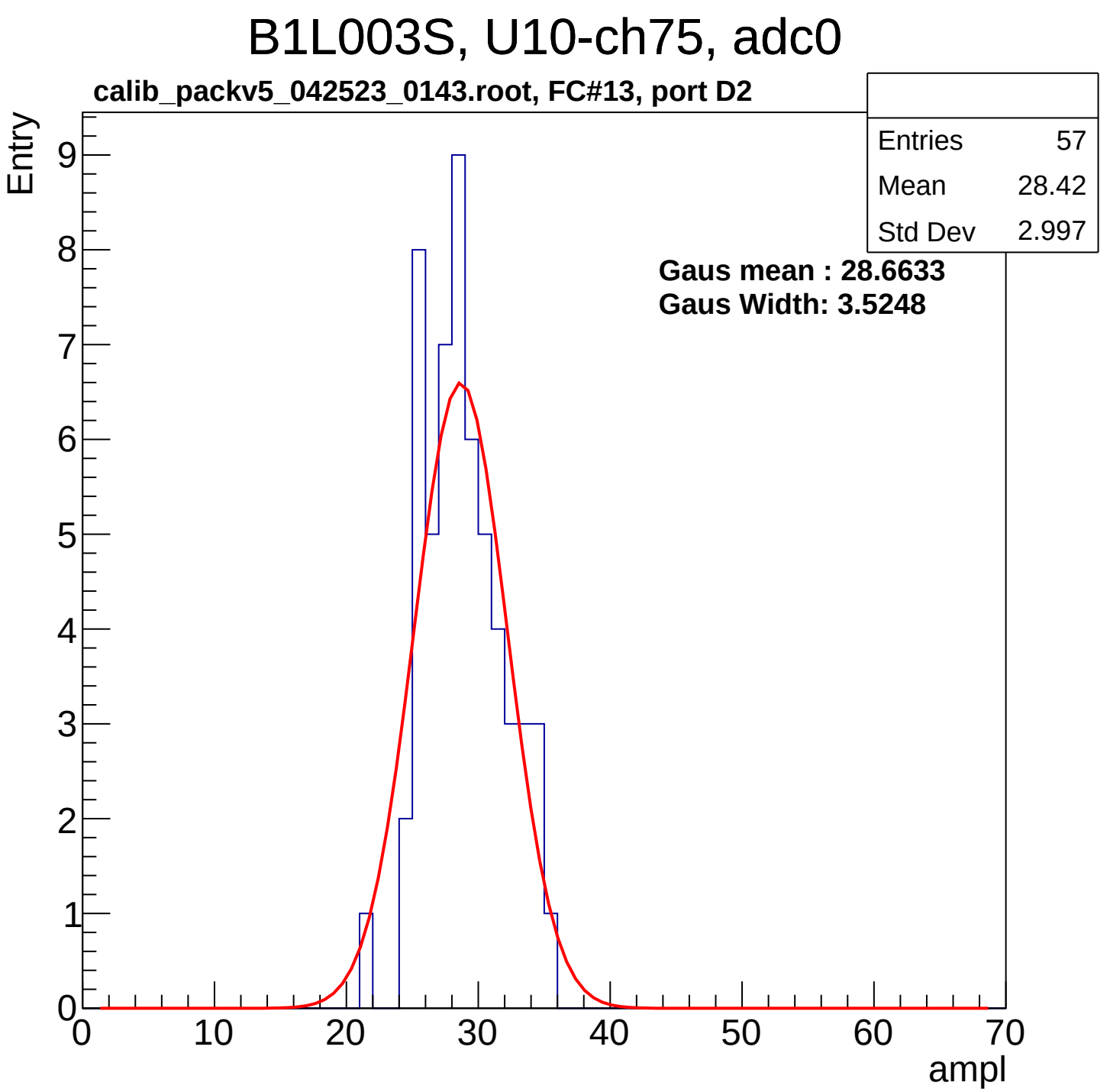

B1L003S, U10-ch74, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 59.06
Std Dev 2.687
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch74, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.5
Std Dev 18.53
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch74, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch75, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.42
Std Dev 2.997
Gaus mean : 28.6633
Gaus Width: 3.5248
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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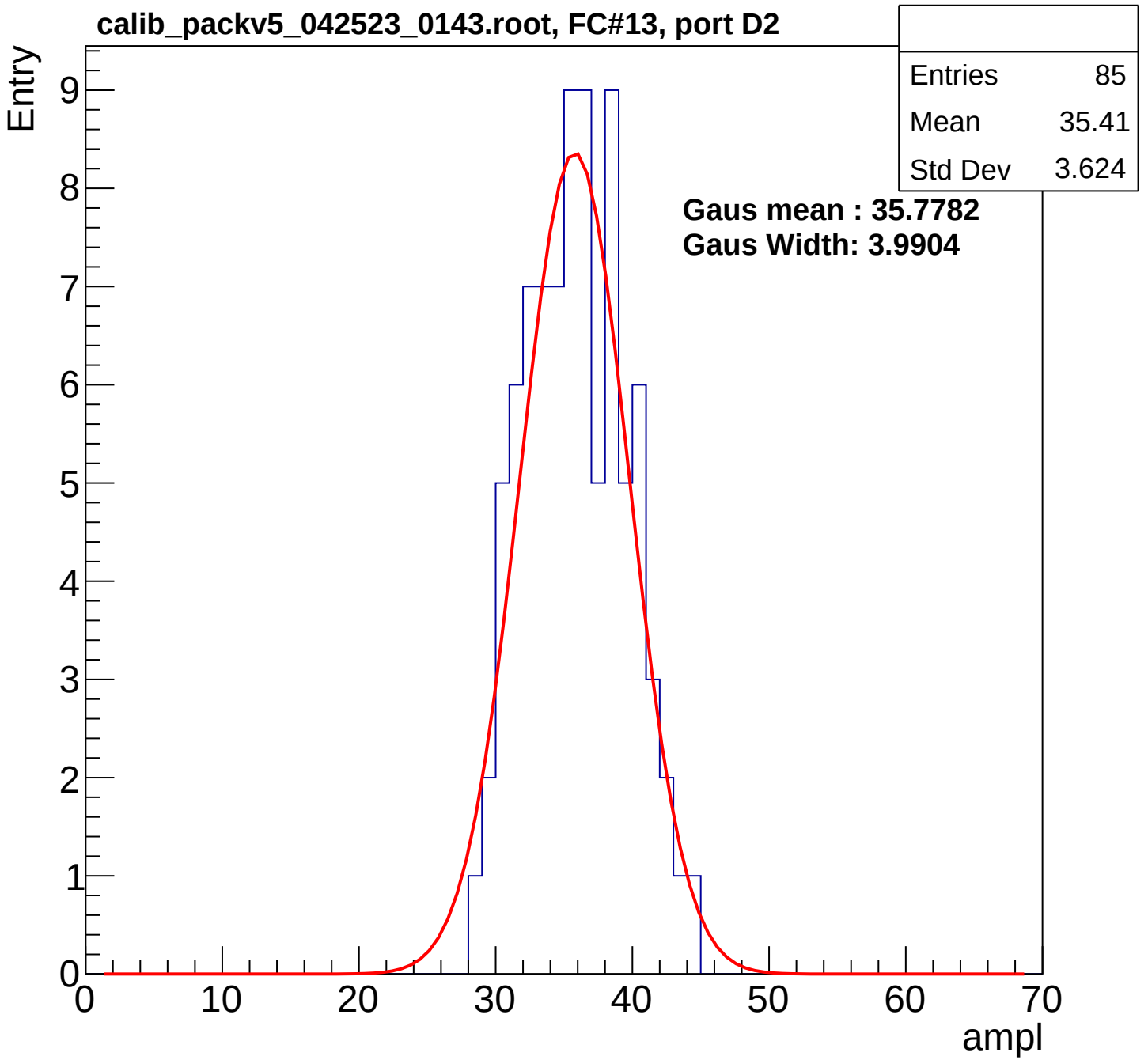
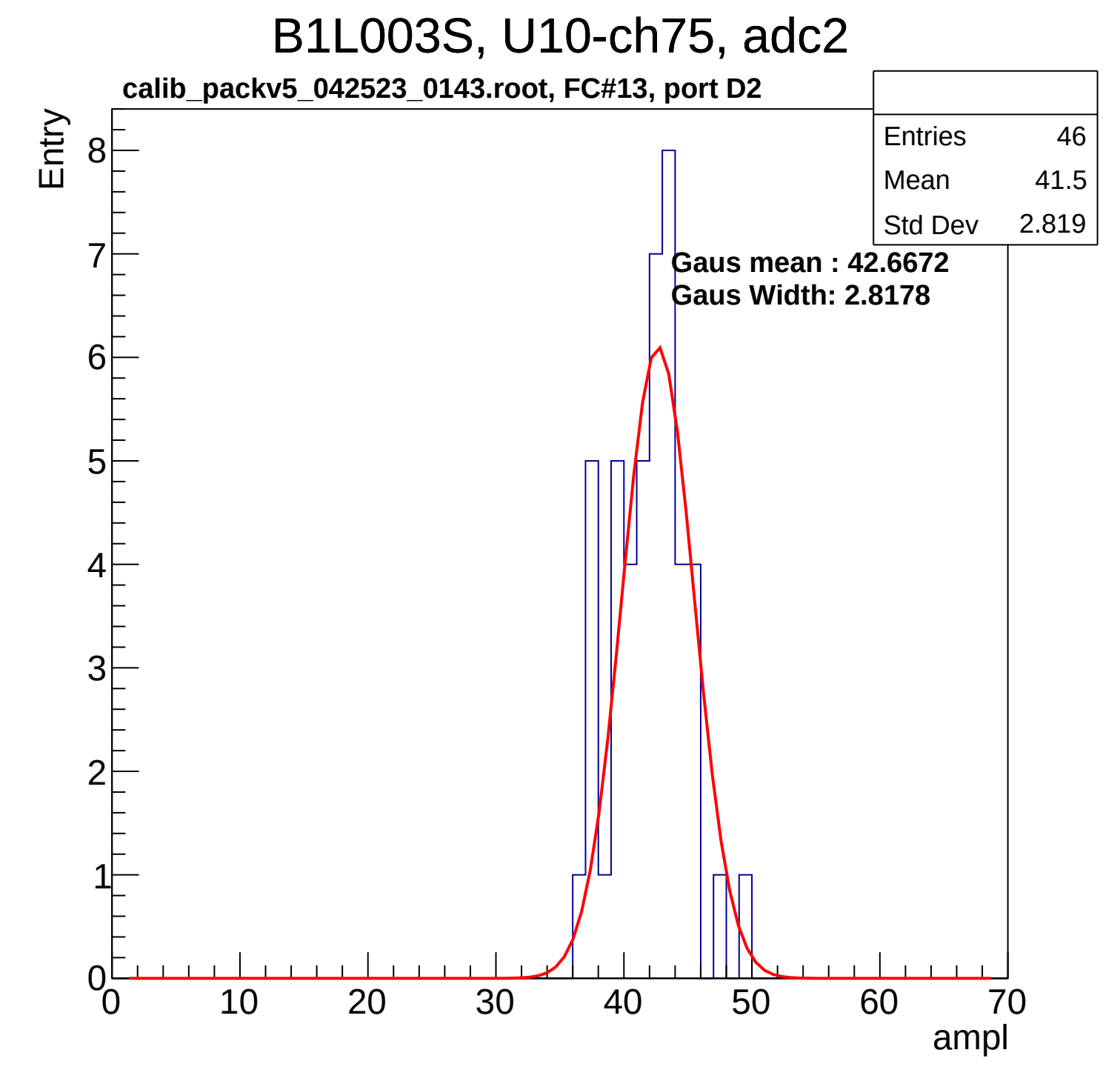

B1L003S, U10-ch75, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 35.41
Std Dev 3.624
Gaus mean : 35.7782
Gaus Width: 3.9904
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch75, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 41.5
Std Dev 2.819
Gaus mean : 42.6672
Gaus Width: 2.8178
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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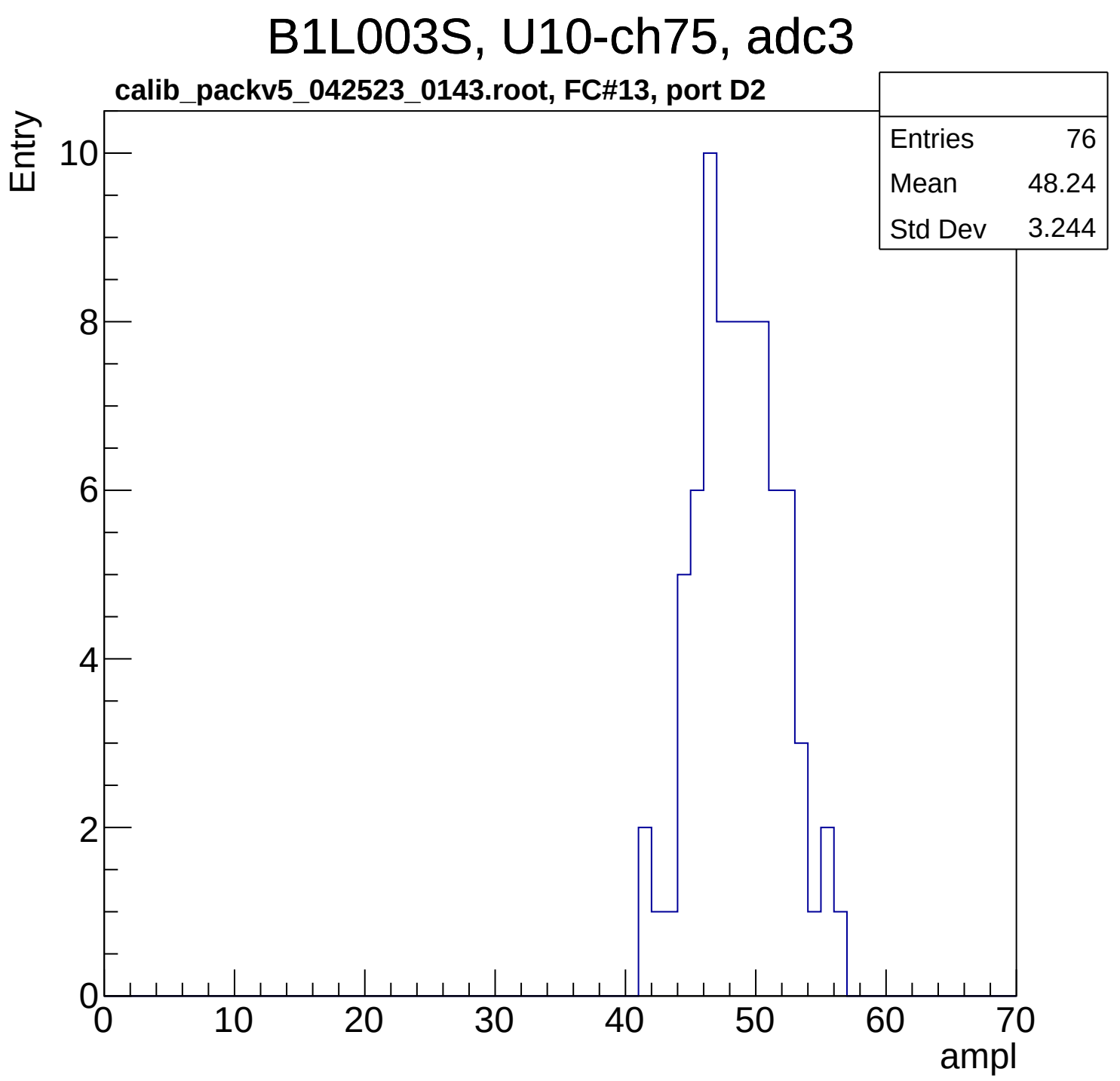
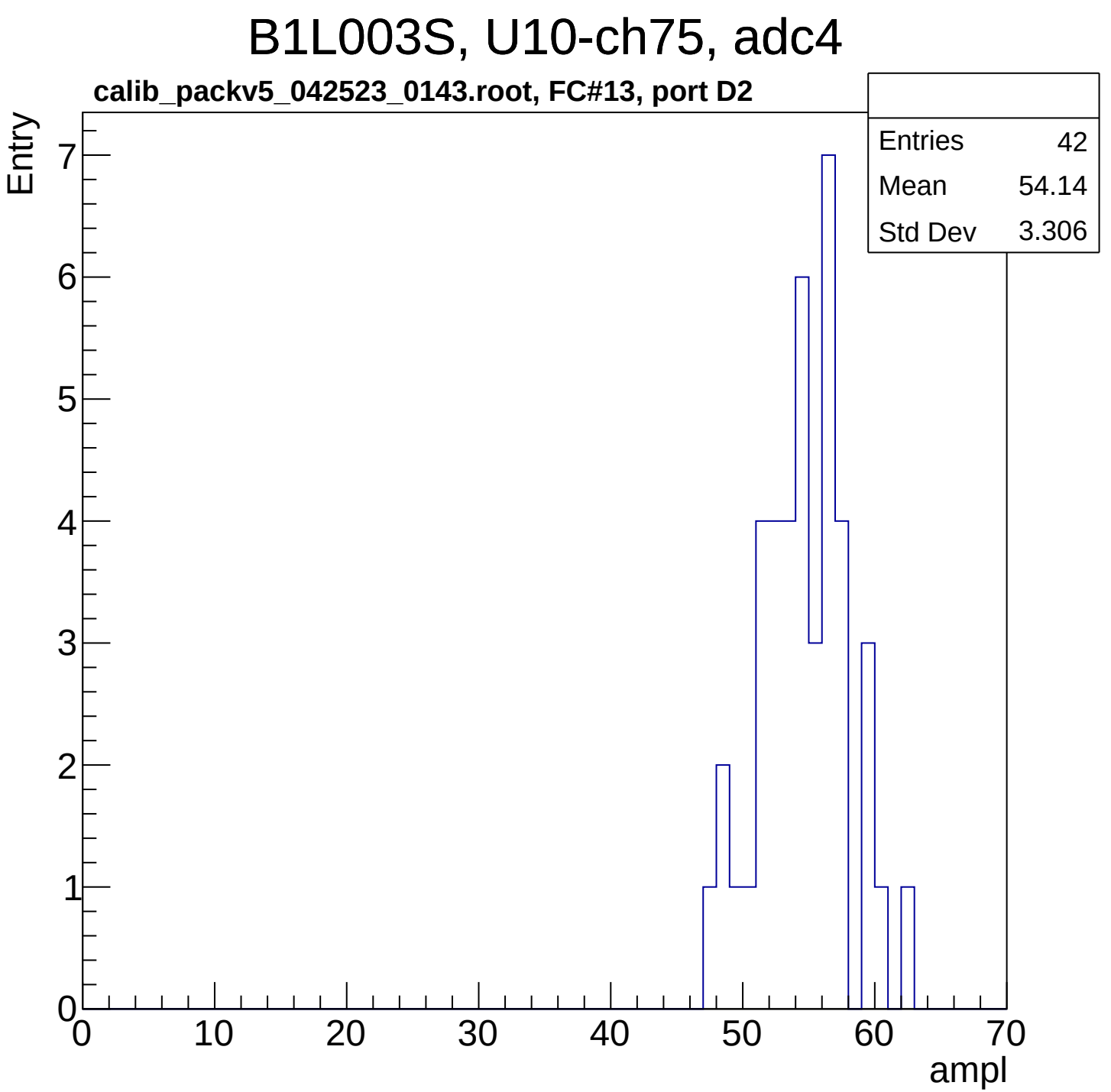
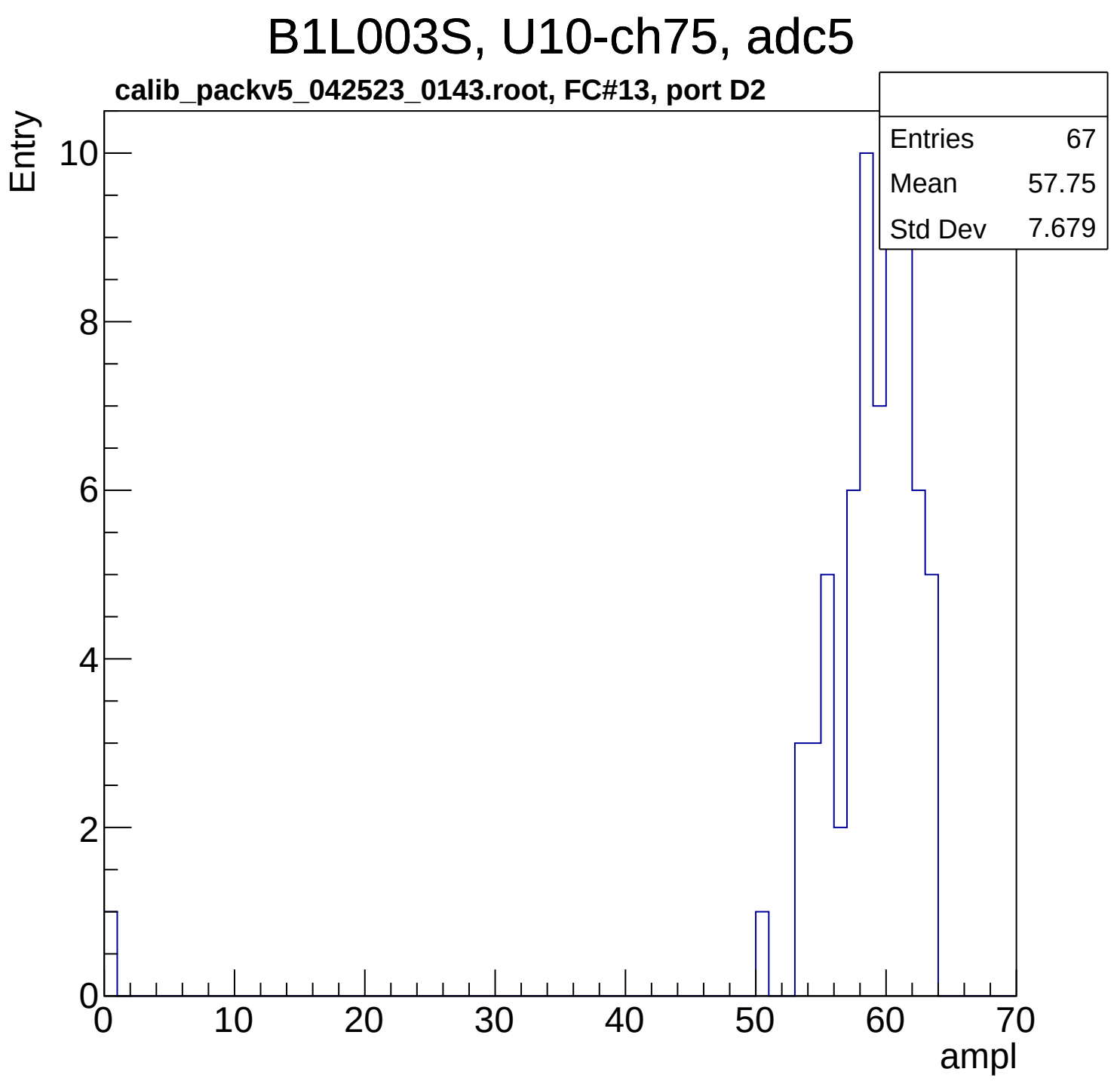

B1L003S, U10-ch75, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 48.24
Std Dev 3.244
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch75, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 54.14
Std Dev 3.306
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch75, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 57.75
Std Dev 7.679
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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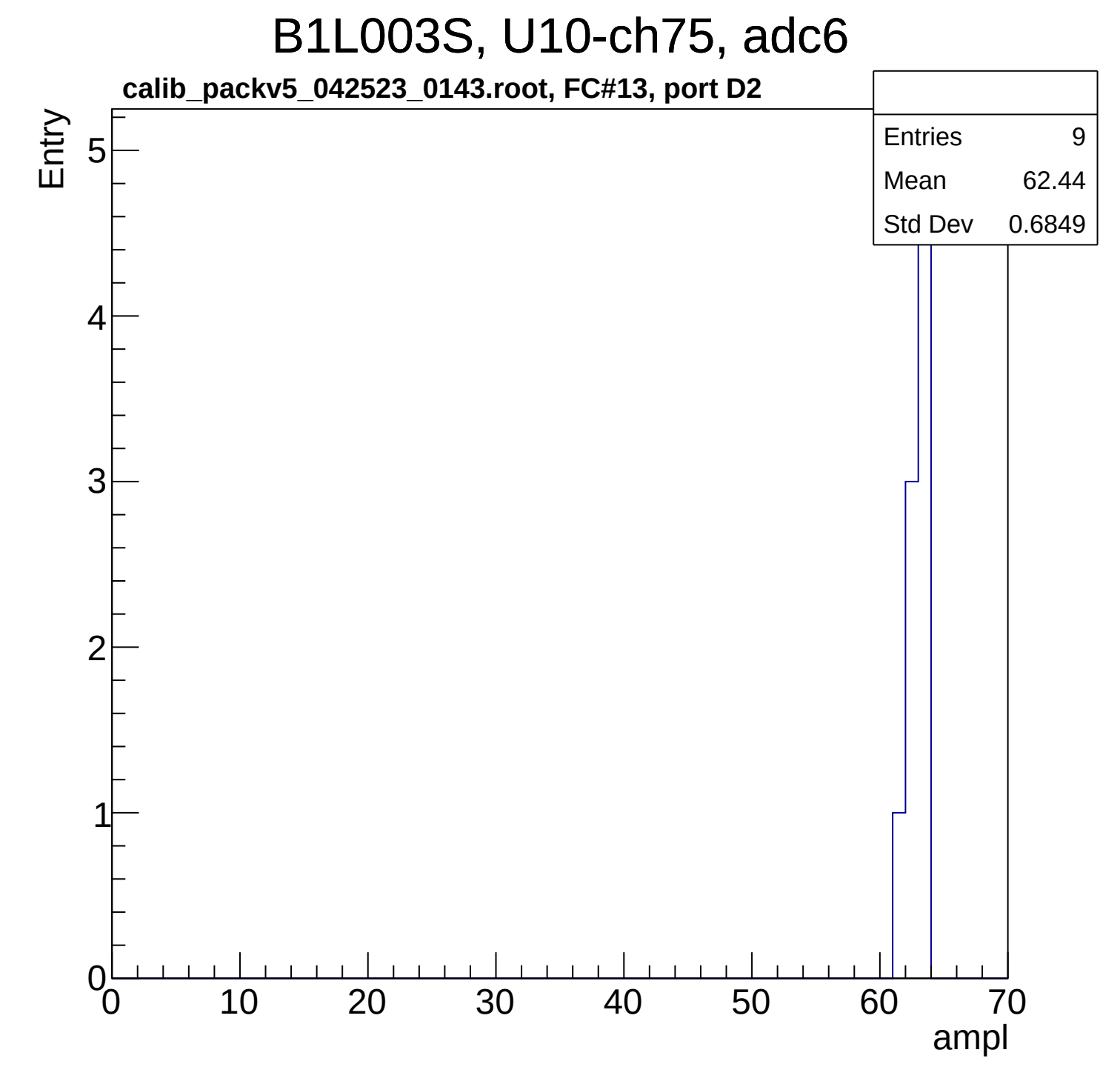
B1L003S, U10-ch75, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.44
Std Dev 0.6849
Entry
0
1
2
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10
20
30
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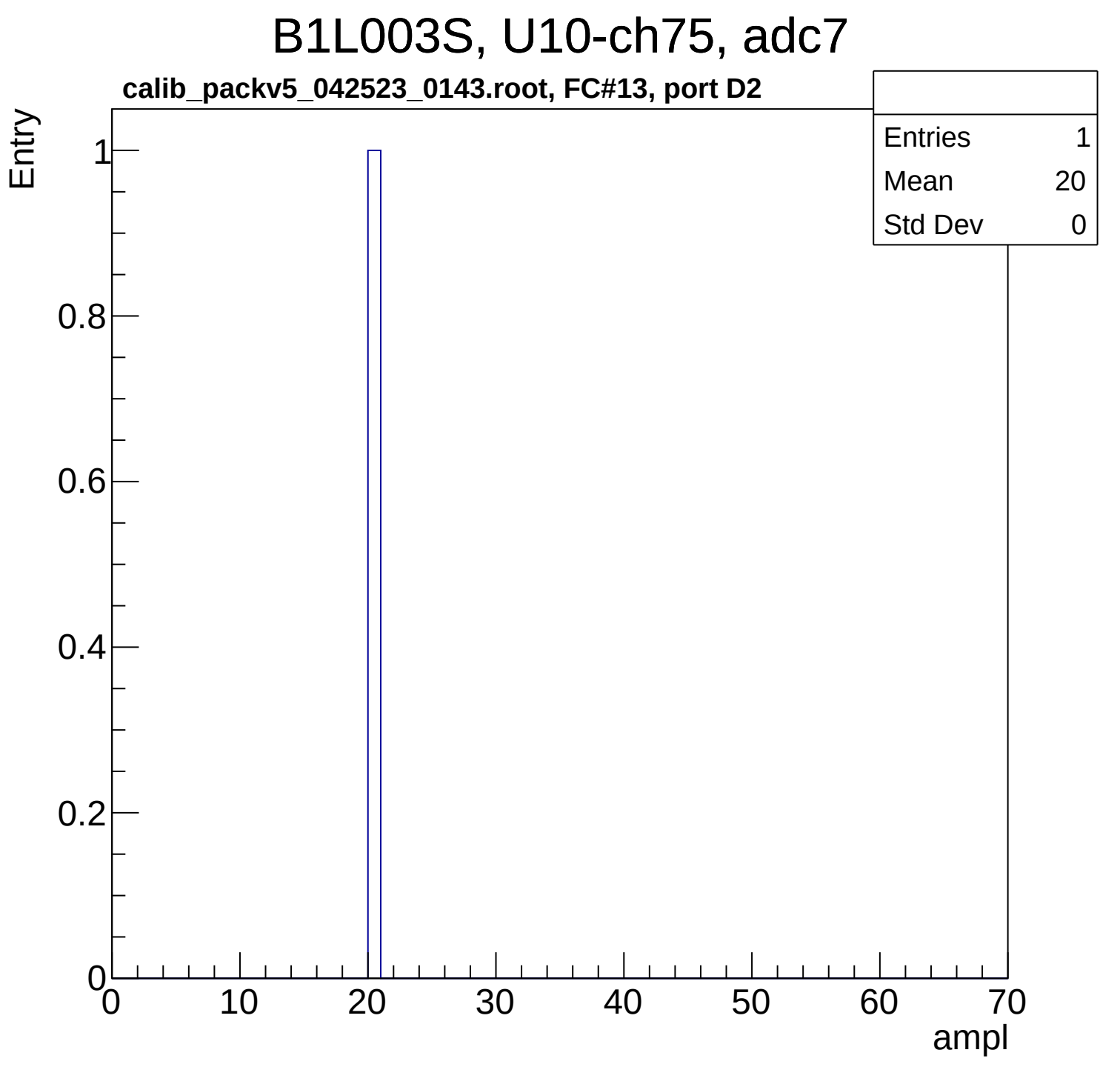
50
60
70
ampl


B1L003S, U10-ch75, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 20
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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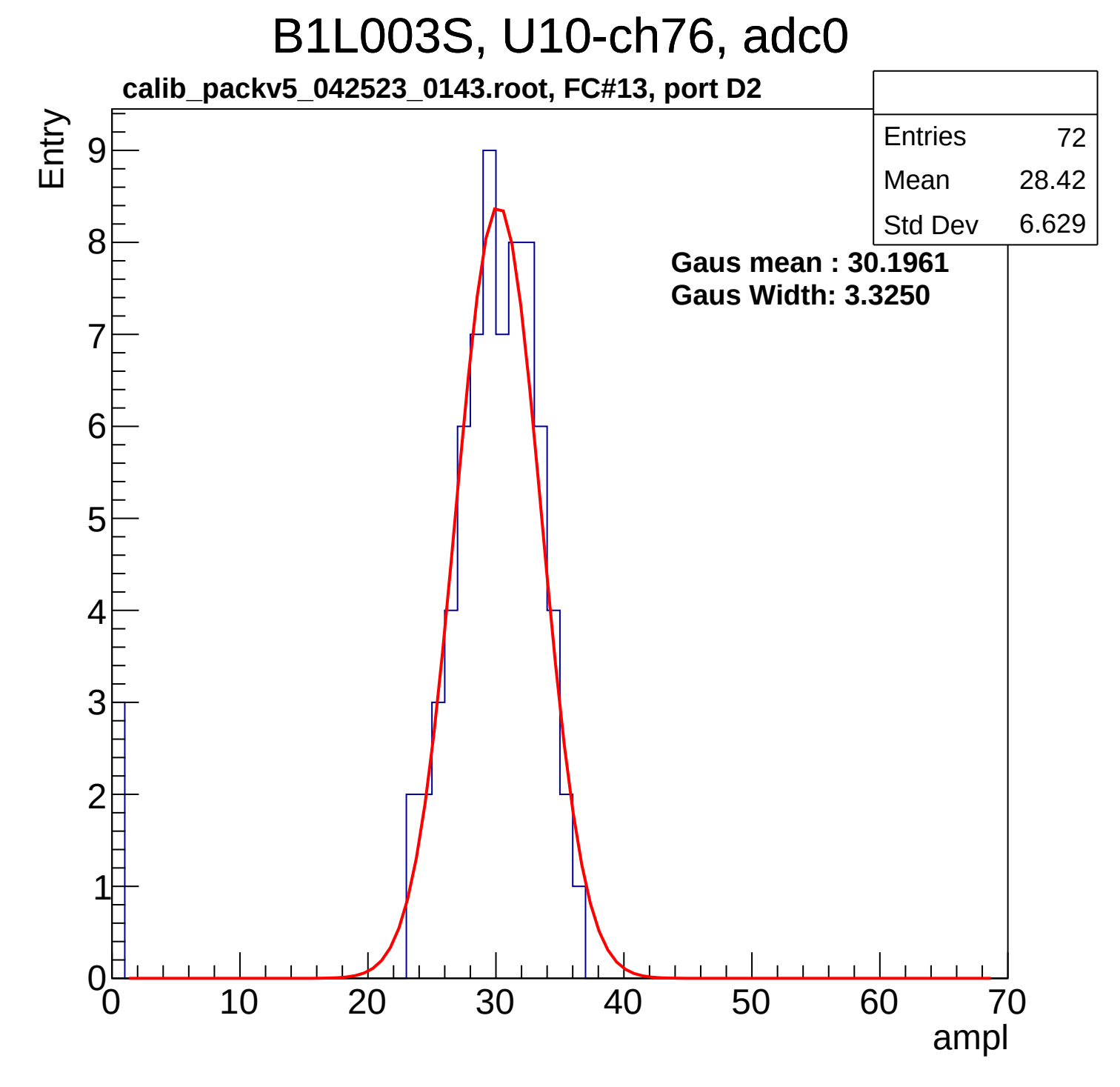
50
60
70
ampl


B1L003S, U10-ch76, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 28.42
Std Dev 6.629
Gaus mean : 30.1961
Gaus Width: 3.3250
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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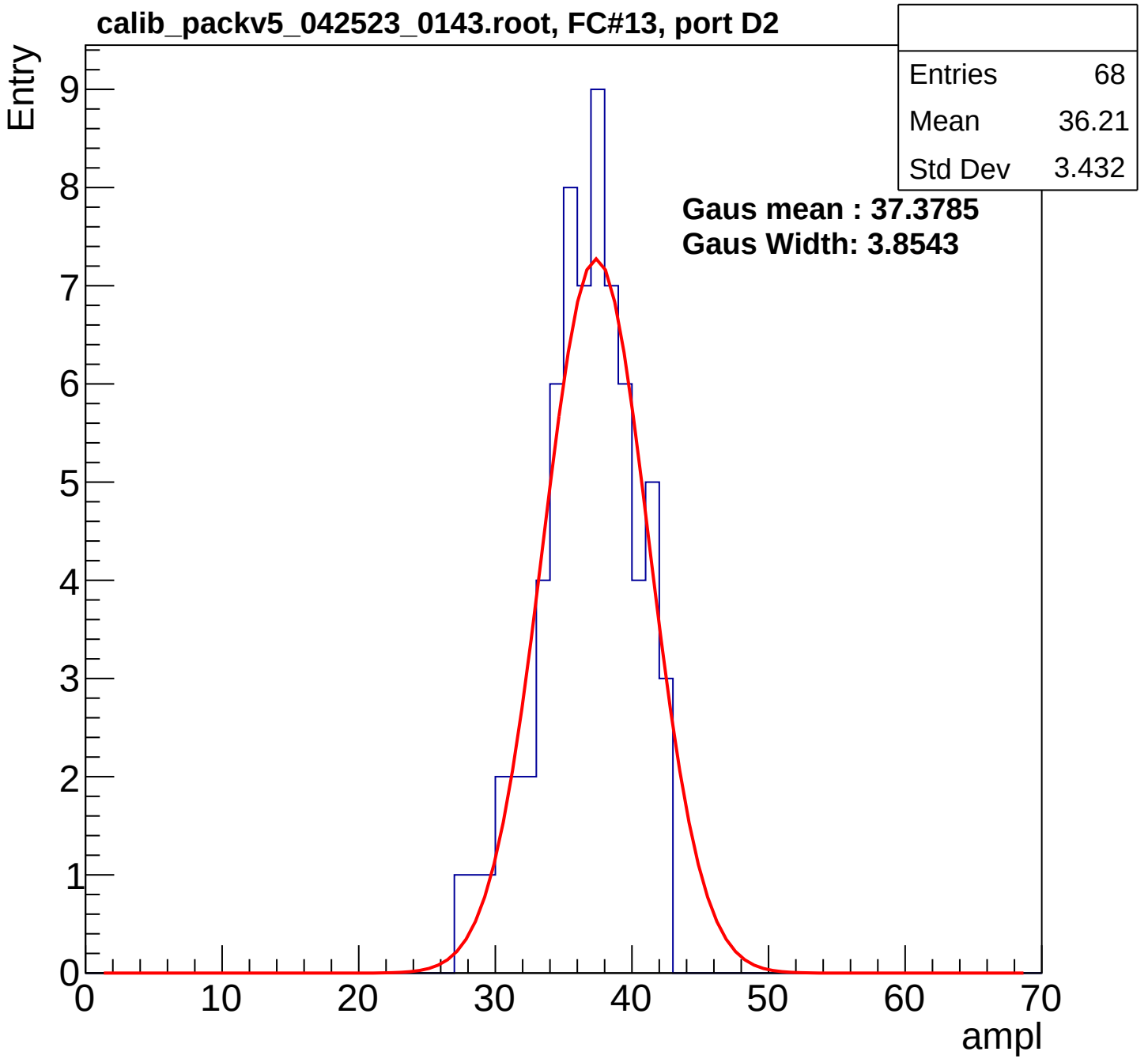
20
30
40
50
60
70


B1L003S, U10-ch76, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.21
Std Dev 3.432
Gaus mean : 37.3785
Gaus Width: 3.8543
Entry
ampl
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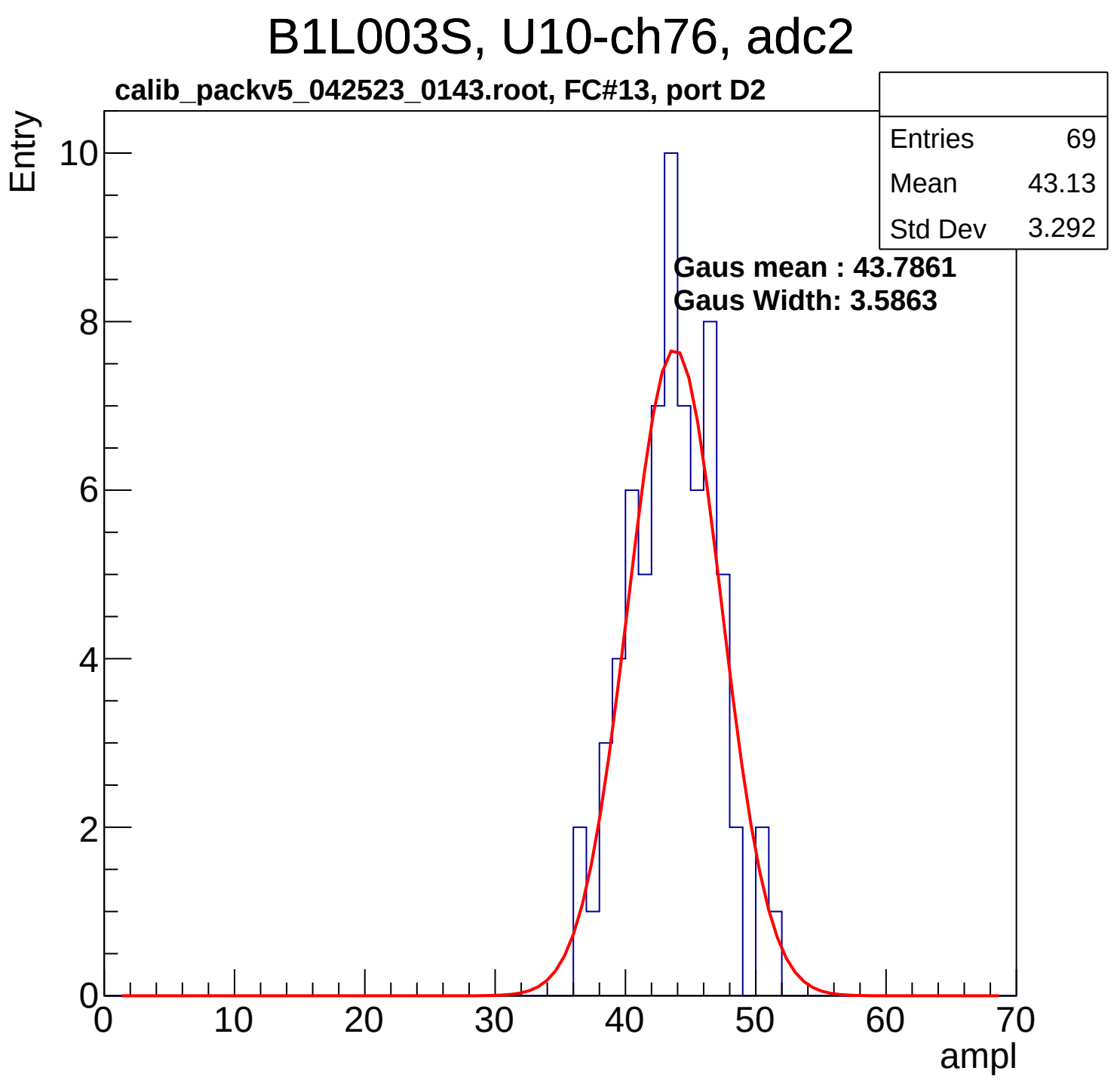
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch76, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 43.13
Std Dev 3.292
Gaus mean : 43.7861
Gaus Width: 3.5863
Entry
ampl
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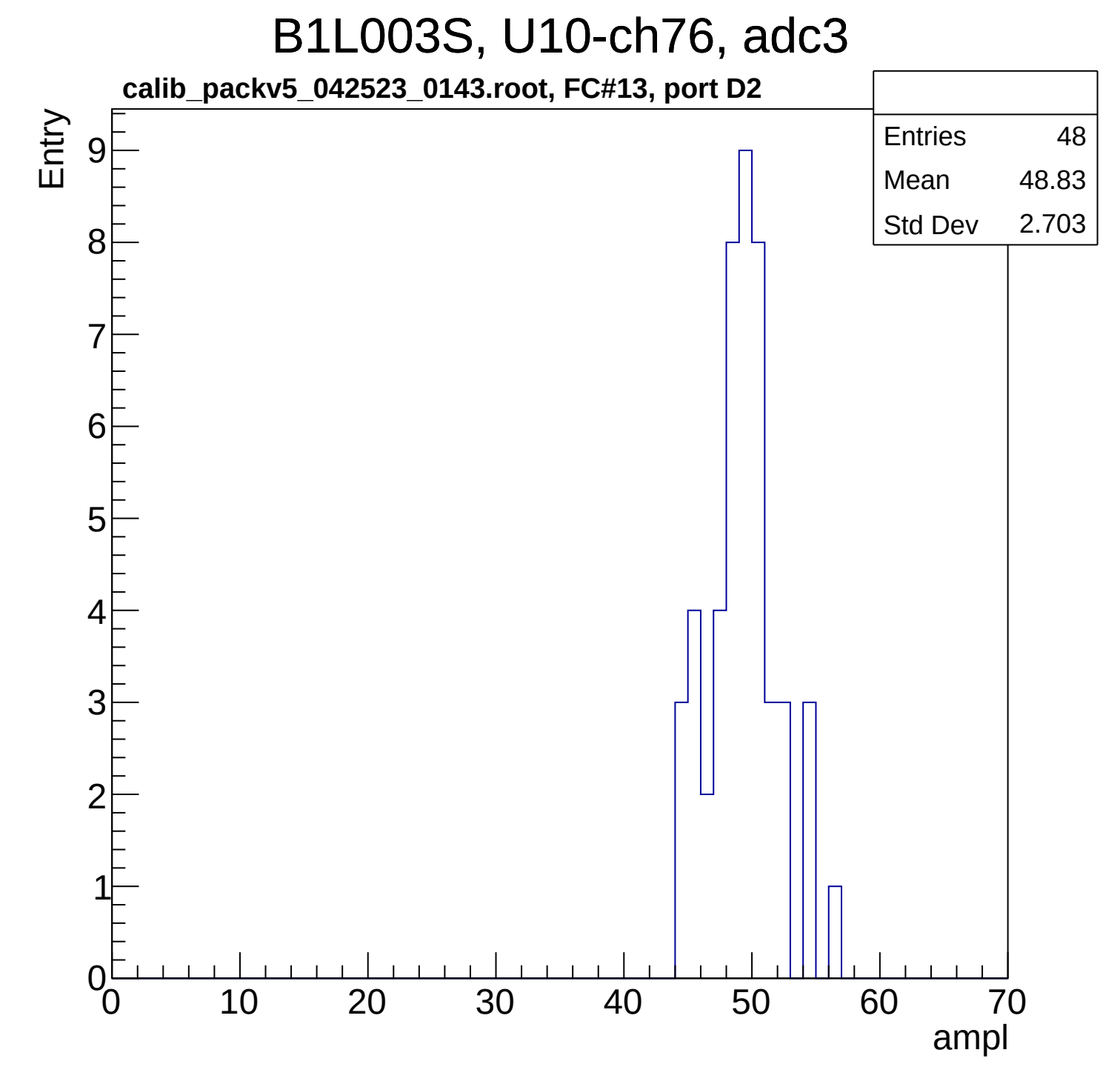
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch76, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 48.83
Std Dev 2.703
Entry
ampl
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10
20
30
40
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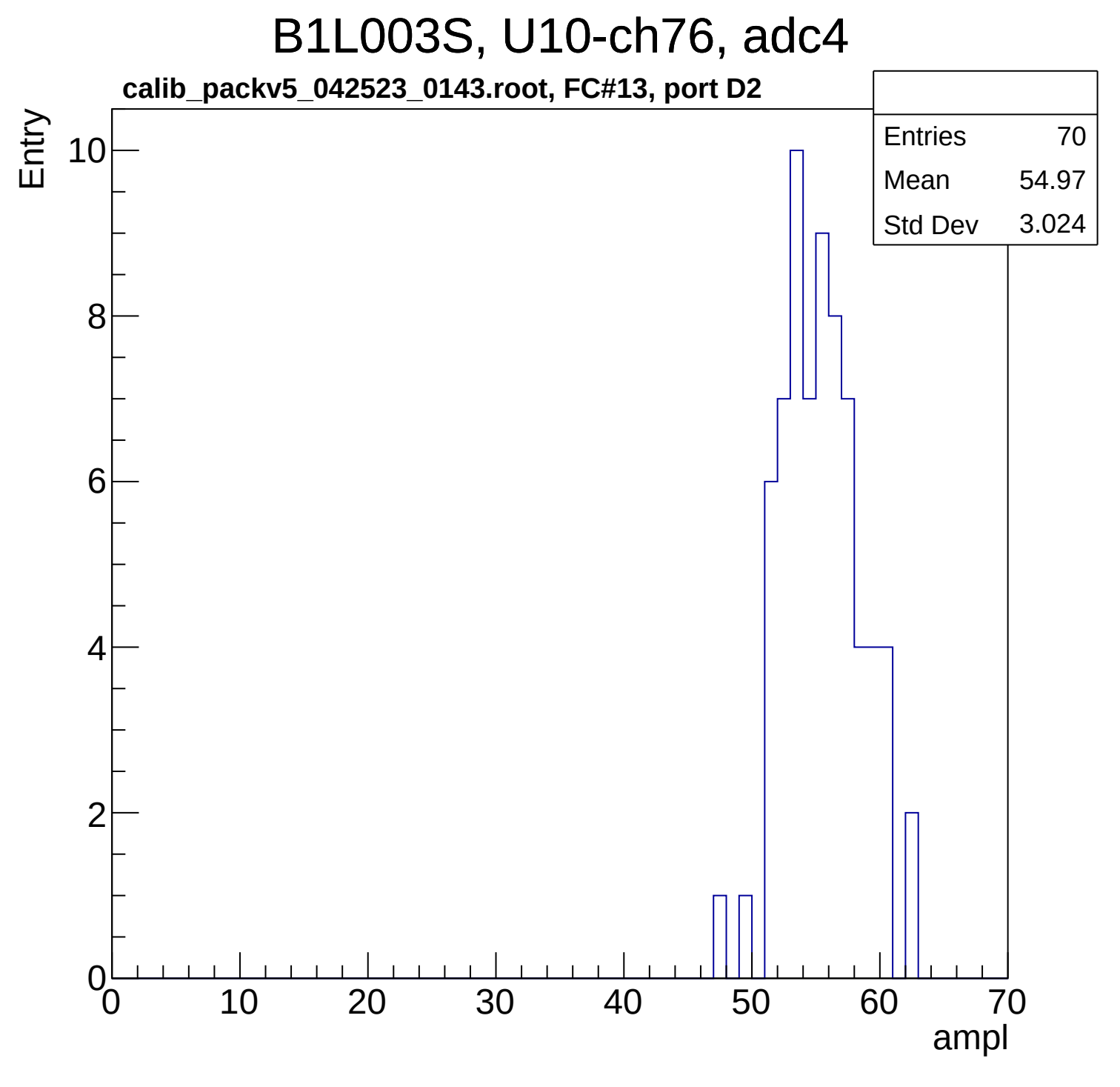
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch76, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 54.97
Std Dev 3.024
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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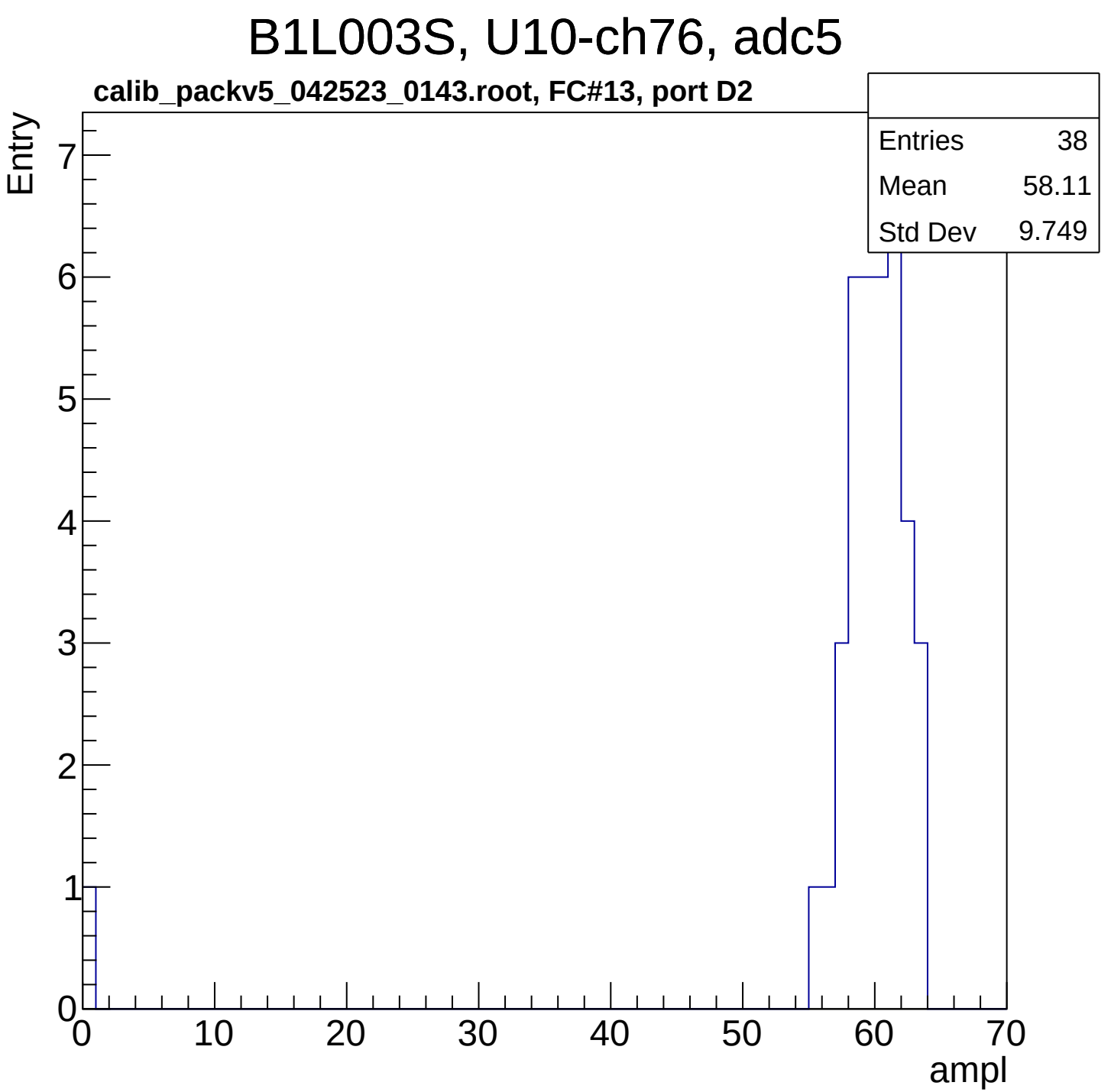
B1L003S, U10-ch76, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 58.11
Std Dev 9.749
Entry
ampl
0
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2
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4
5
6
7
0
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20
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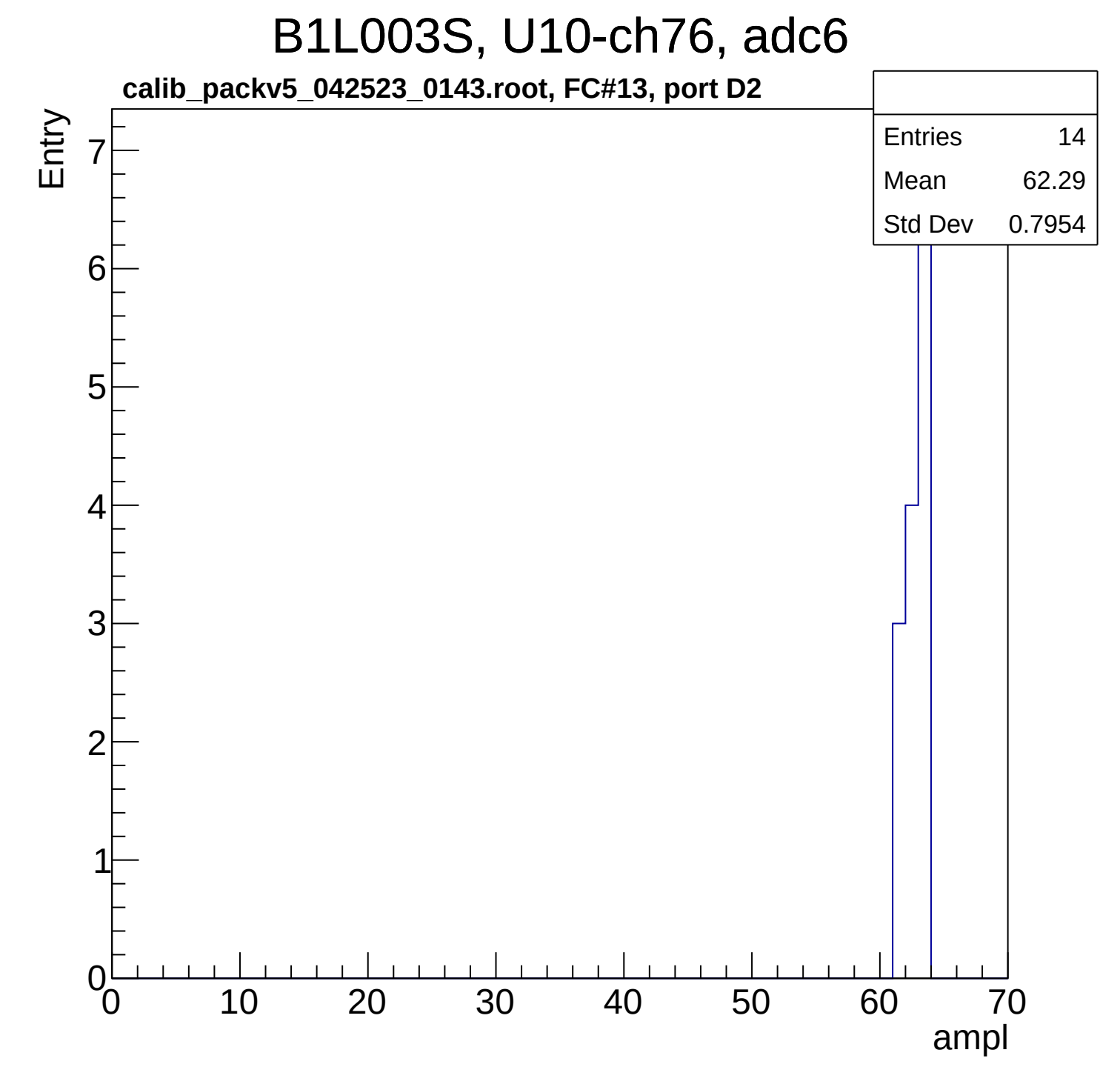
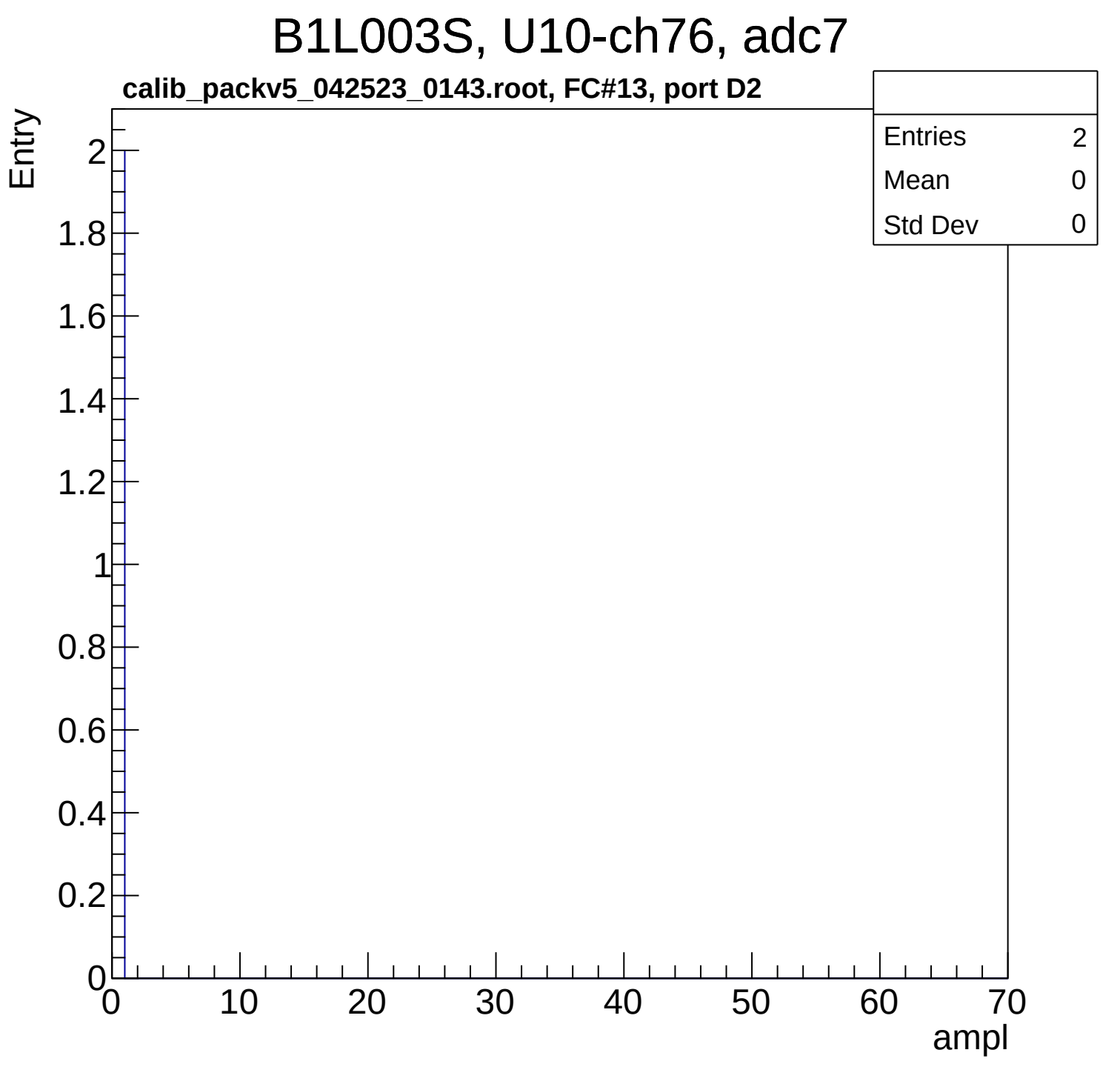
40
50
60
70


B1L003S, U10-ch76, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 62.29
Std Dev 0.7954
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch76, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
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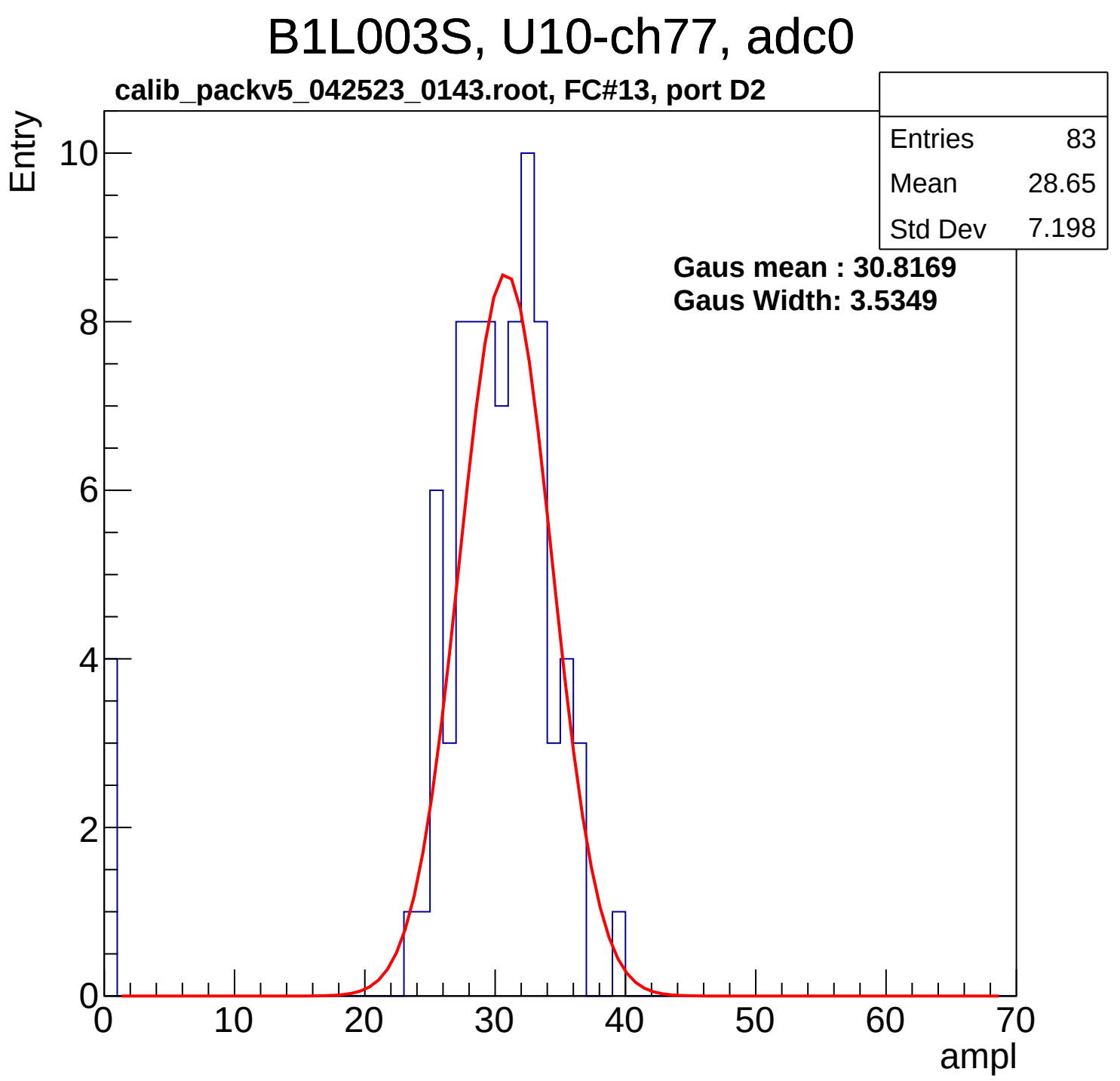
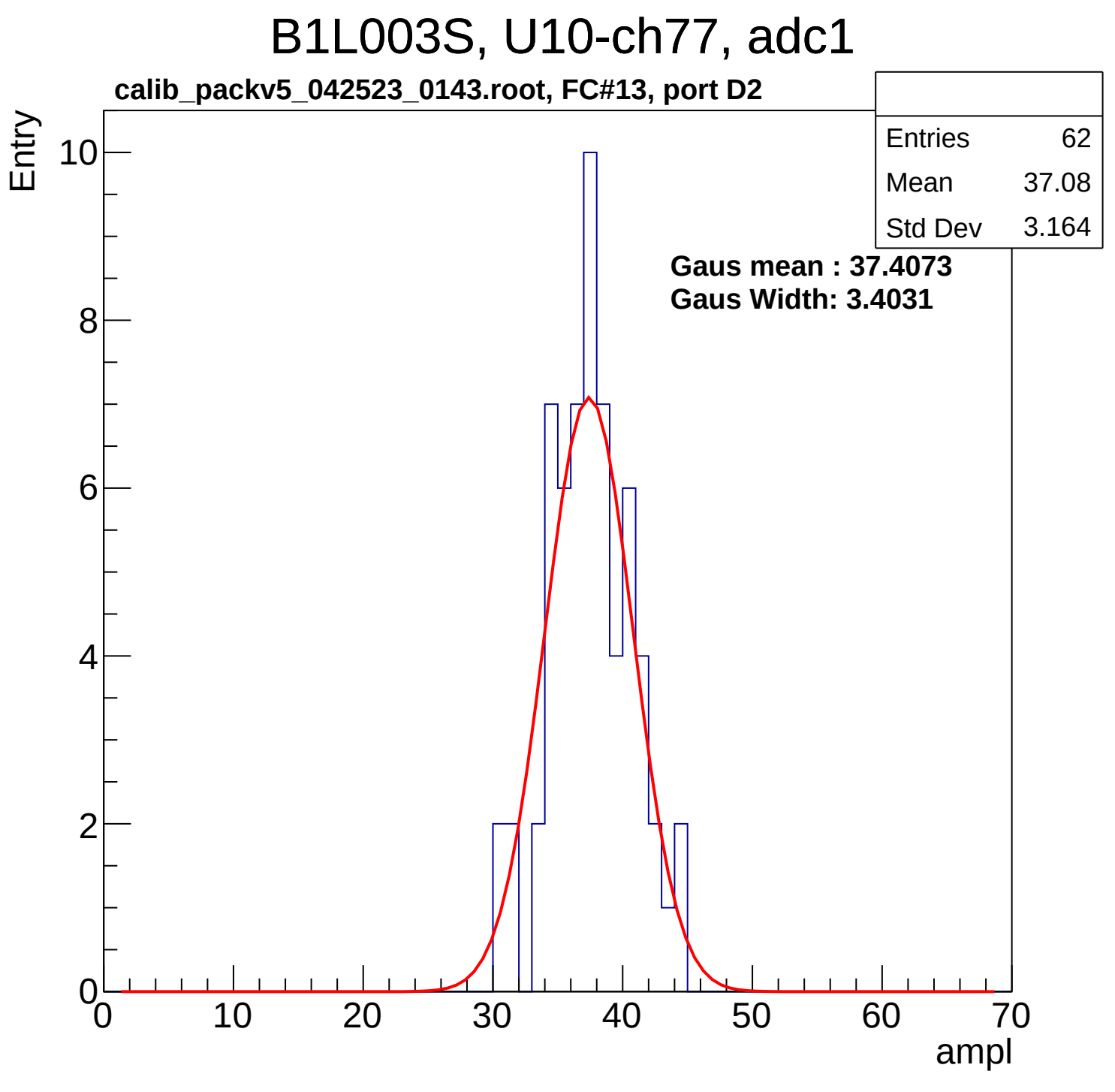
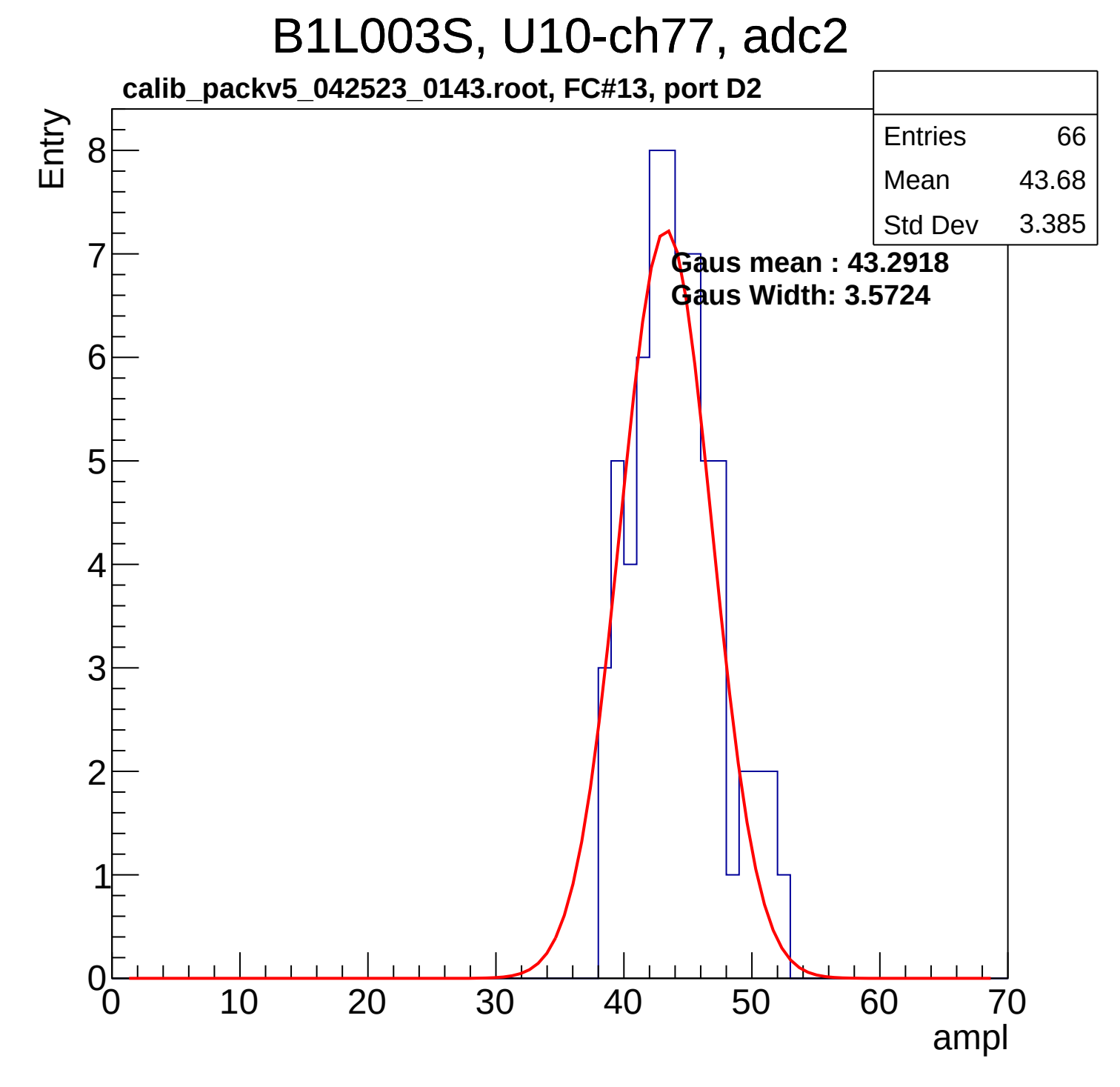
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch77, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 28.65
Std Dev 7.198
Gaus mean : 30.8169
Gaus Width: 3.5349
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch77, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.08
Std Dev 3.164
Gaus mean : 37.4073
Gaus Width: 3.4031
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch77, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.68
Std Dev 3.385
Gaus mean : 43.2918
Gaus Width: 3.5724
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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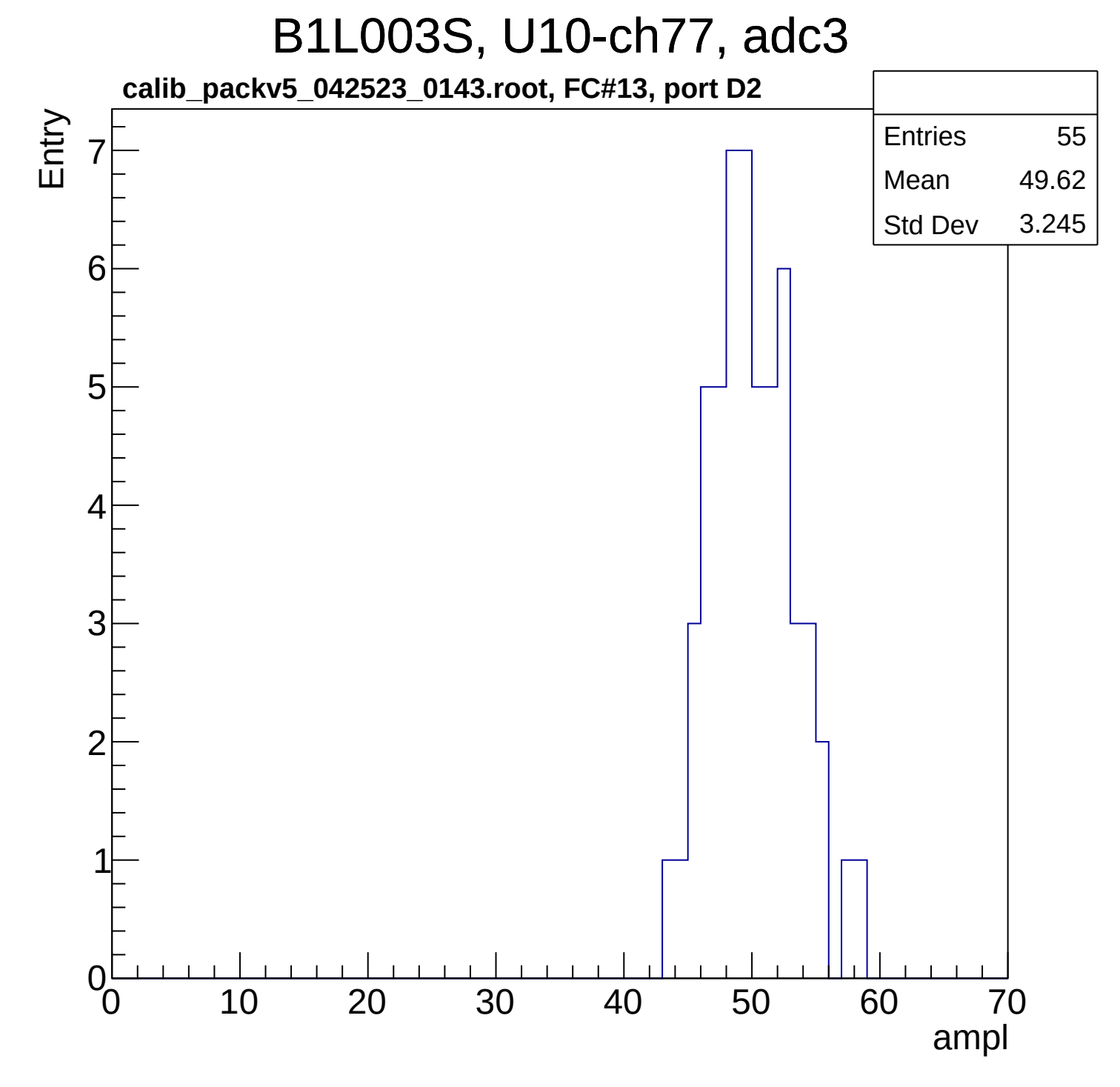
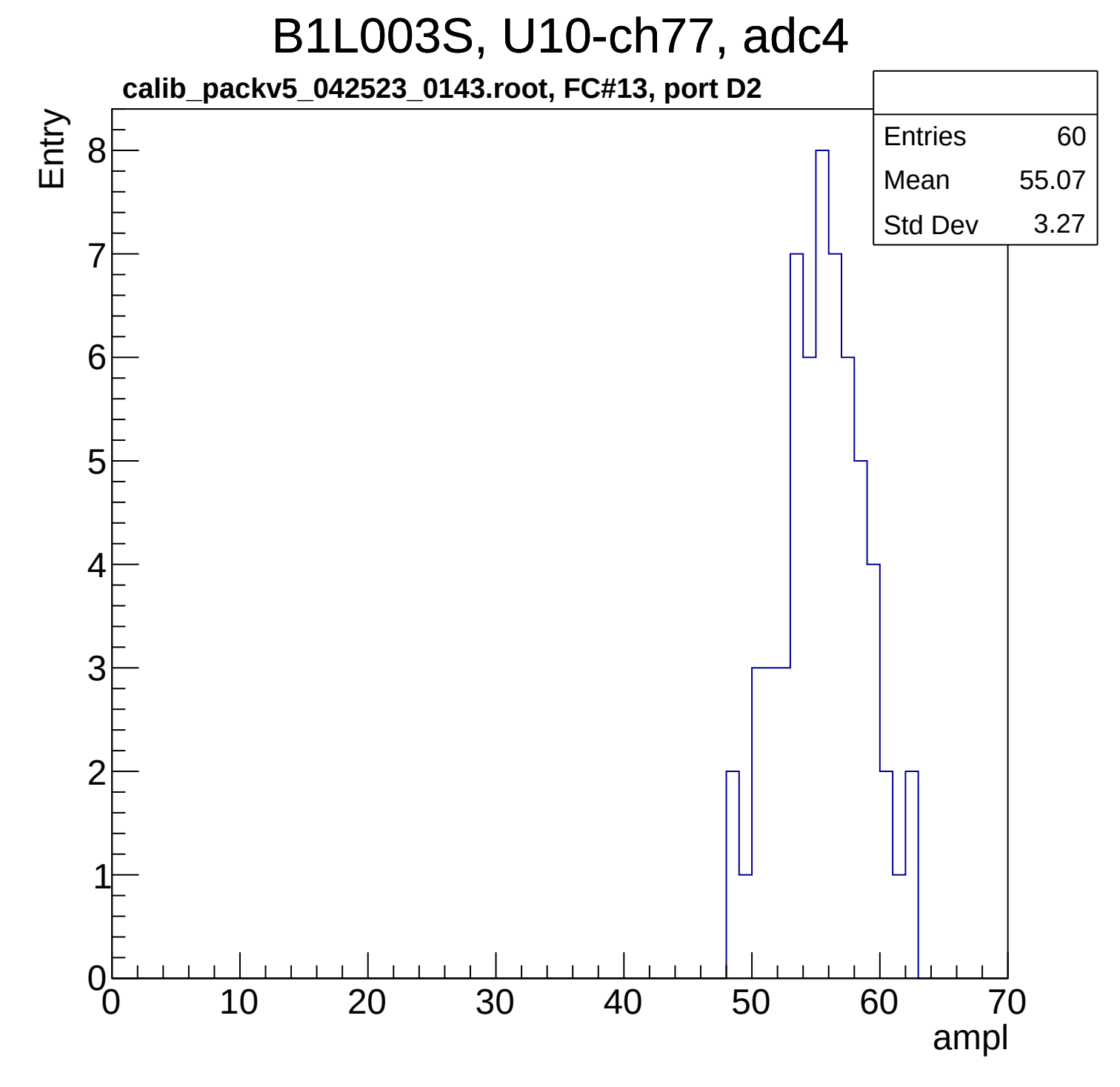

B1L003S, U10-ch77, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 49.62
Std Dev 3.245
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch77, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.07
Std Dev 3.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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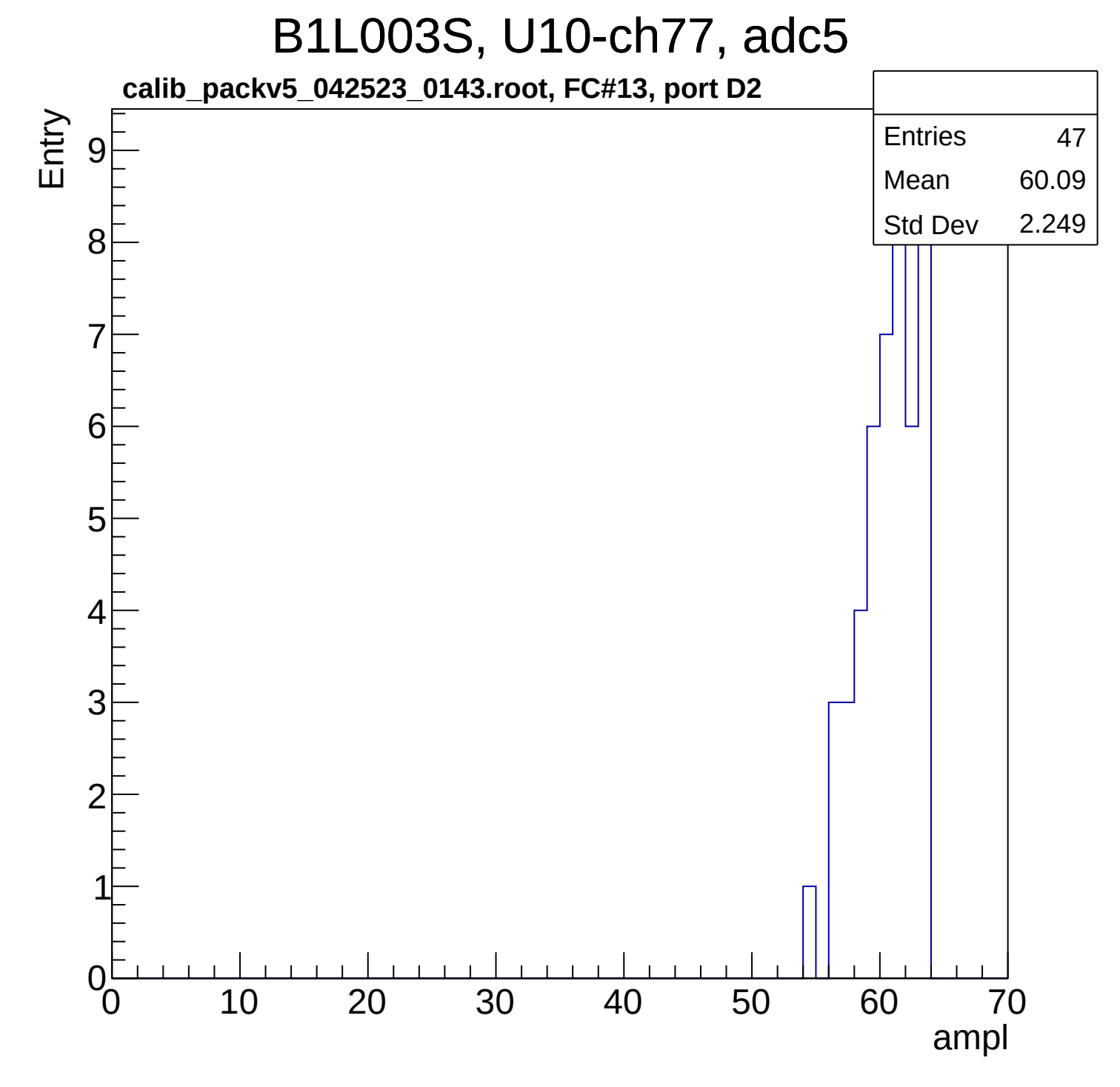
B1L003S, U10-ch77, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 60.09
Std Dev 2.249
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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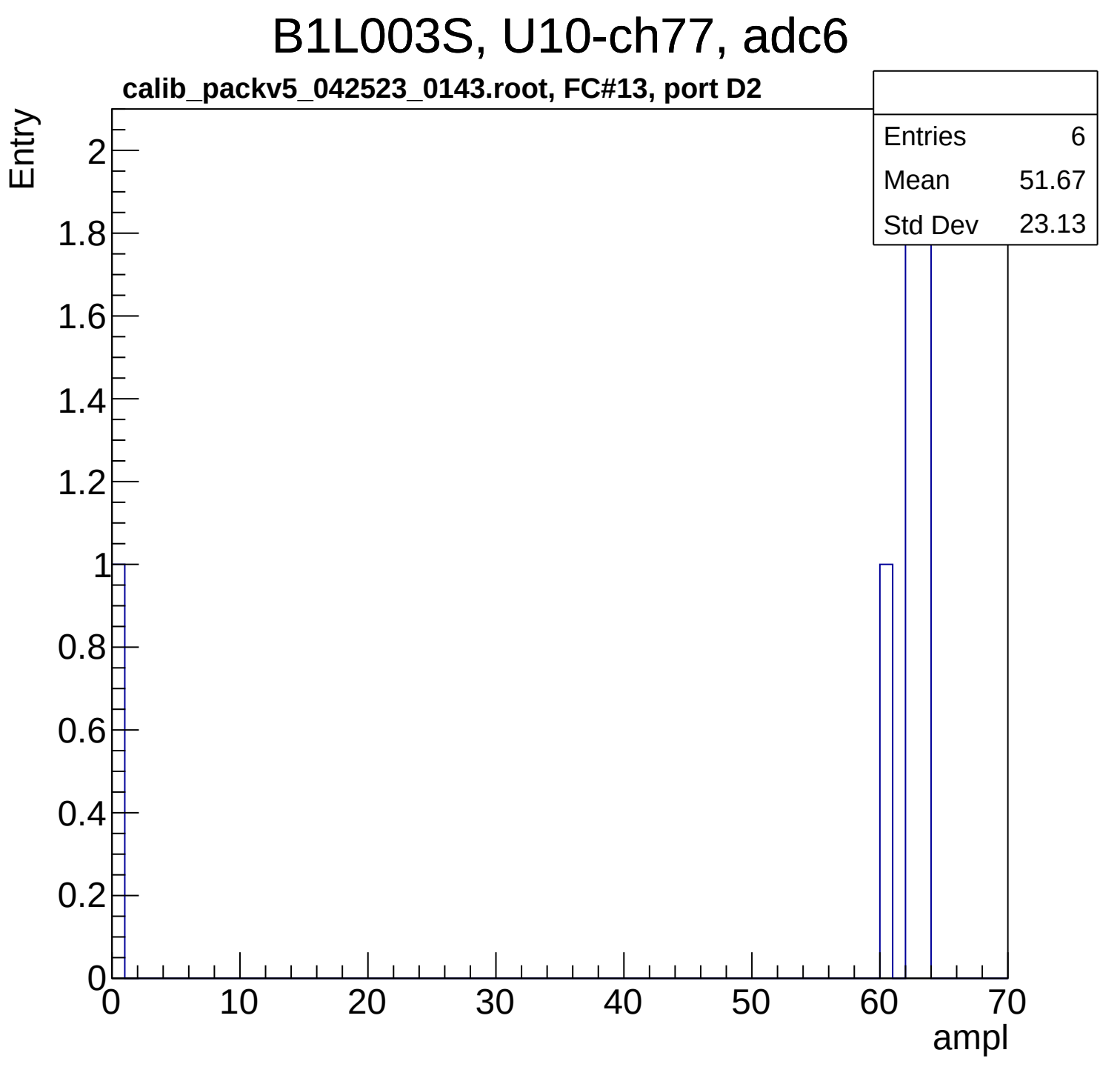
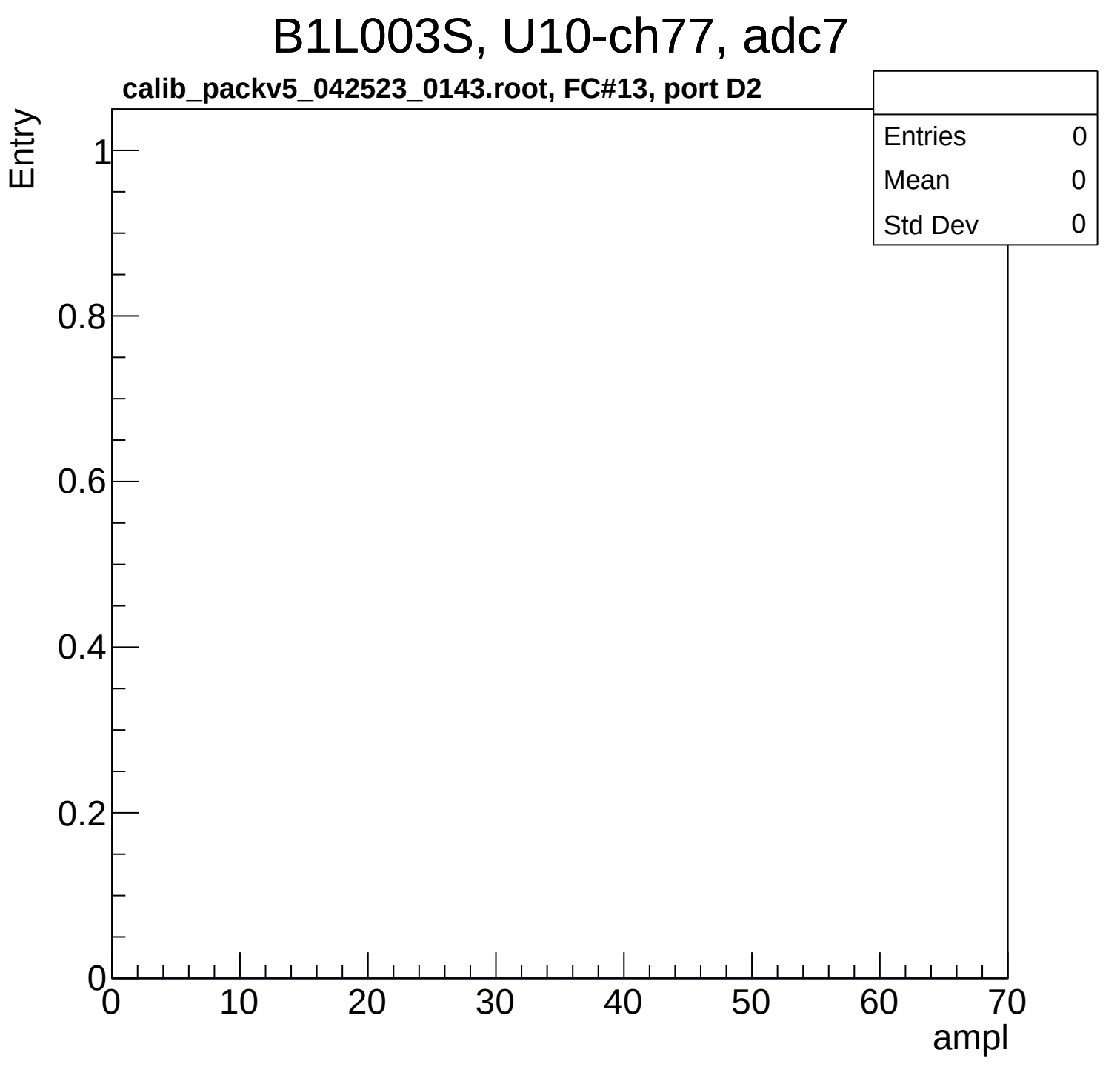
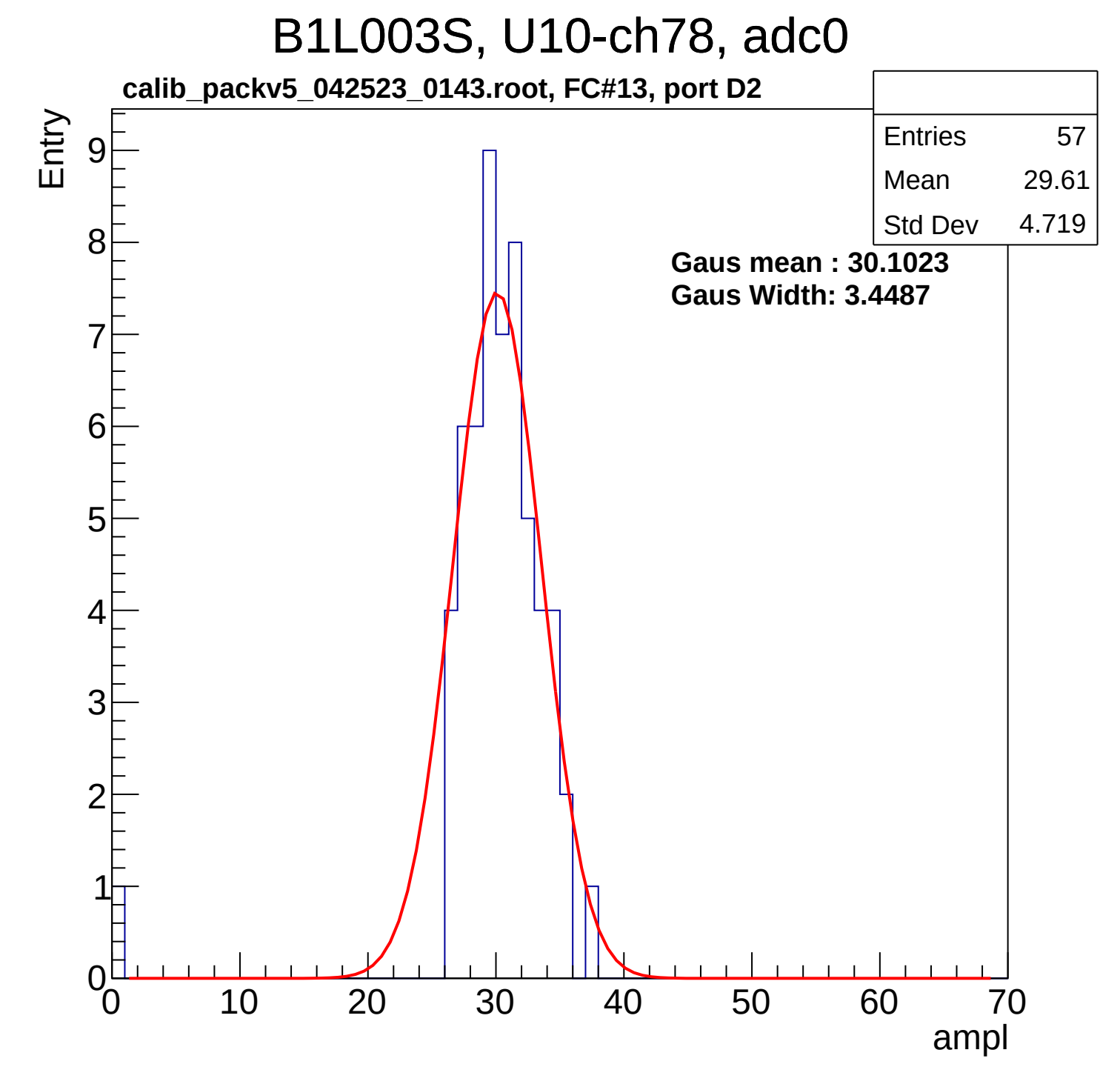
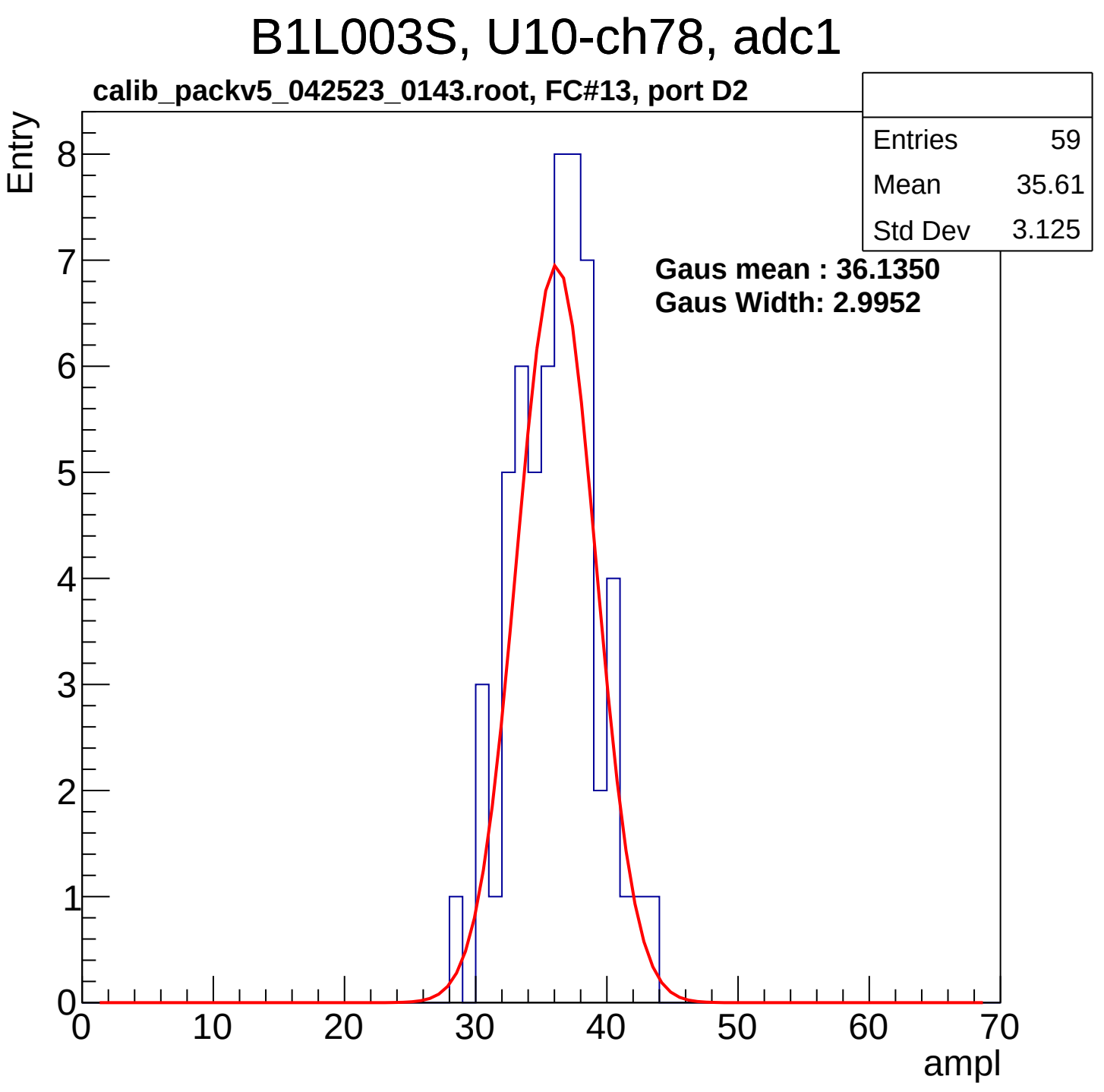
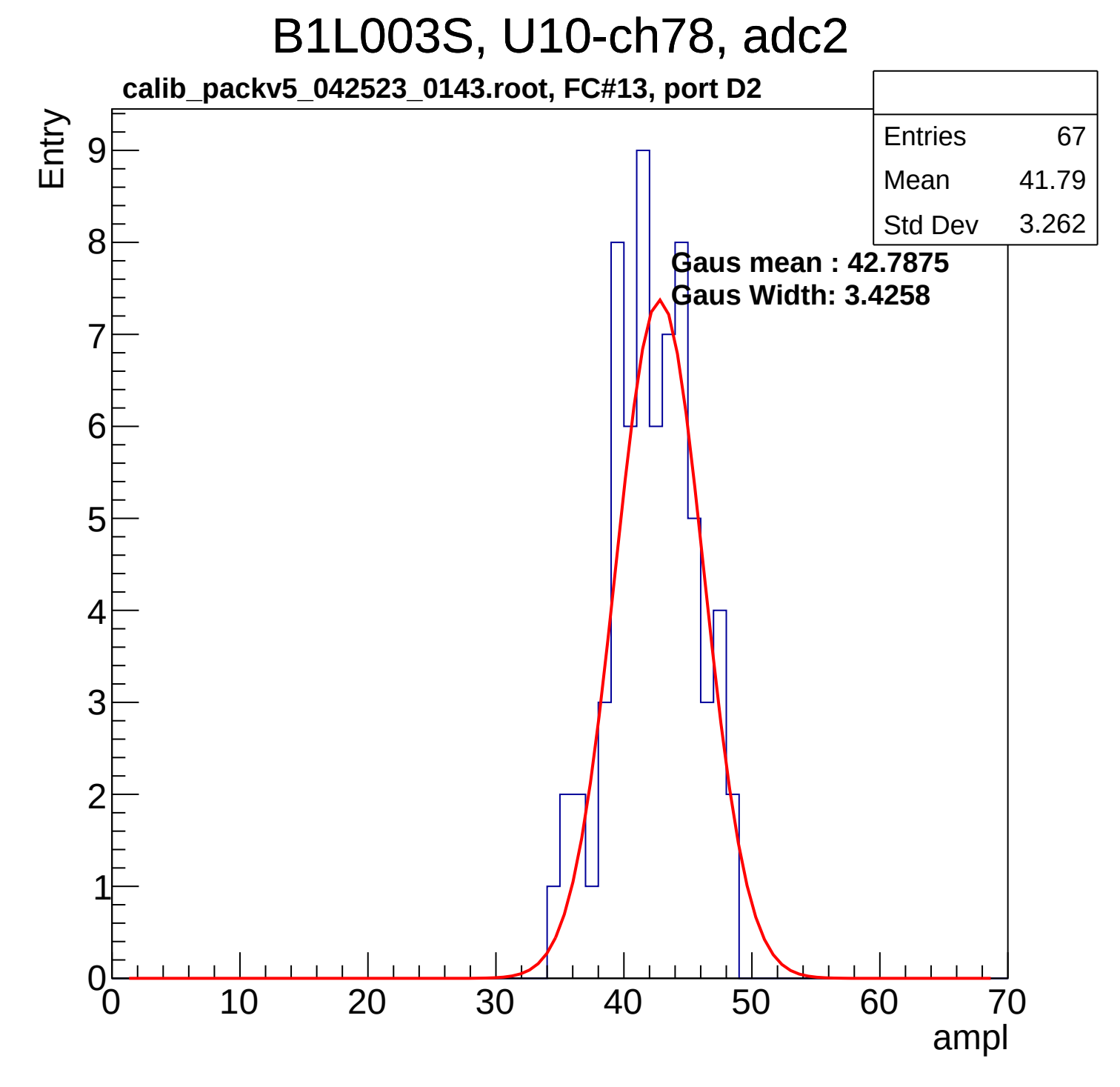
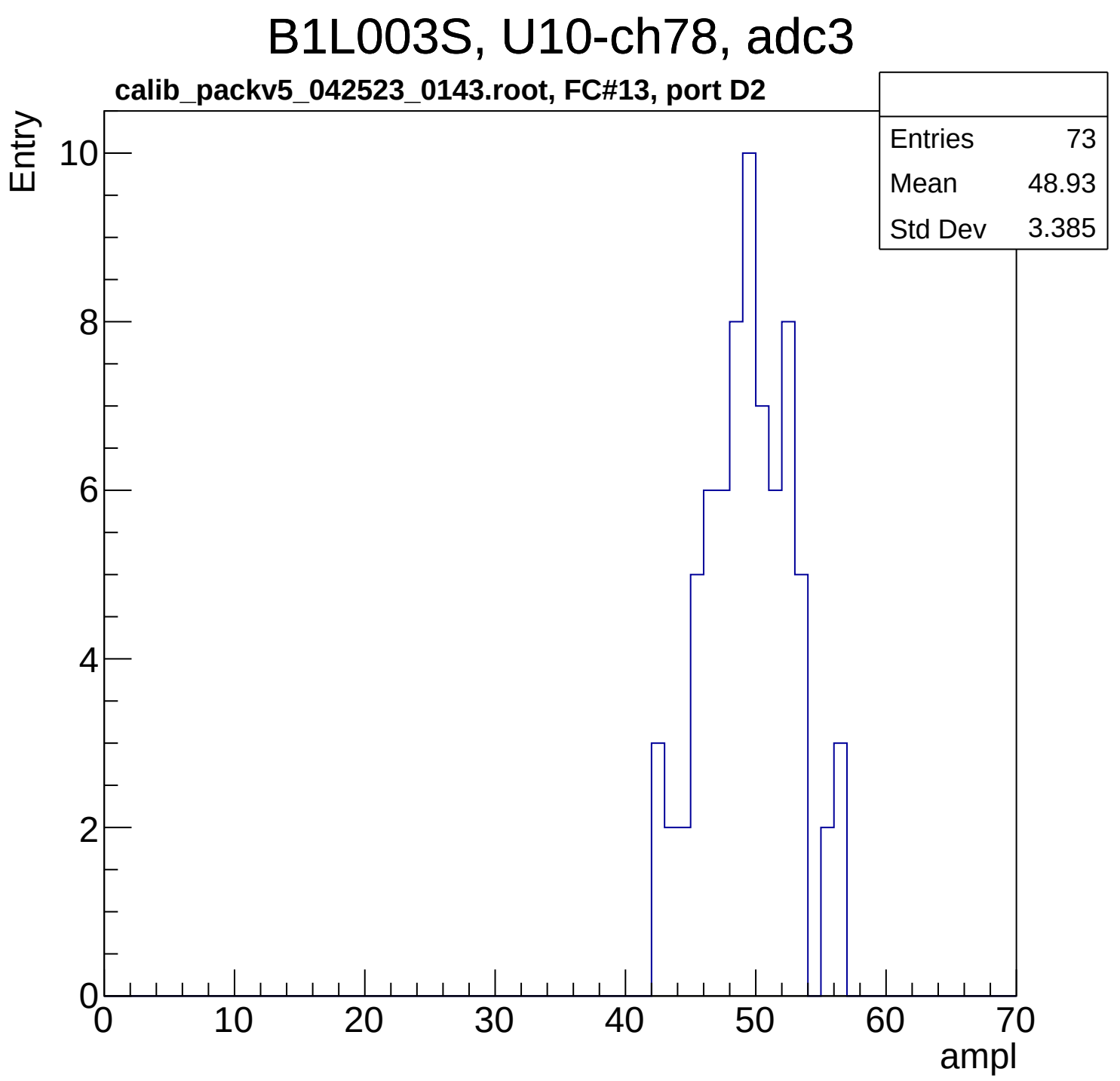

B1L003S, U10-ch77, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 51.67
Std Dev 23.13
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

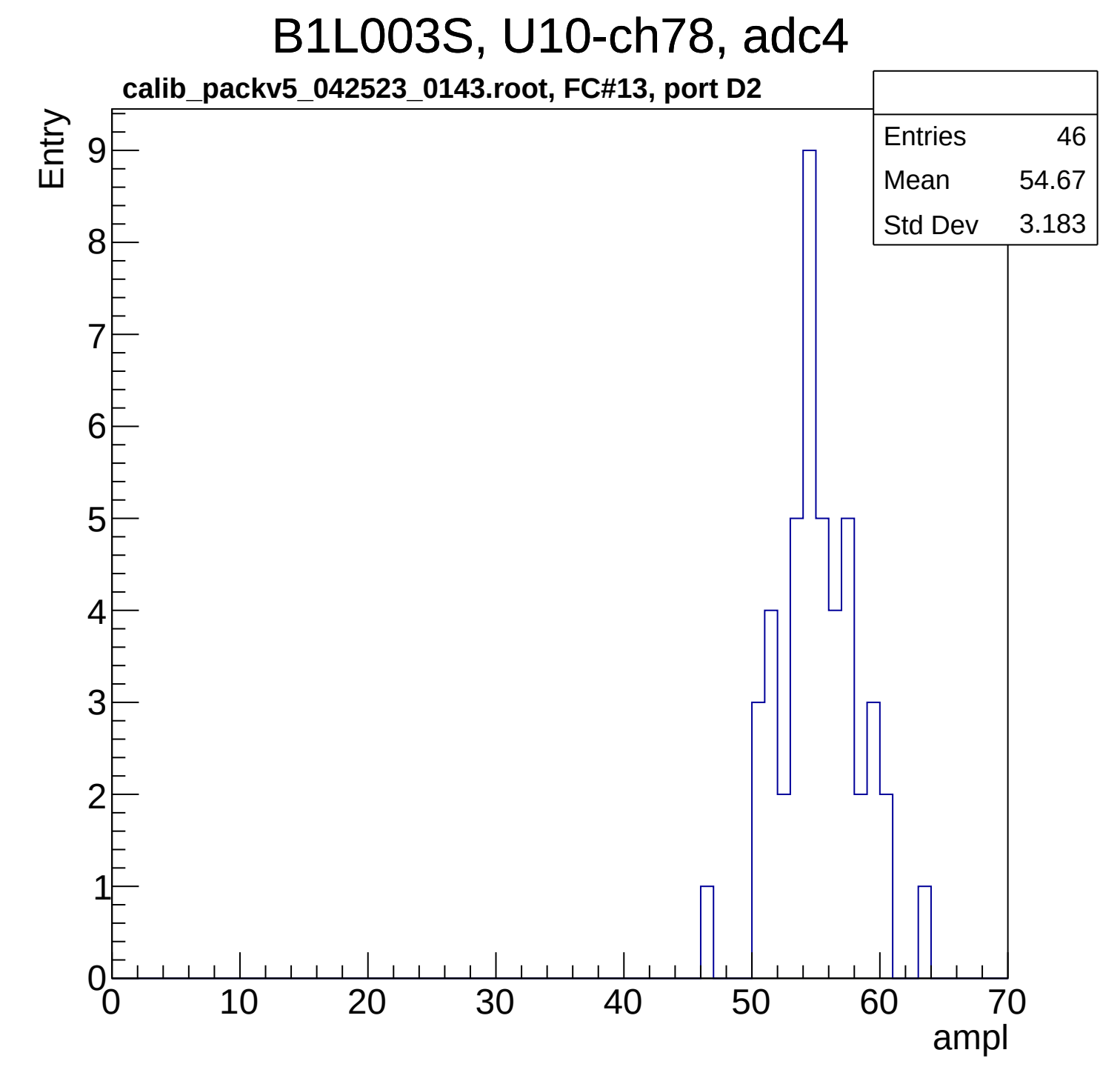

B1L003S, U10-ch77, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

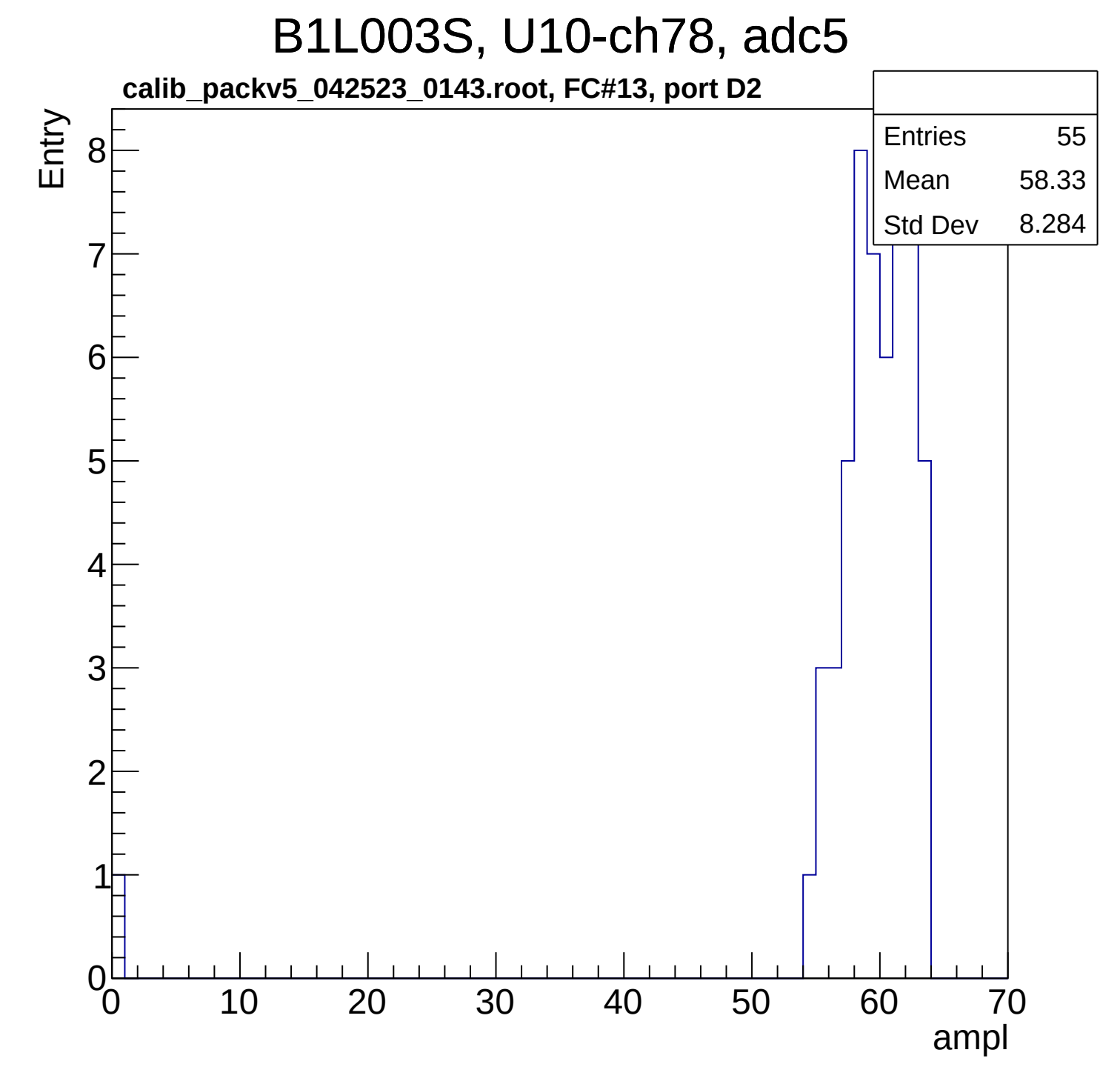

B1L003S, U10-ch78, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 29.61
Std Dev 4.719
Gaus mean : 30.1023
Gaus Width: 3.4487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch78, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 35.61
Std Dev 3.125
Gaus mean : 36.1350
Gaus Width: 2.9952
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch78, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 41.79
Std Dev 3.262
Gaus mean : 42.7875
Gaus Width: 3.4258
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch78, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 48.93
Std Dev 3.385
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch78, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 54.67
Std Dev 3.183
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch78, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.33
Std Dev 8.284
Entry
ampl
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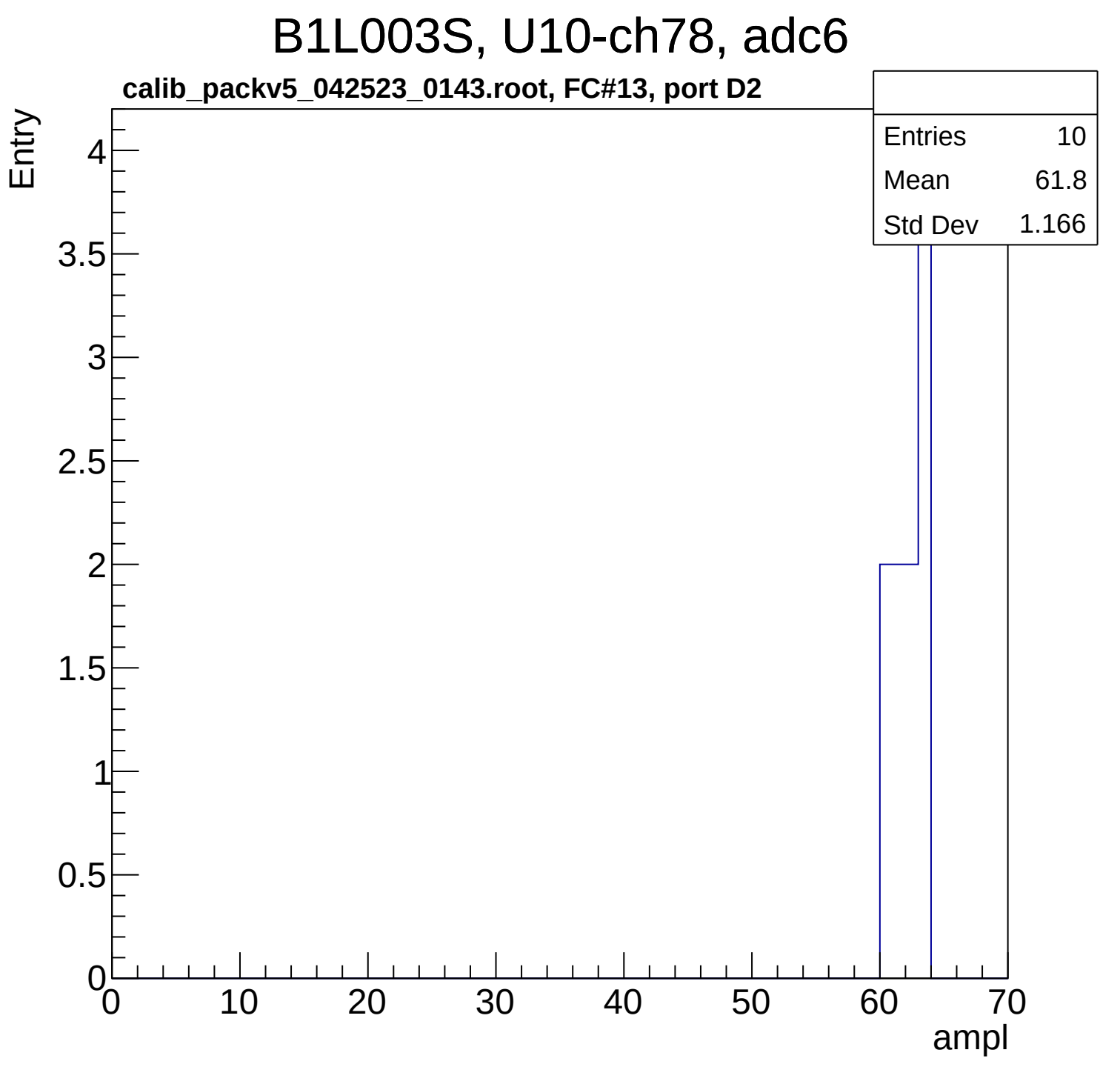
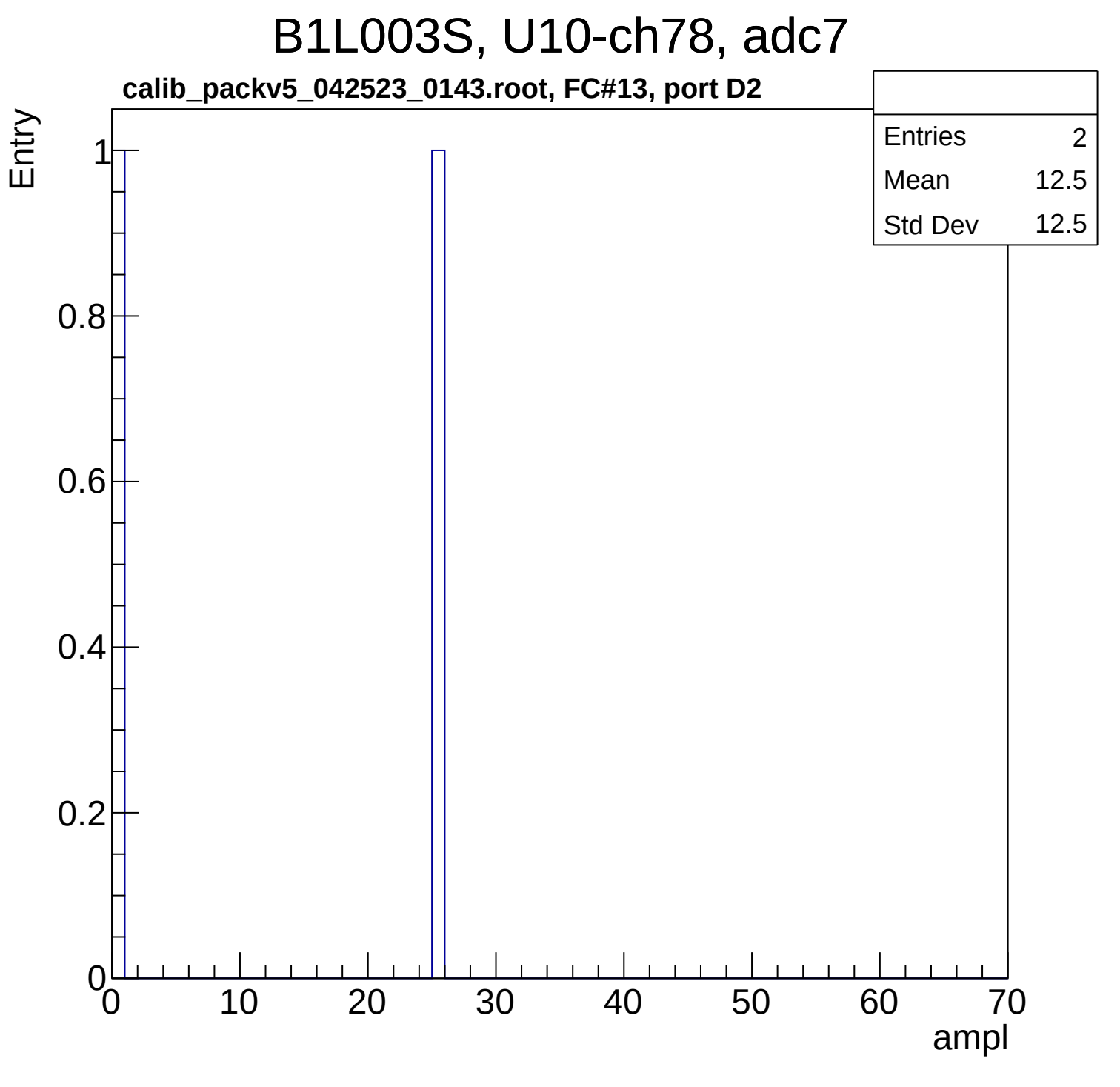
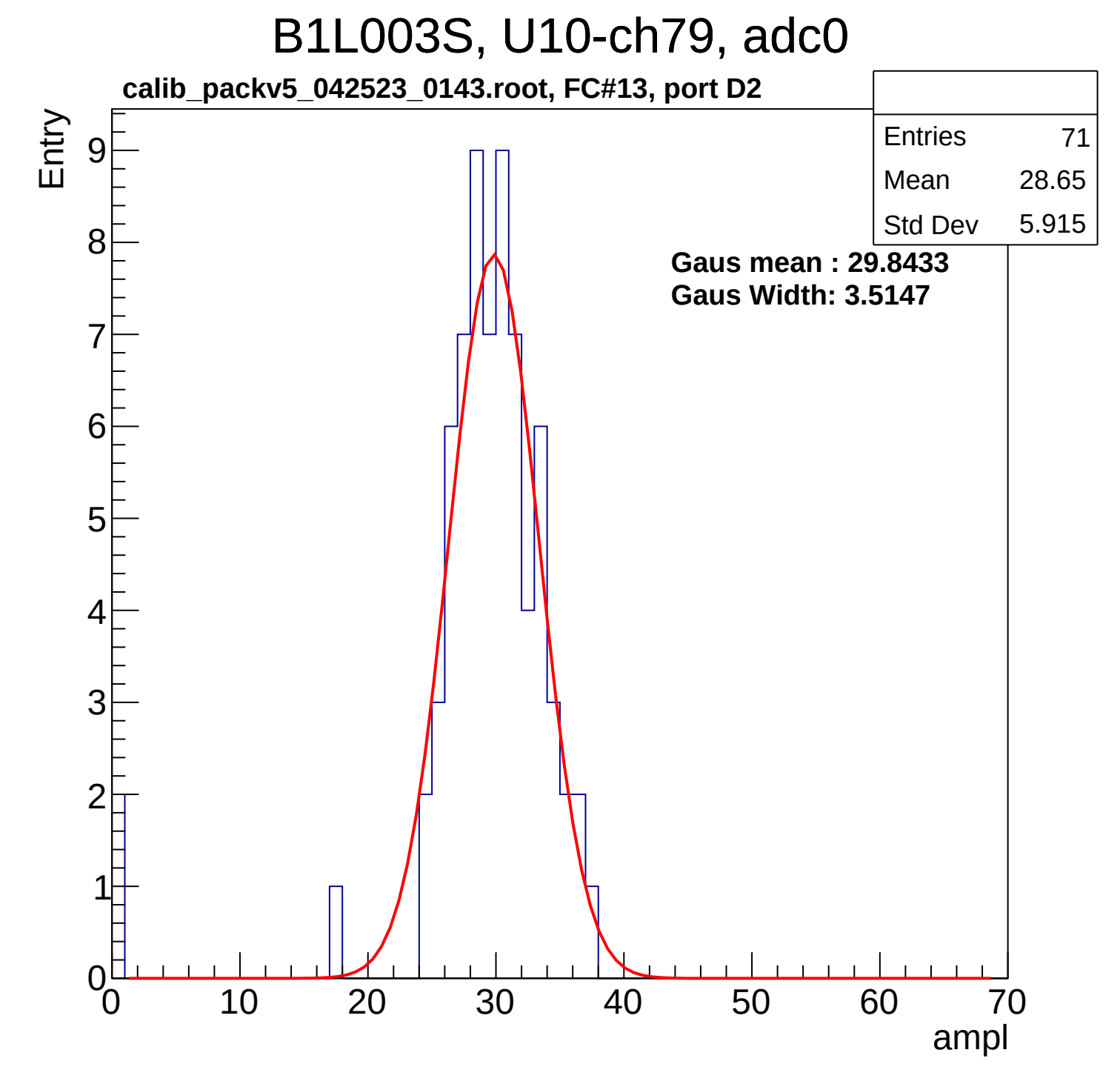
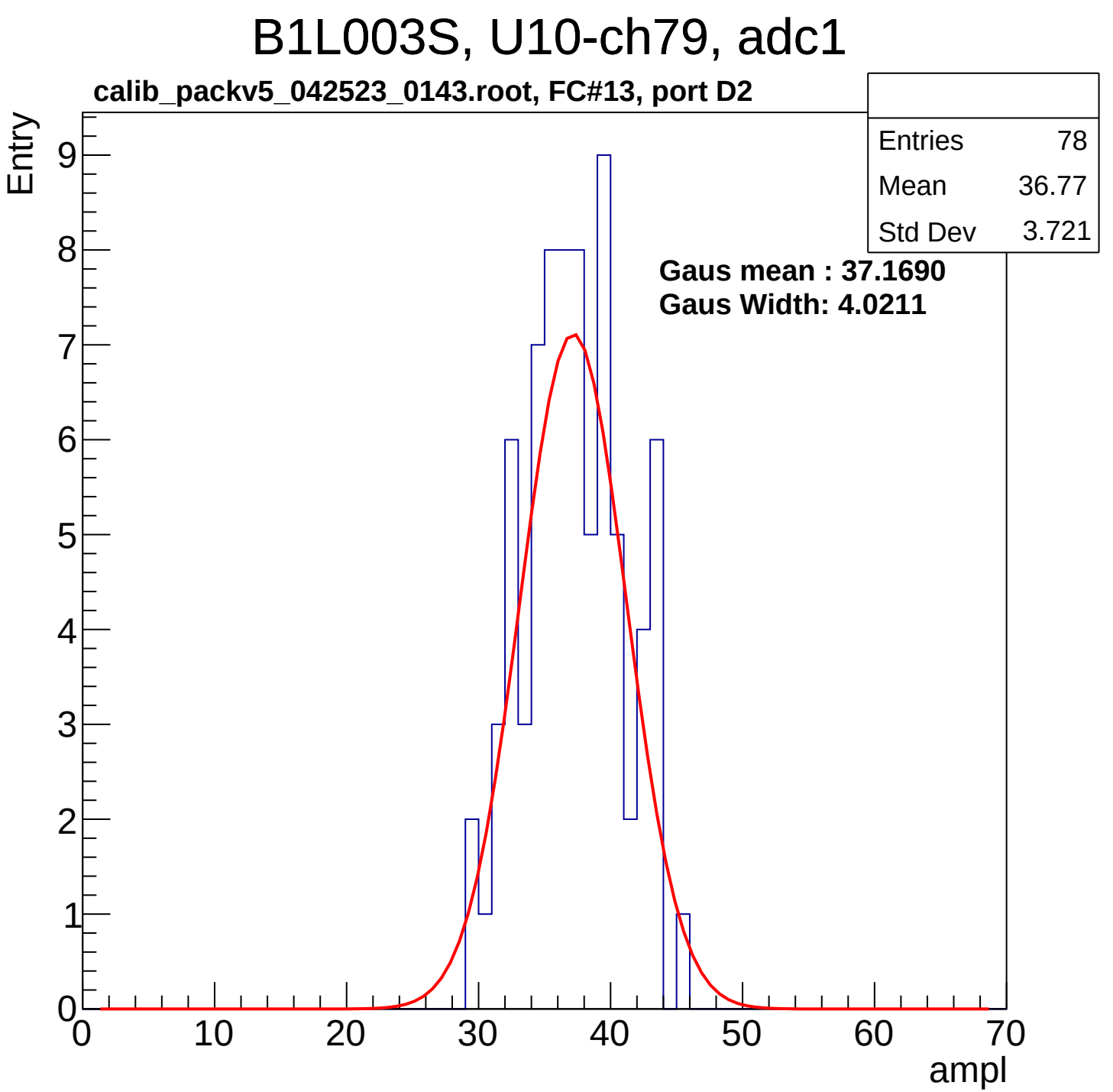
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch78, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch78, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch79, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 28.65
Std Dev 5.915
Gaus mean : 29.8433
Gaus Width: 3.5147
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch79, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 36.77
Std Dev 3.721
Gaus mean : 37.1690
Gaus Width: 4.0211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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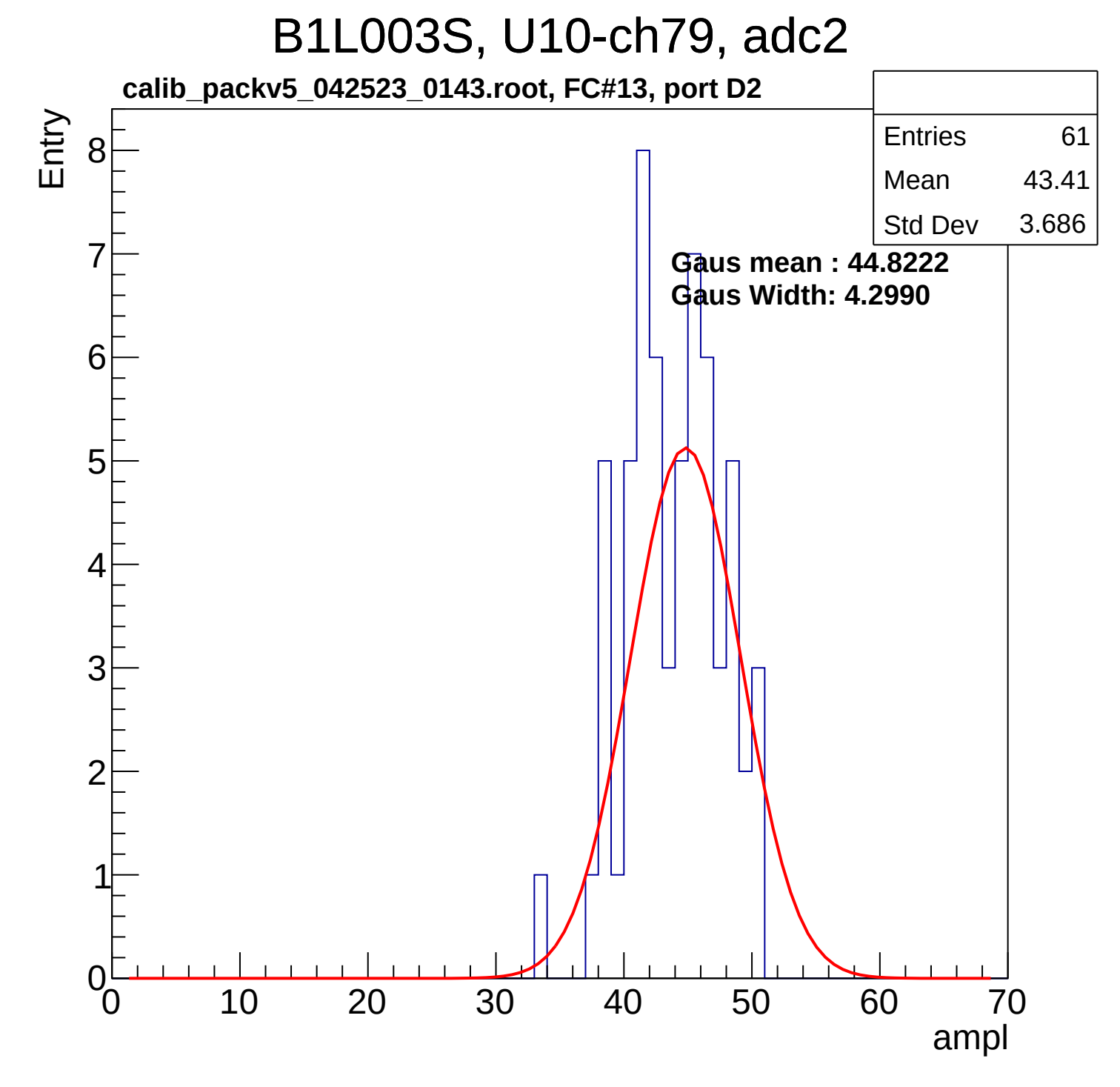
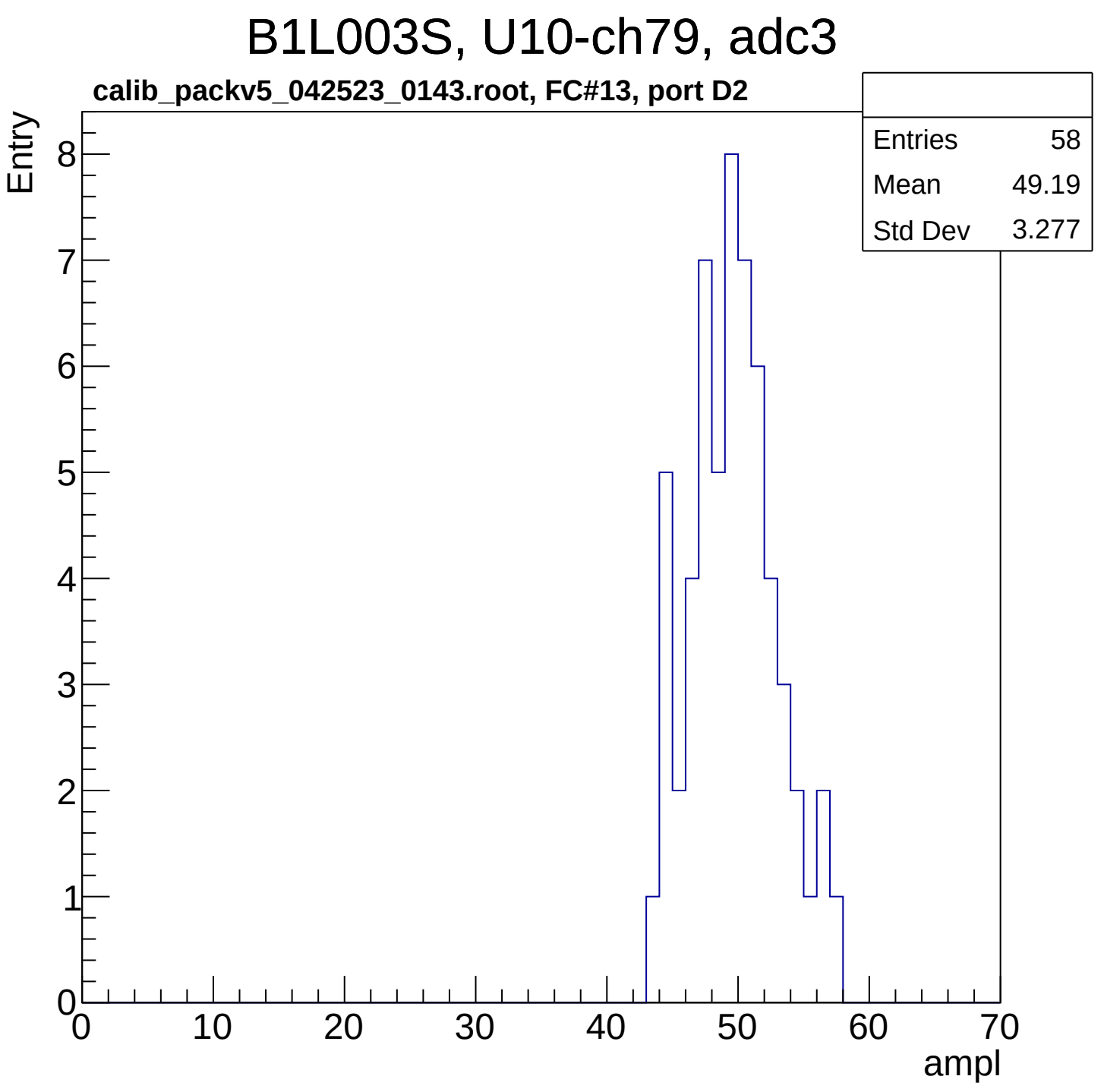

B1L003S, U10-ch79, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 43.41
Std Dev 3.686
Gaus mean : 44.8222
Gaus Width: 4.2990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch79, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 49.19
Std Dev 3.277
Entry
ampl
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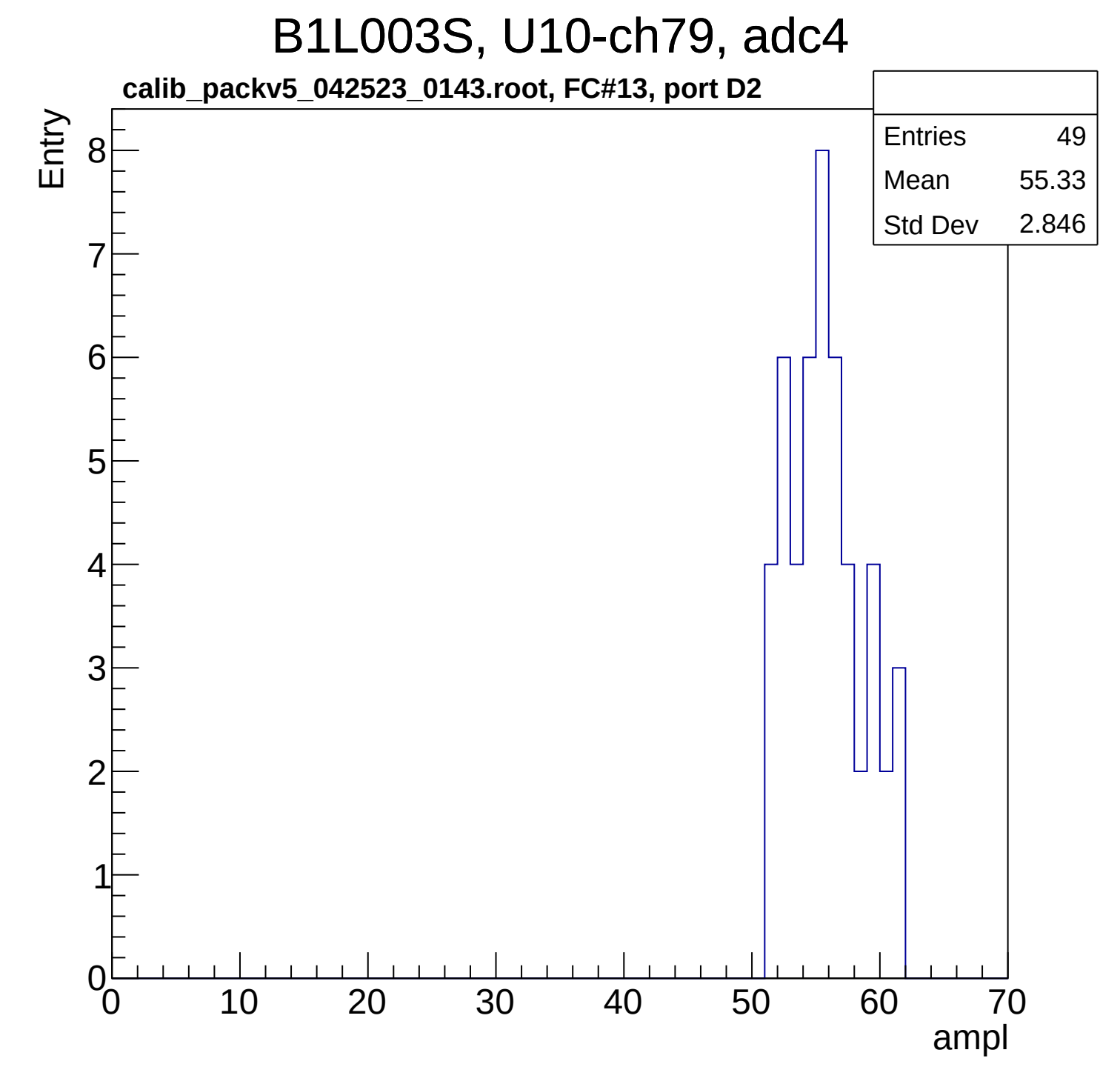
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch79, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 55.33
Std Dev 2.846
Entry
ampl
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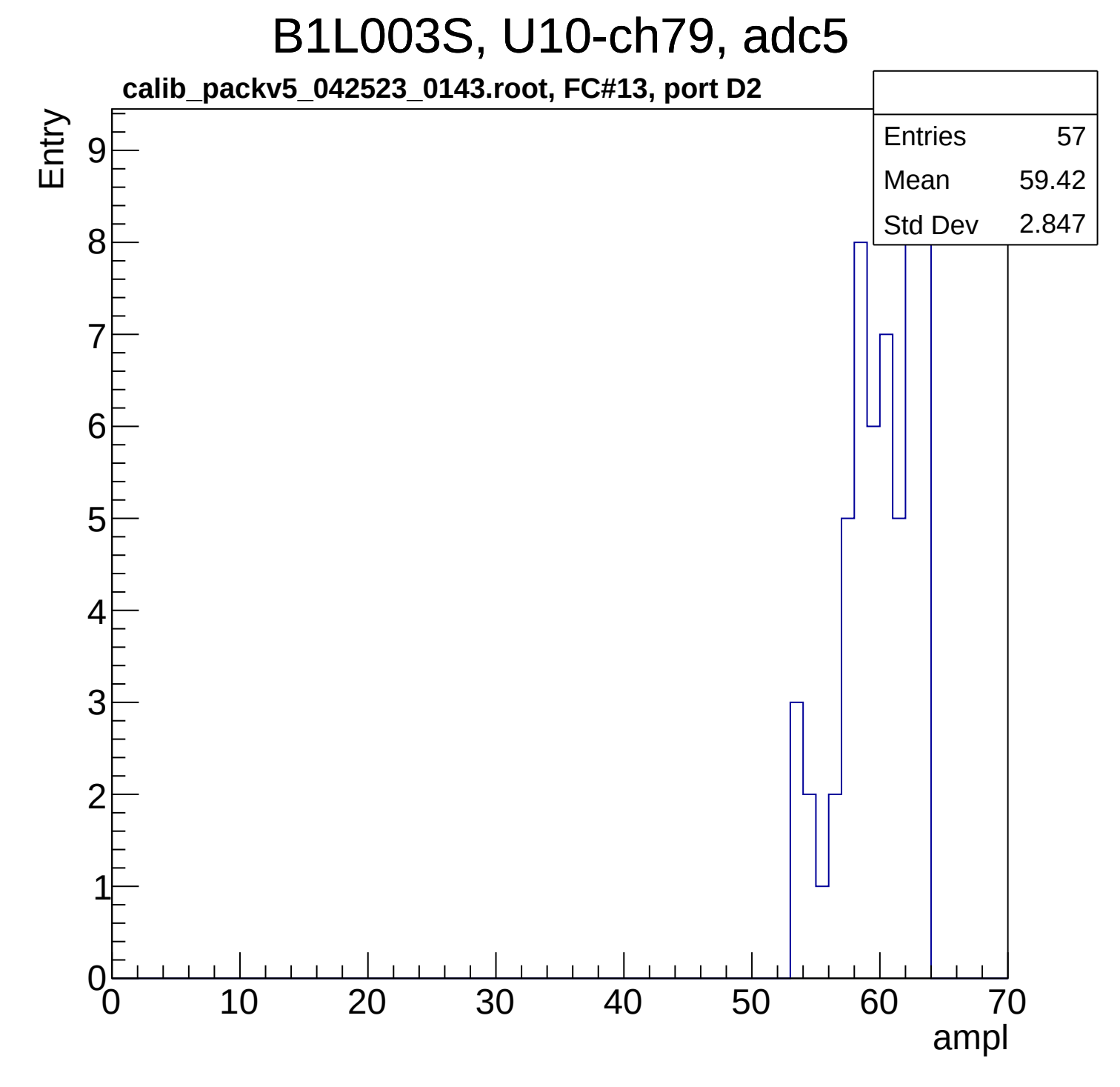
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch79, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 59.42
Std Dev 2.847
Entry
ampl
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3
4
5
6
7
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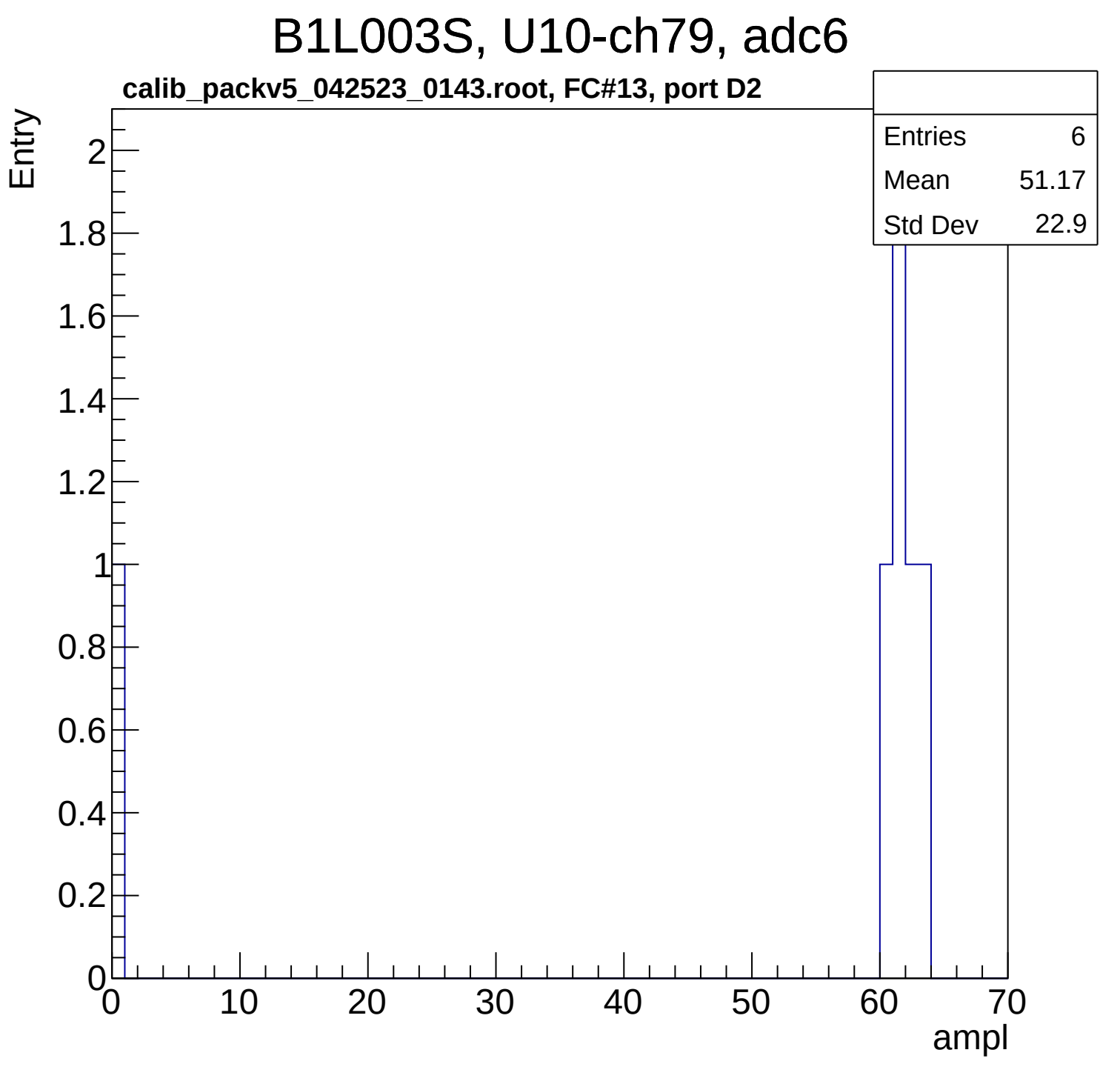
B1L003S, U10-ch79, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 51.17
Std Dev 22.9
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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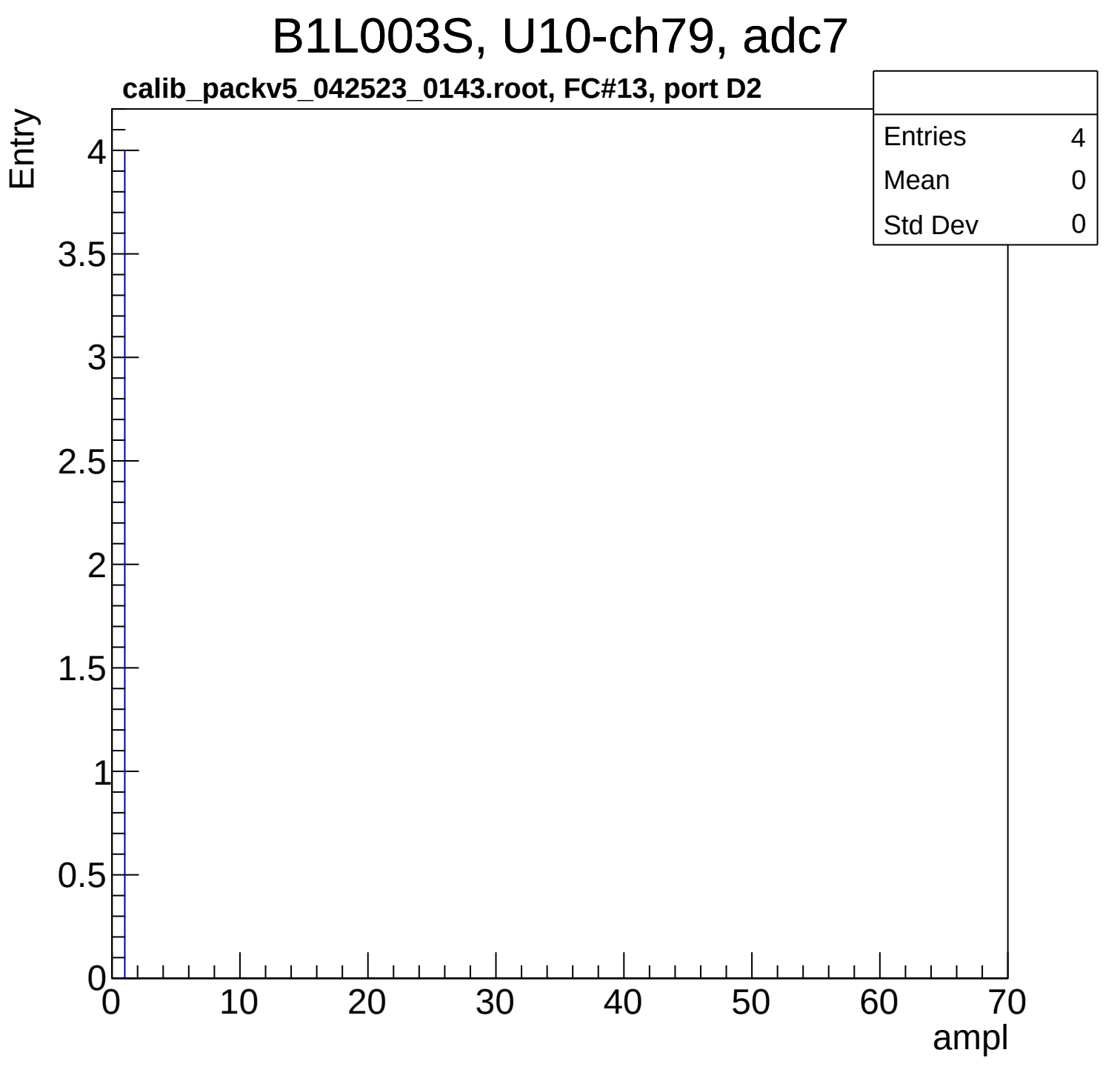
B1L003S, U10-ch79, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 0
Std Dev 0
Entry
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0.5
1
1.5
2
2.5
3
3.5
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0
10
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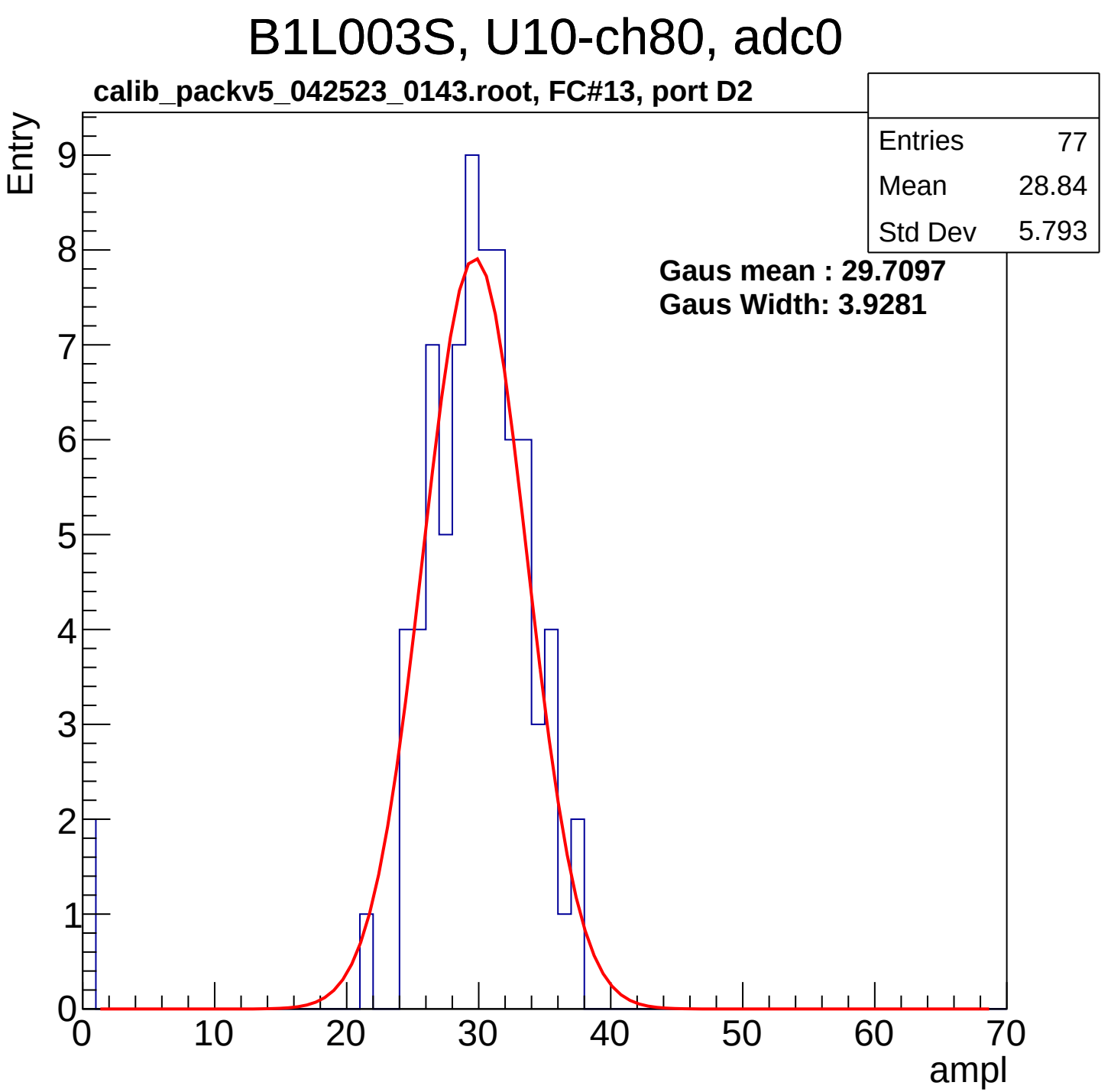
30
40
50
60
70
ampl


B1L003S, U10-ch80, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.84
Std Dev 5.793
Gaus mean : 29.7097
Gaus Width: 3.9281
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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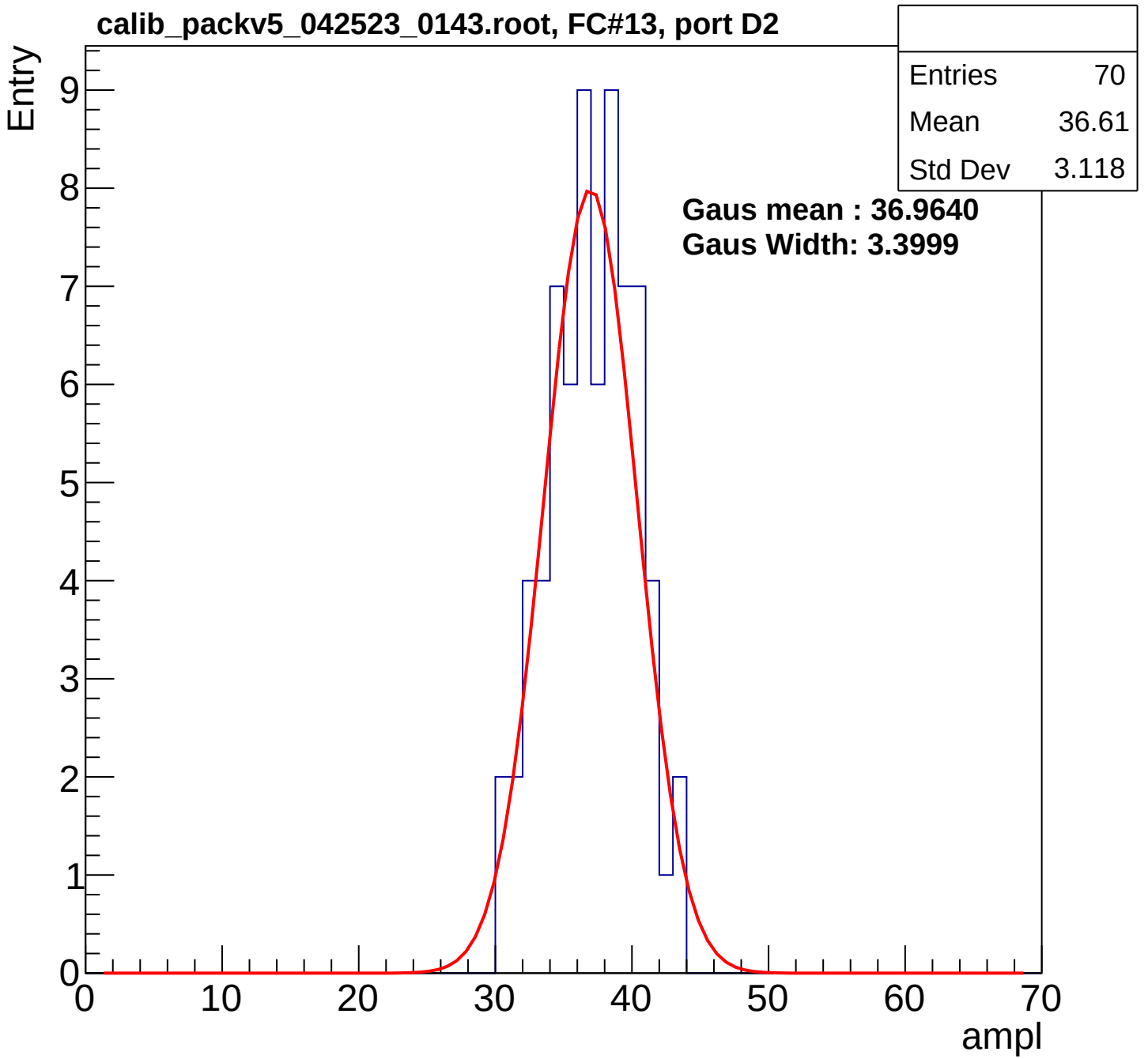
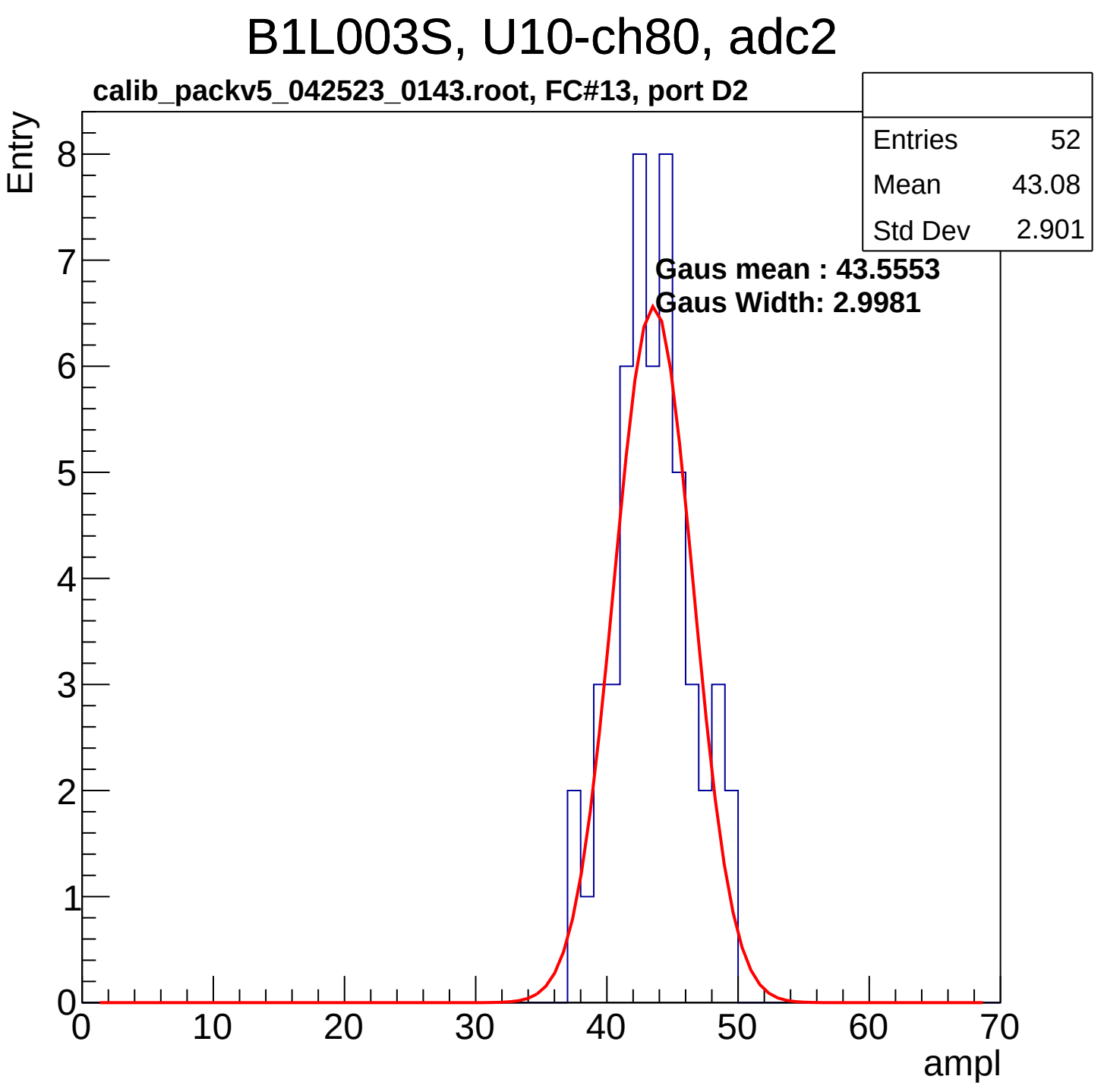

B1L003S, U10-ch80, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36.61
Std Dev 3.118
Gaus mean : 36.9640
Gaus Width: 3.3999
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch80, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 43.08
Std Dev 2.901
Gaus mean : 43.5553
Gaus Width: 2.9981
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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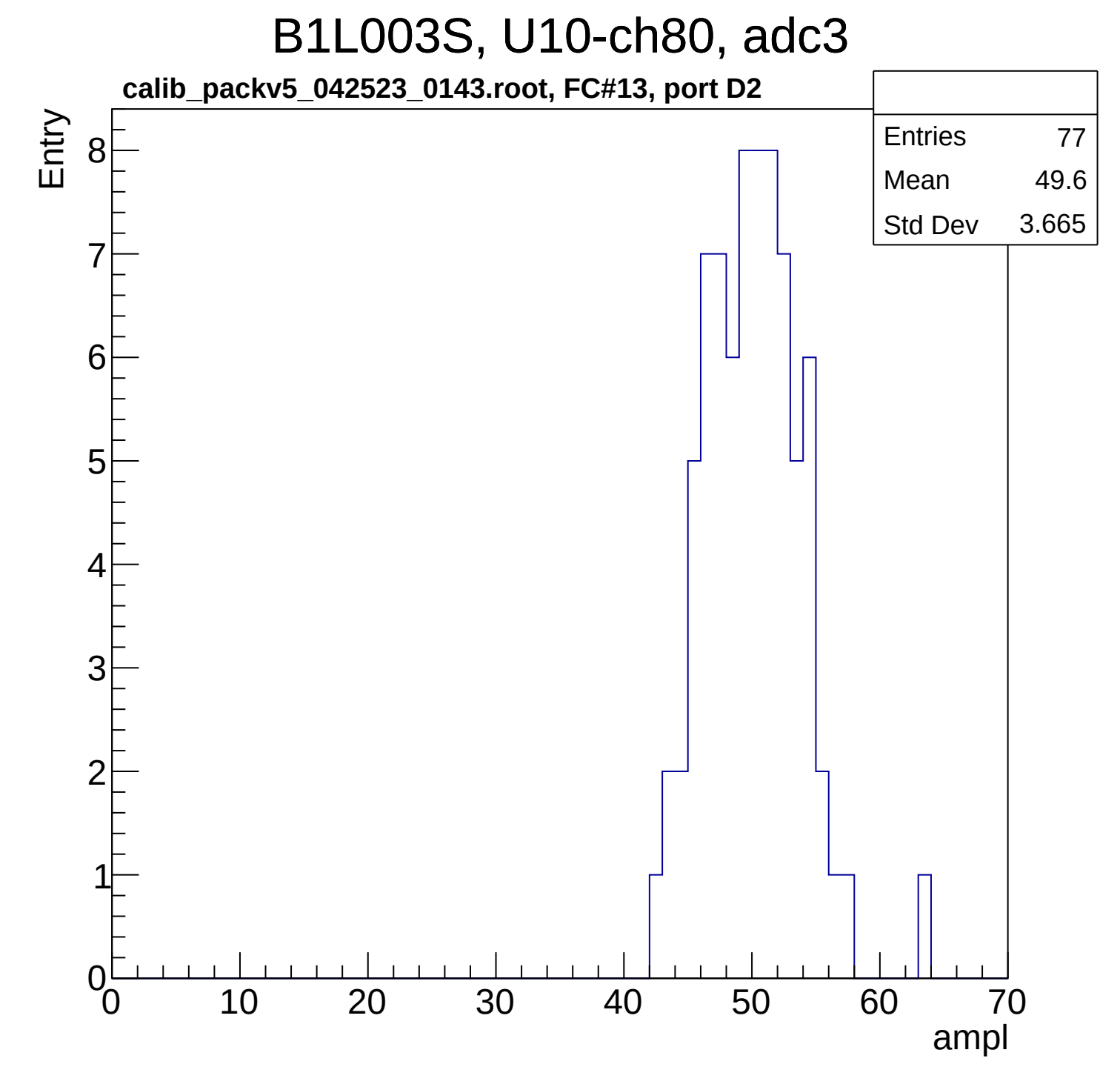
40
50
60
70


B1L003S, U10-ch80, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 49.6
Std Dev 3.665
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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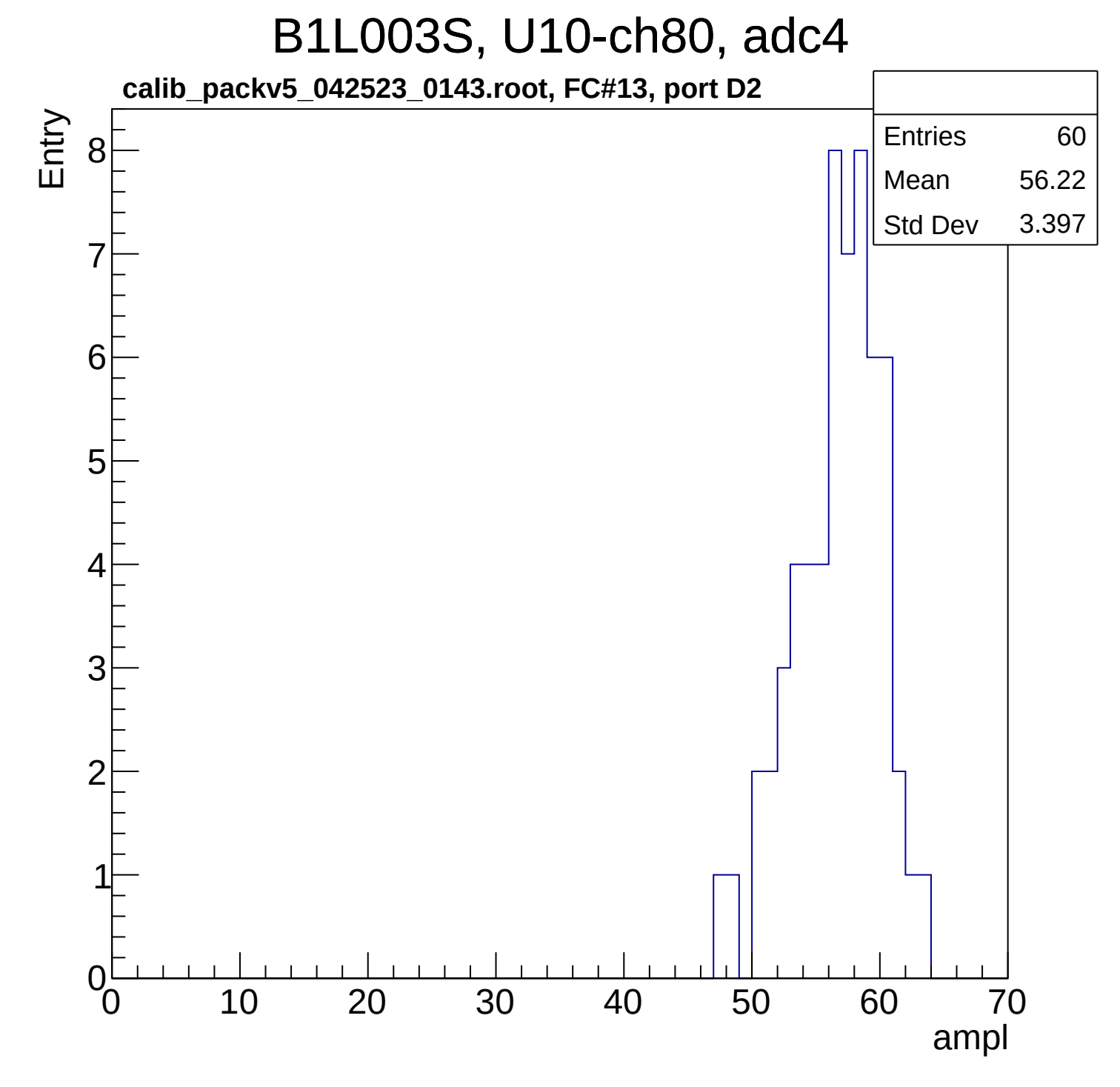
B1L003S, U10-ch80, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 56.22
Std Dev 3.397
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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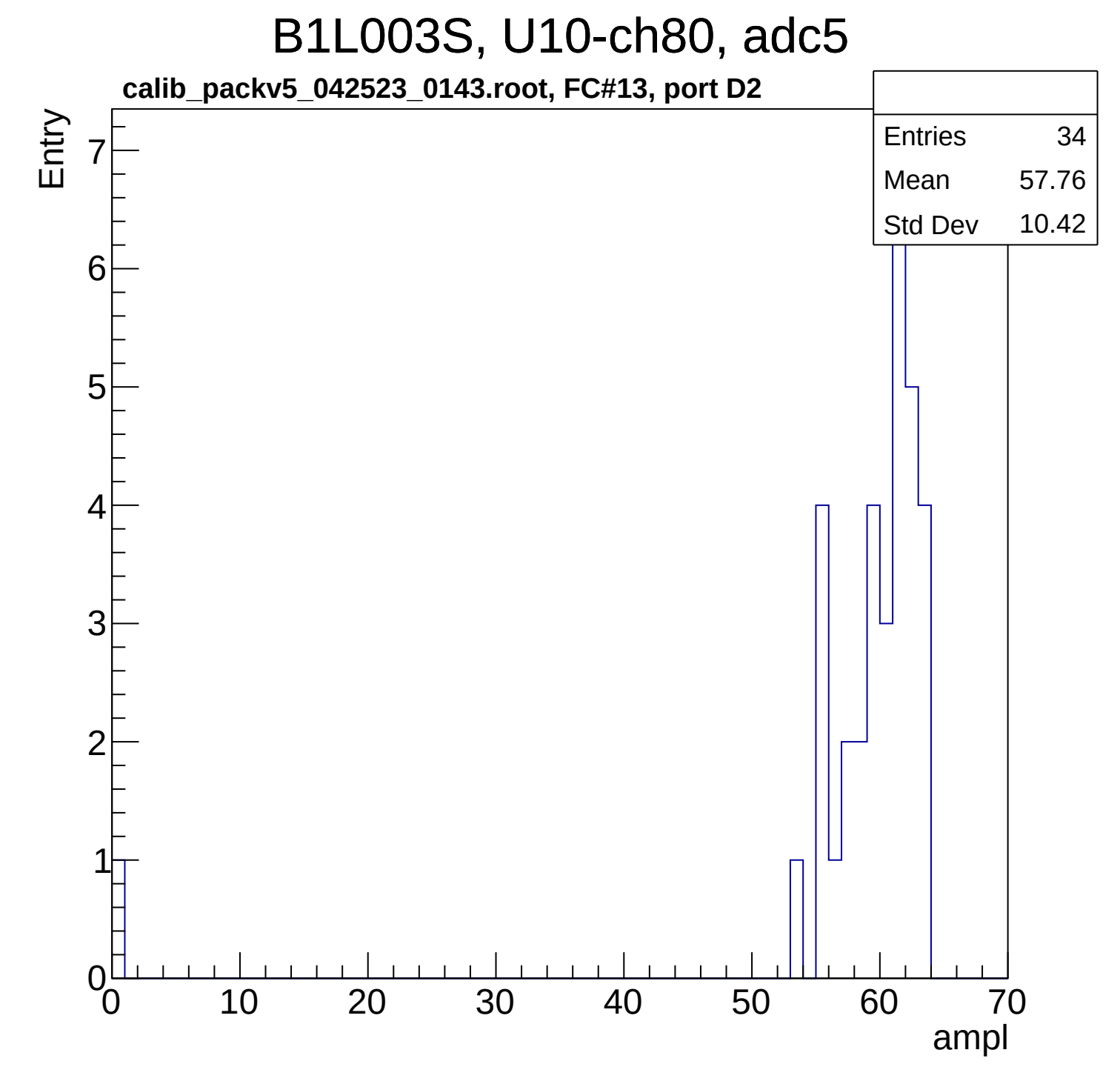
B1L003S, U10-ch80, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 57.76
Std Dev 10.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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50
60
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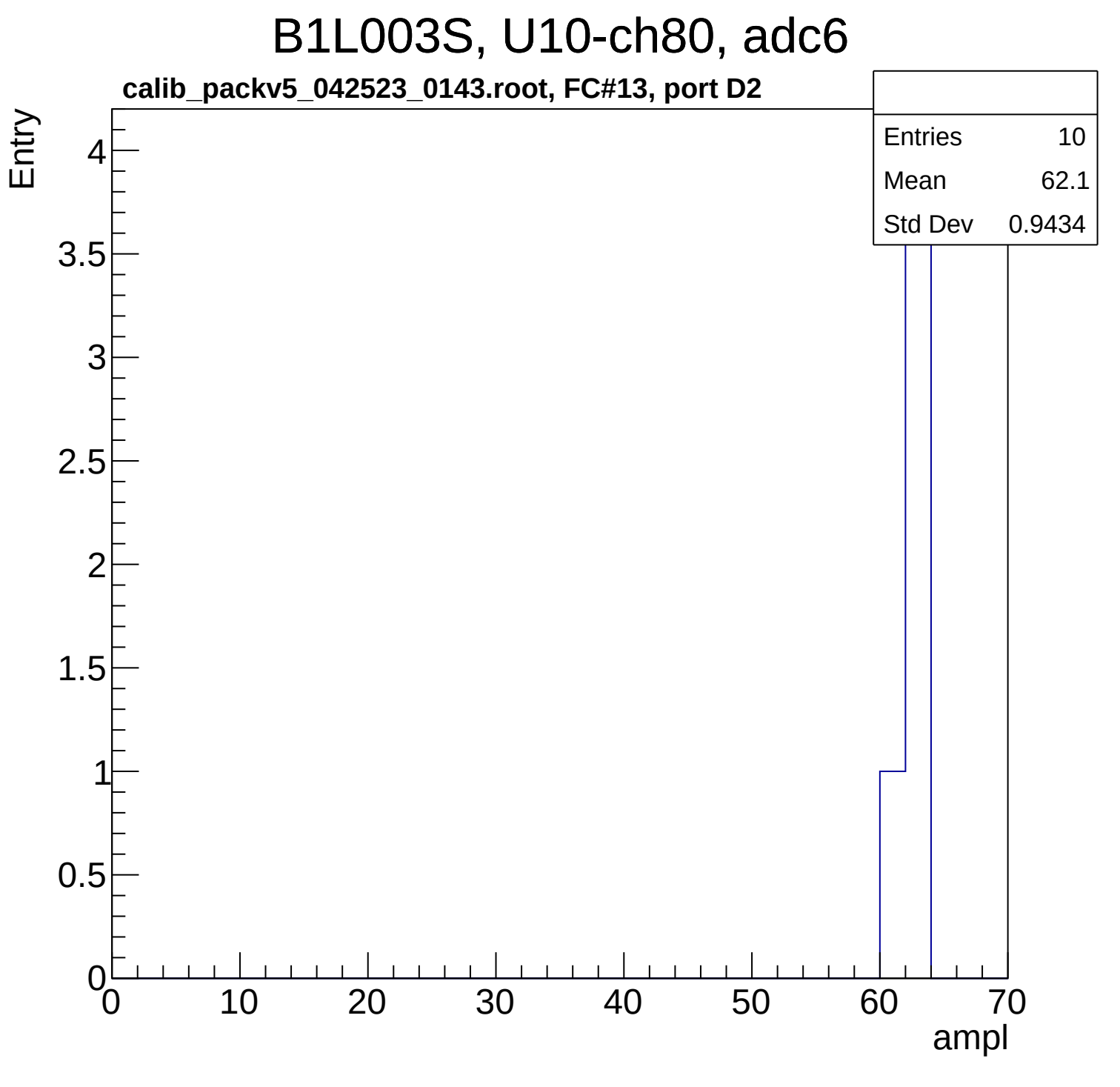
B1L003S, U10-ch80, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 62.1
Std Dev 0.9434
Entry
ampl
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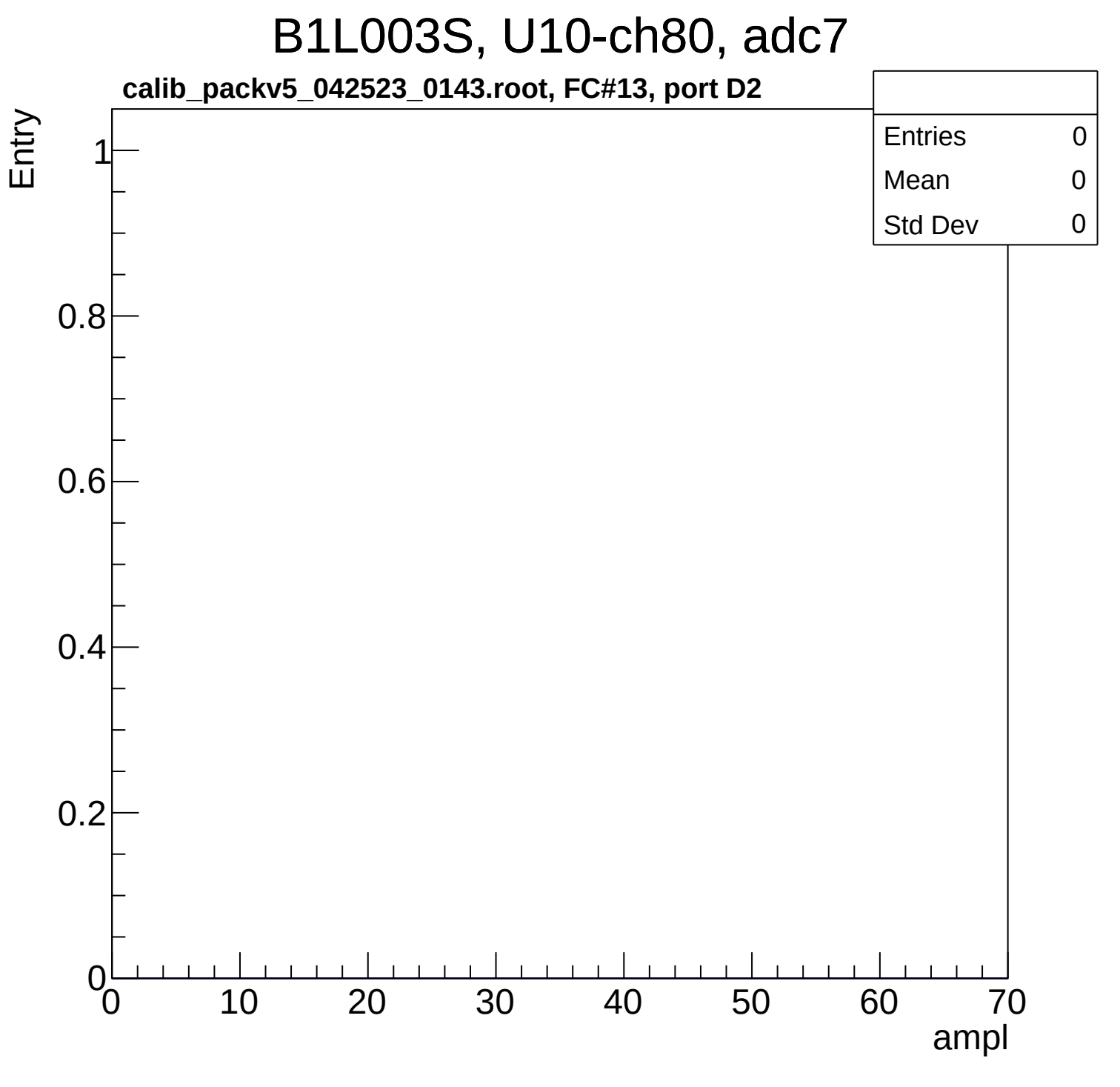
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch80, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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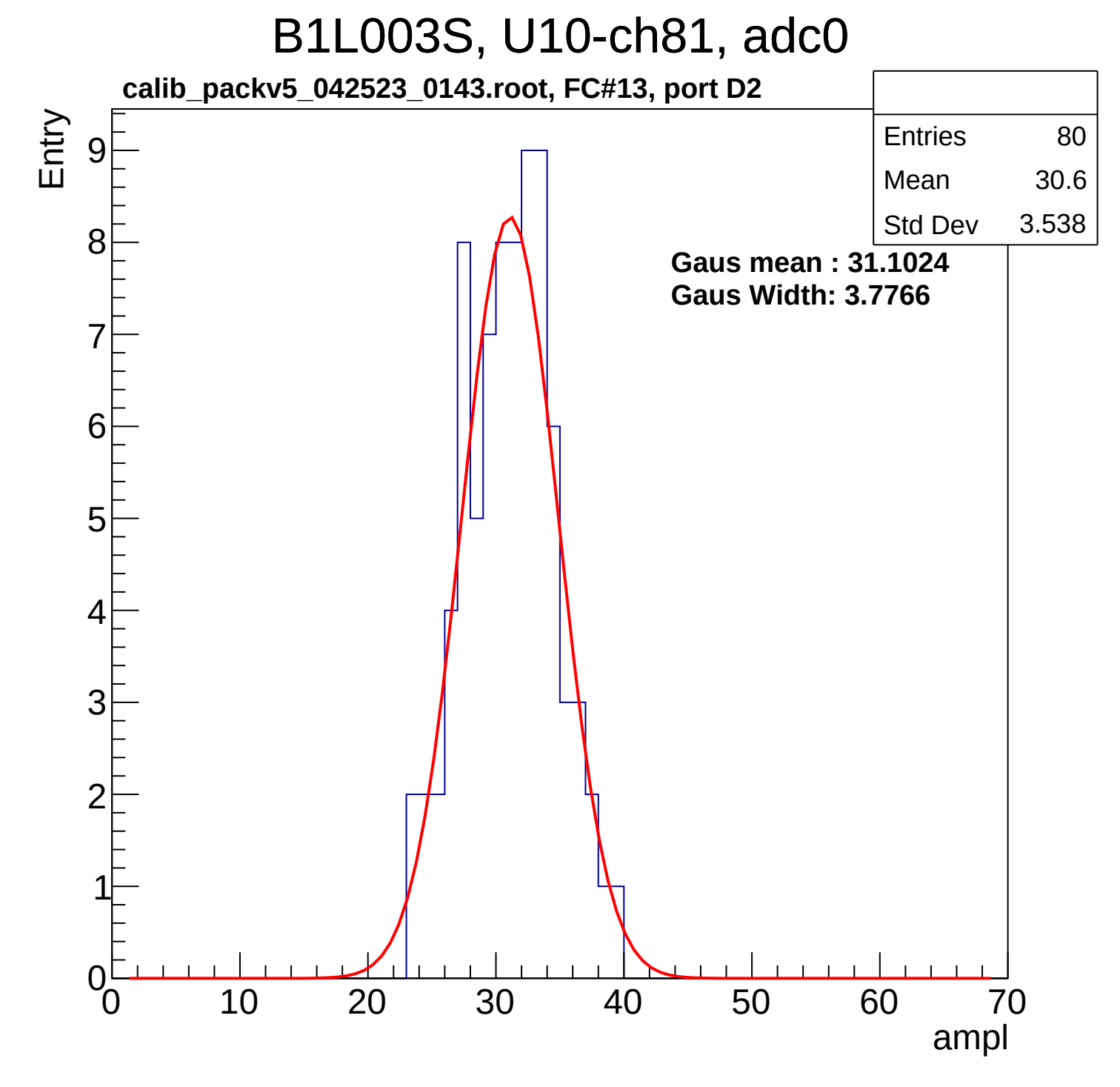
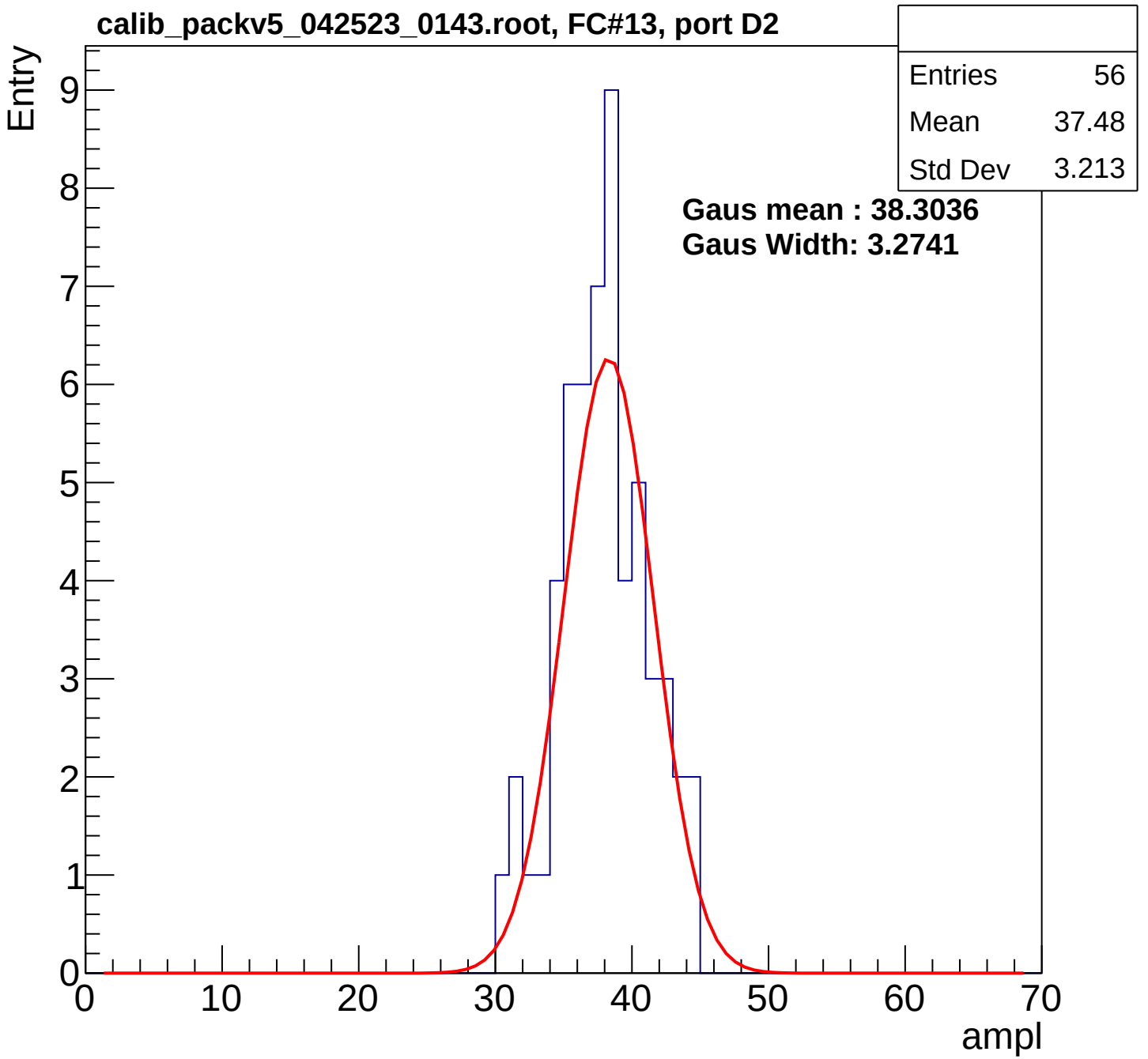
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch81, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 30.6
Std Dev 3.538
Gaus mean : 31.1024
Gaus Width: 3.7766
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch81, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 37.48
Std Dev 3.213
Gaus mean : 38.3036
Gaus Width: 3.2741
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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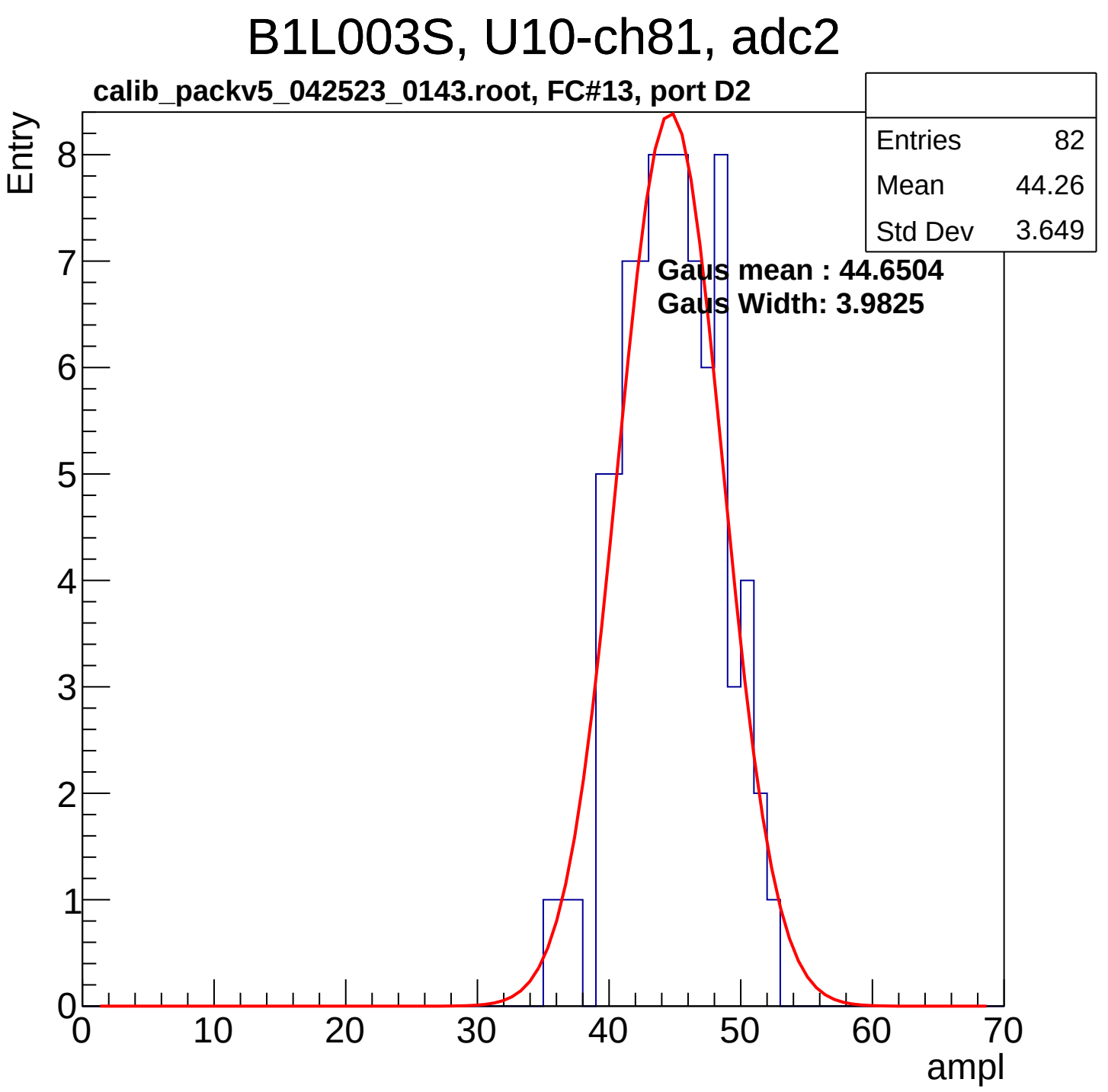
B1L003S, U10-ch81, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 44.26
Std Dev 3.649
Gaus mean : 44.6504
Gaus Width: 3.9825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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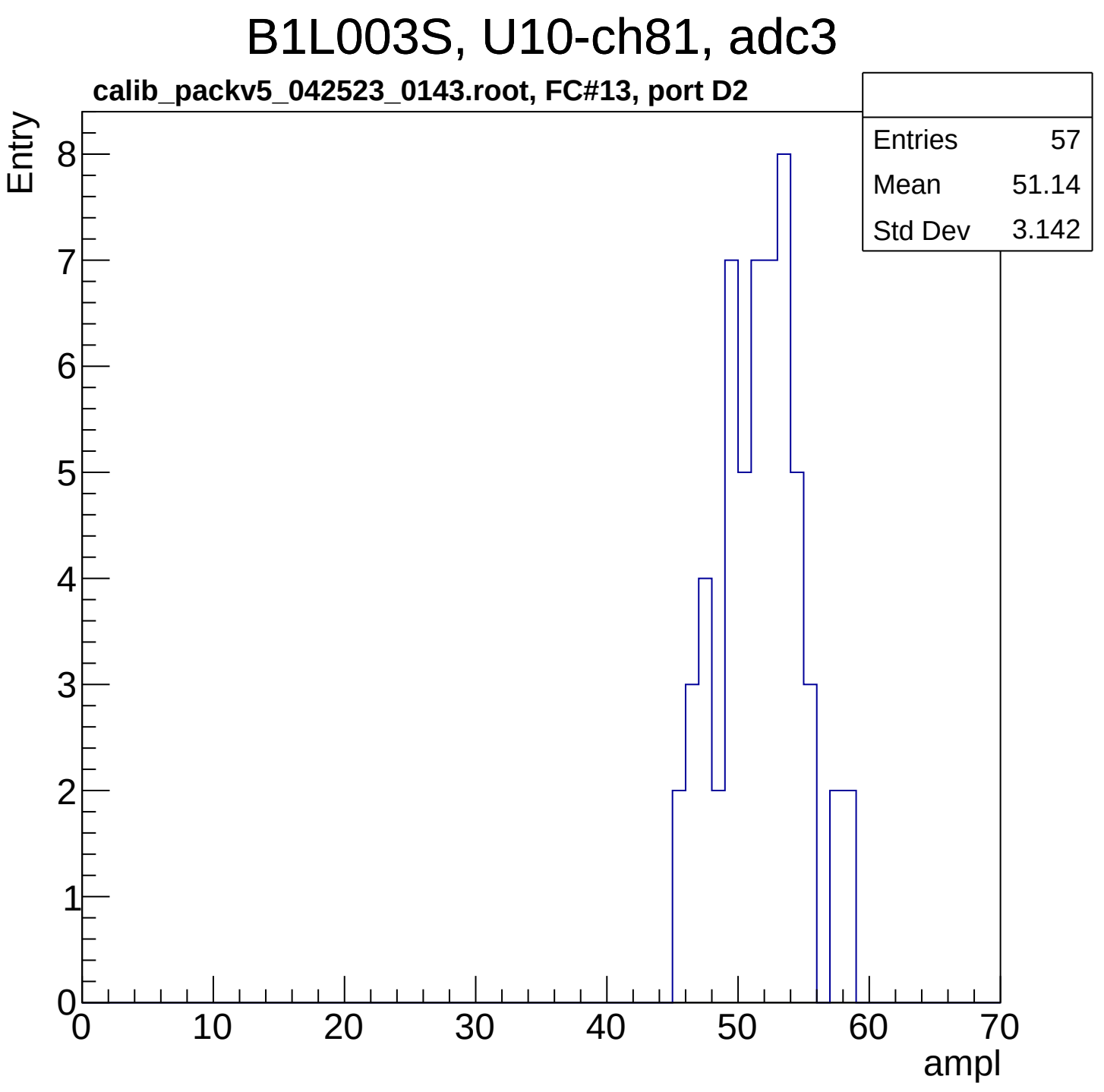
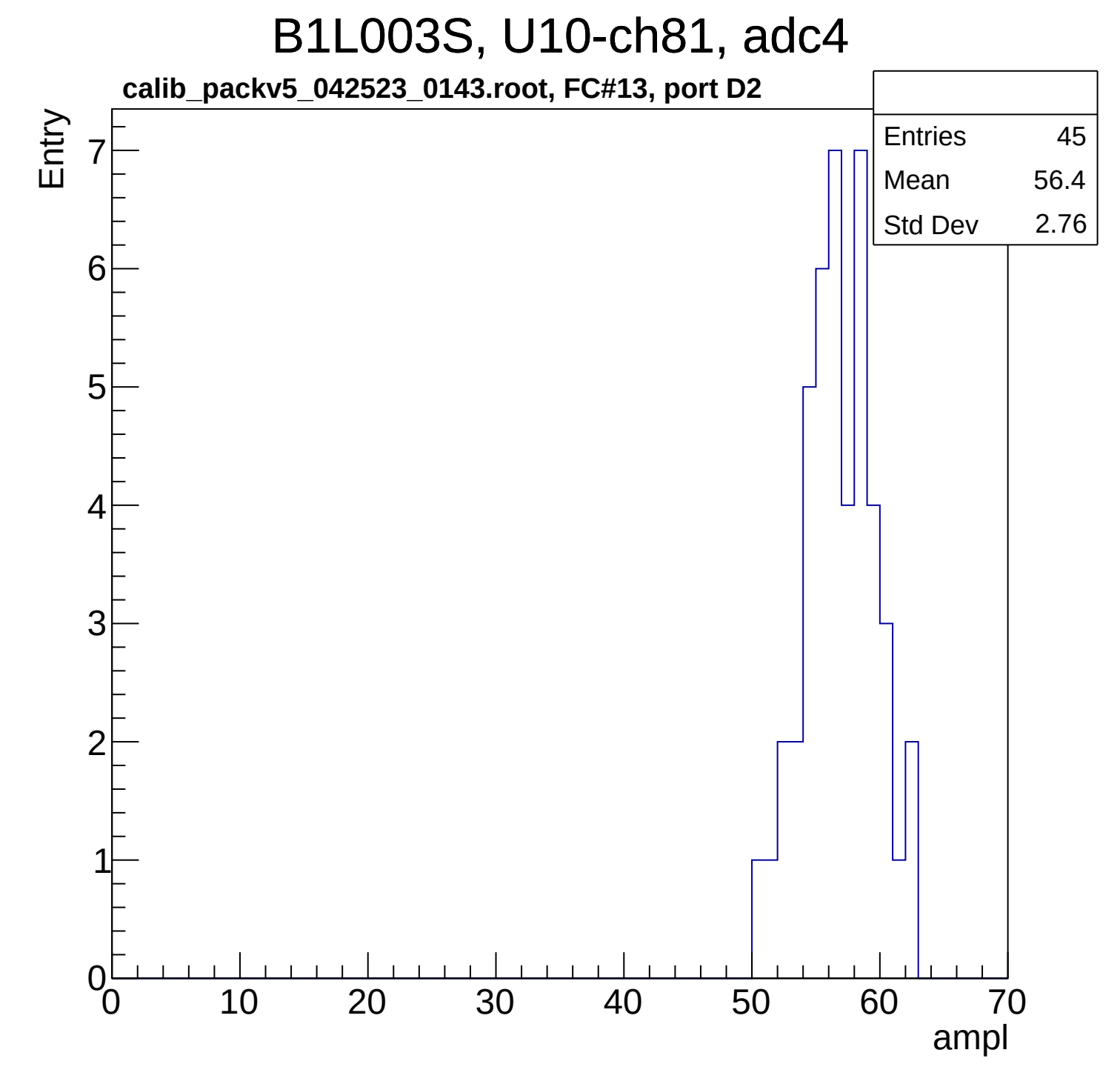

B1L003S, U10-ch81, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 51.14
Std Dev 3.142
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch81, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 56.4
Std Dev 2.76
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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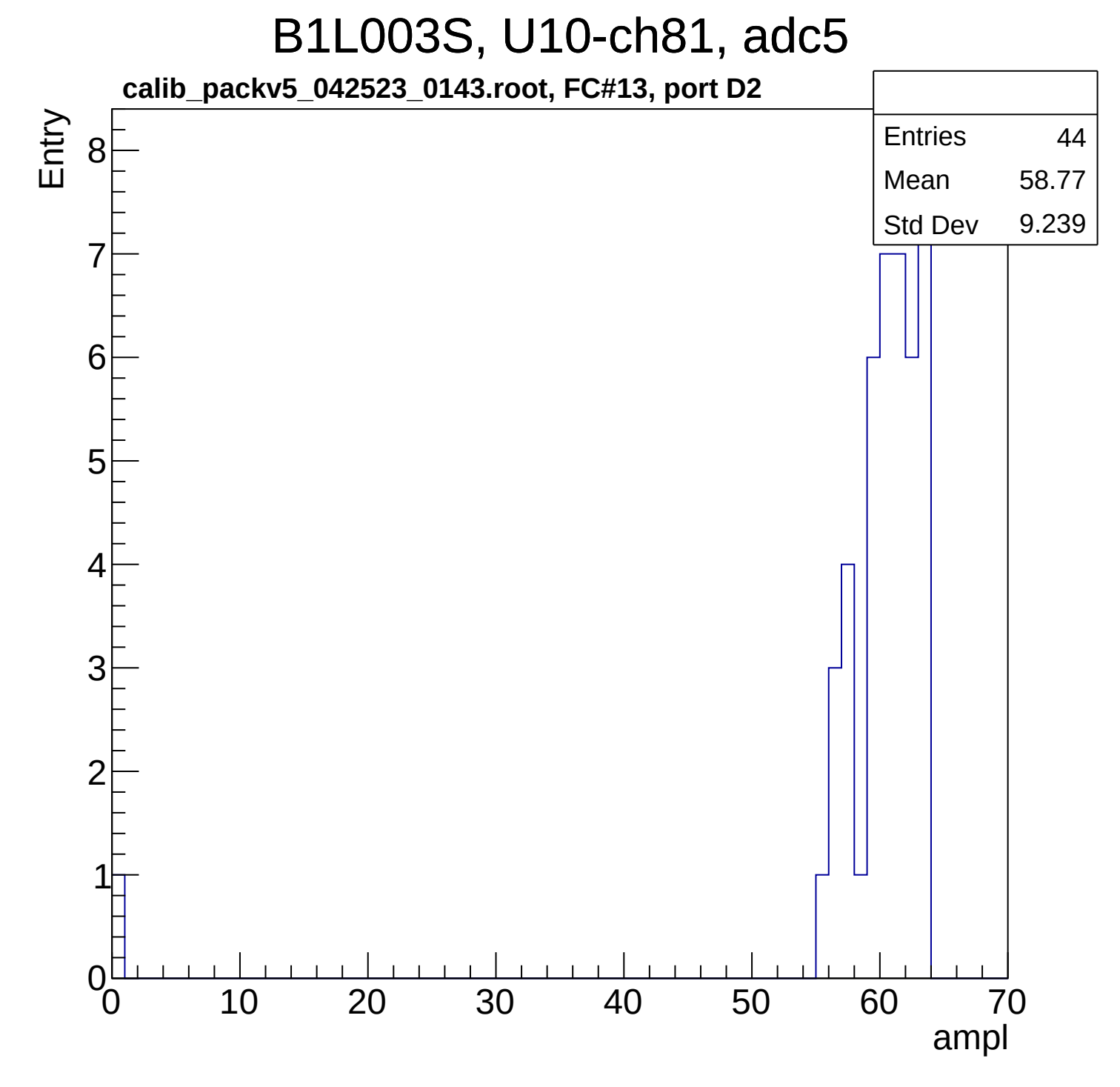
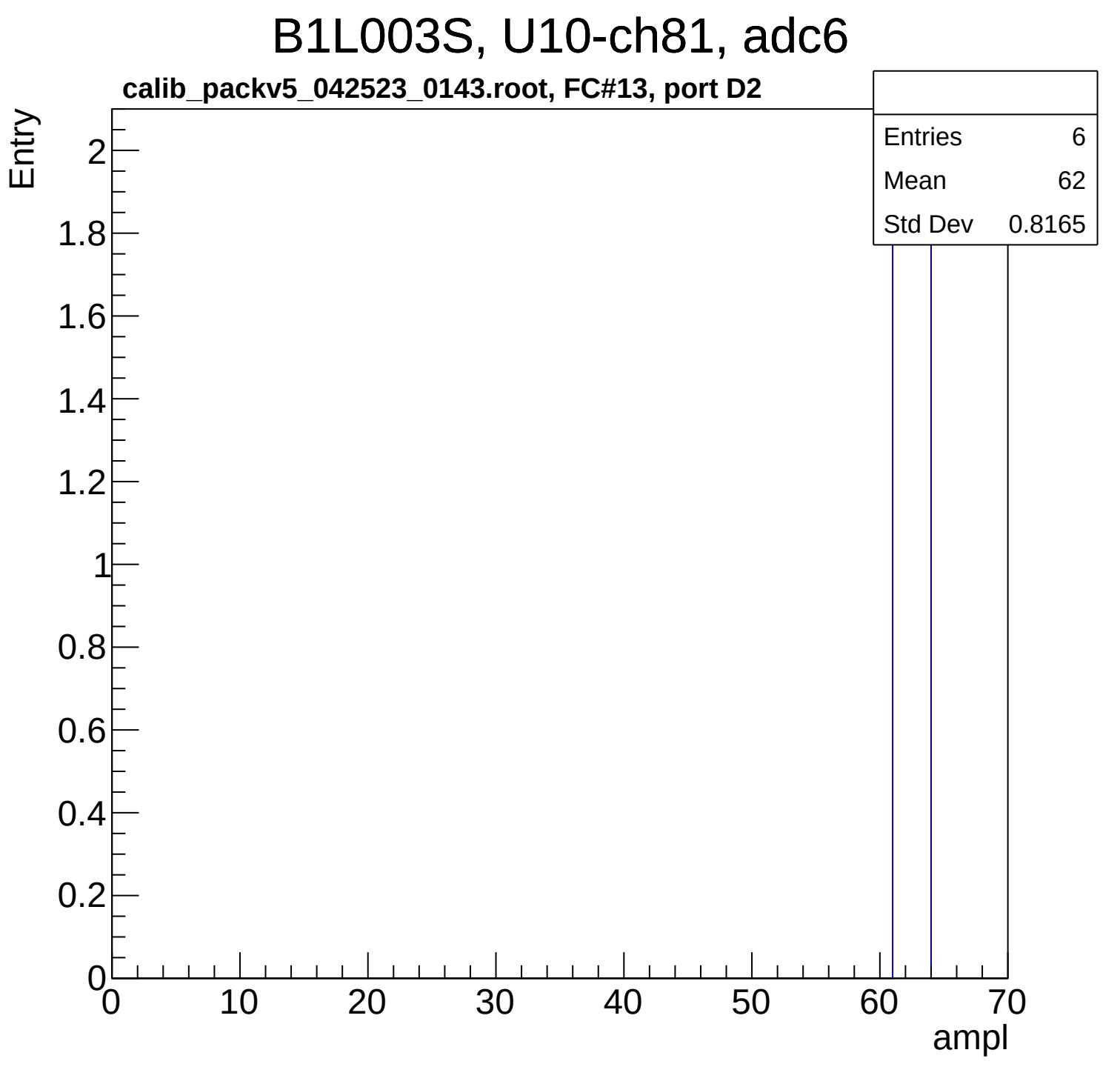
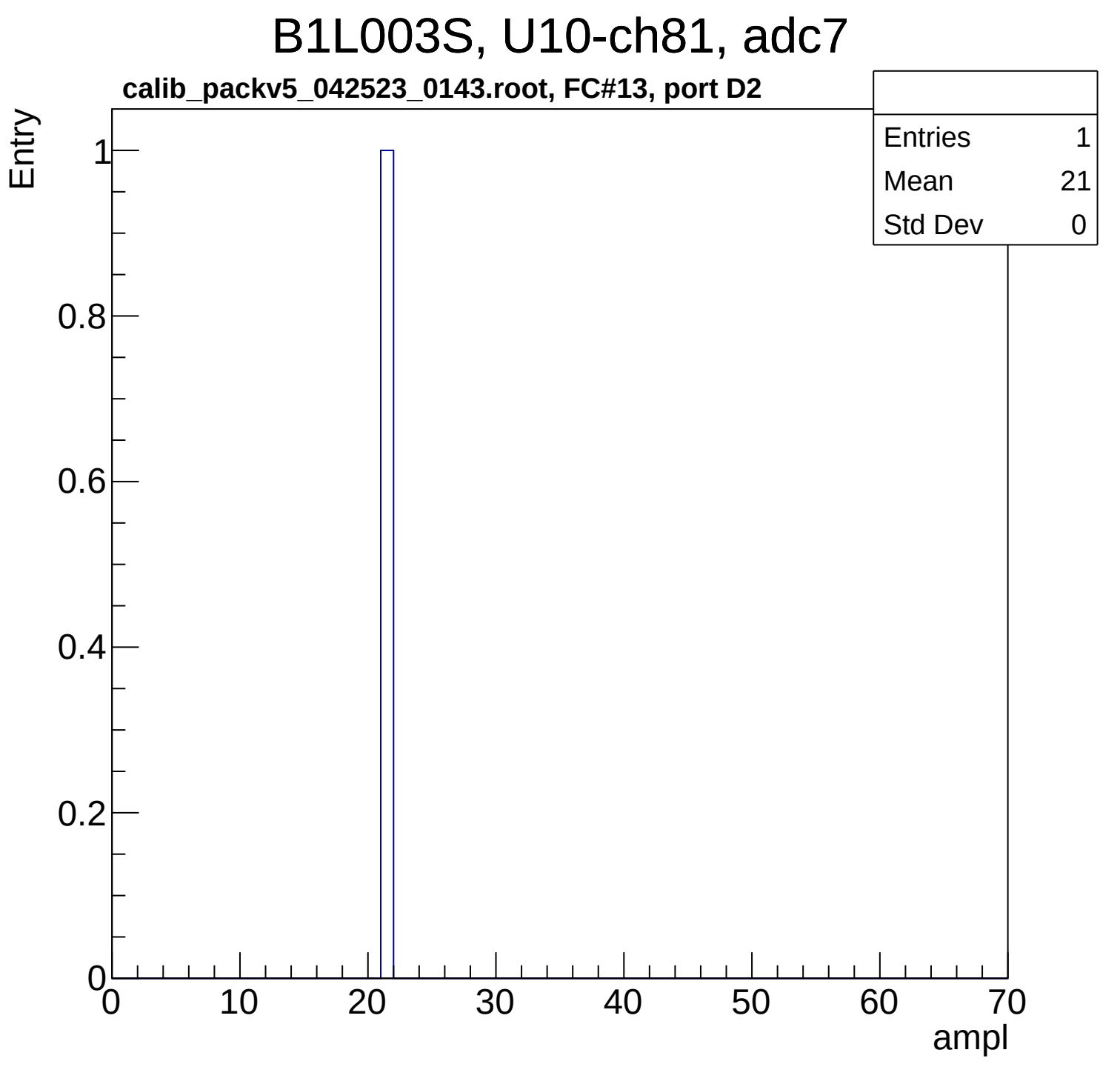
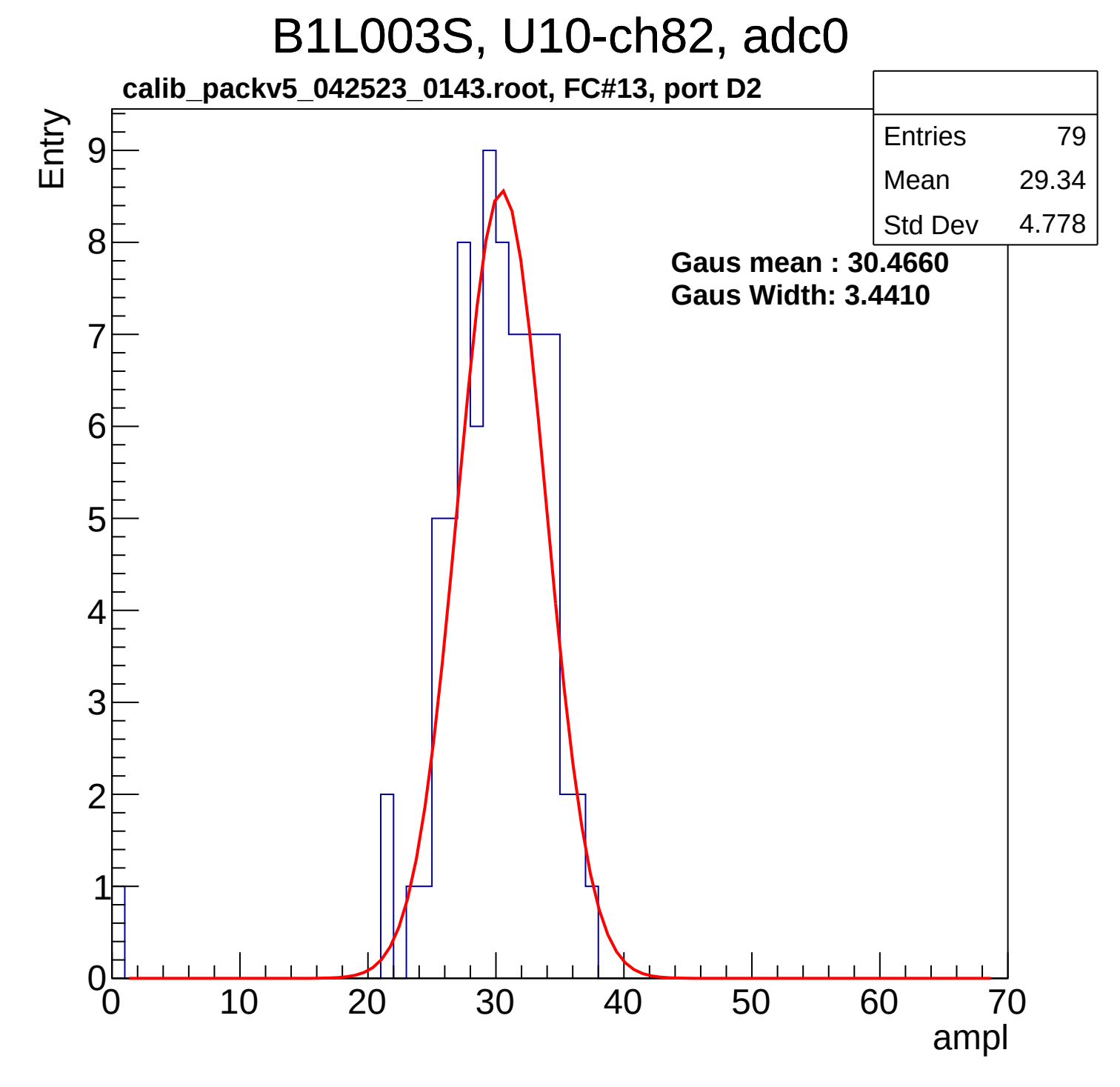
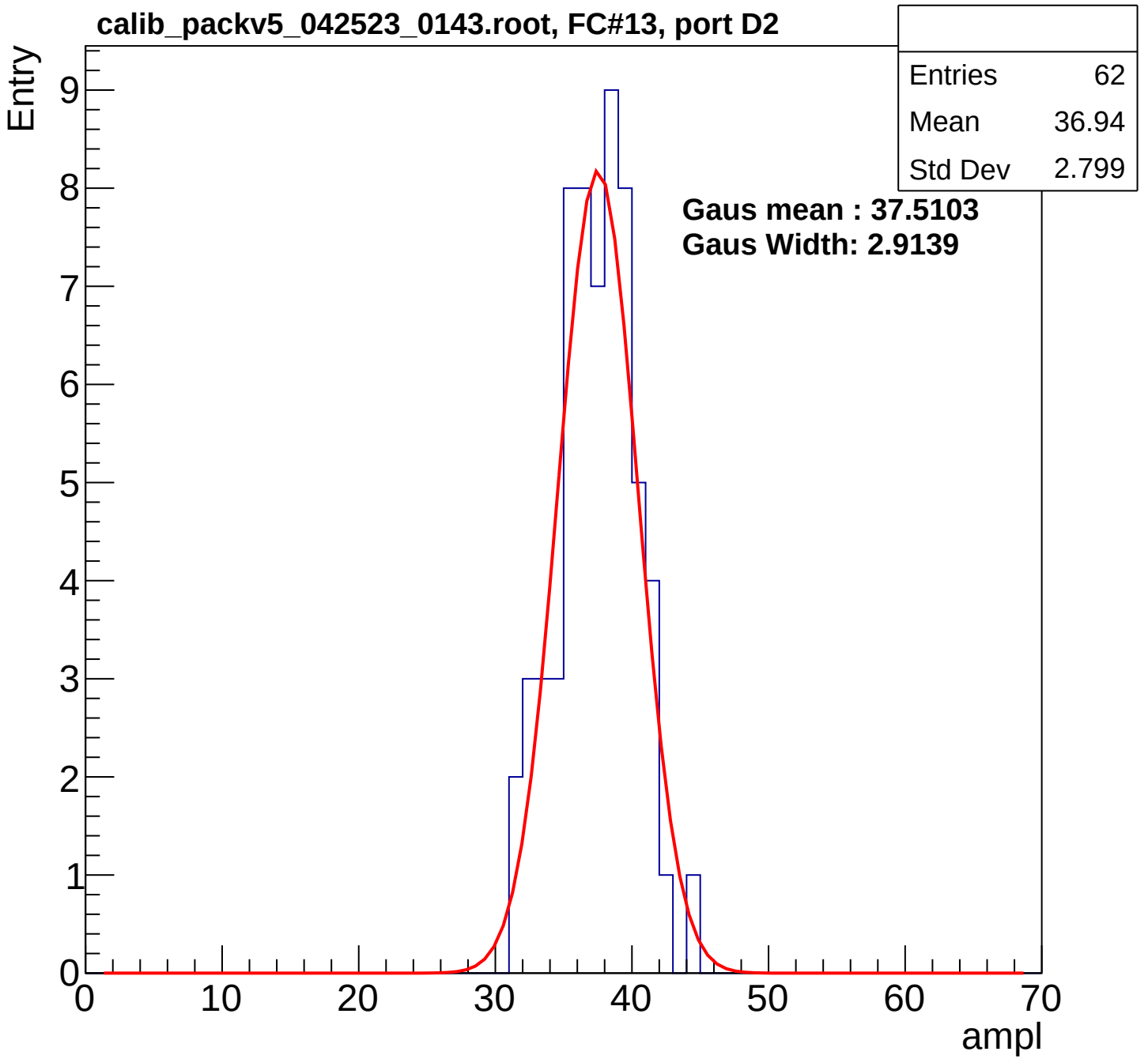
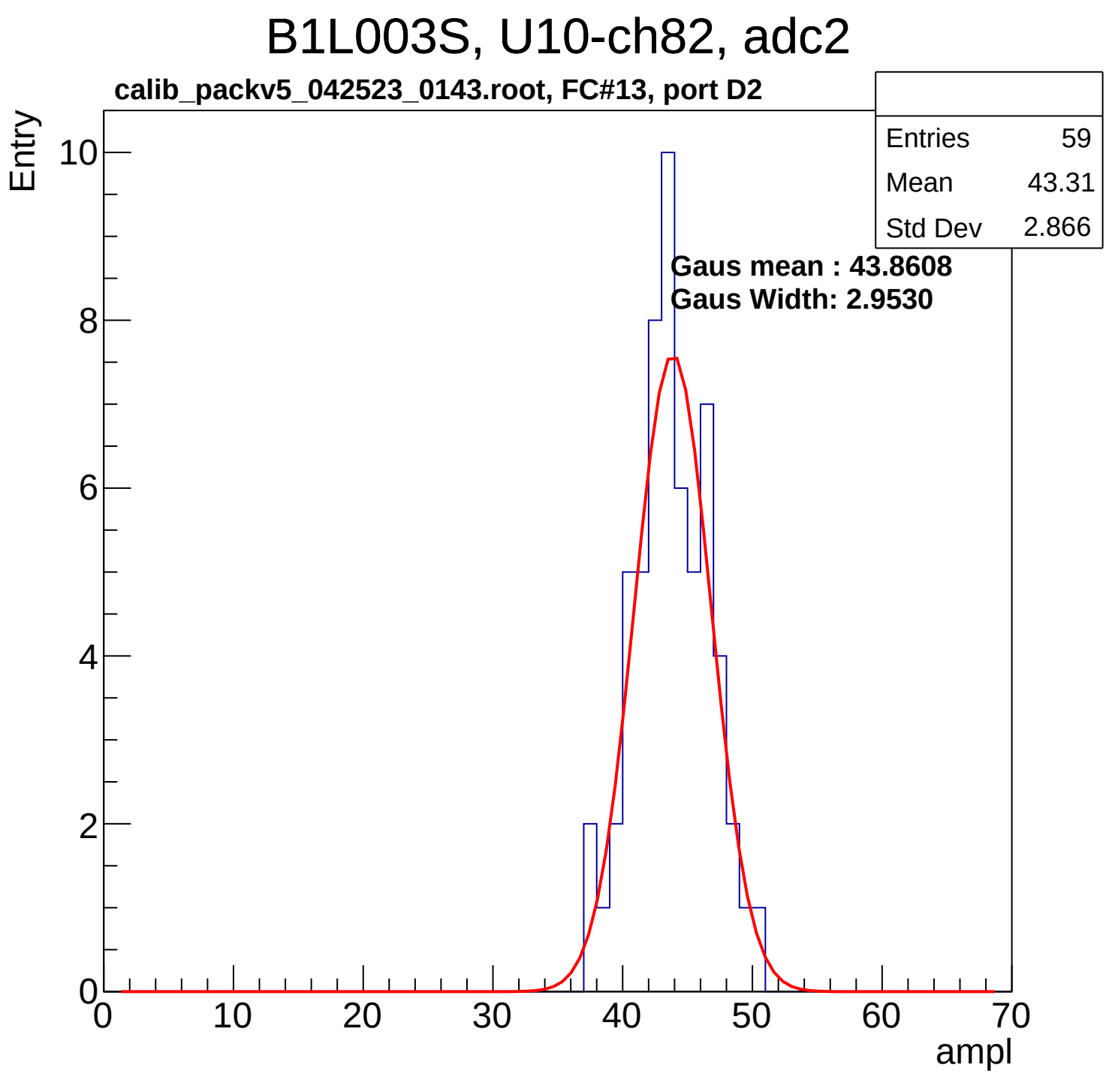

B1L003S, U10-ch81, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.77
Std Dev 9.239
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

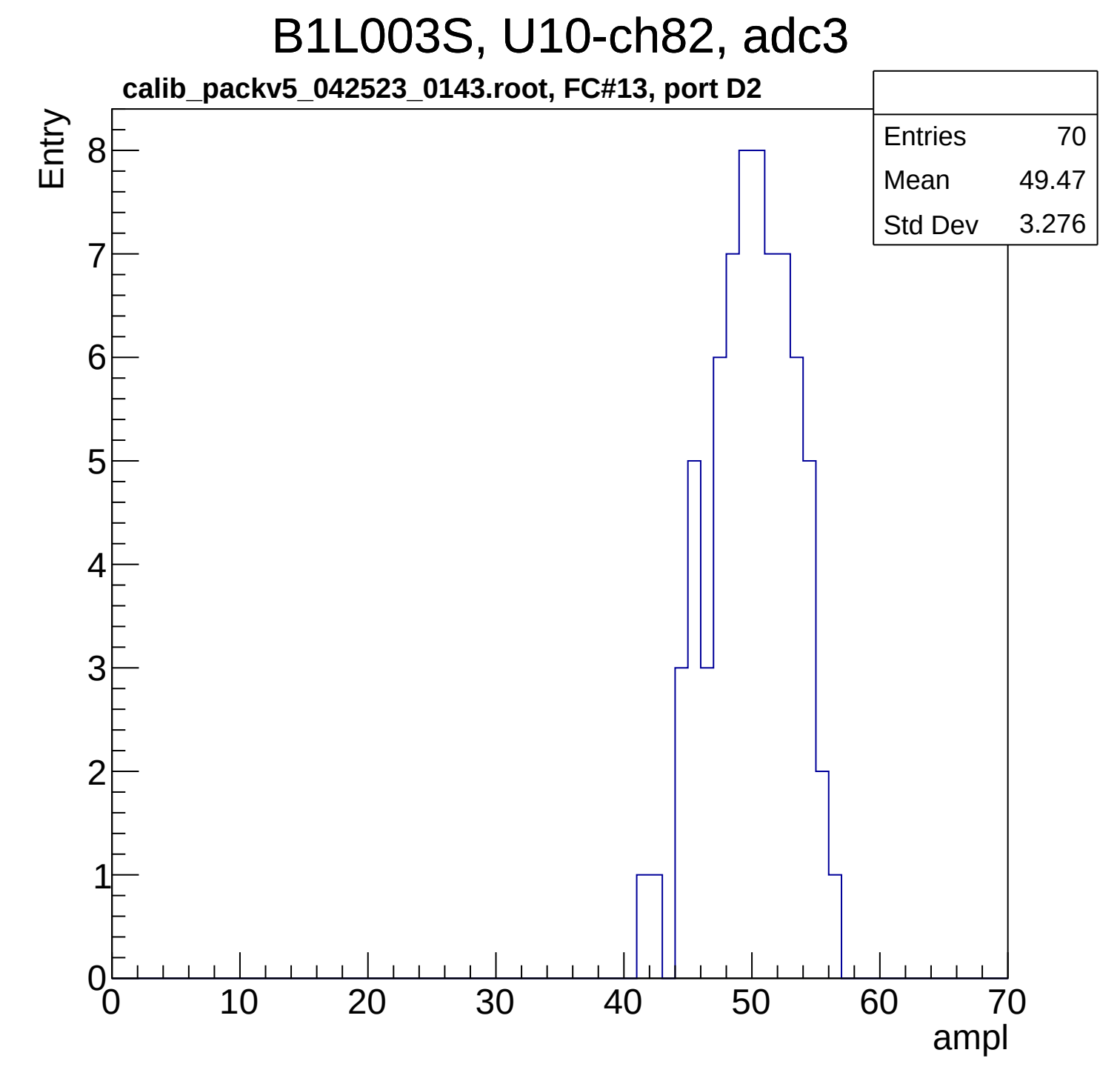

B1L003S, U10-ch81, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

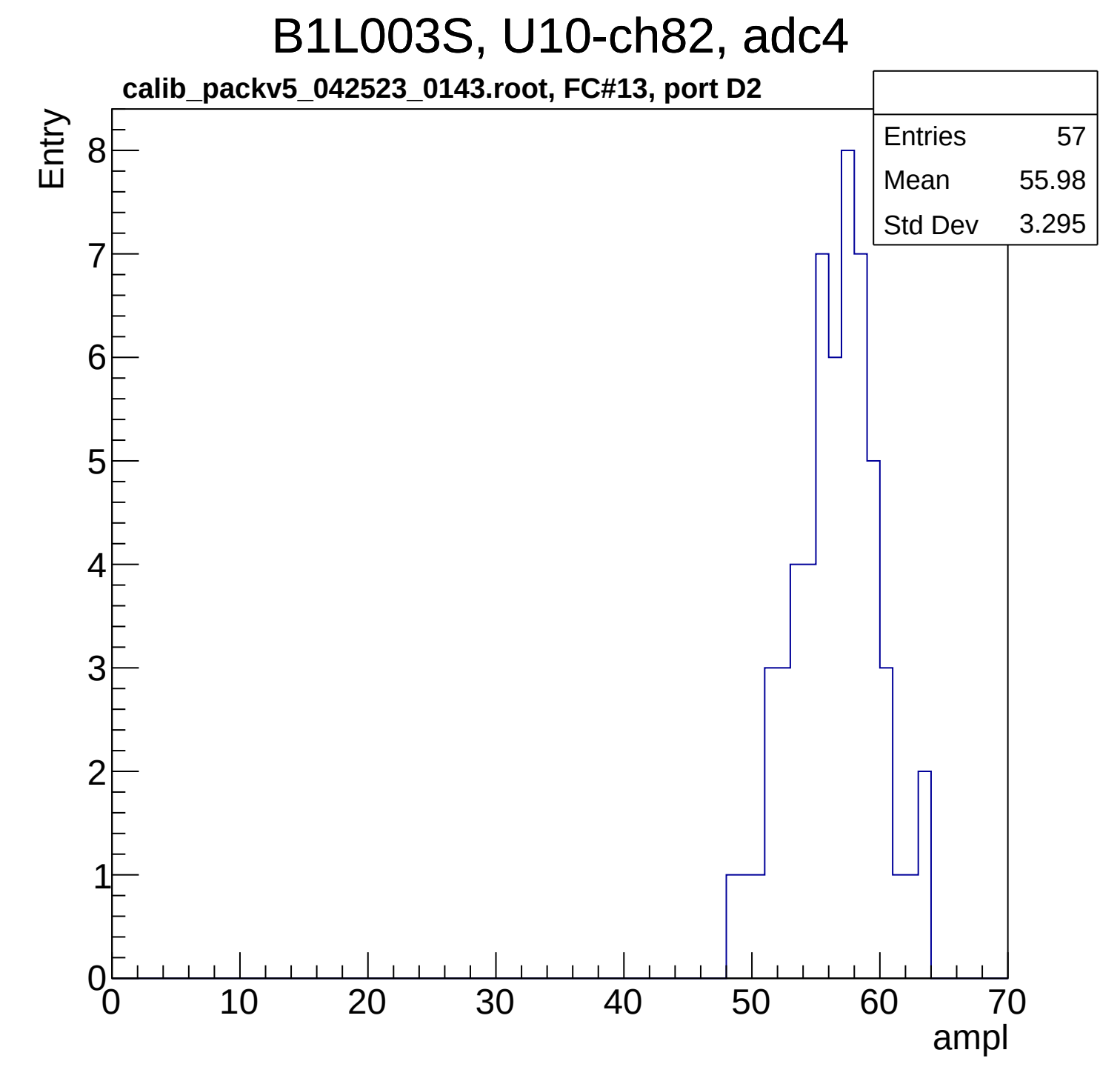

B1L003S, U10-ch81, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

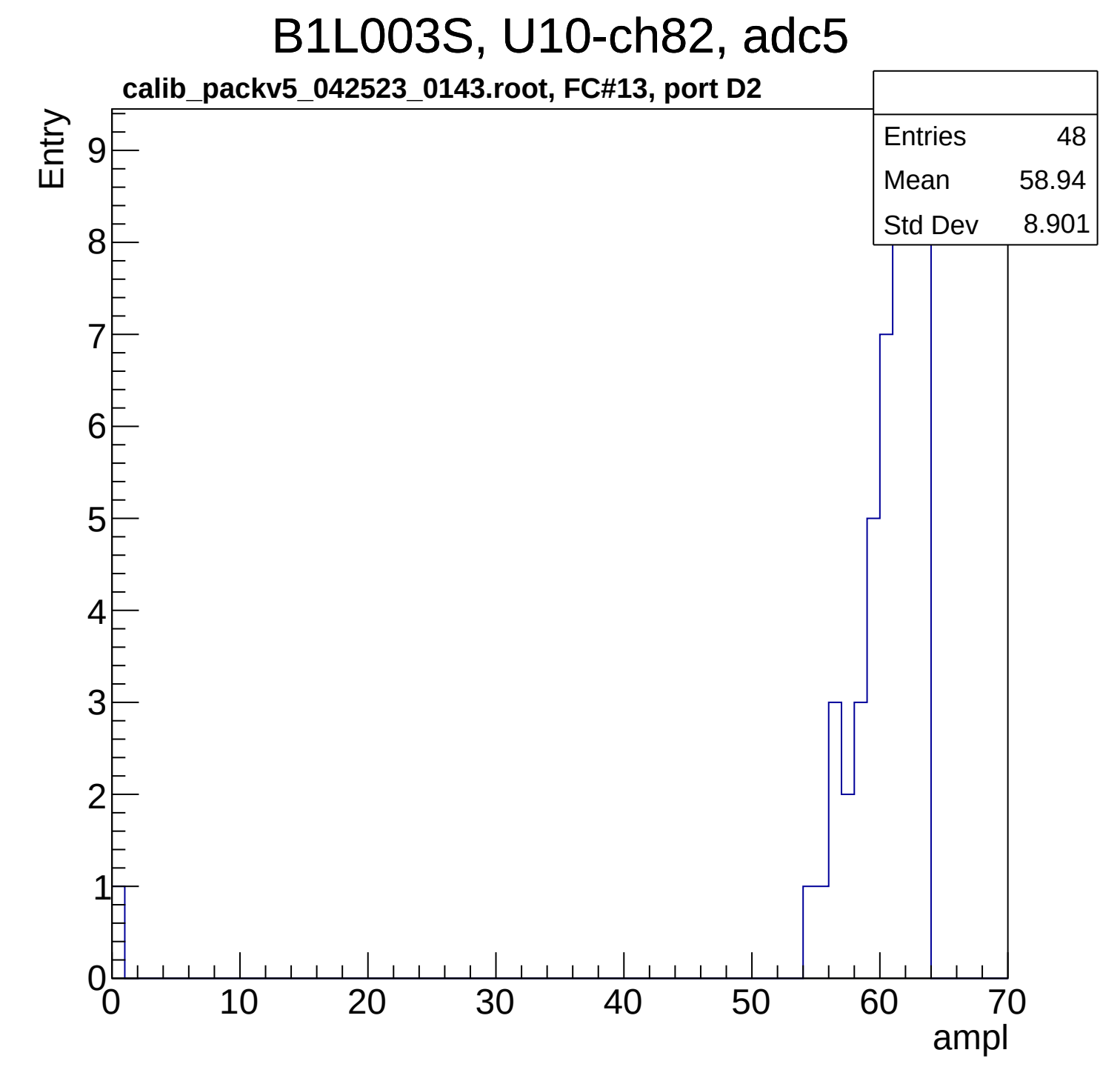

B1L003S, U10-ch82, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 29.34
Std Dev 4.778
Gaus mean : 30.4660
Gaus Width: 3.4410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

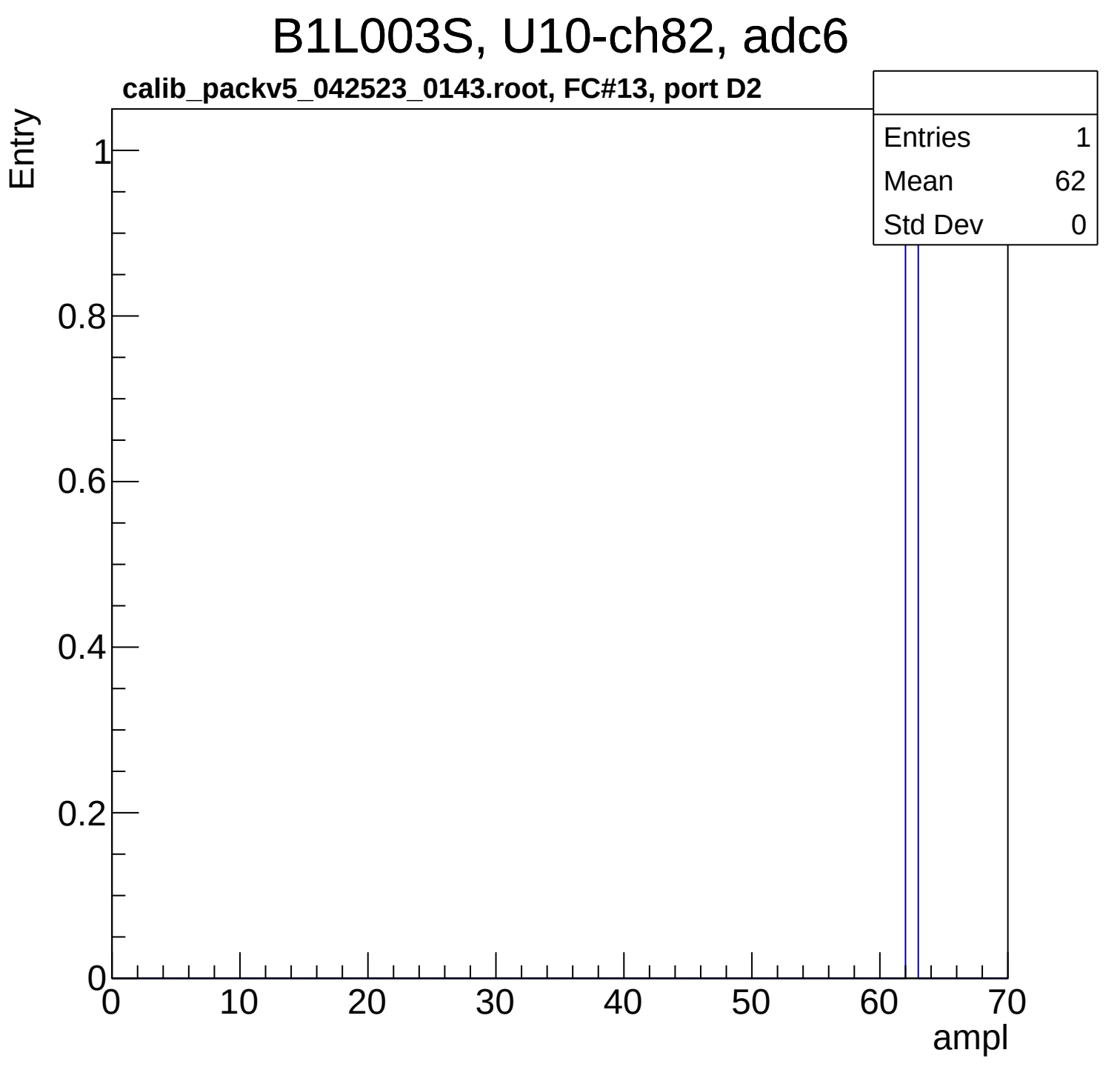

B1L003S, U10-ch82, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.94
Std Dev 2.799
Gaus mean : 37.5103
Gaus Width: 2.9139
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

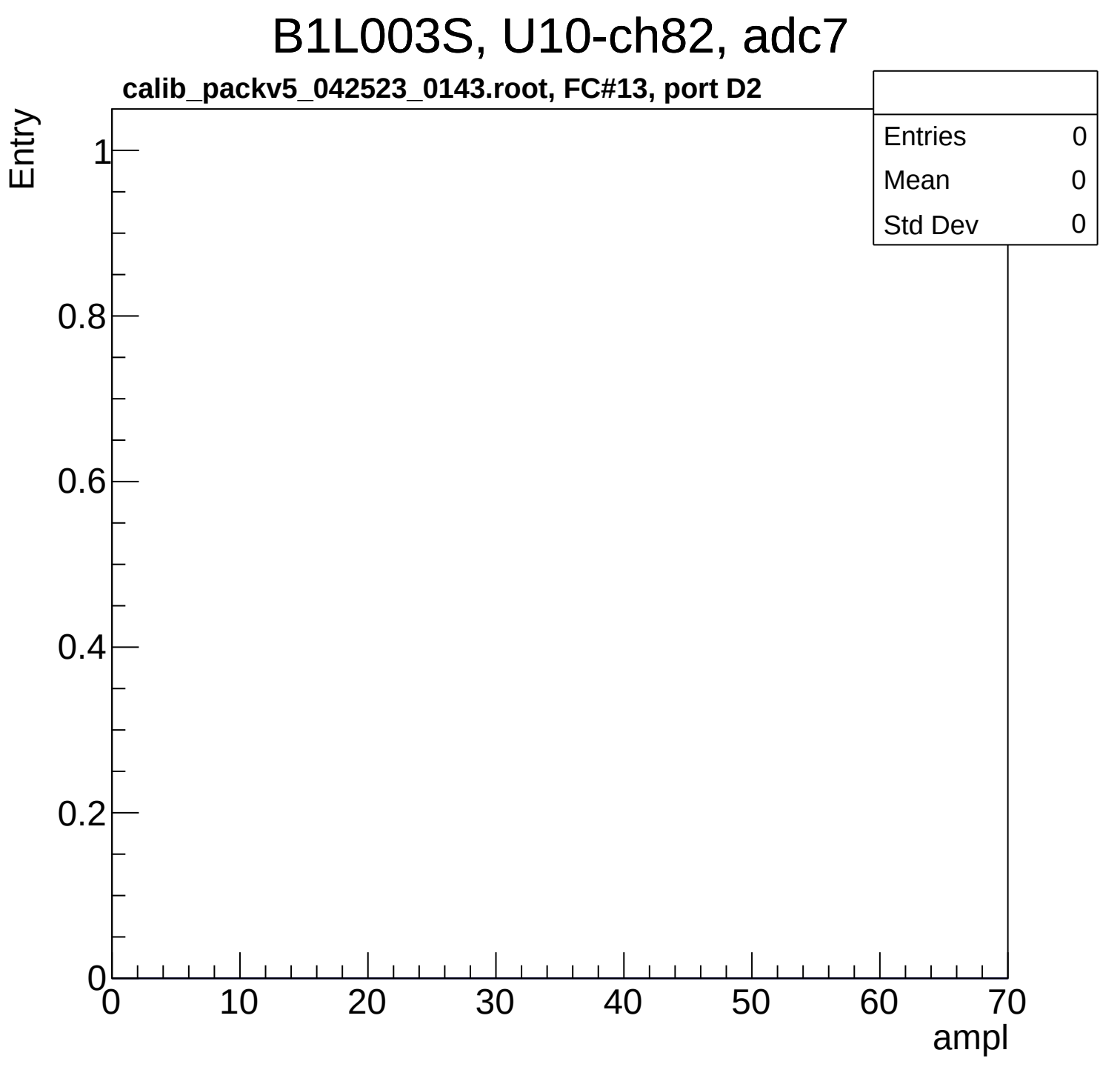

B1L003S, U10-ch82, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.31
Std Dev 2.866
Gaus mean : 43.8608
Gaus Width: 2.9530
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

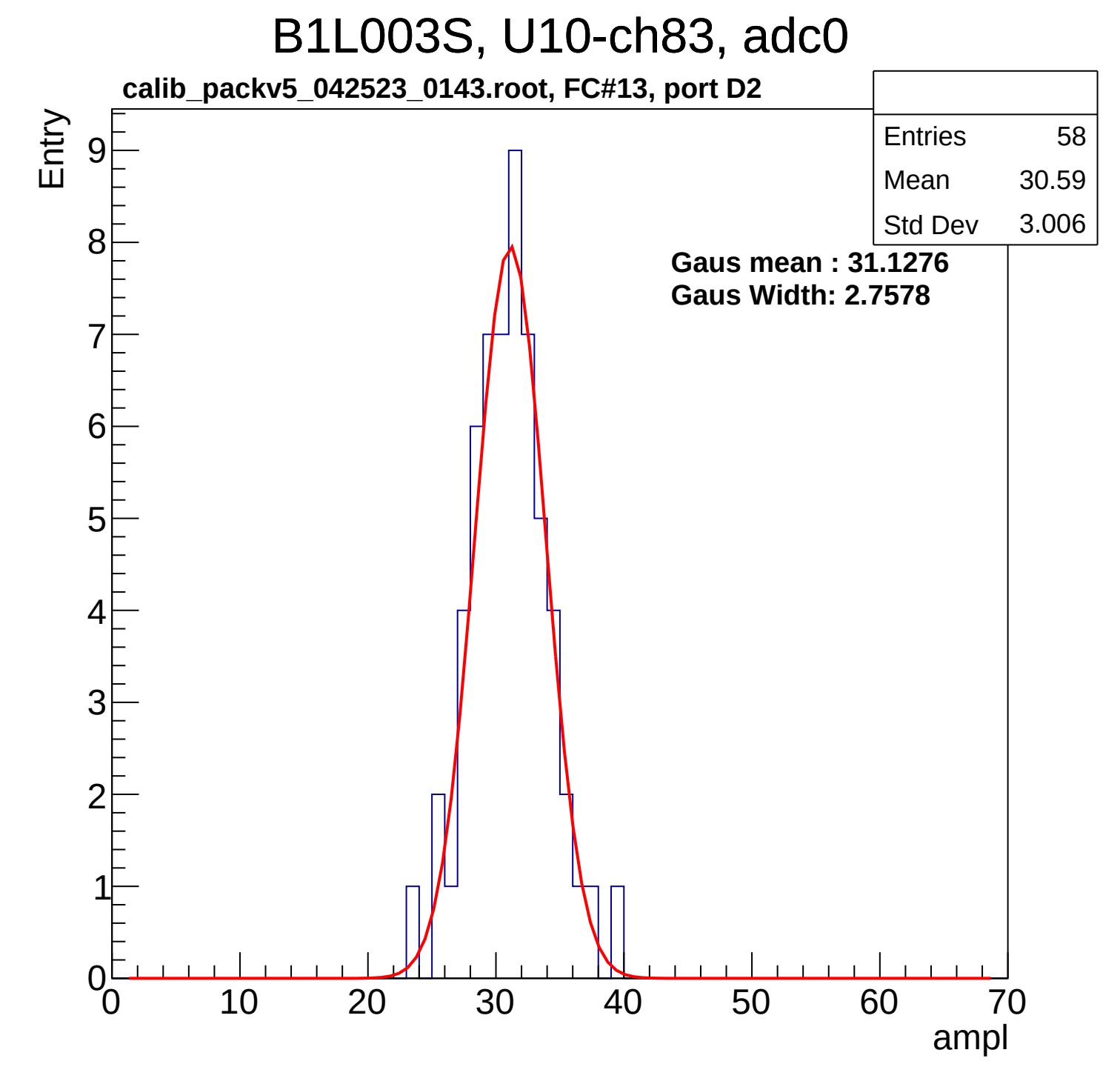

B1L003S, U10-ch82, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 49.47
Std Dev 3.276
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

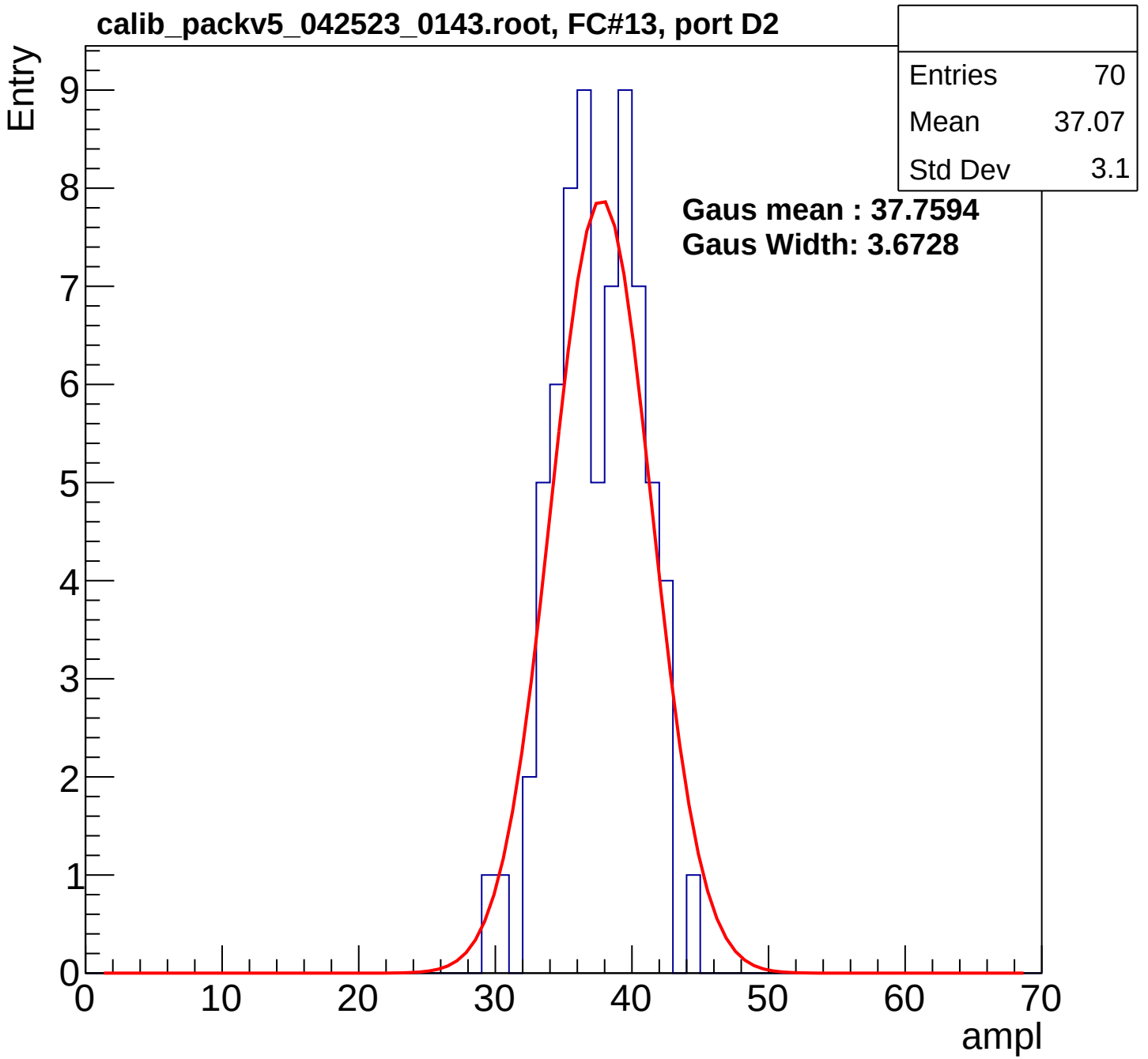

B1L003S, U10-ch82, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.98
Std Dev 3.295
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

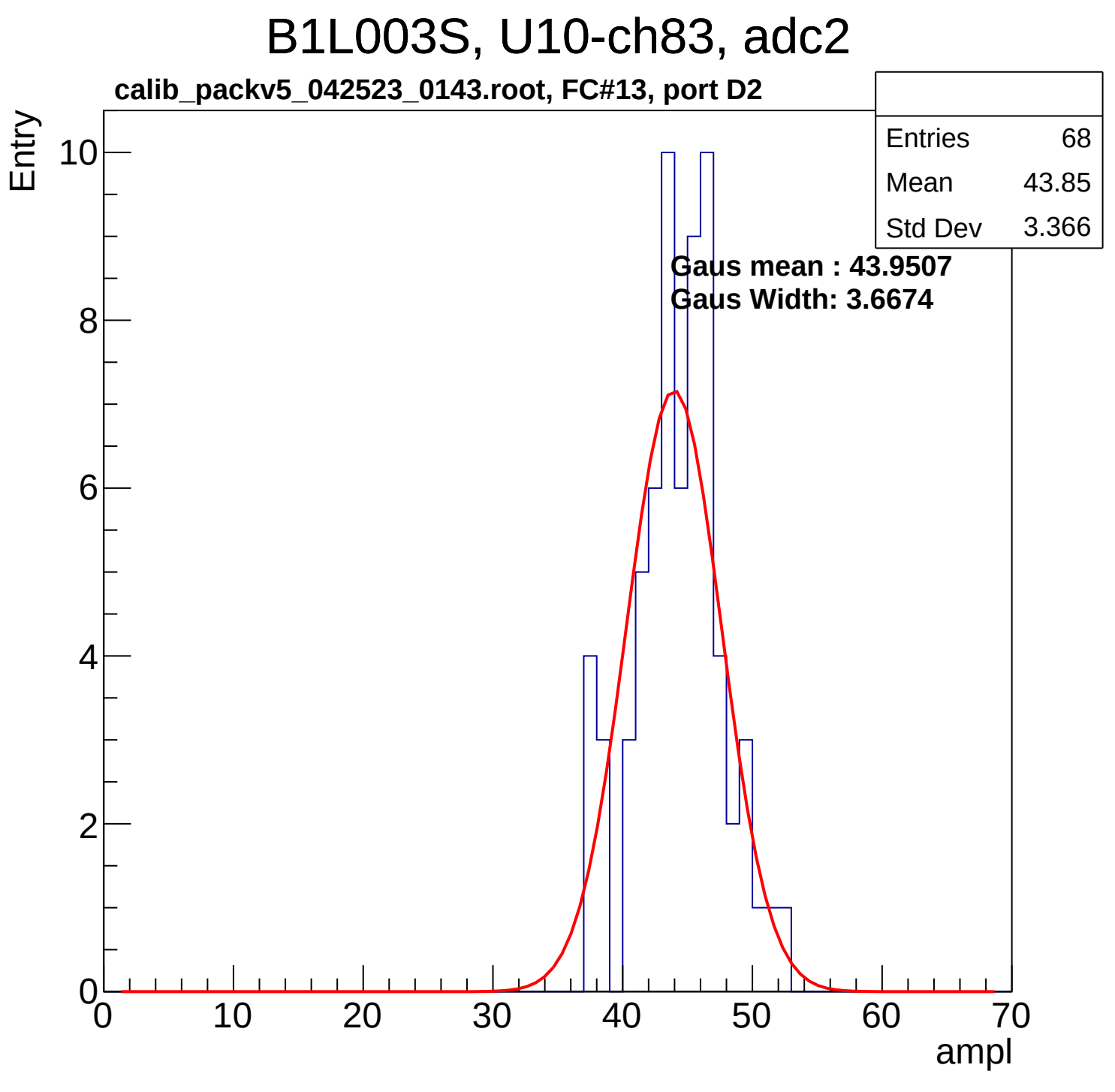

B1L003S, U10-ch82, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.94
Std Dev 8.901
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

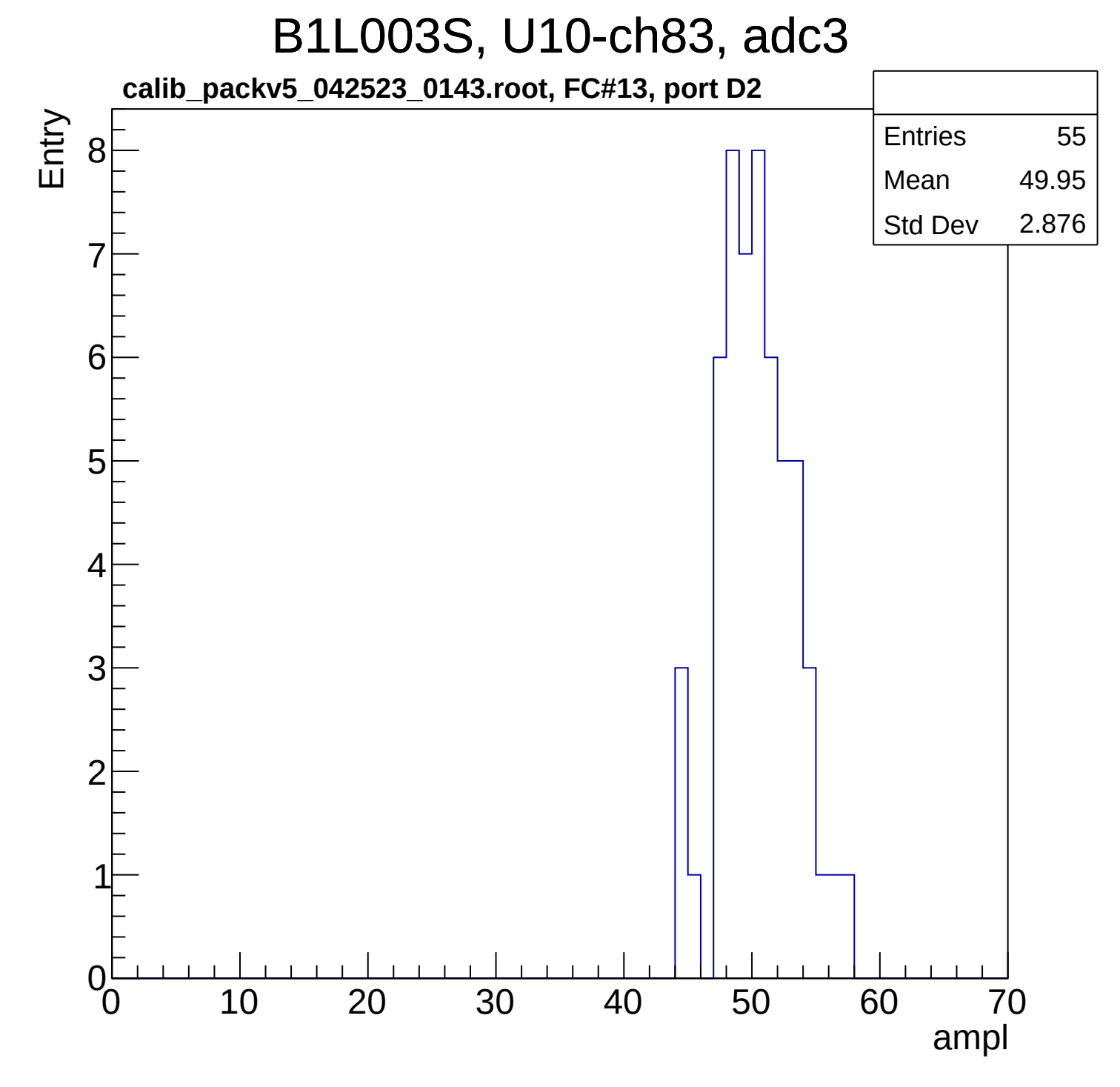

B1L003S, U10-ch82, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch82, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch83, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 30.59
Std Dev 3.006
Gaus mean : 31.1276
Gaus Width: 2.7578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch83, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 37.07
Std Dev 3.1
Gaus mean : 37.7594
Gaus Width: 3.6728
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch83, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 43.85
Std Dev 3.366
Gaus mean : 43.9507
Gaus Width: 3.6674
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch83, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 49.95
Std Dev 2.876
Entry
ampl
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1
2
3
4
5
6
7
8
0
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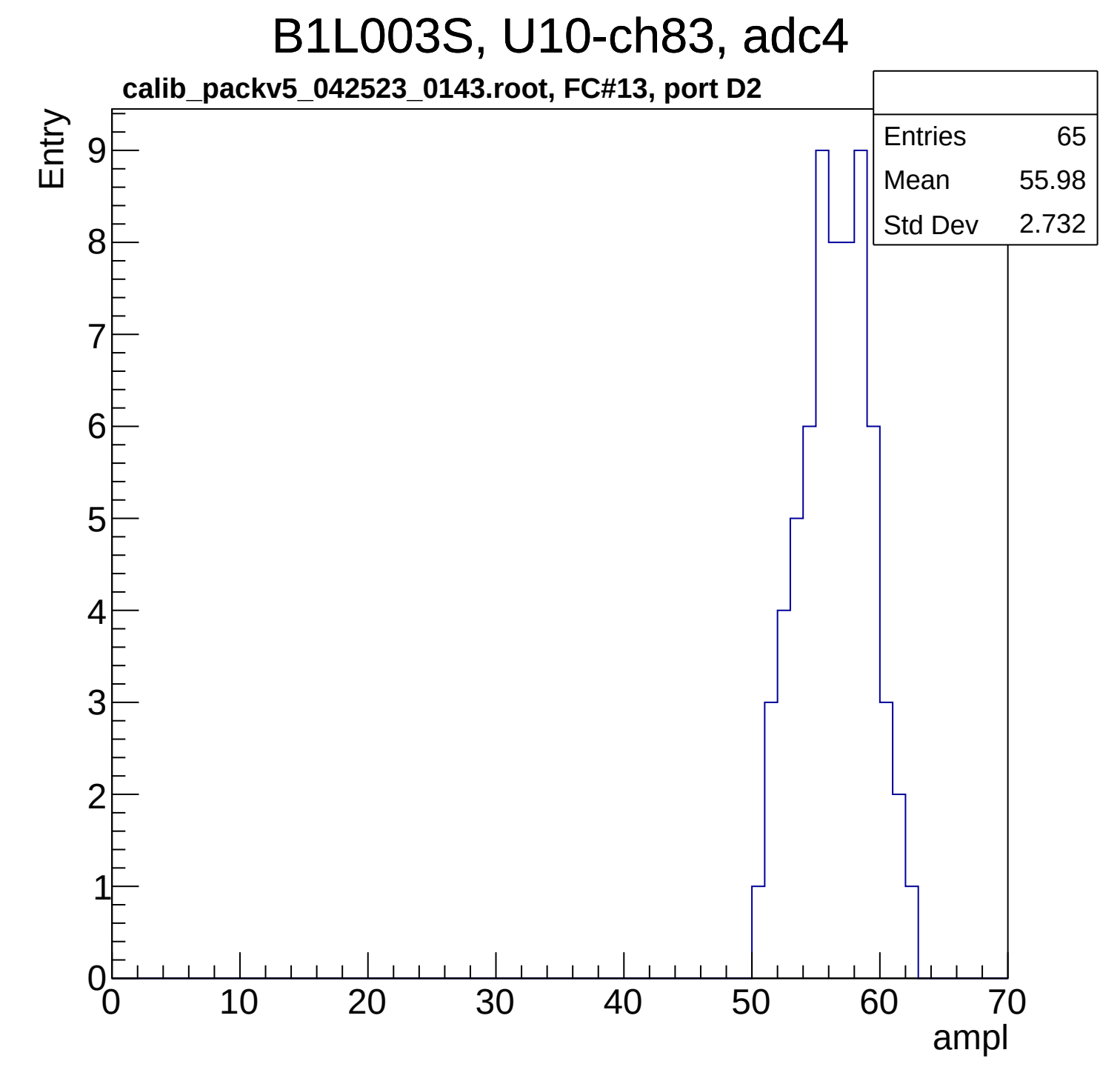
40
50
60
70


B1L003S, U10-ch83, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 55.98
Std Dev 2.732
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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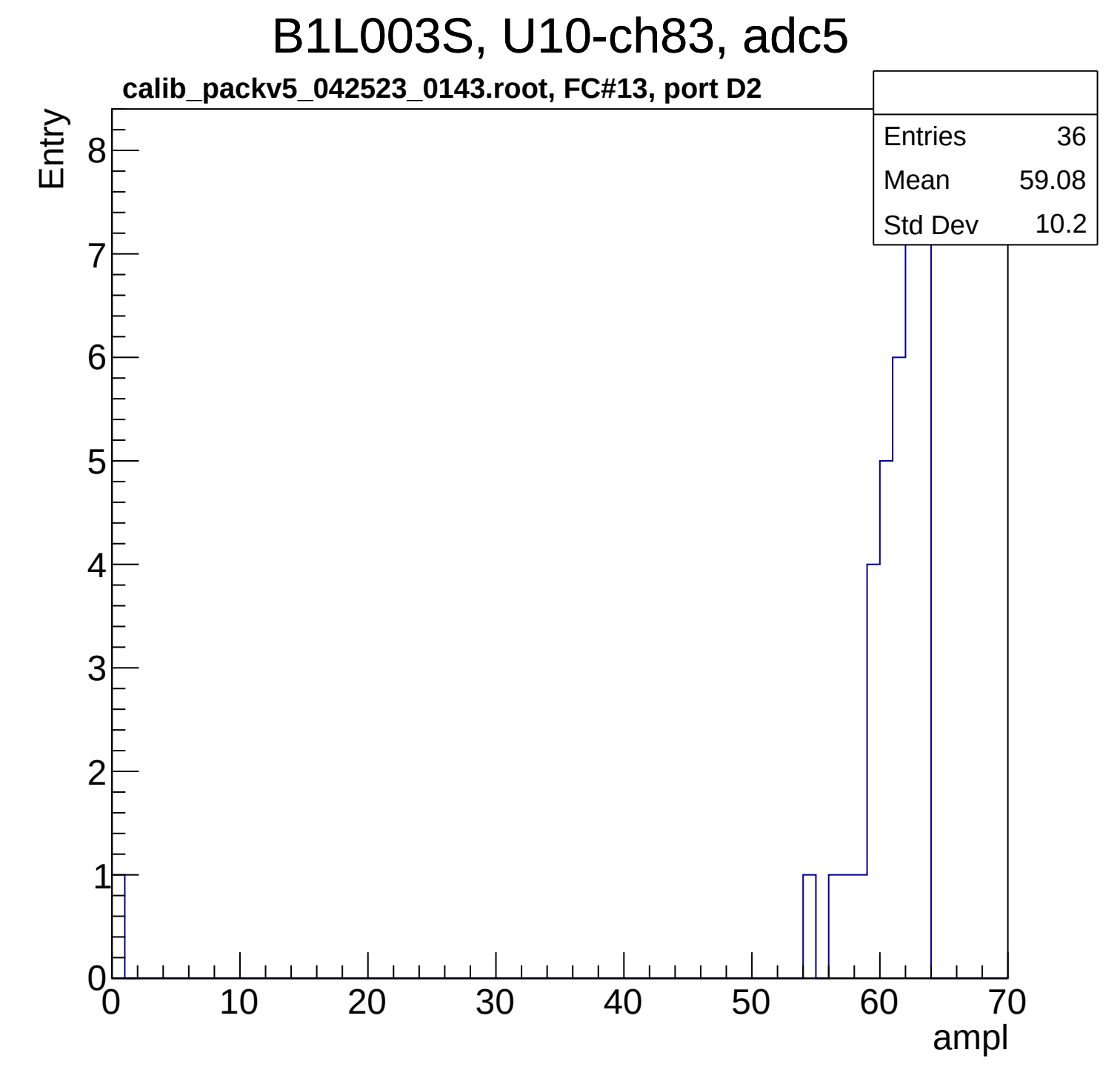
B1L003S, U10-ch83, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 59.08
Std Dev 10.2
Entry
0
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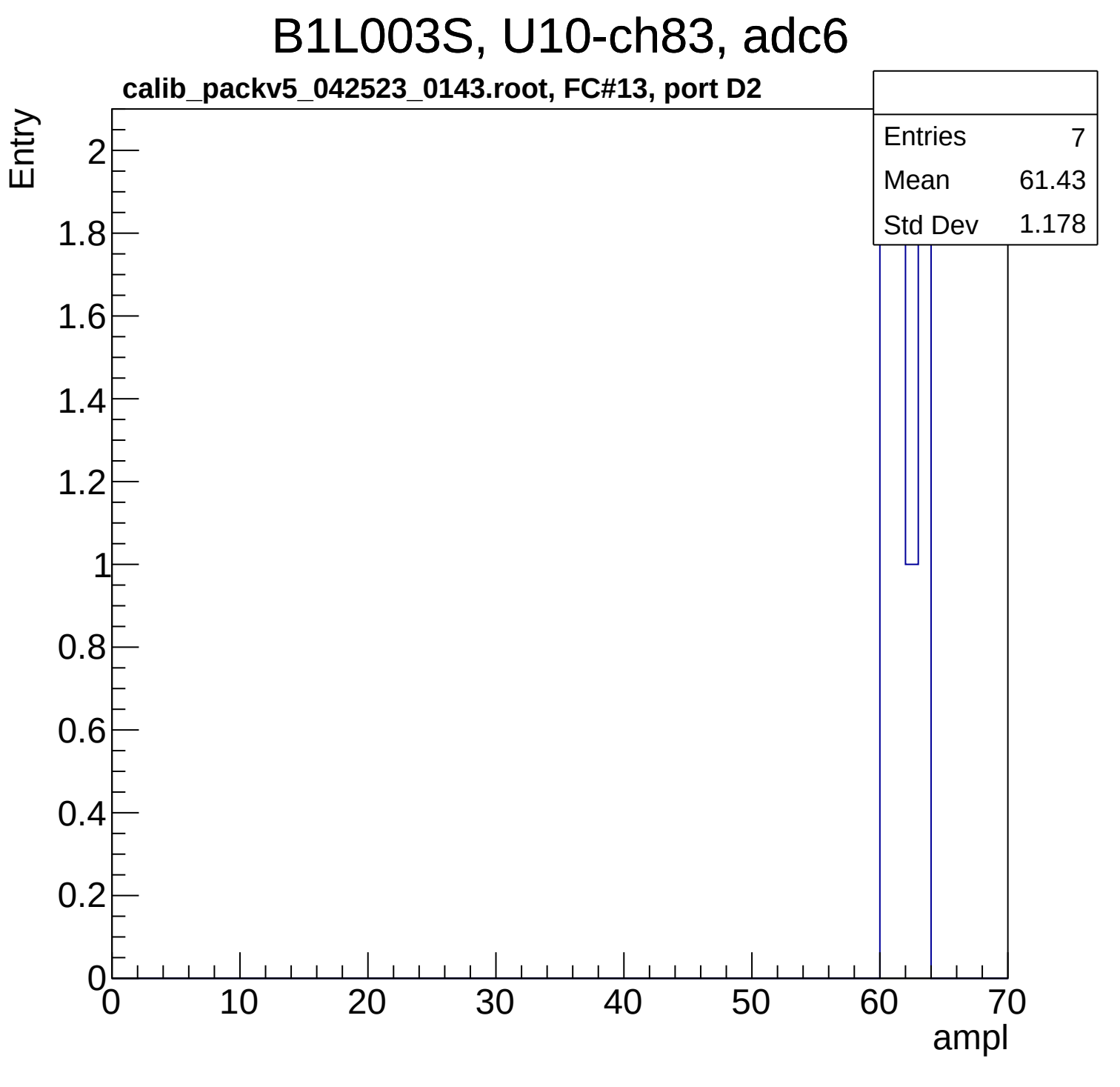
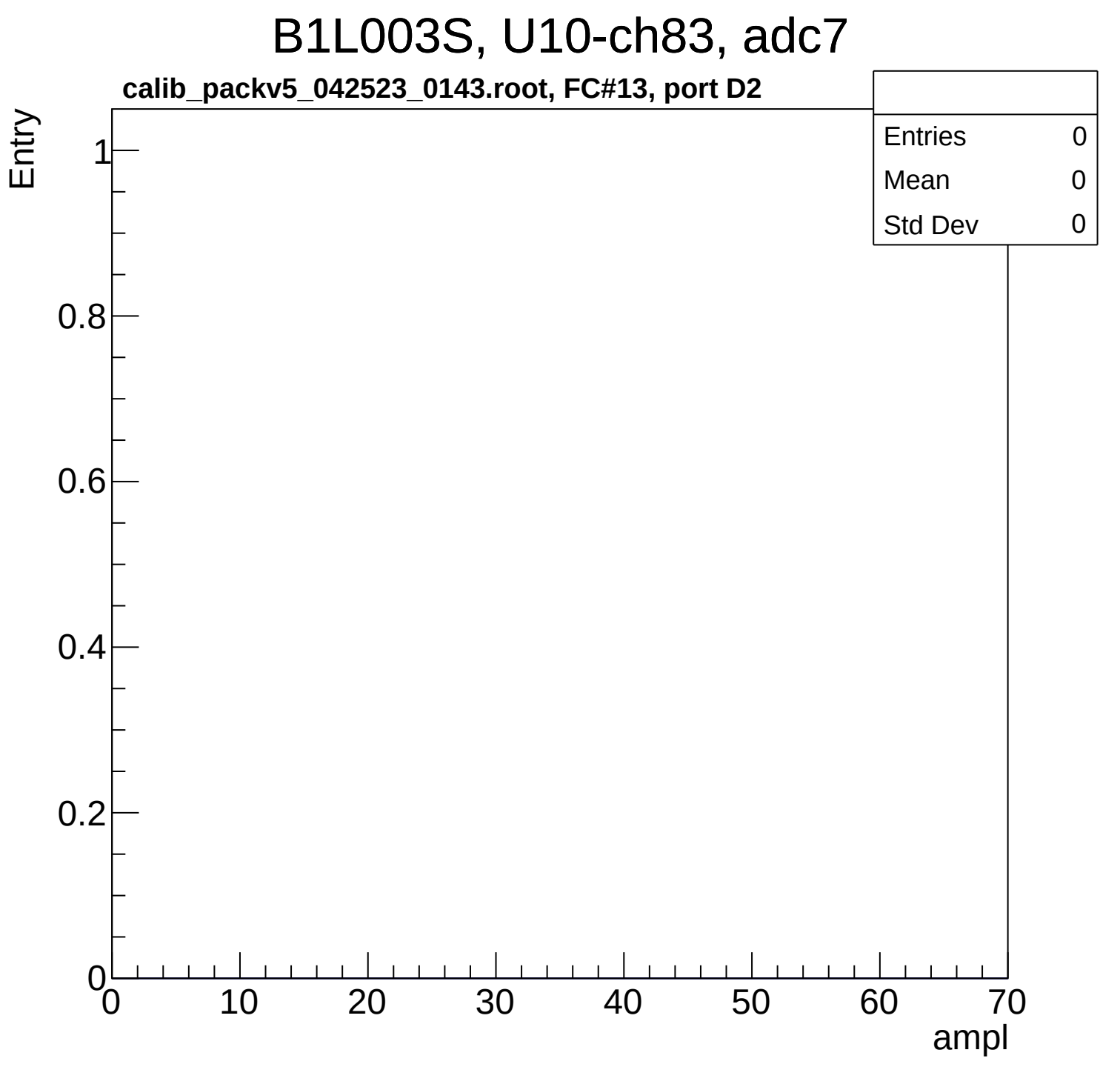
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch83, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.43
Std Dev 1.178
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch83, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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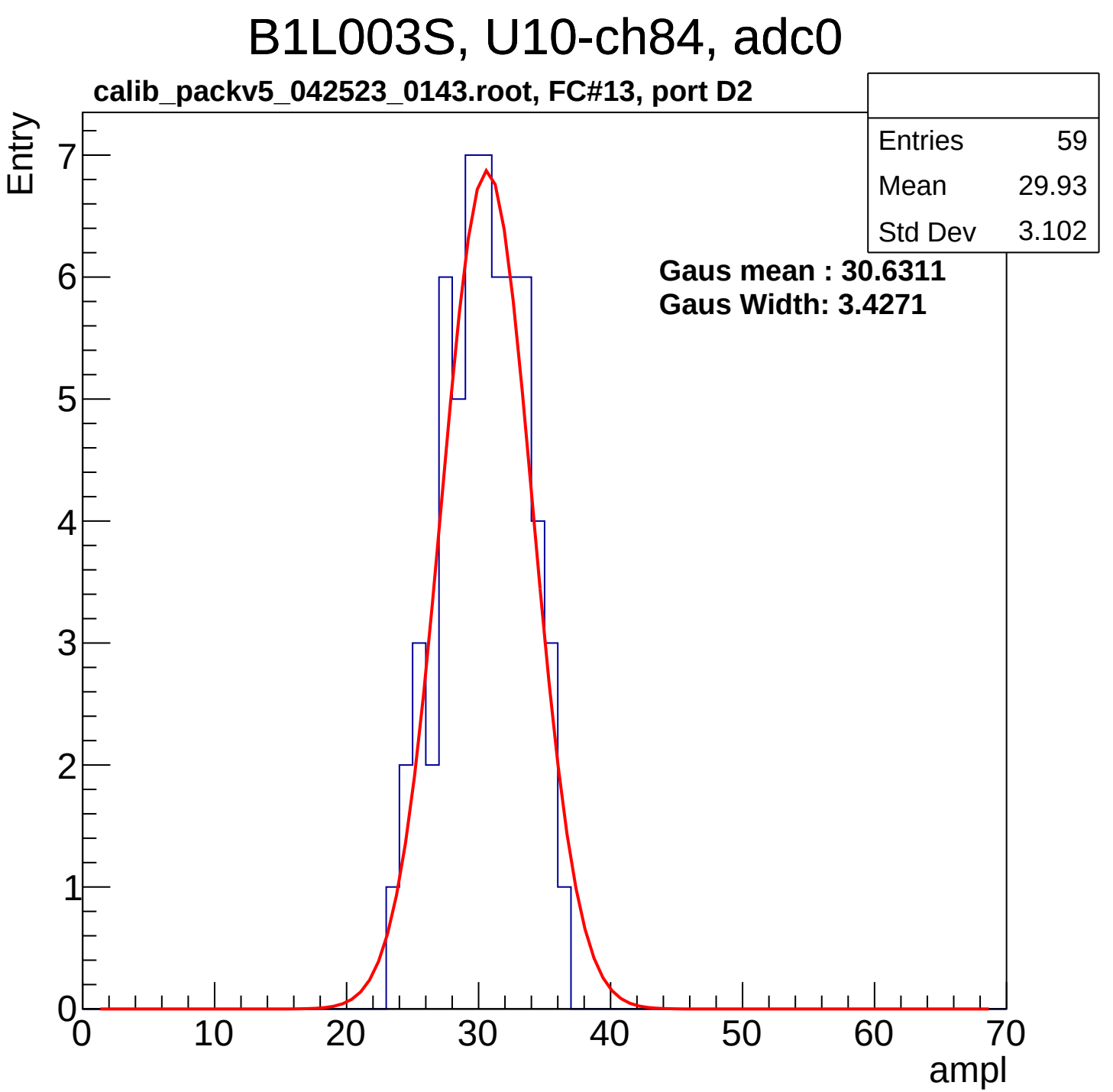
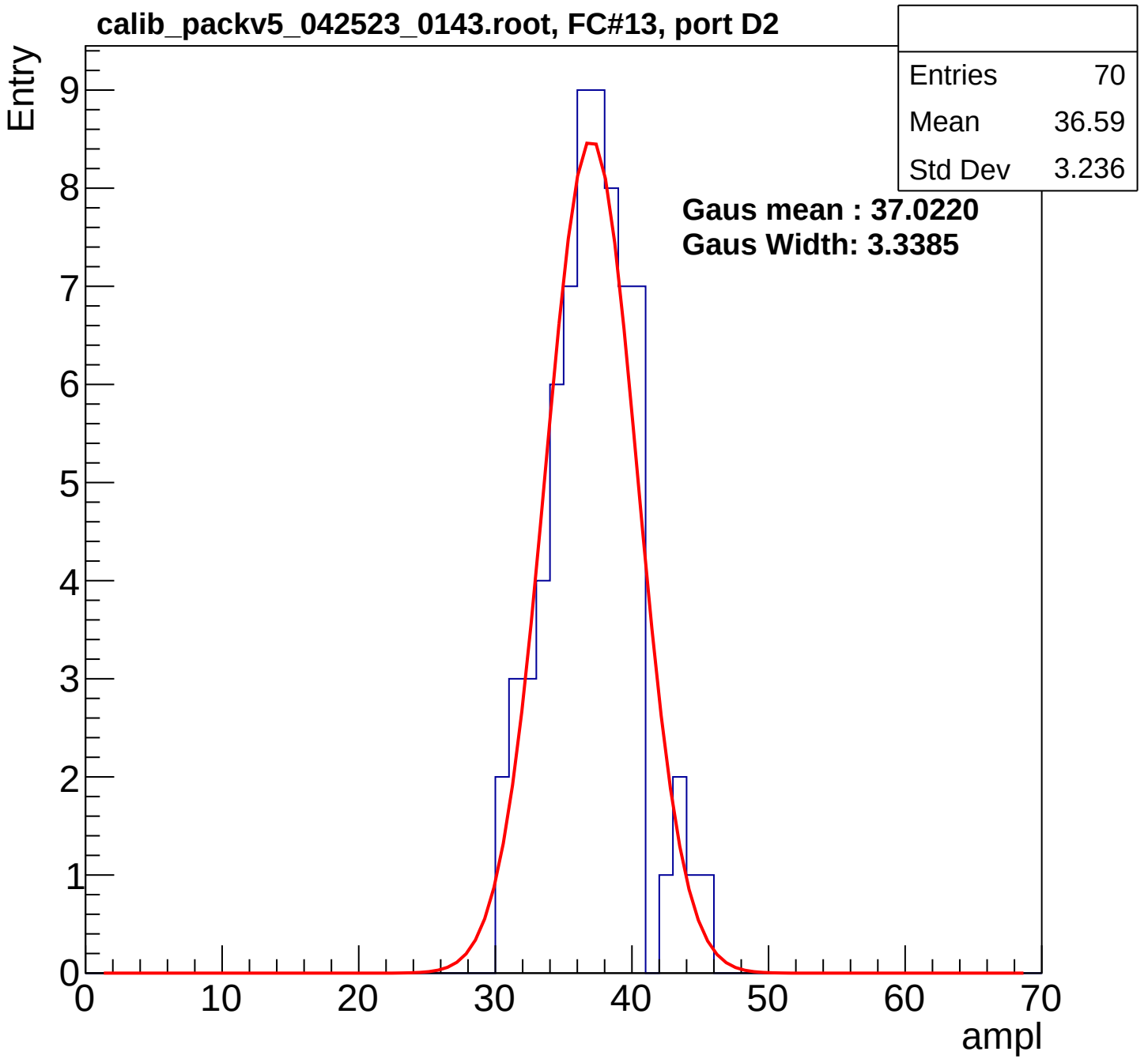
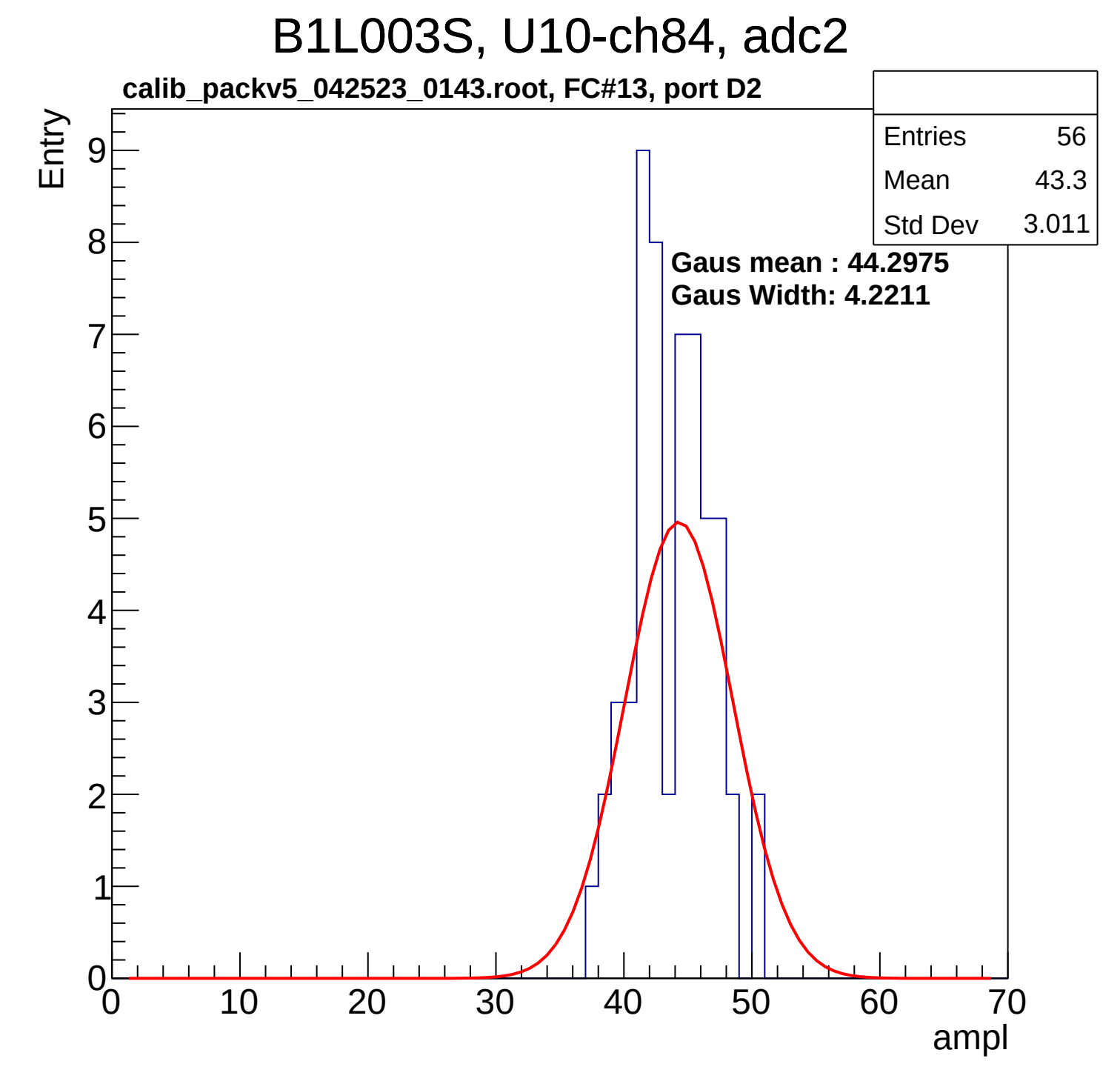
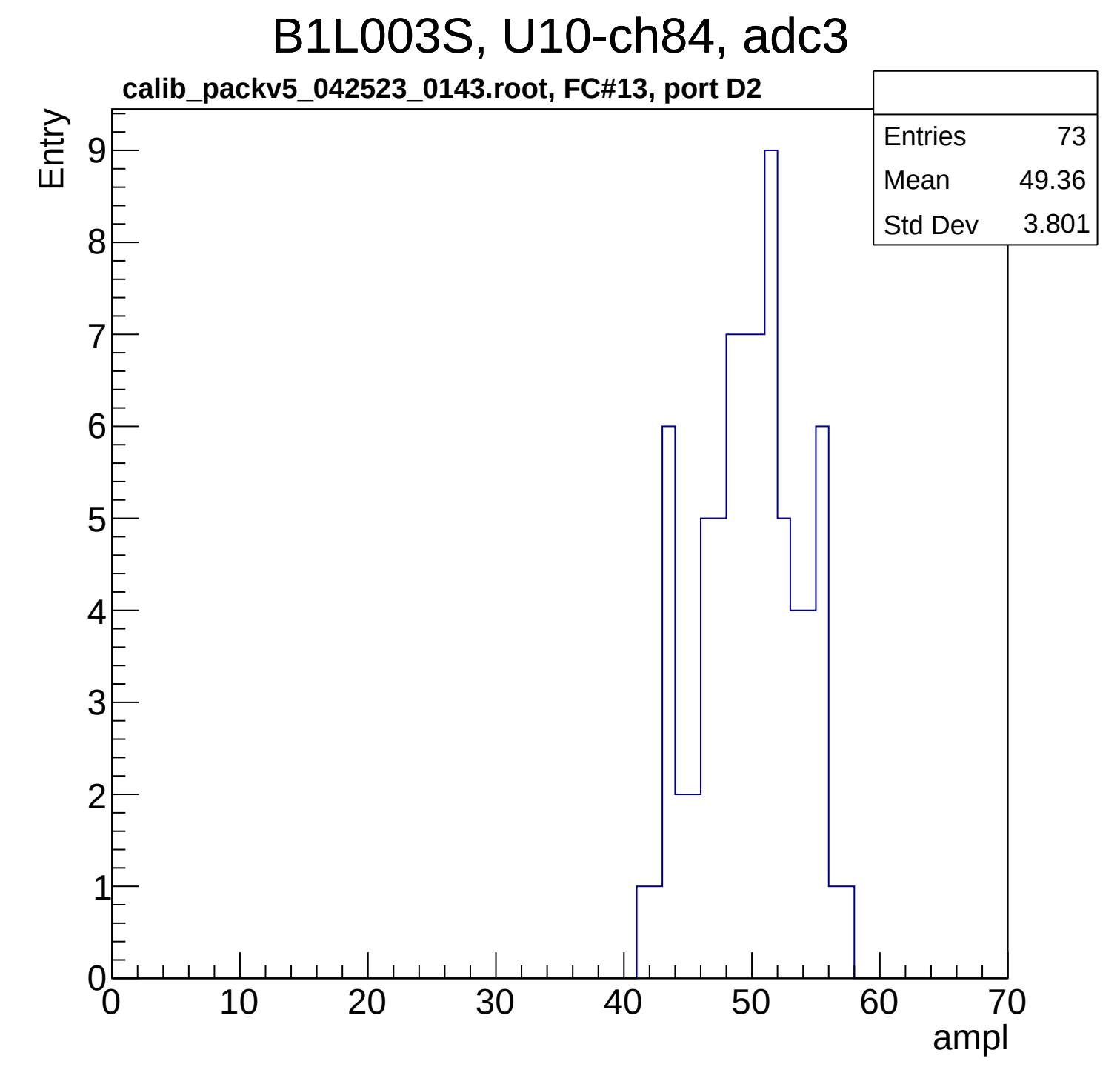
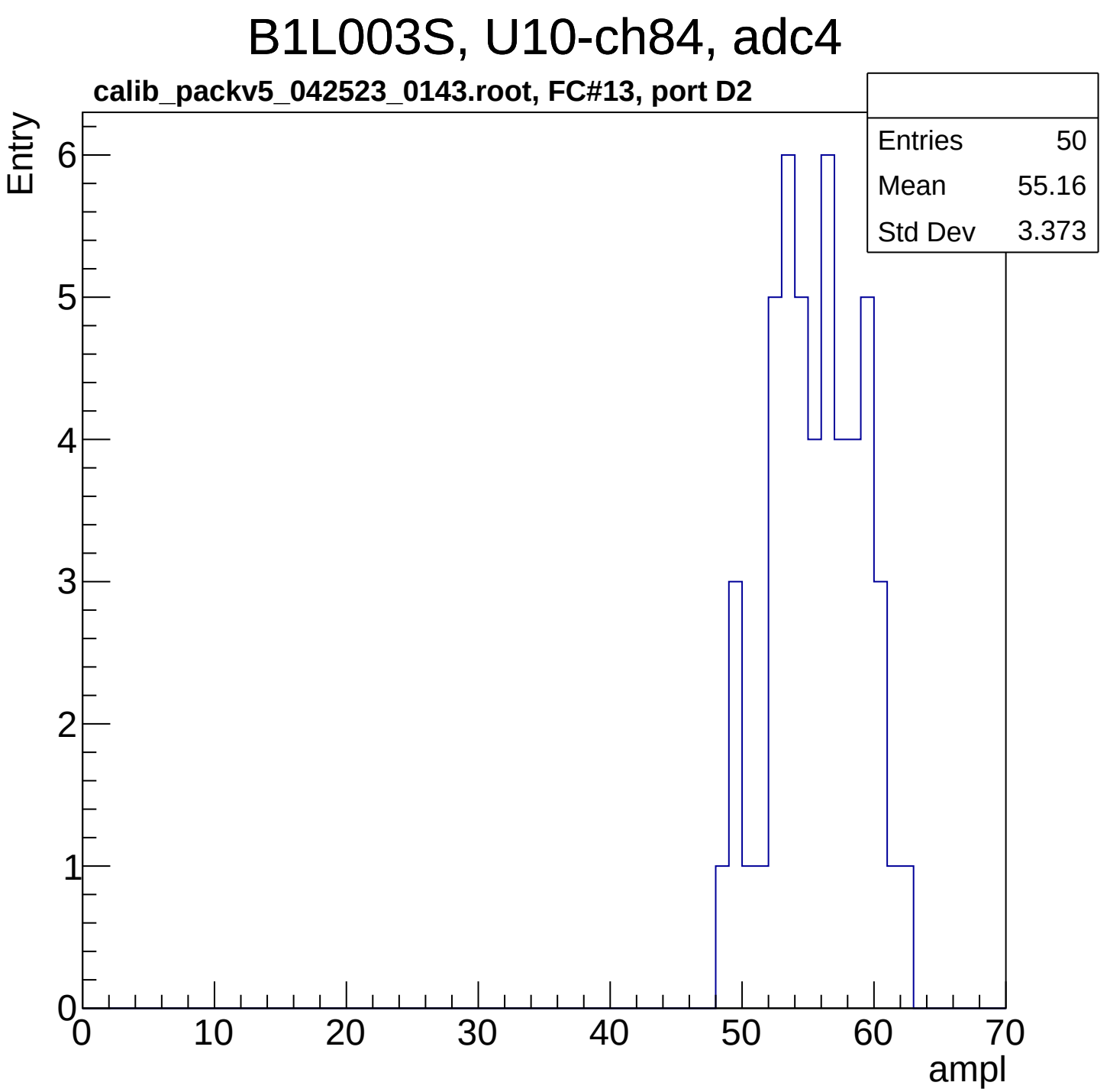
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch84, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 29.93
Std Dev 3.102
Gaus mean : 30.6311
Gaus Width: 3.4271
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch84, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36.59
Std Dev 3.236
Gaus mean : 37.0220
Gaus Width: 3.3385
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch84, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 43.3
Std Dev 3.011
Gaus mean : 44.2975
Gaus Width: 4.2211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch84, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.36
Std Dev 3.801
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch84, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 55.16
Std Dev 3.373
Entry
0
1
2
3
4
5
6
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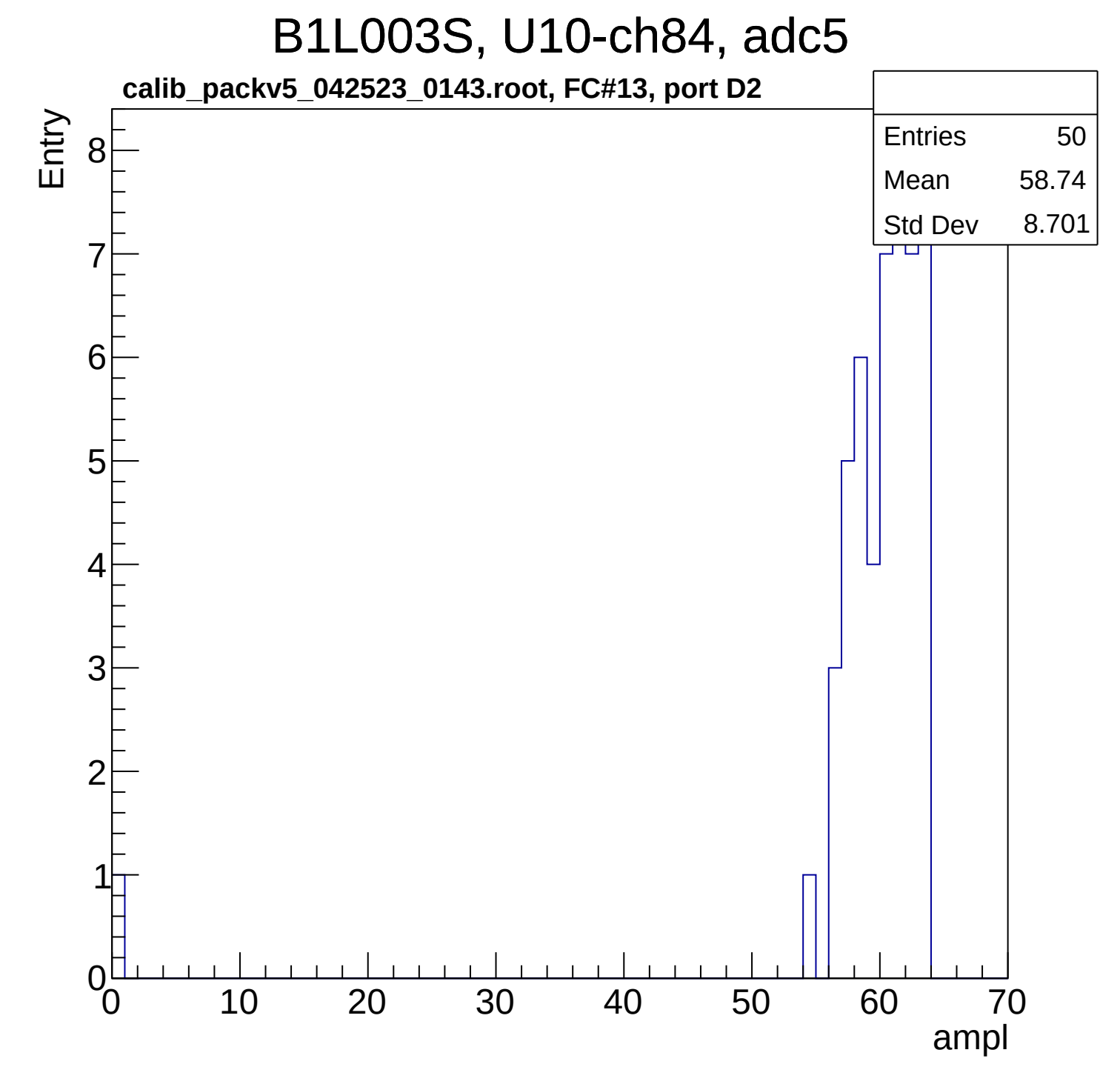
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch84, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.74
Std Dev 8.701
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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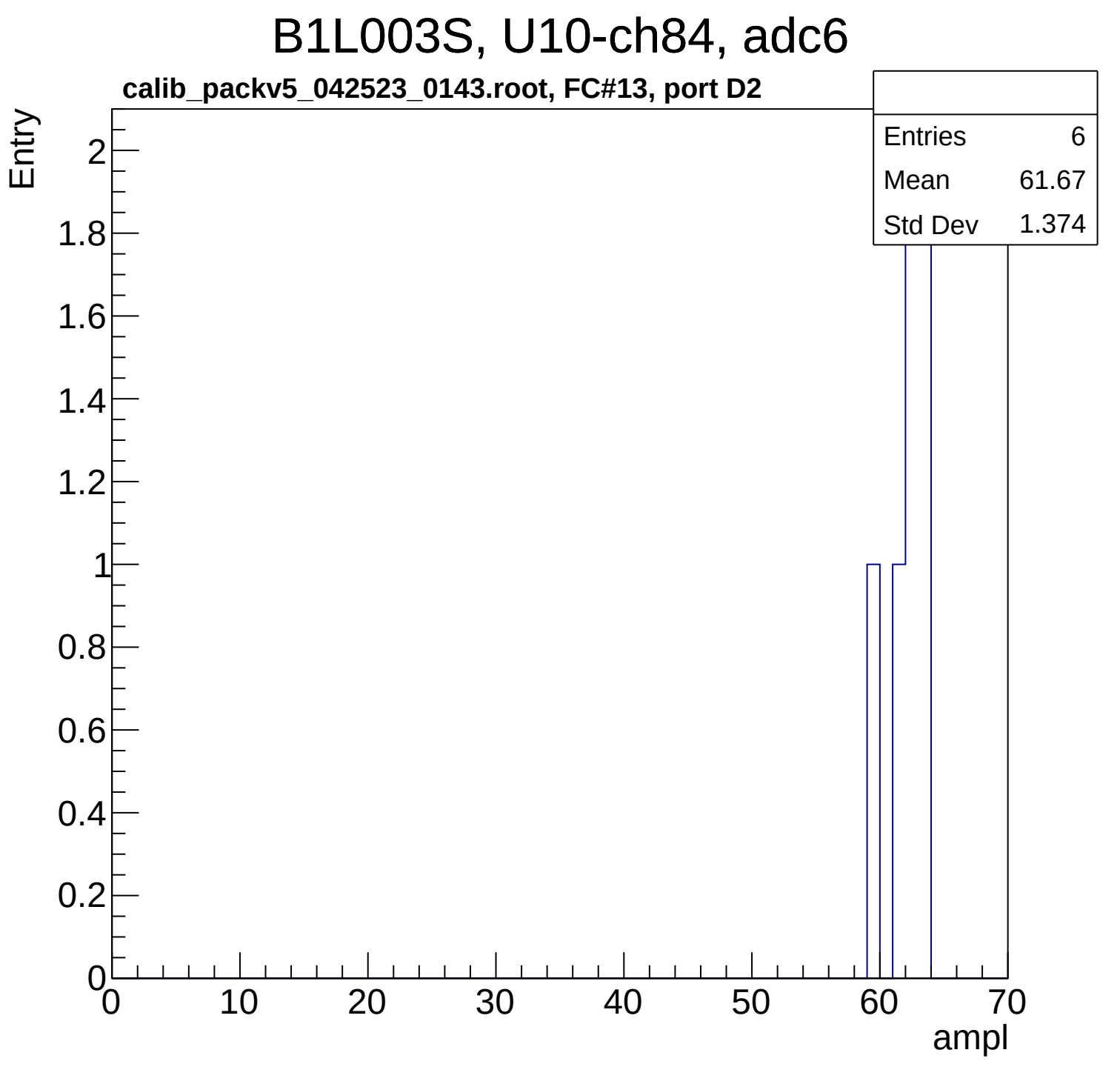
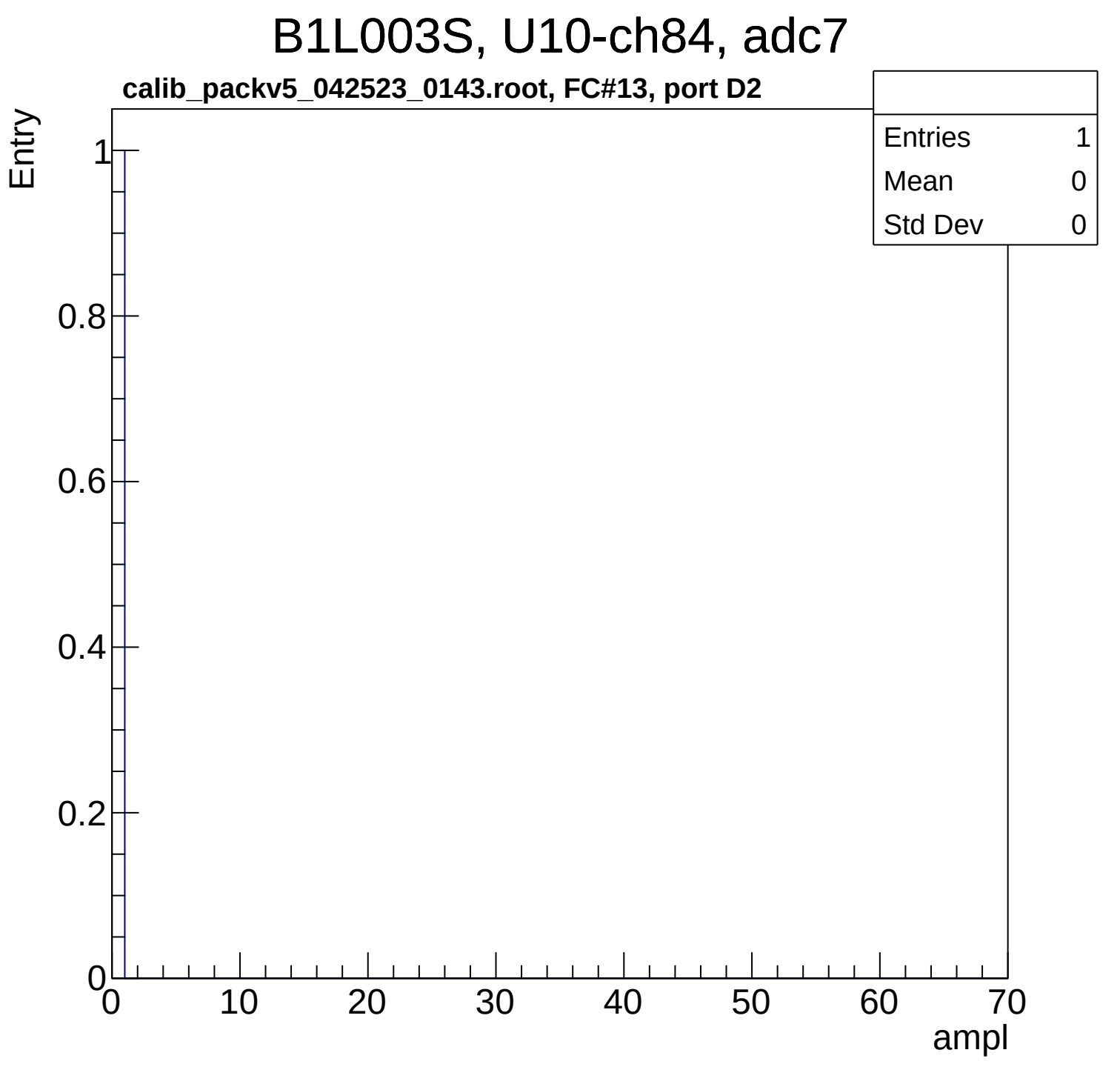

B1L003S, U10-ch84, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.67
Std Dev 1.374
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch84, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
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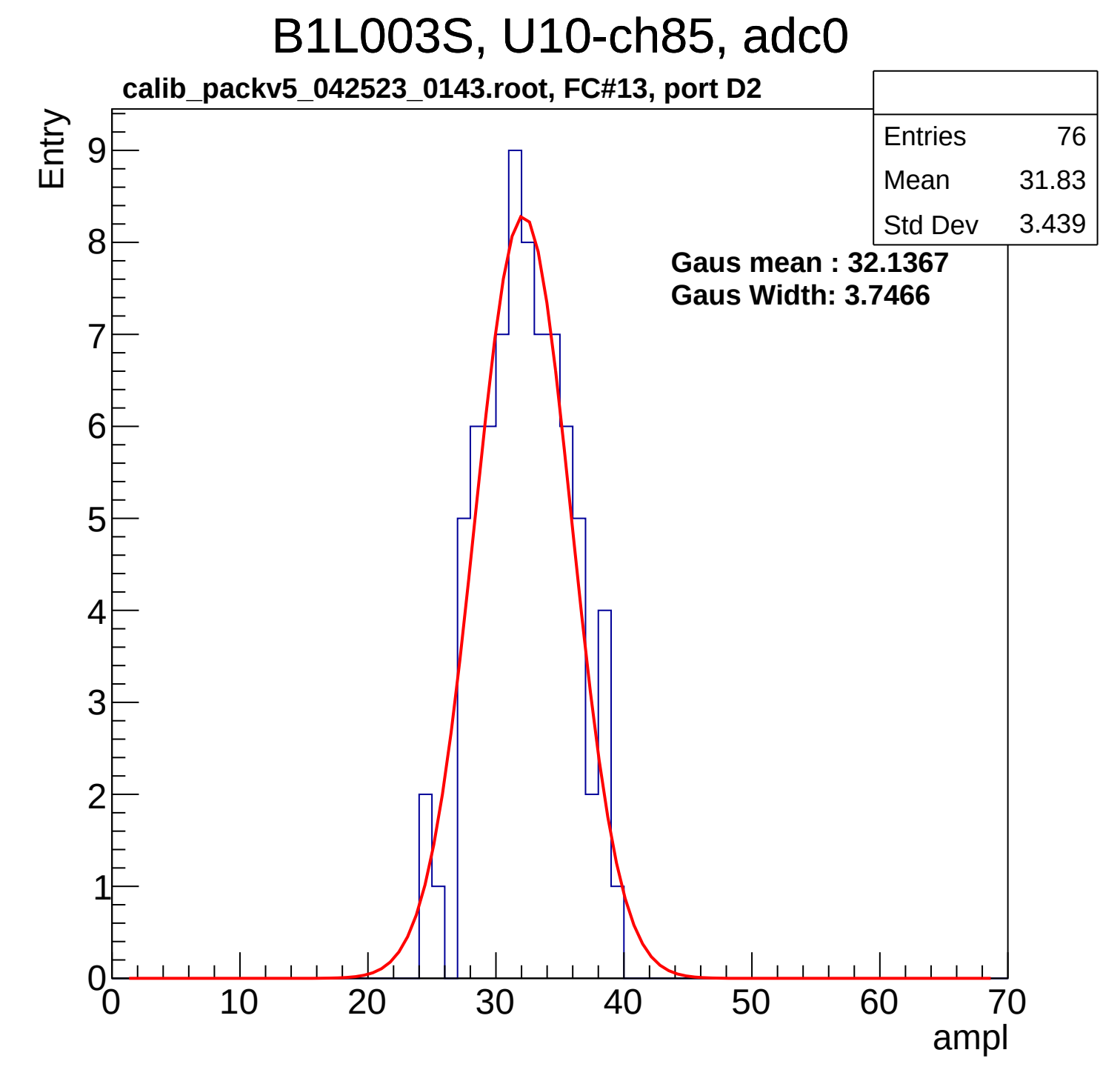
70
ampl


B1L003S, U10-ch85, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 31.83
Std Dev 3.439
Gaus mean : 32.1367
Gaus Width: 3.7466
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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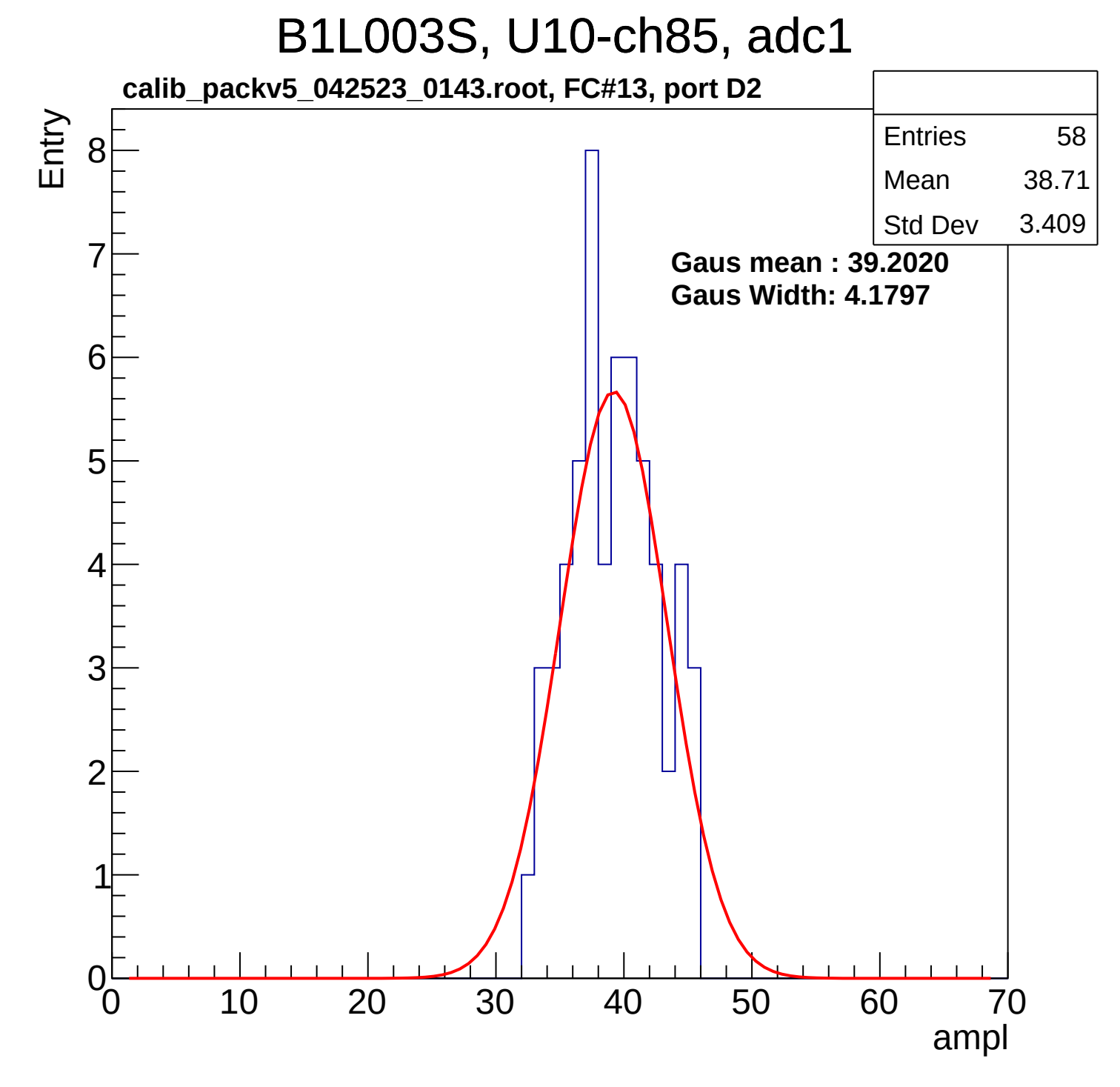
B1L003S, U10-ch85, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 38.71
Std Dev 3.409
Gaus mean : 39.2020
Gaus Width: 4.1797
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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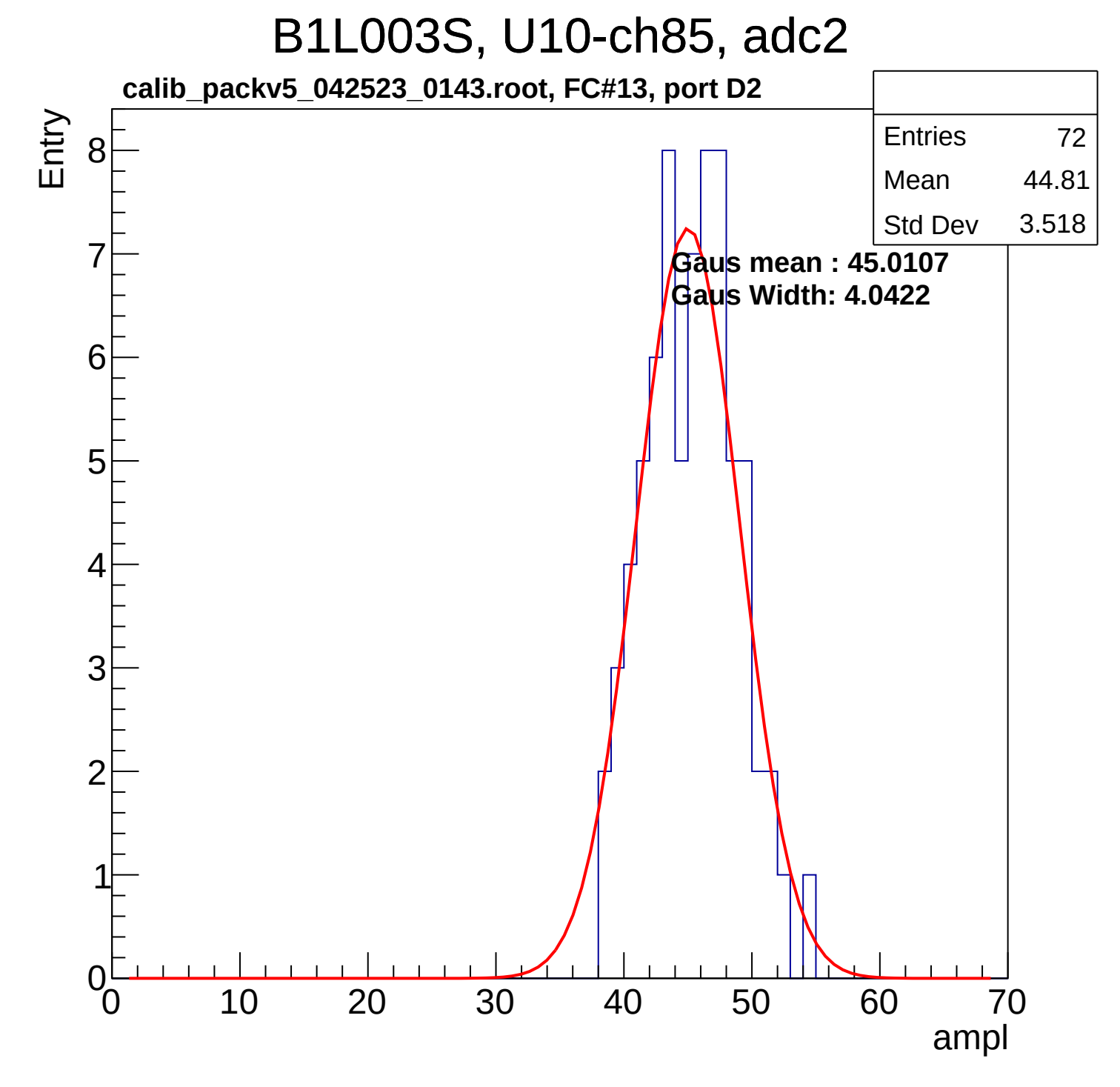
B1L003S, U10-ch85, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 44.81
Std Dev 3.518
Gaus mean : 45.0107
Gaus Width: 4.0422
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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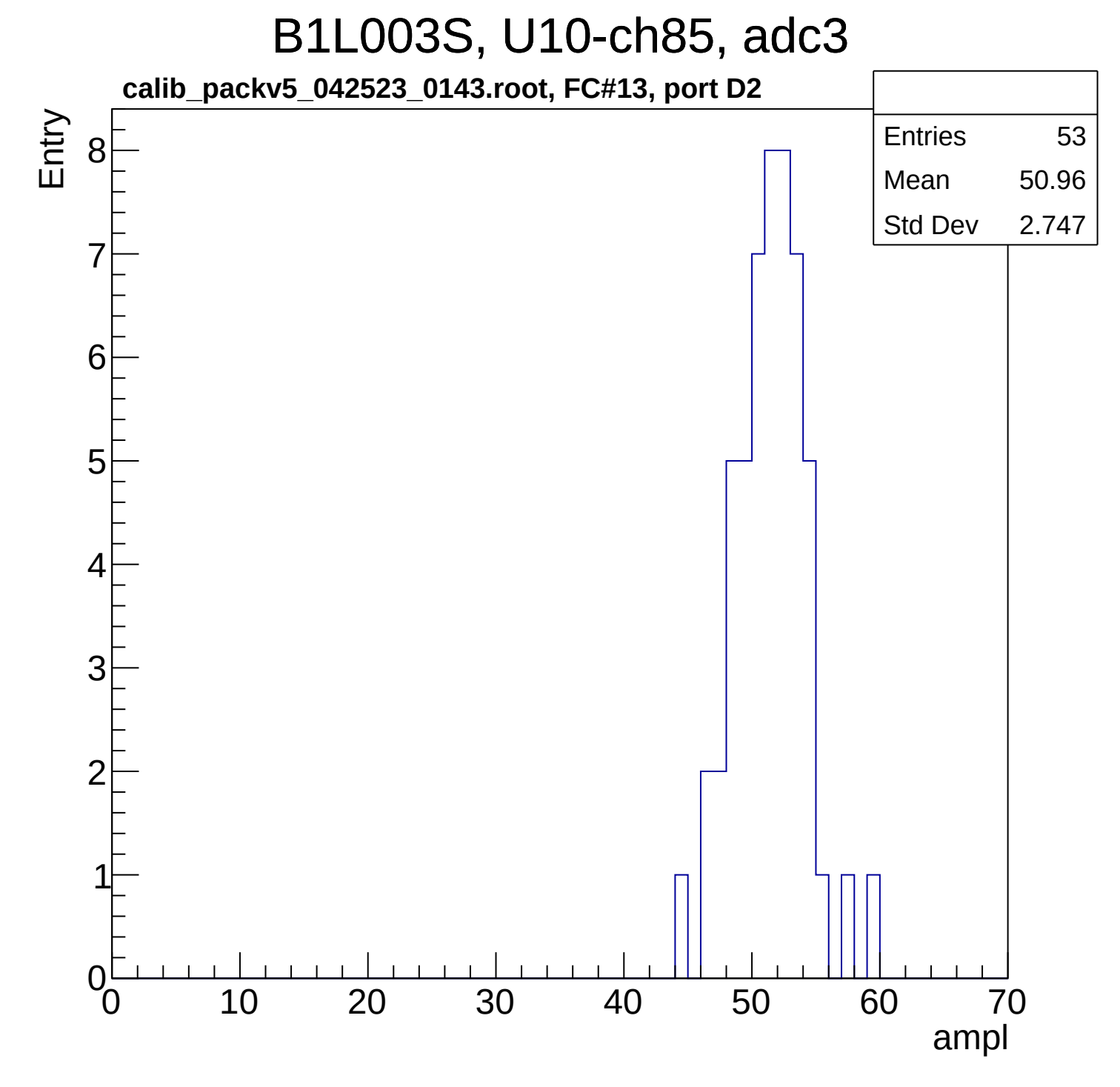
B1L003S, U10-ch85, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 50.96
Std Dev 2.747
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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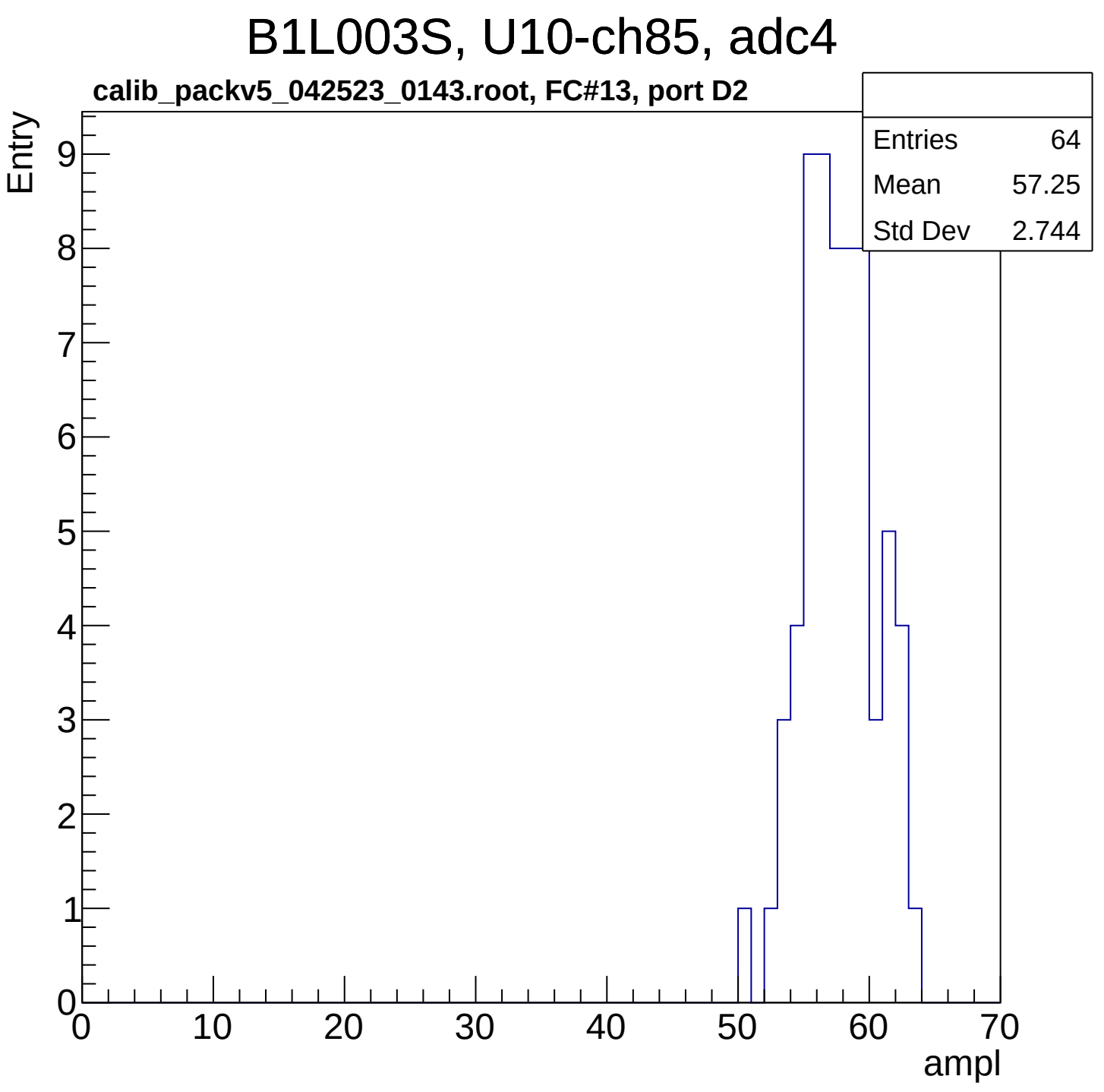
B1L003S, U10-ch85, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 57.25
Std Dev 2.744
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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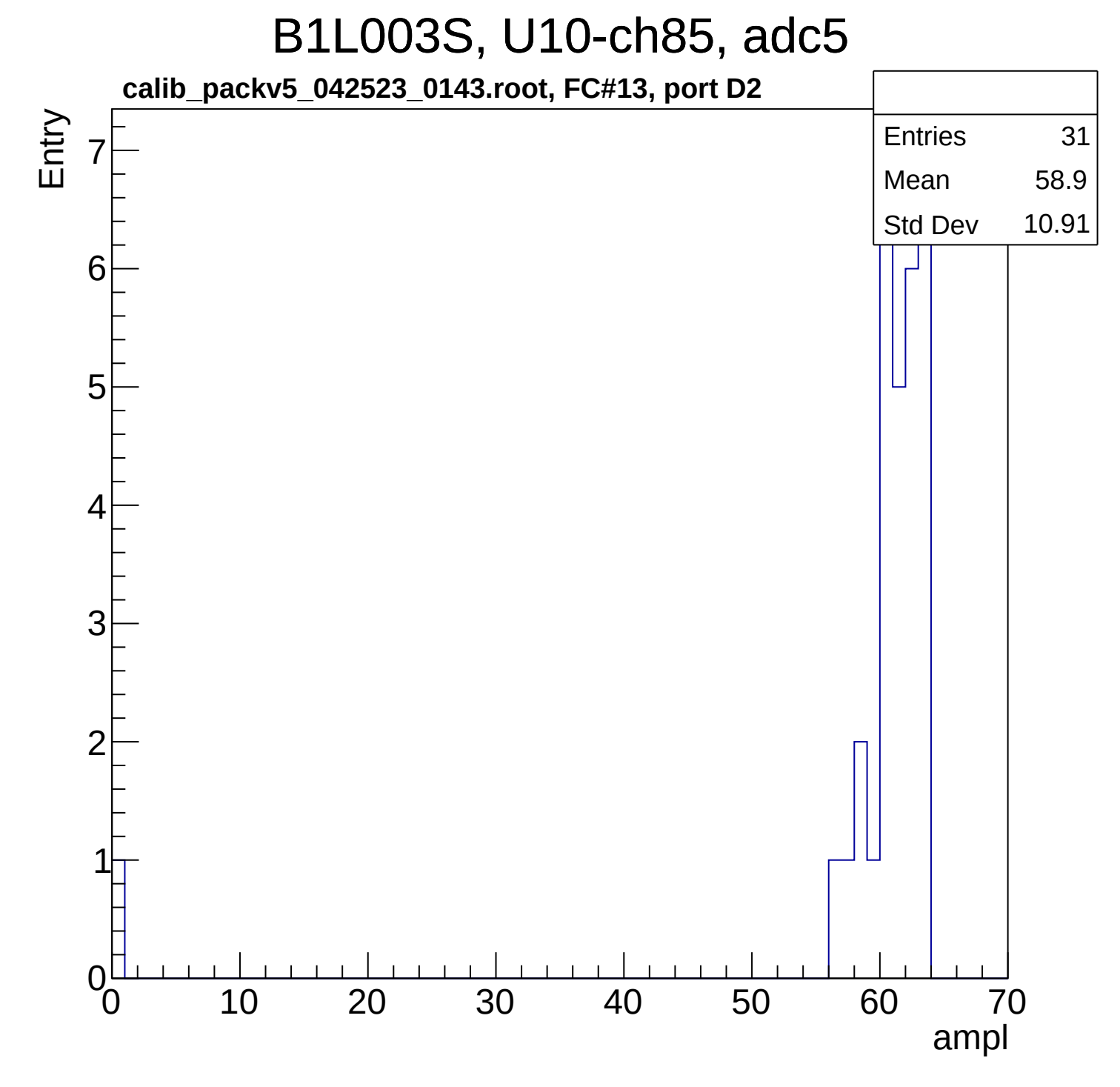
B1L003S, U10-ch85, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 31
Mean 58.9
Std Dev 10.91
Entry
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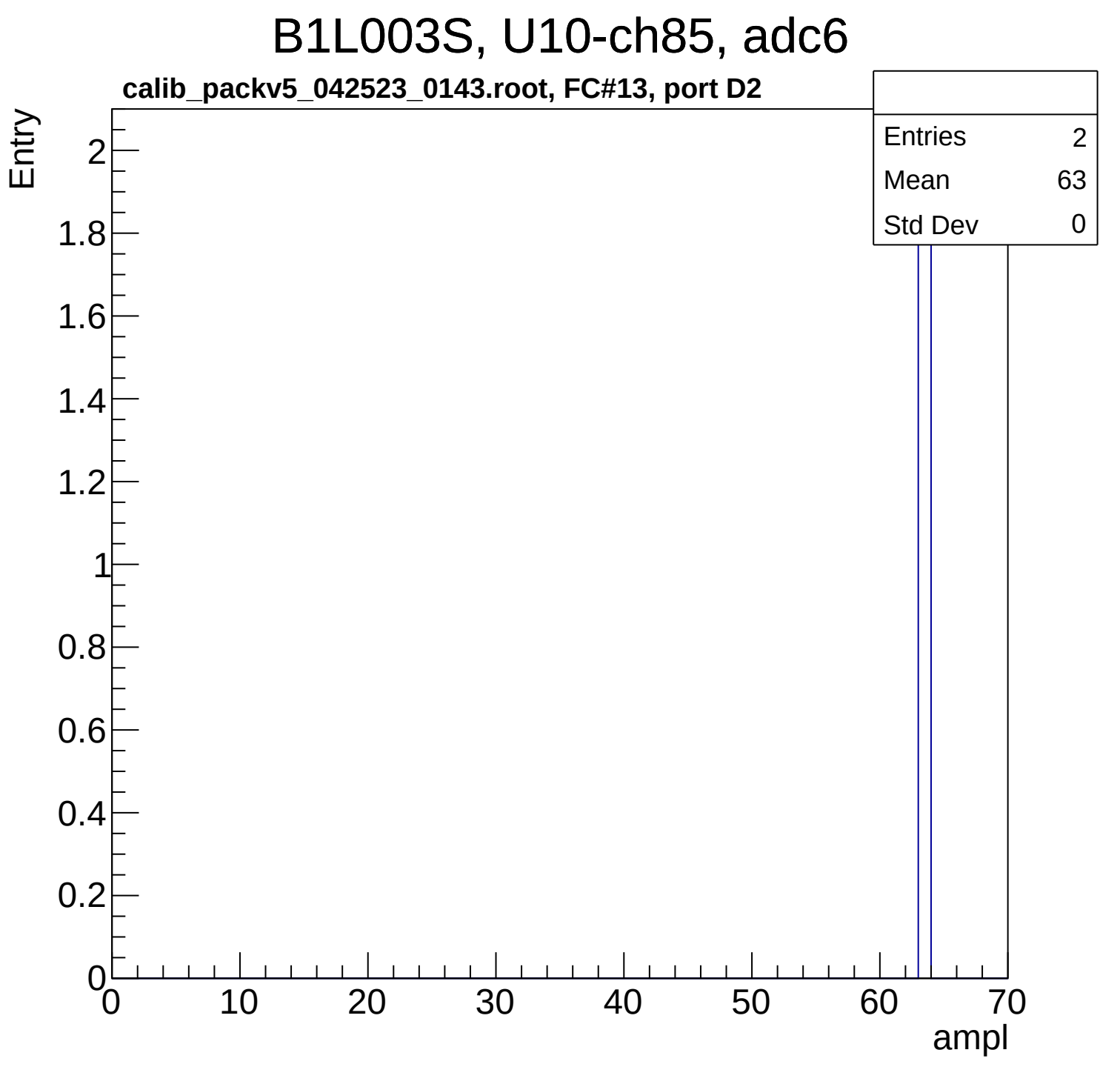
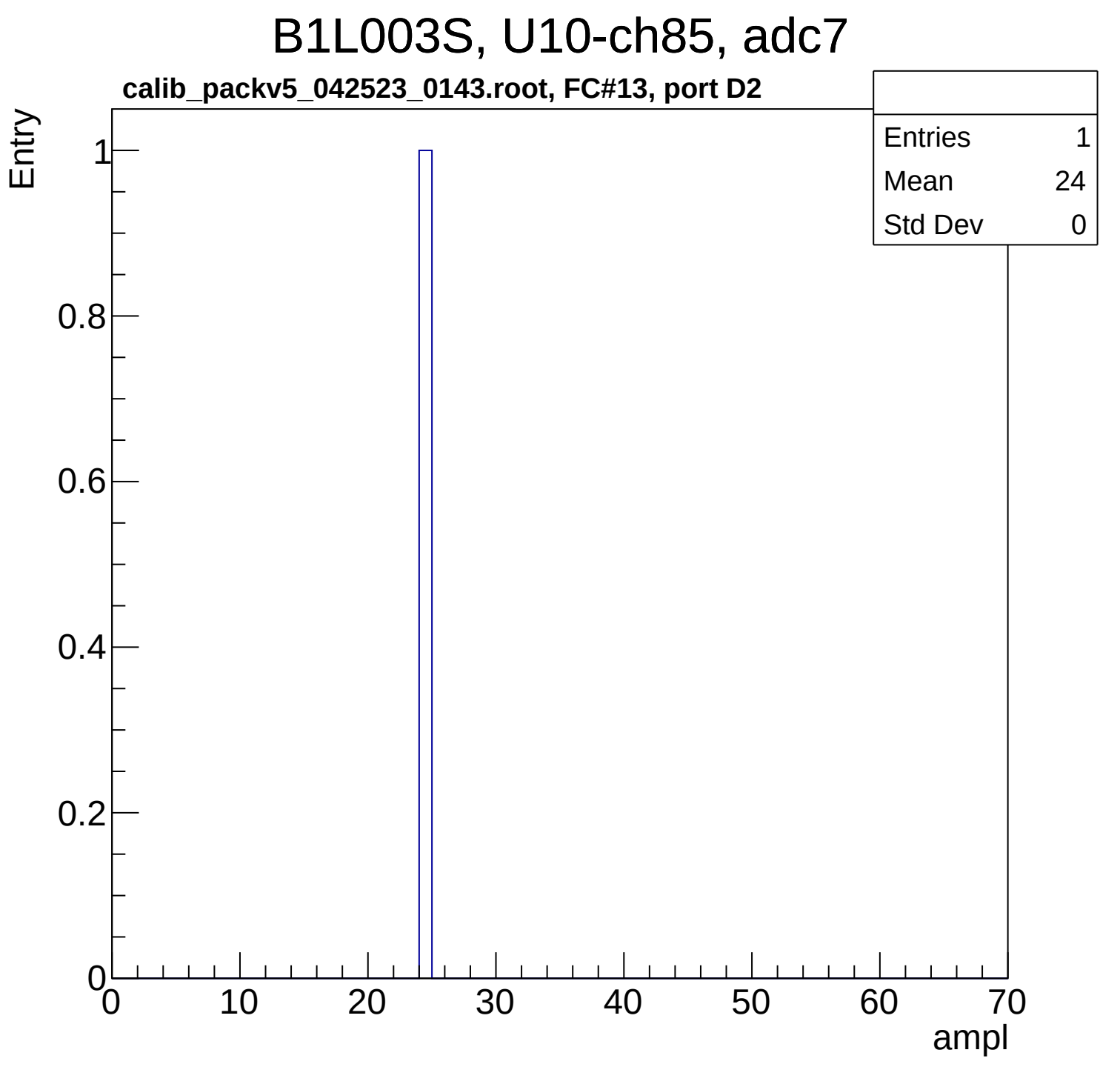
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch85, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch85, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
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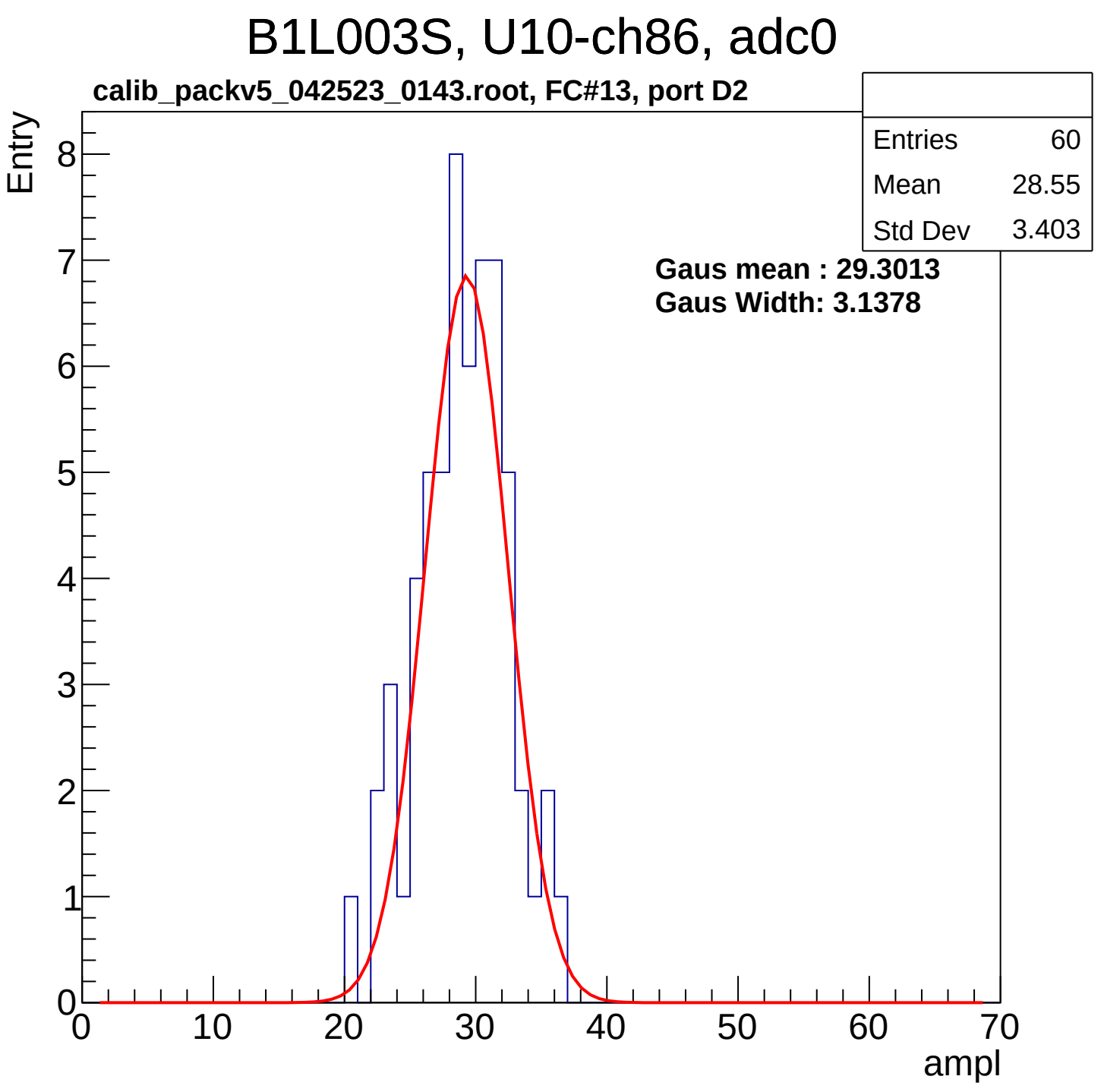
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch86, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.55
Std Dev 3.403
Gaus mean : 29.3013
Gaus Width: 3.1378
Entry
ampl
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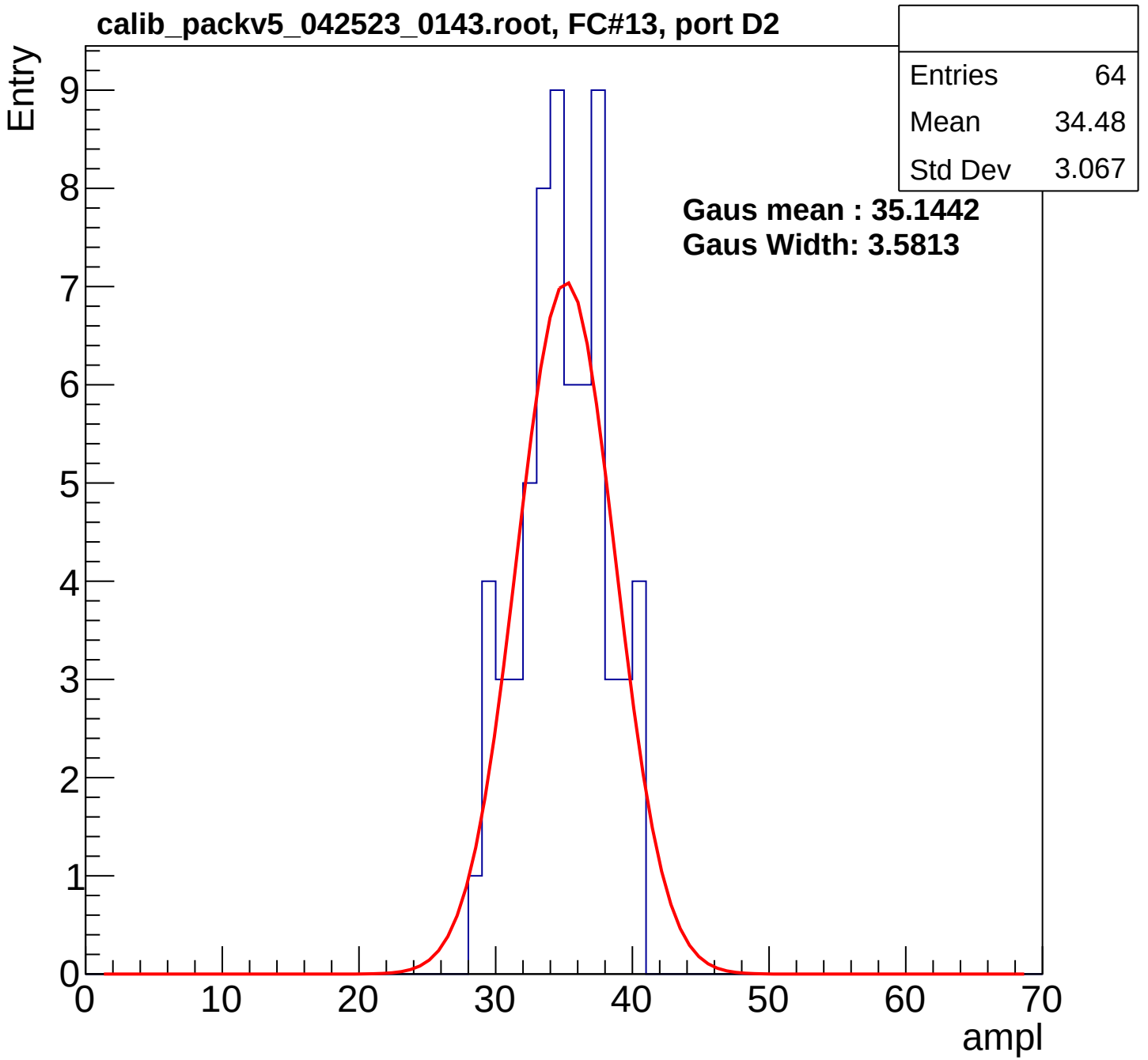
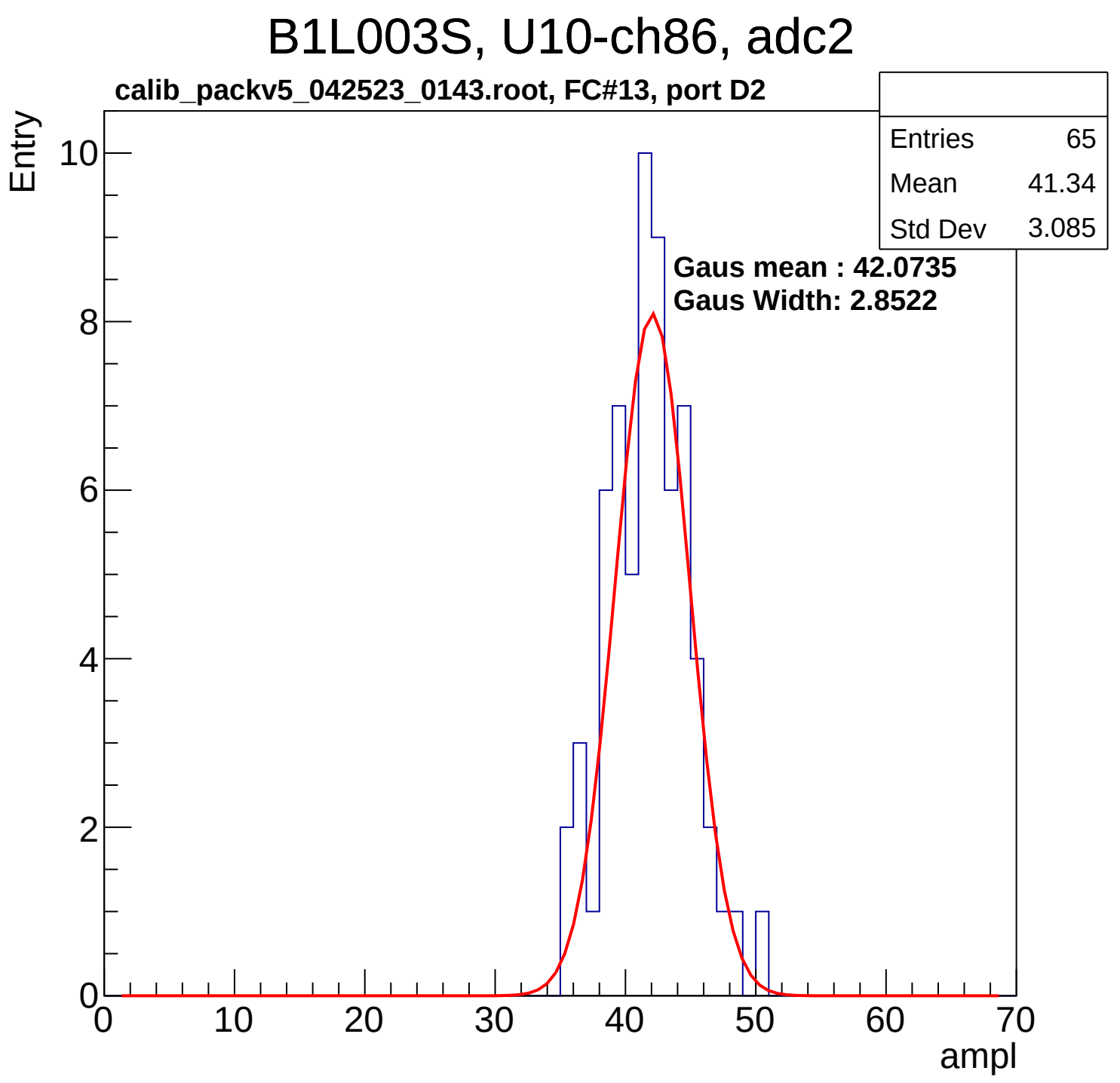
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch86, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 34.48
Std Dev 3.067
Gaus mean : 35.1442
Gaus Width: 3.5813
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch86, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 41.34
Std Dev 3.085
Gaus mean : 42.0735
Gaus Width: 2.8522
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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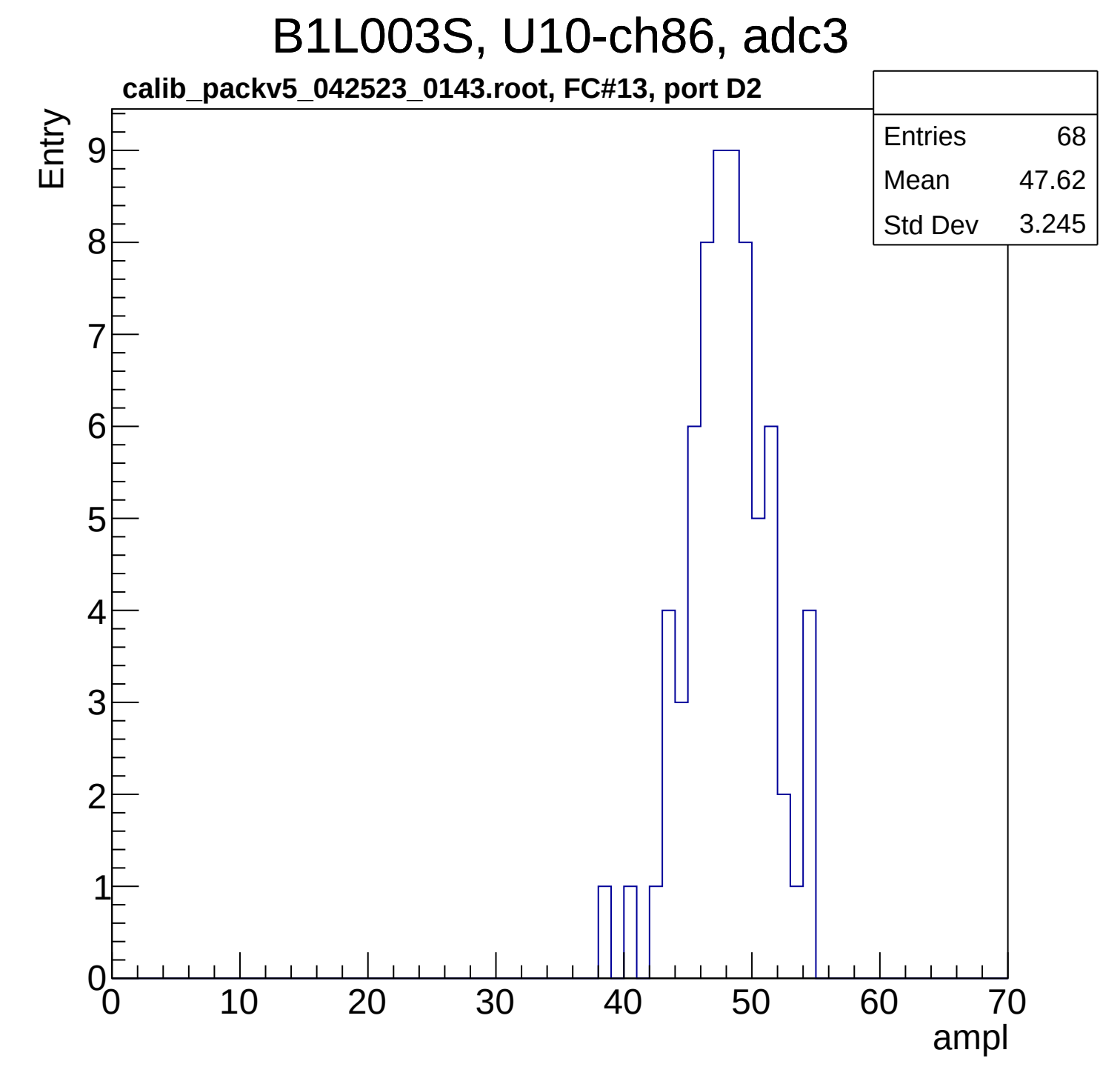
B1L003S, U10-ch86, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 47.62
Std Dev 3.245
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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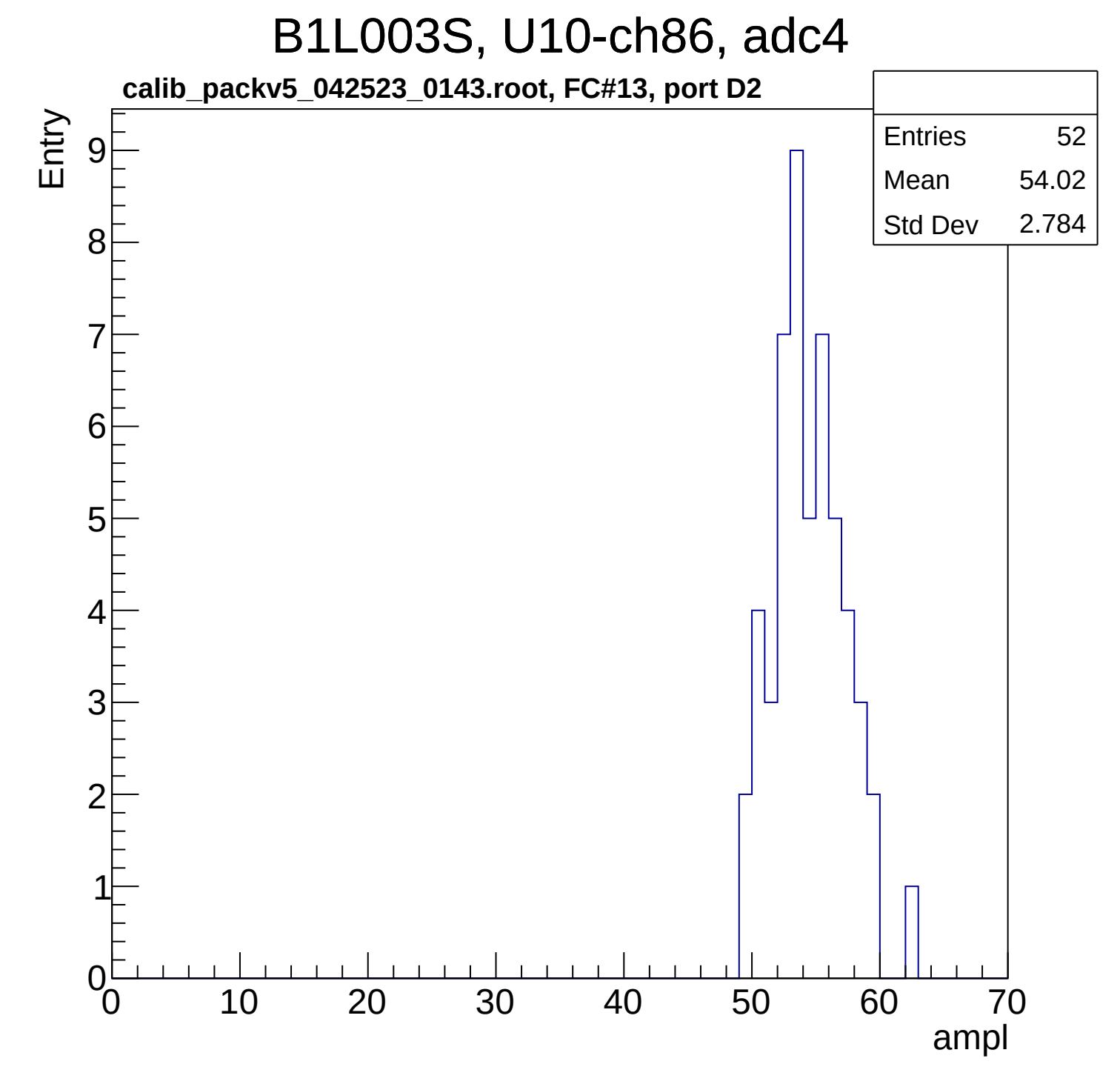
B1L003S, U10-ch86, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.02
Std Dev 2.784
Entry
ampl
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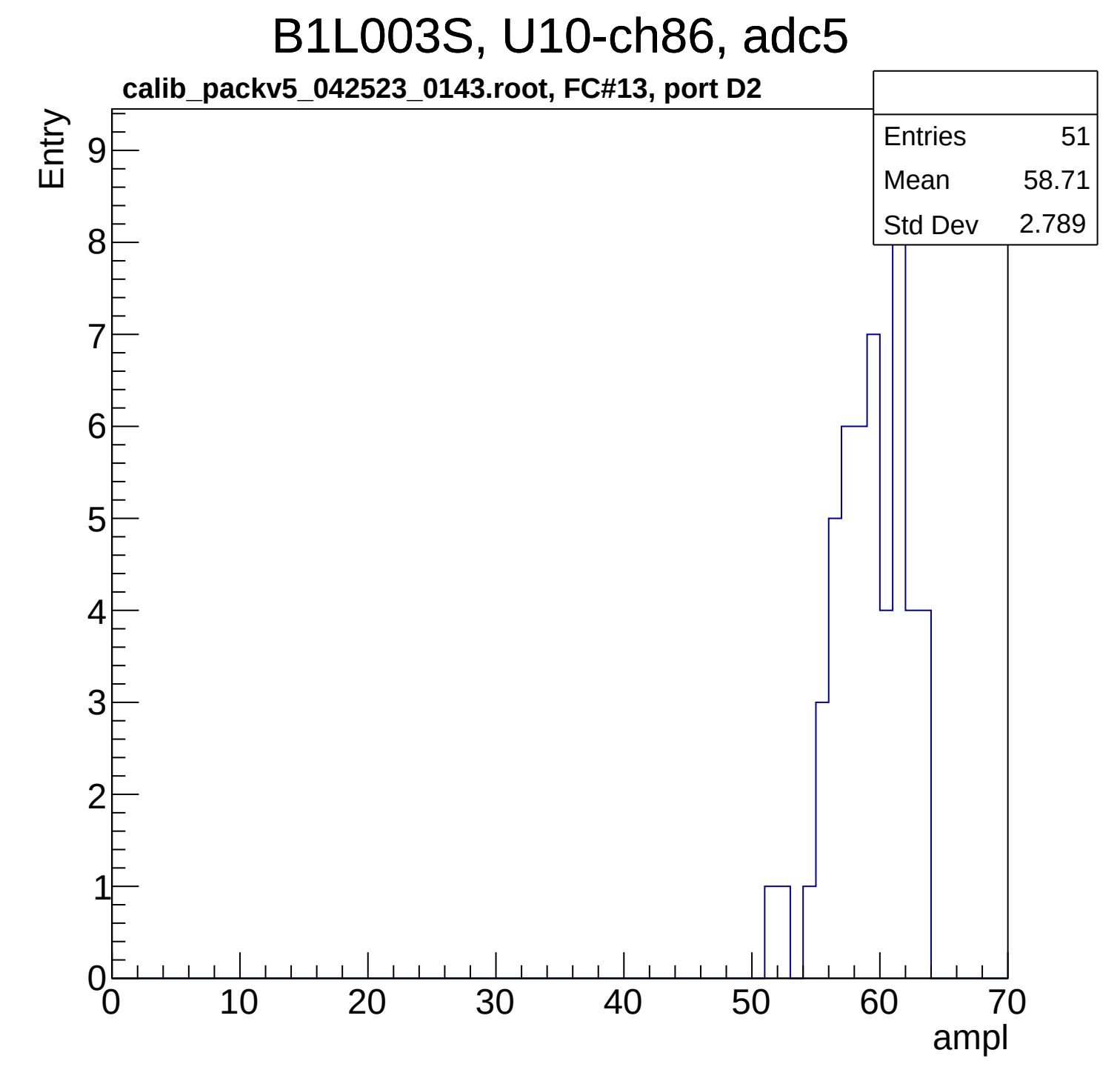
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch86, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.71
Std Dev 2.789
Entry
ampl
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3
4
5
6
7
8
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0
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60
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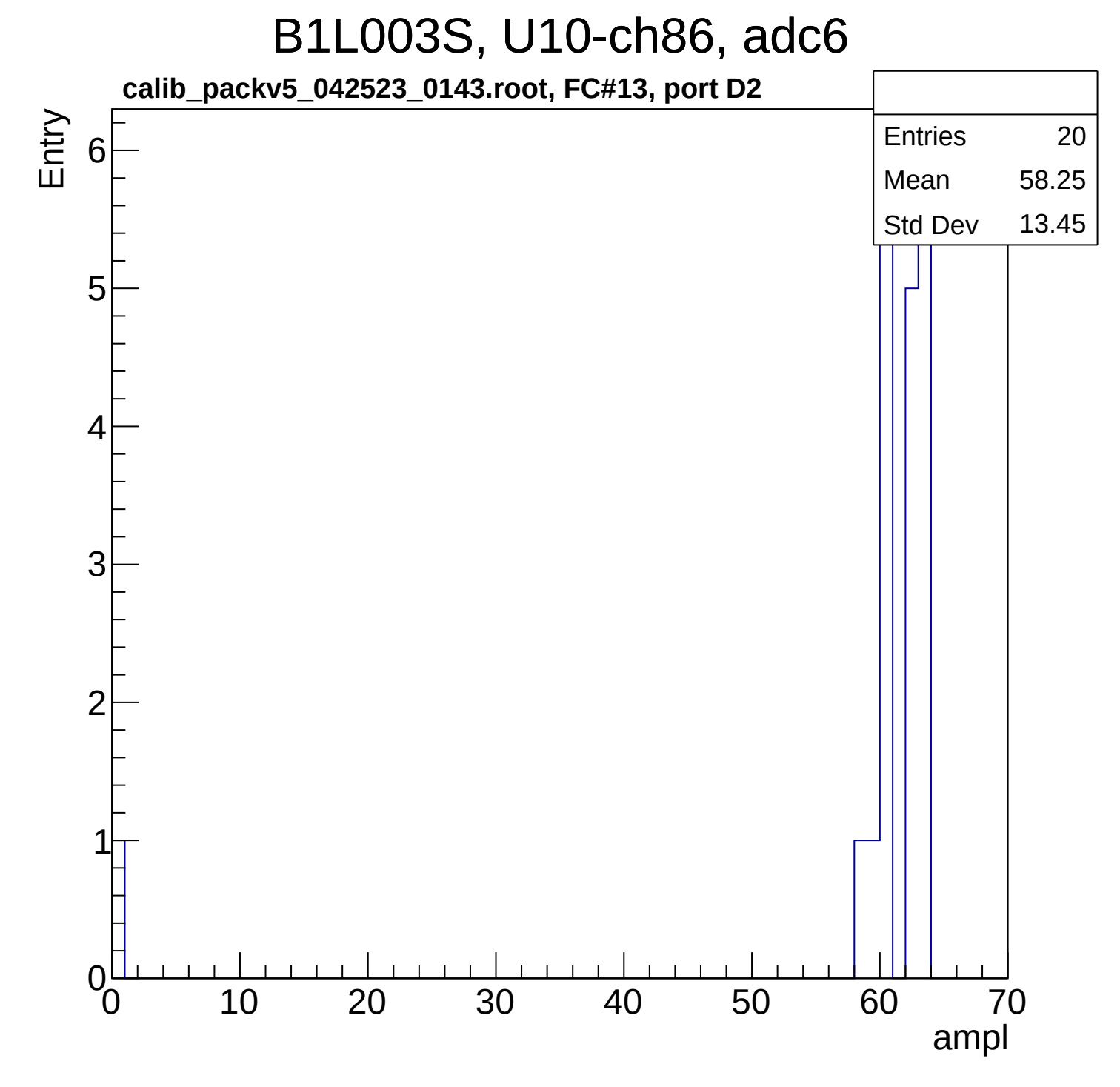
B1L003S, U10-ch86, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 58.25
Std Dev 13.45
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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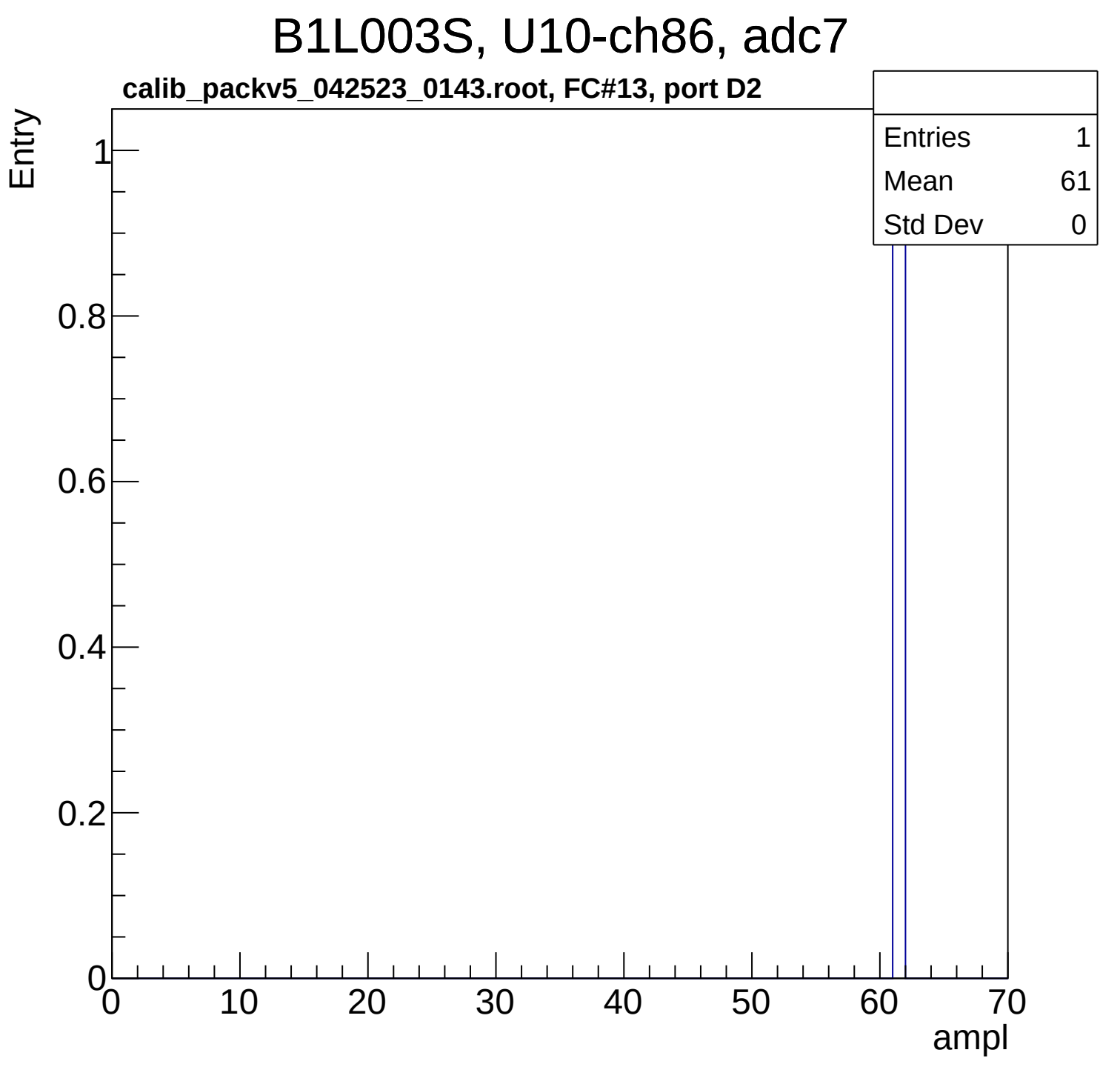
B1L003S, U10-ch86, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
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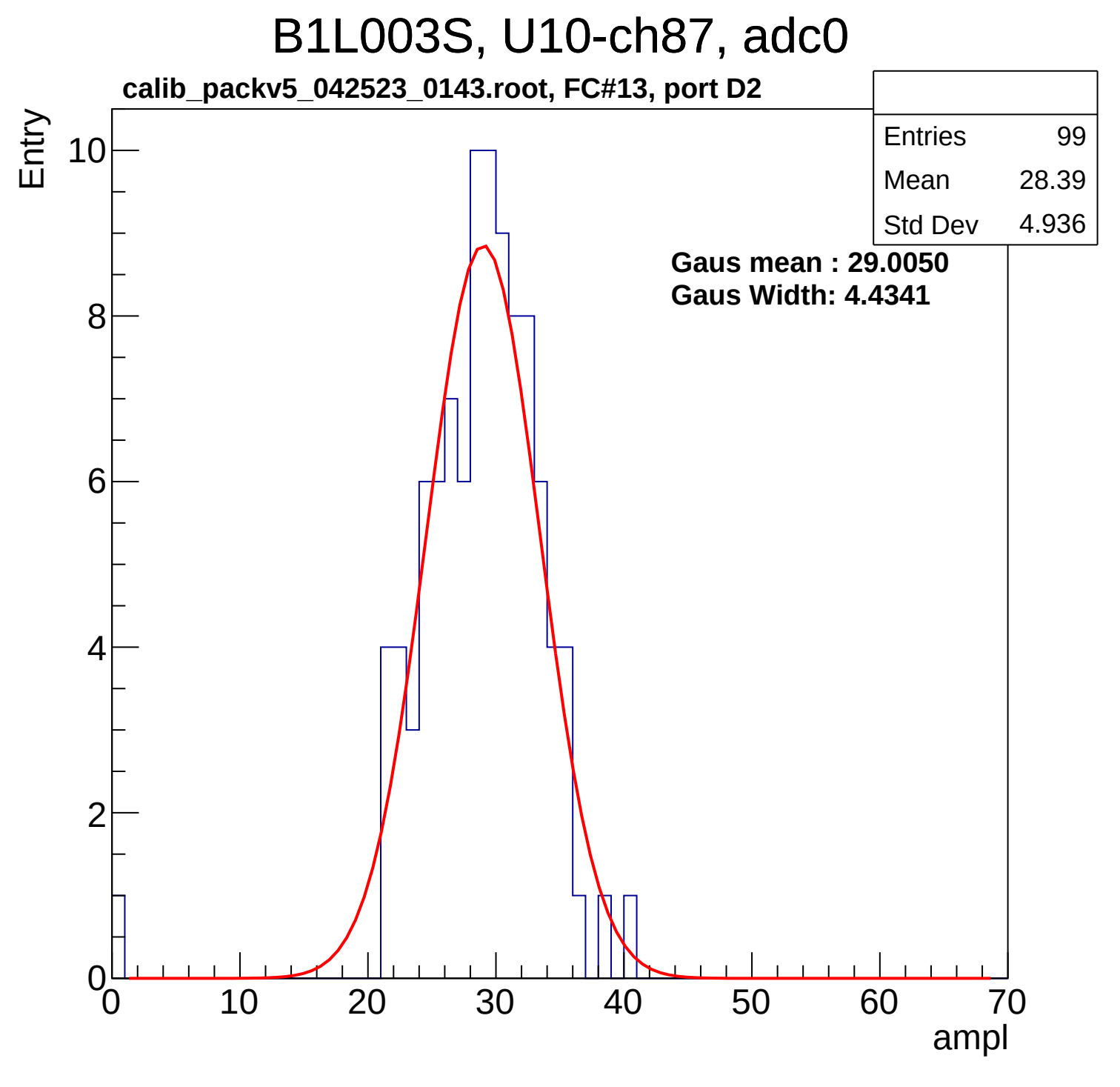
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch87, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 99
Mean 28.39
Std Dev 4.936
Gaus mean : 29.0050
Gaus Width: 4.4341
Entry
ampl
0
2
4
6
8
10
0
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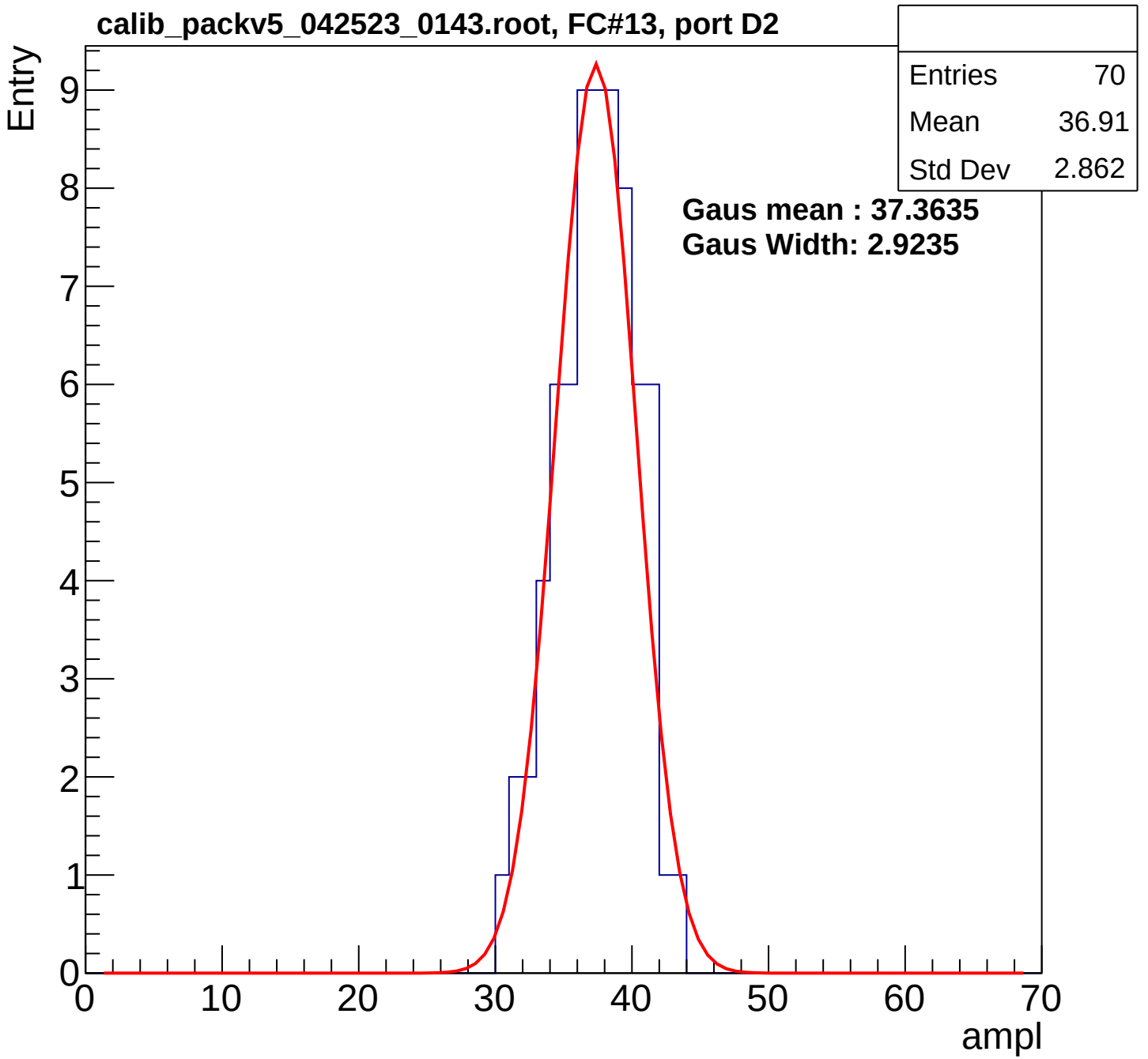
20
30
40
50
60
70


B1L003S, U10-ch87, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36.91
Std Dev 2.862
Gaus mean : 37.3635
Gaus Width: 2.9235
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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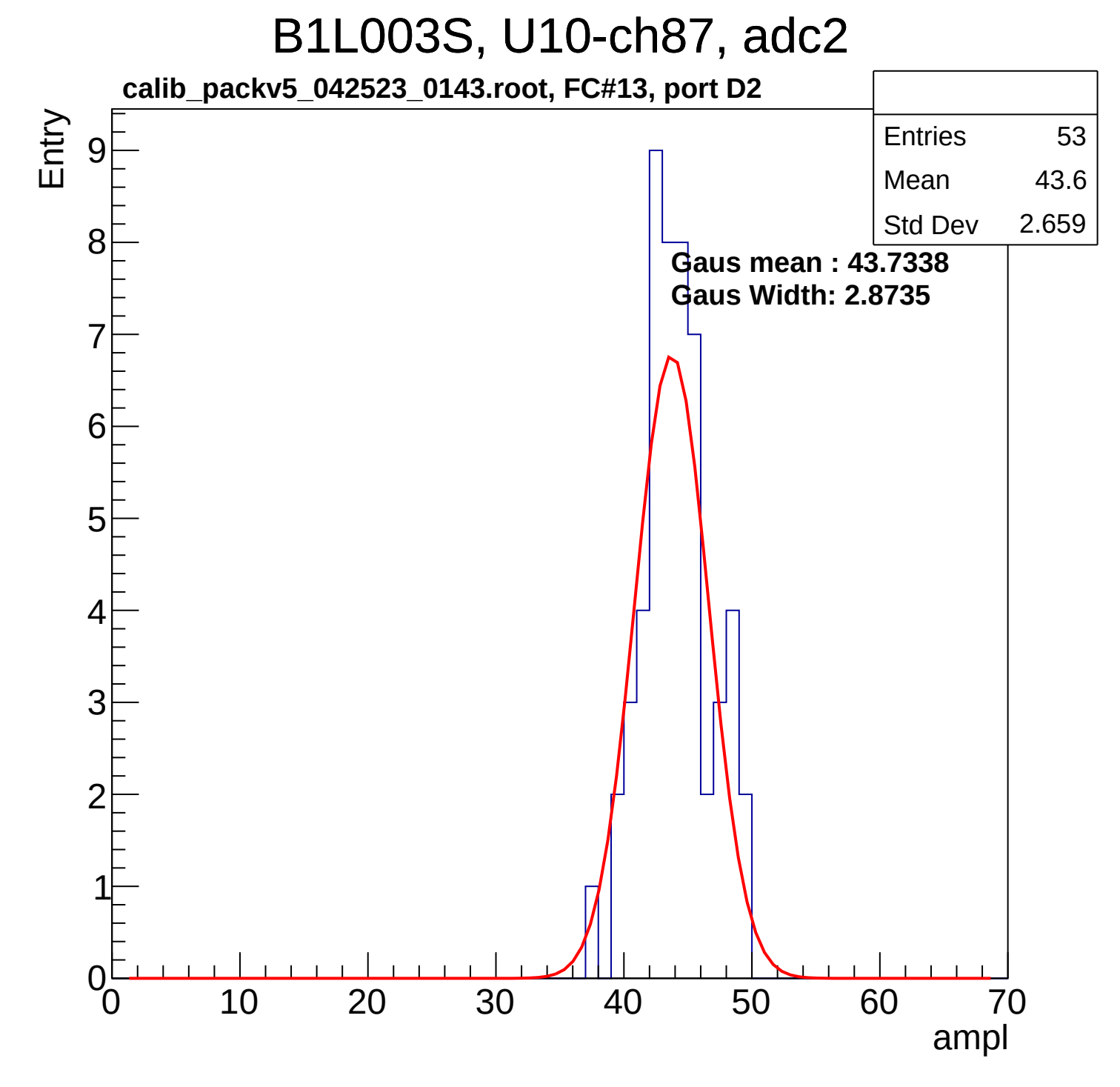
B1L003S, U10-ch87, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 43.6
Std Dev 2.659
Gaus mean : 43.7338
Gaus Width: 2.8735
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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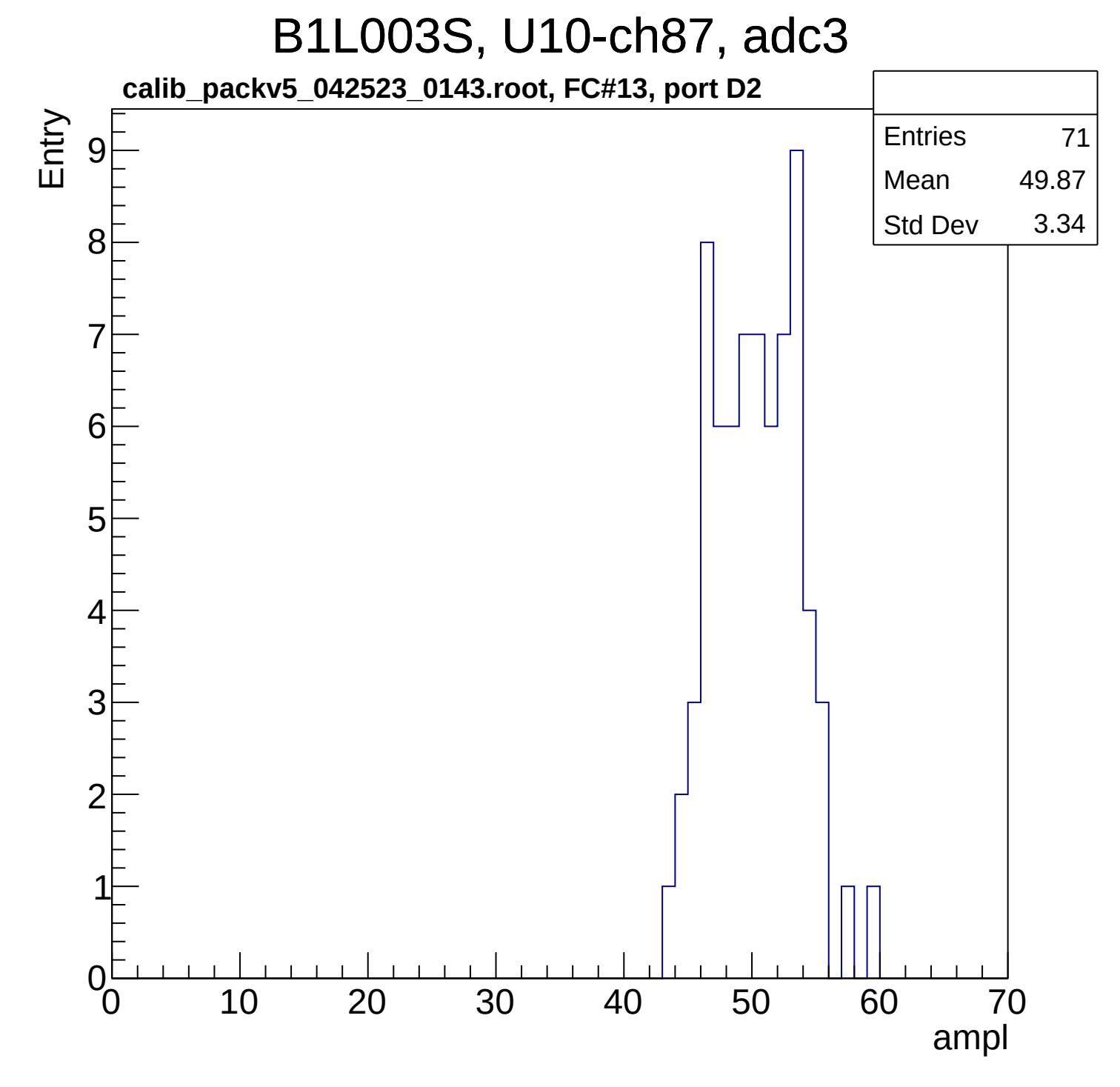
B1L003S, U10-ch87, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.87
Std Dev 3.34
Entry
ampl
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3
4
5
6
7
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0
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60
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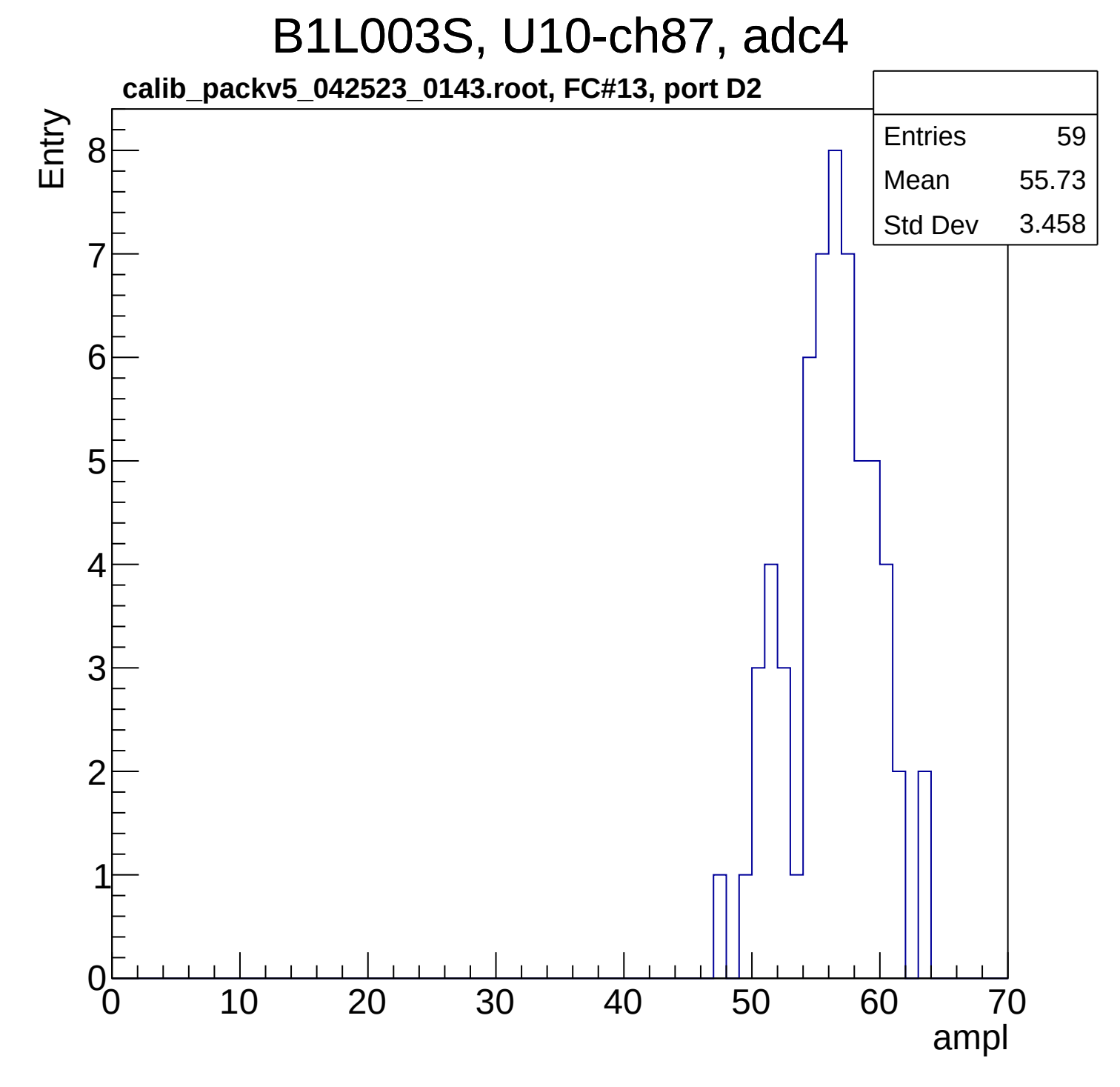
B1L003S, U10-ch87, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.73
Std Dev 3.458
Entry
ampl
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1
2
3
4
5
6
7
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0
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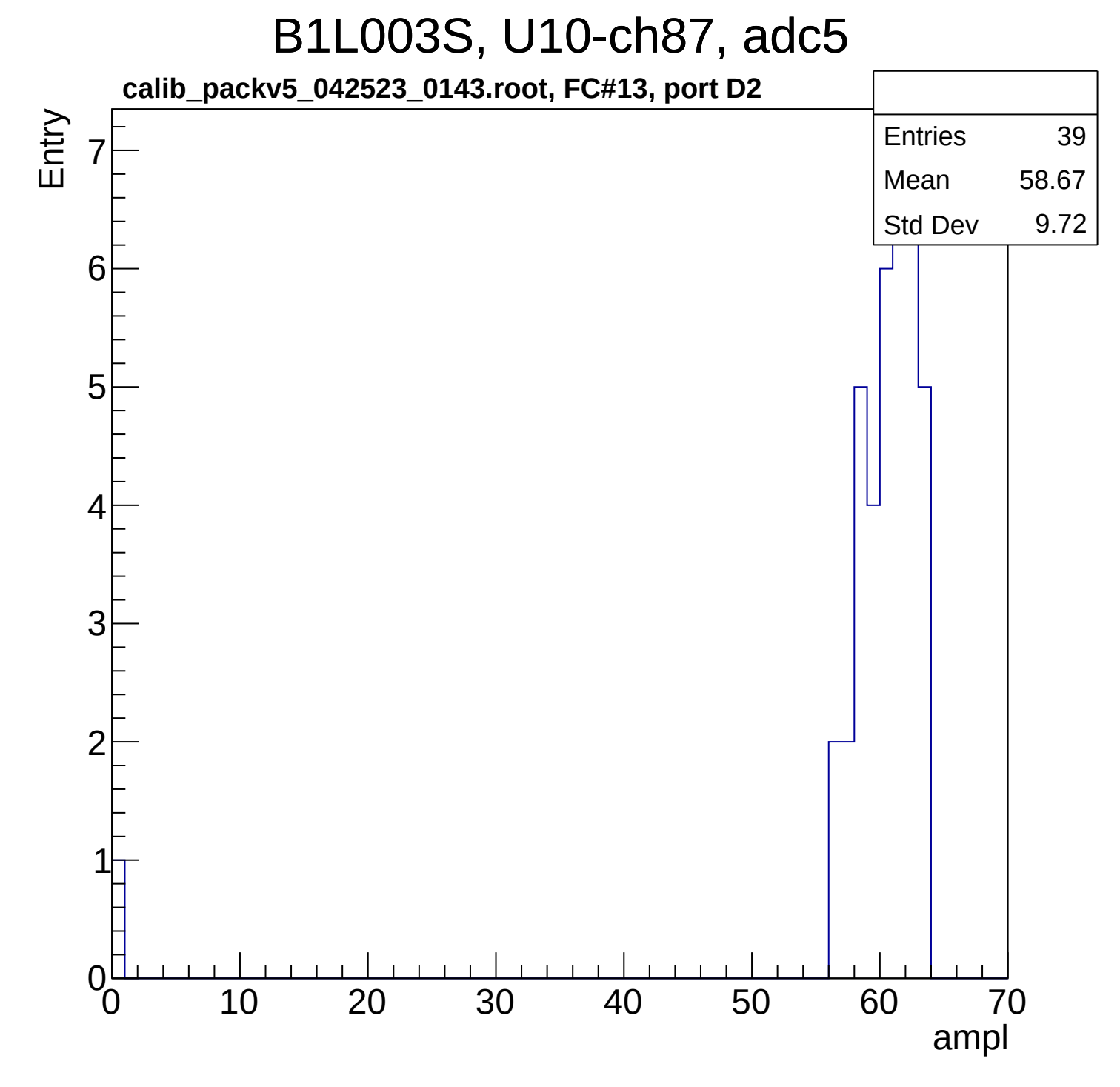
B1L003S, U10-ch87, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 58.67
Std Dev 9.72
Entry
ampl
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3
4
5
6
7
0
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20
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40
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60
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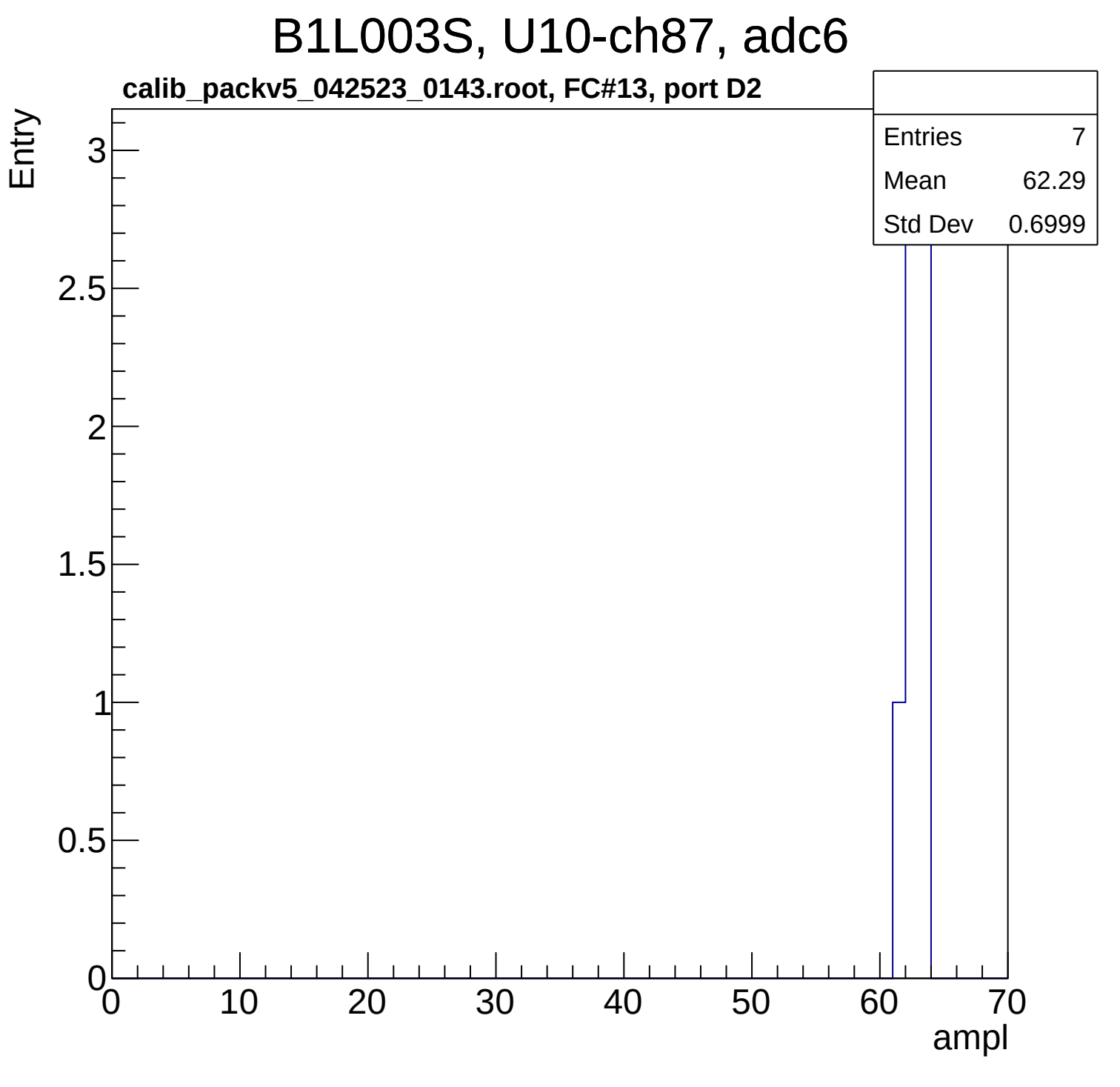
B1L003S, U10-ch87, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
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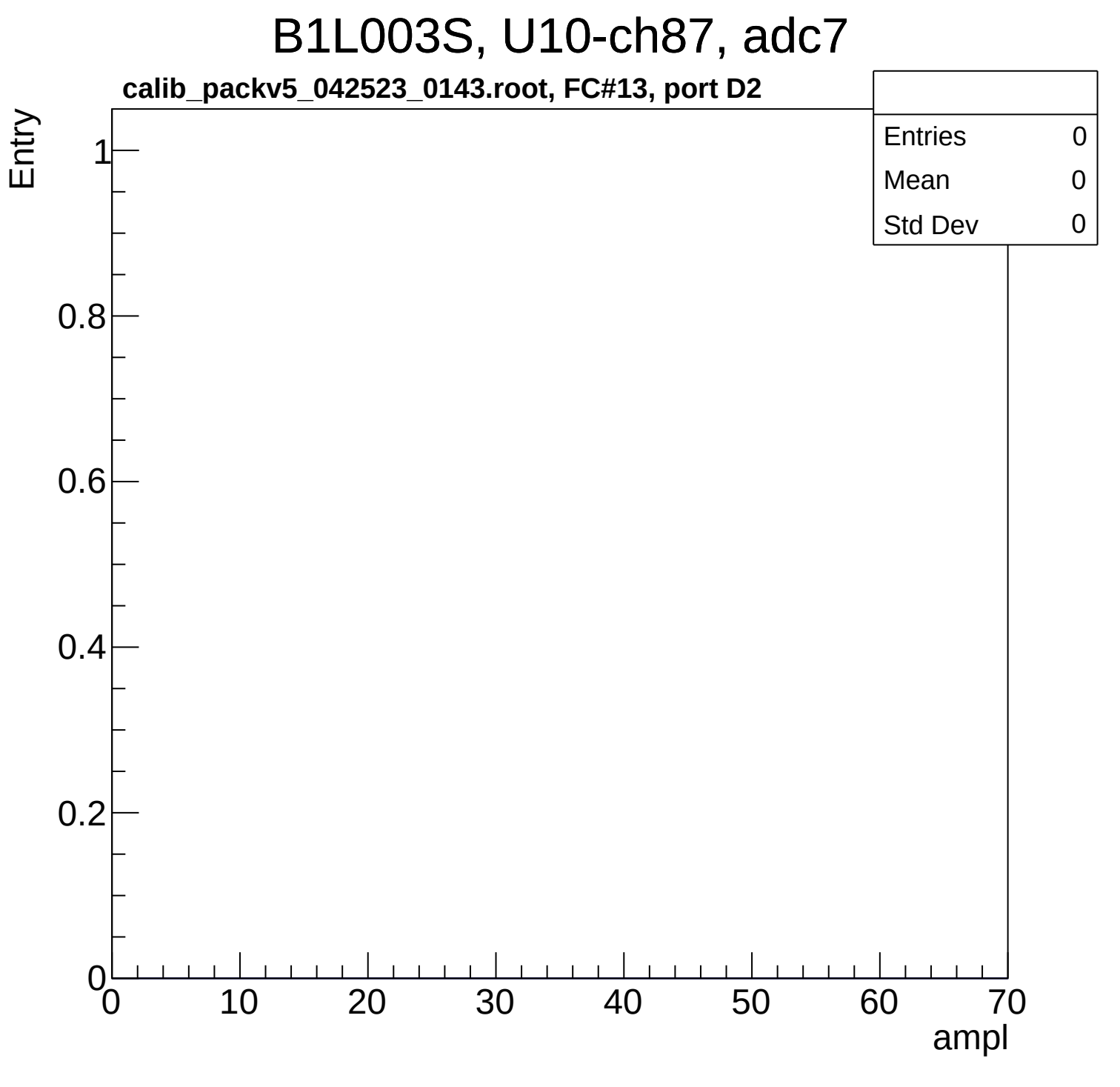
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch87, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
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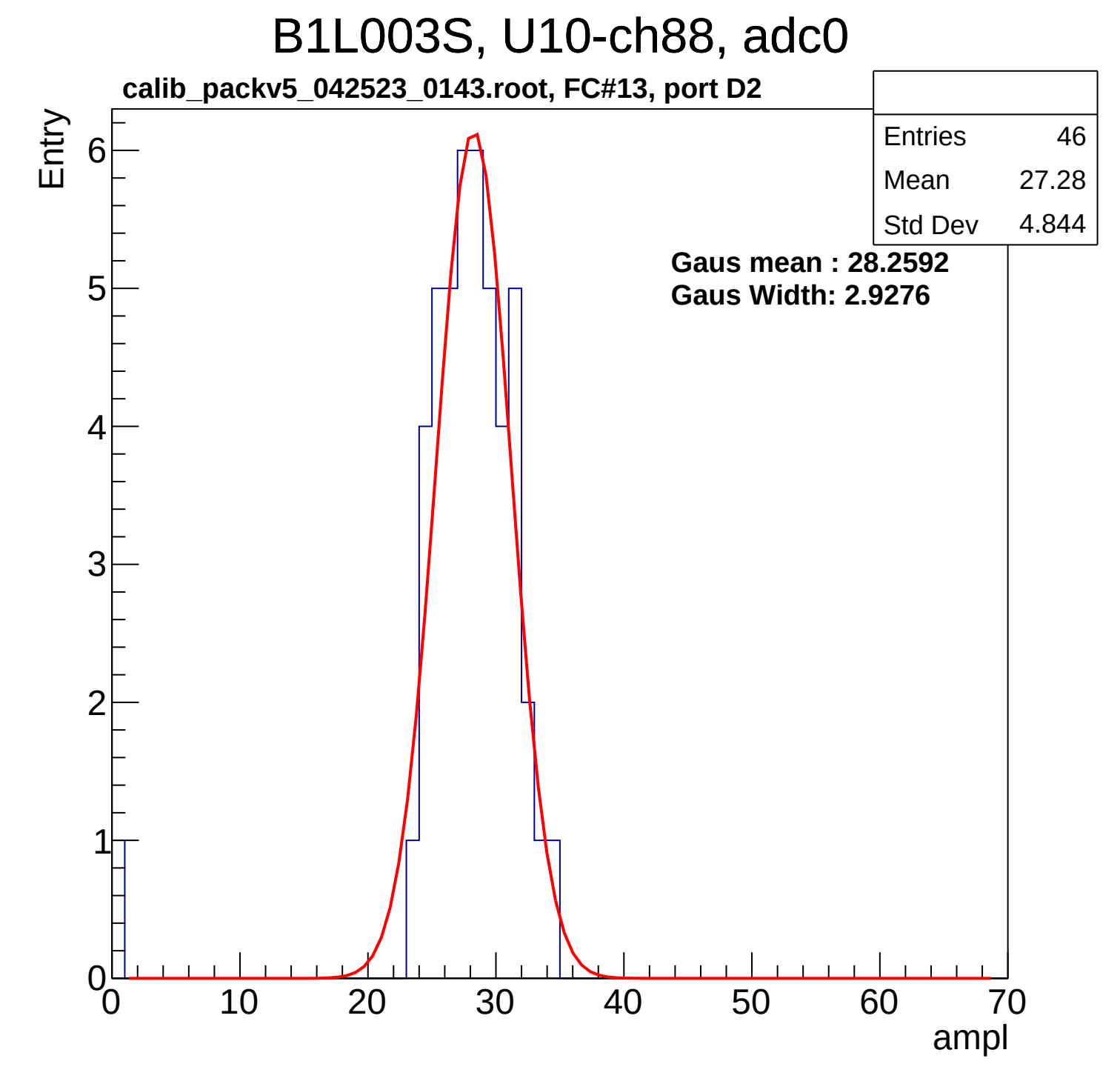
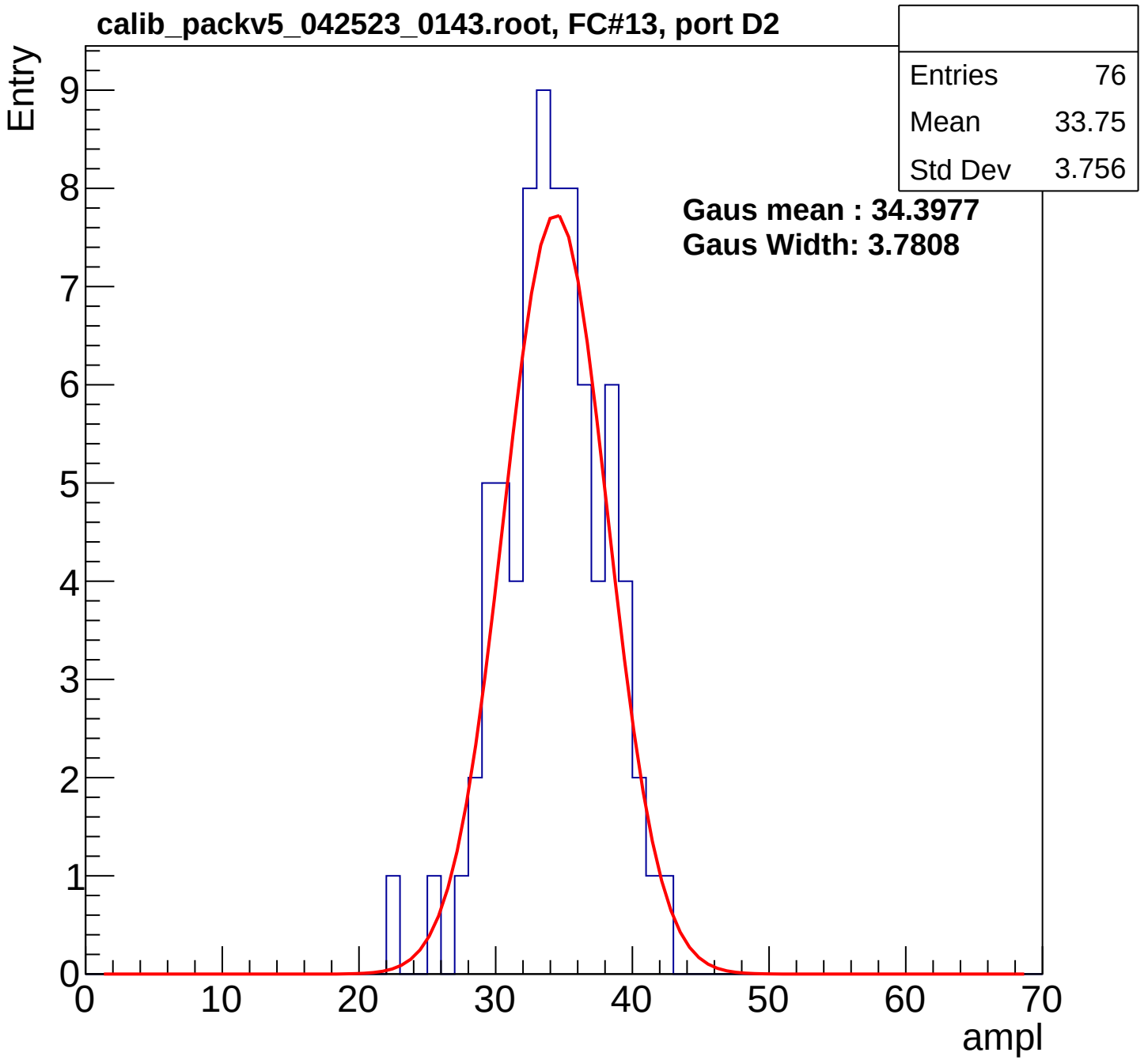
50
60
70
ampl


B1L003S, U10-ch88, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 27.28
Std Dev 4.844
Gaus mean : 28.2592
Gaus Width: 2.9276
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U10-ch88, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 33.75
Std Dev 3.756
Gaus mean : 34.3977
Gaus Width: 3.7808
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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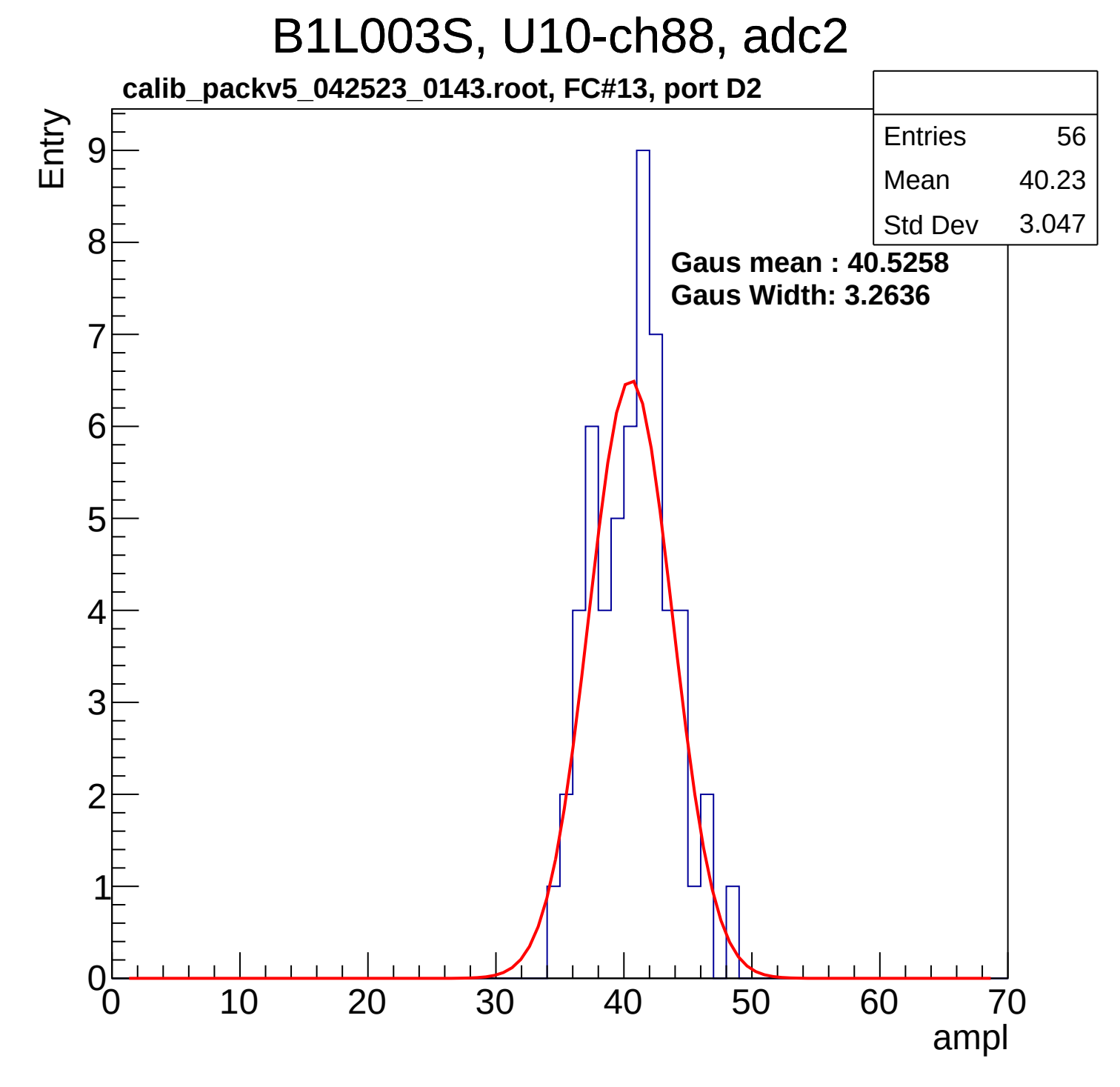
B1L003S, U10-ch88, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 40.23
Std Dev 3.047
Gaus mean : 40.5258
Gaus Width: 3.2636
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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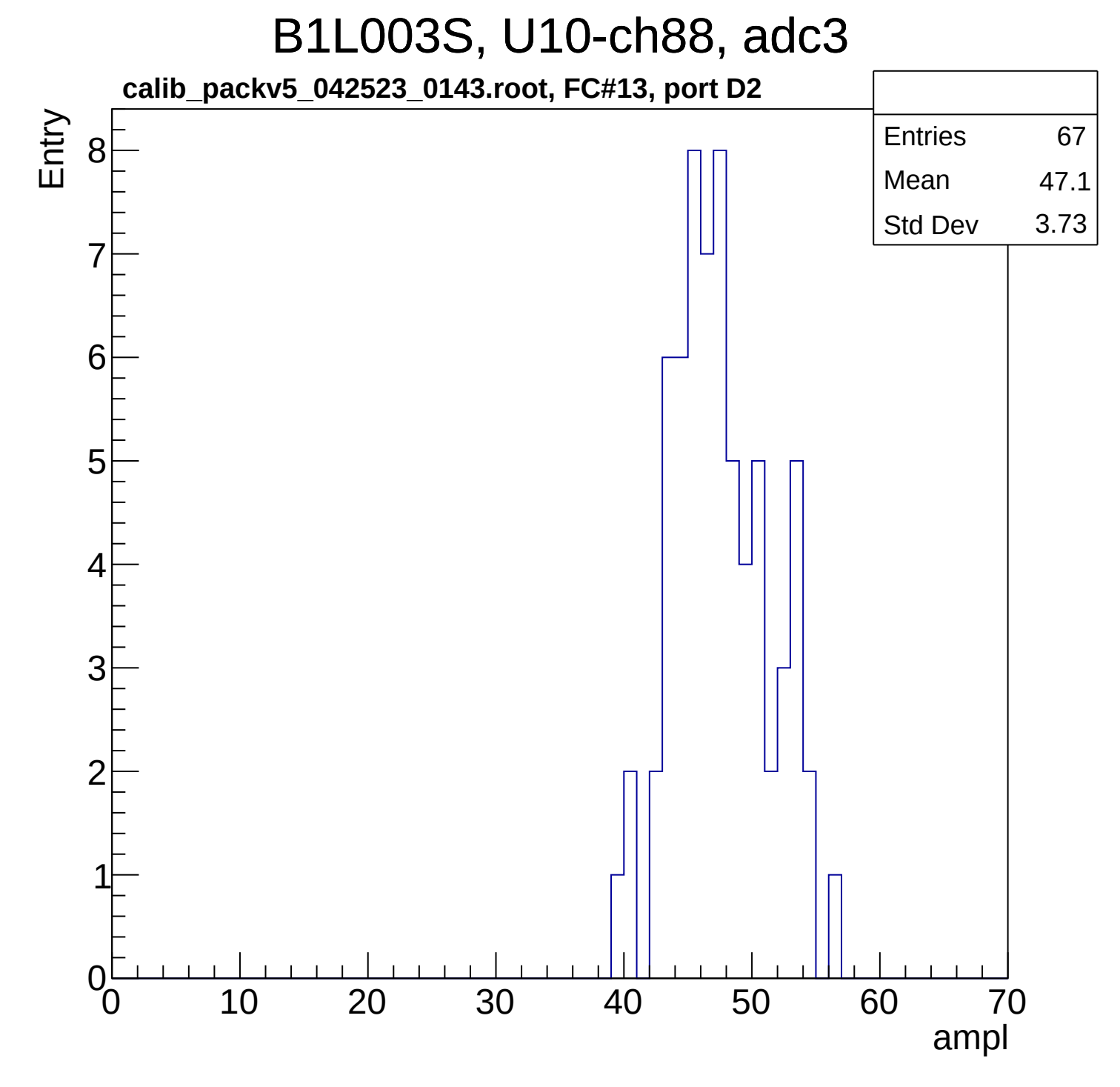
B1L003S, U10-ch88, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 47.1
Std Dev 3.73
Entry
ampl
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3
4
5
6
7
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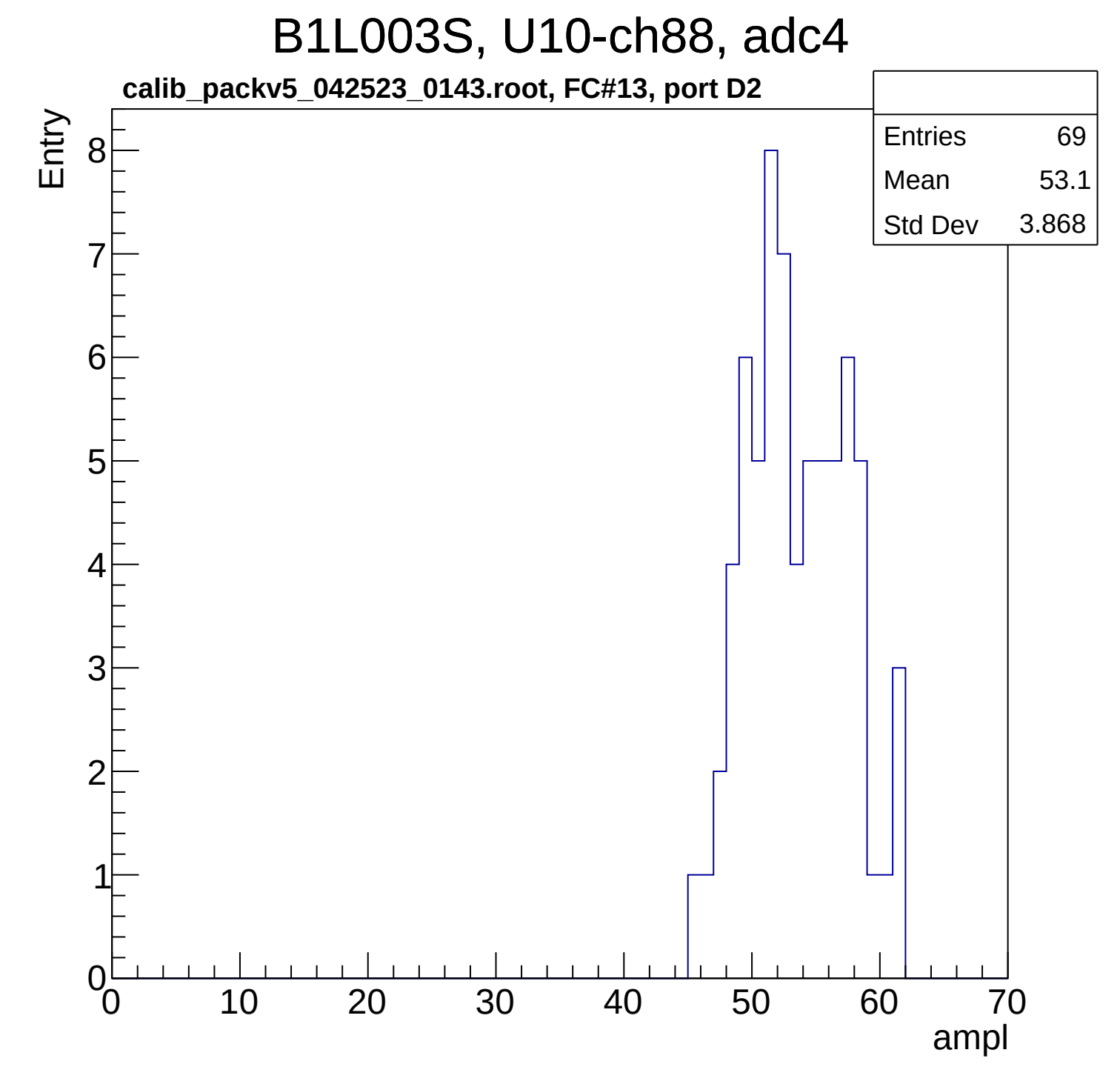
B1L003S, U10-ch88, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 53.1
Std Dev 3.868
Entry
ampl
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3
4
5
6
7
8
0
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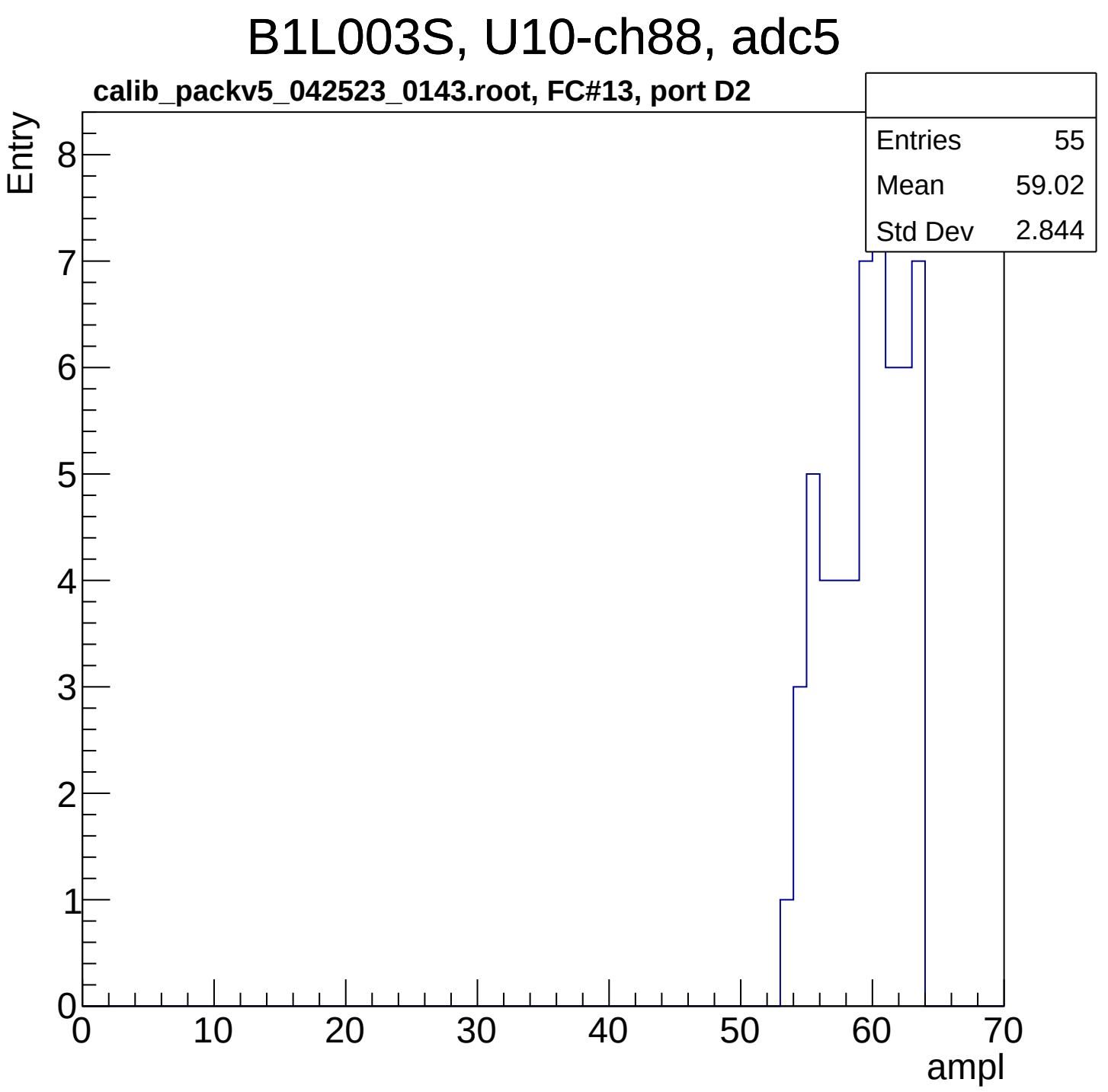
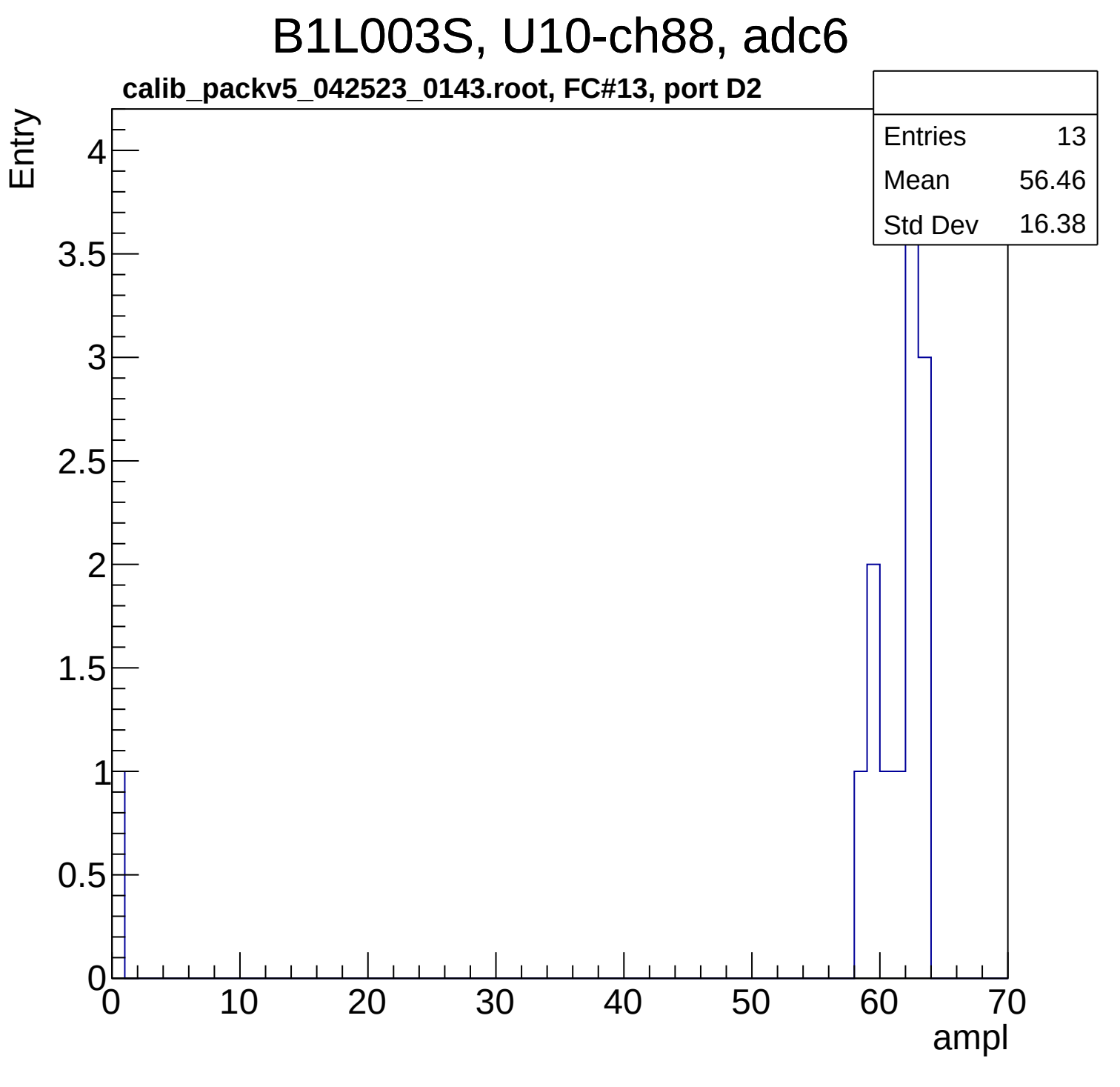

B1L003S, U10-ch88, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.02
Std Dev 2.844
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch88, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.46
Std Dev 16.38
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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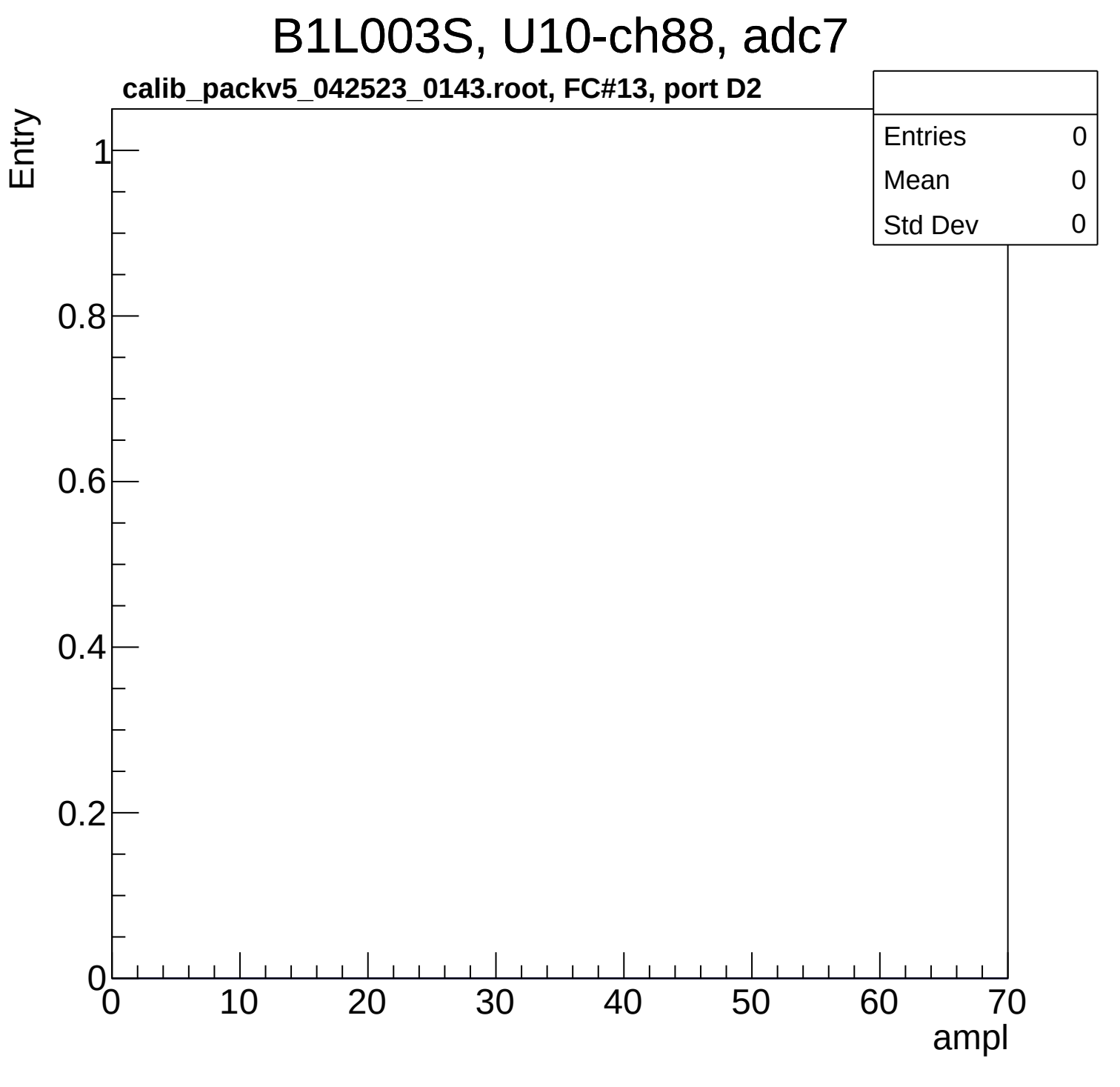
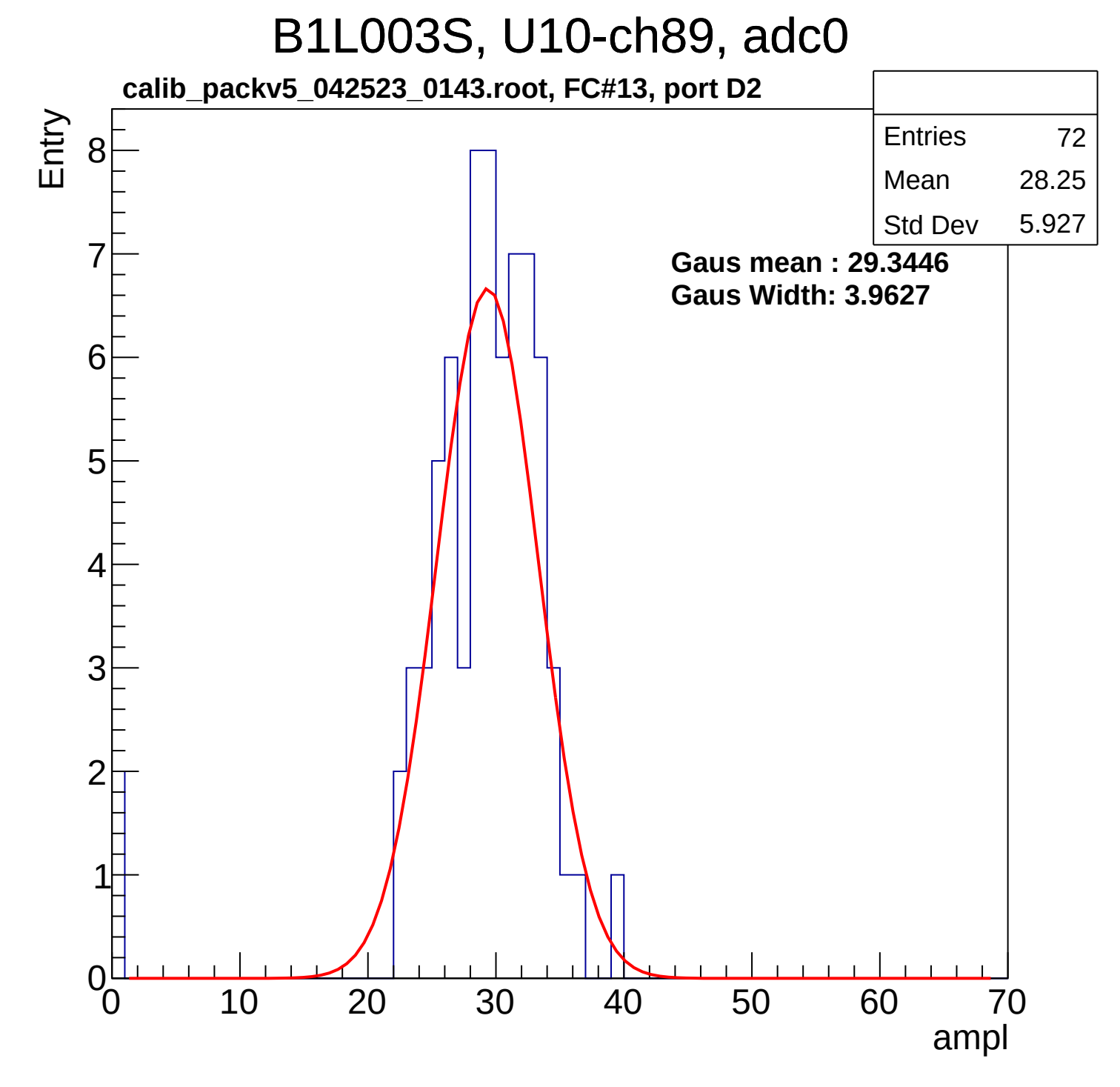

B1L003S, U10-ch88, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch89, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 28.25
Std Dev 5.927
Gaus mean : 29.3446
Gaus Width: 3.9627
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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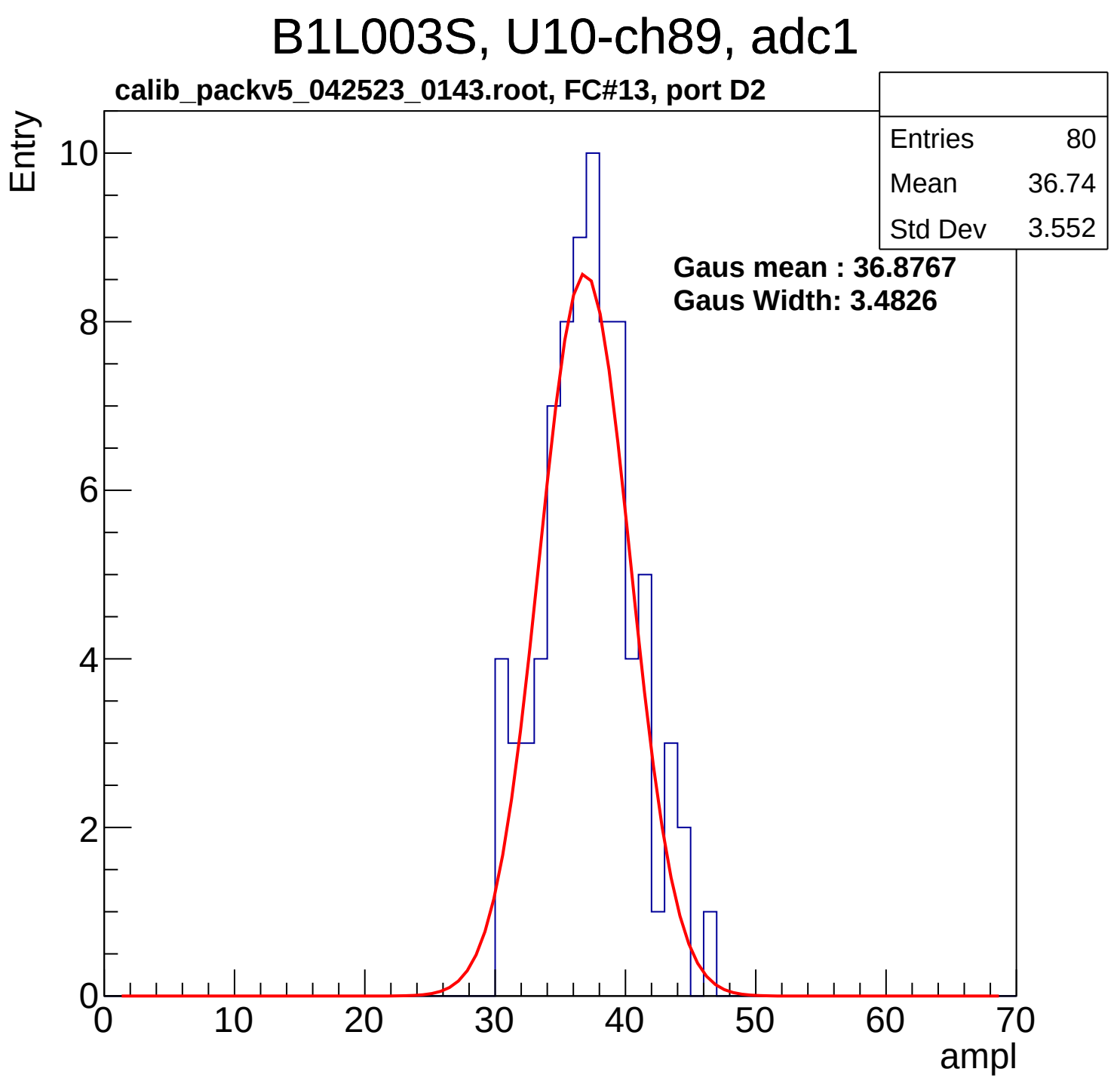
B1L003S, U10-ch89, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 36.74
Std Dev 3.552
Gaus mean : 36.8767
Gaus Width: 3.4826
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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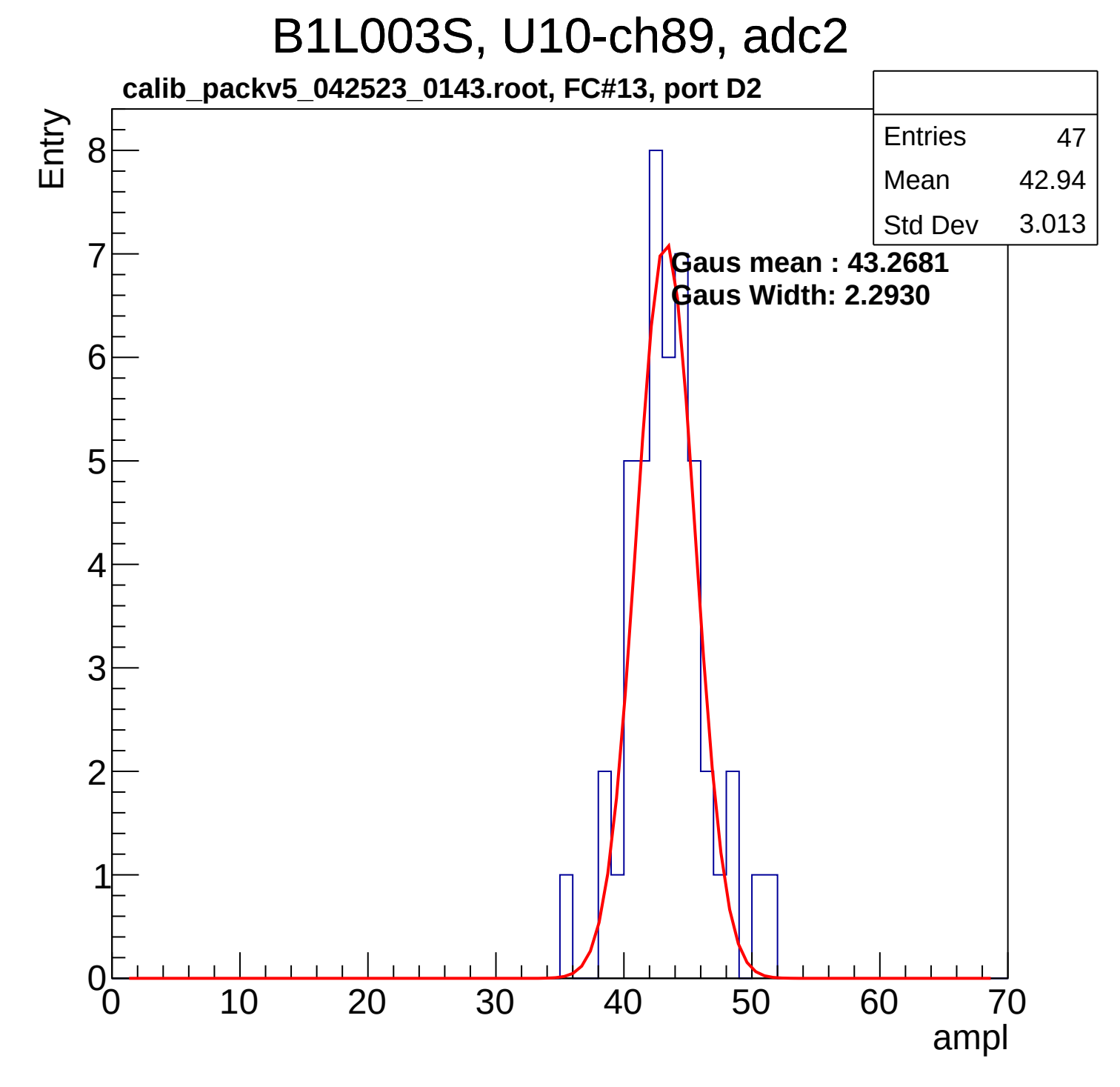
B1L003S, U10-ch89, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 42.94
Std Dev 3.013
Gaus mean : 43.2681
Gaus Width: 2.2930
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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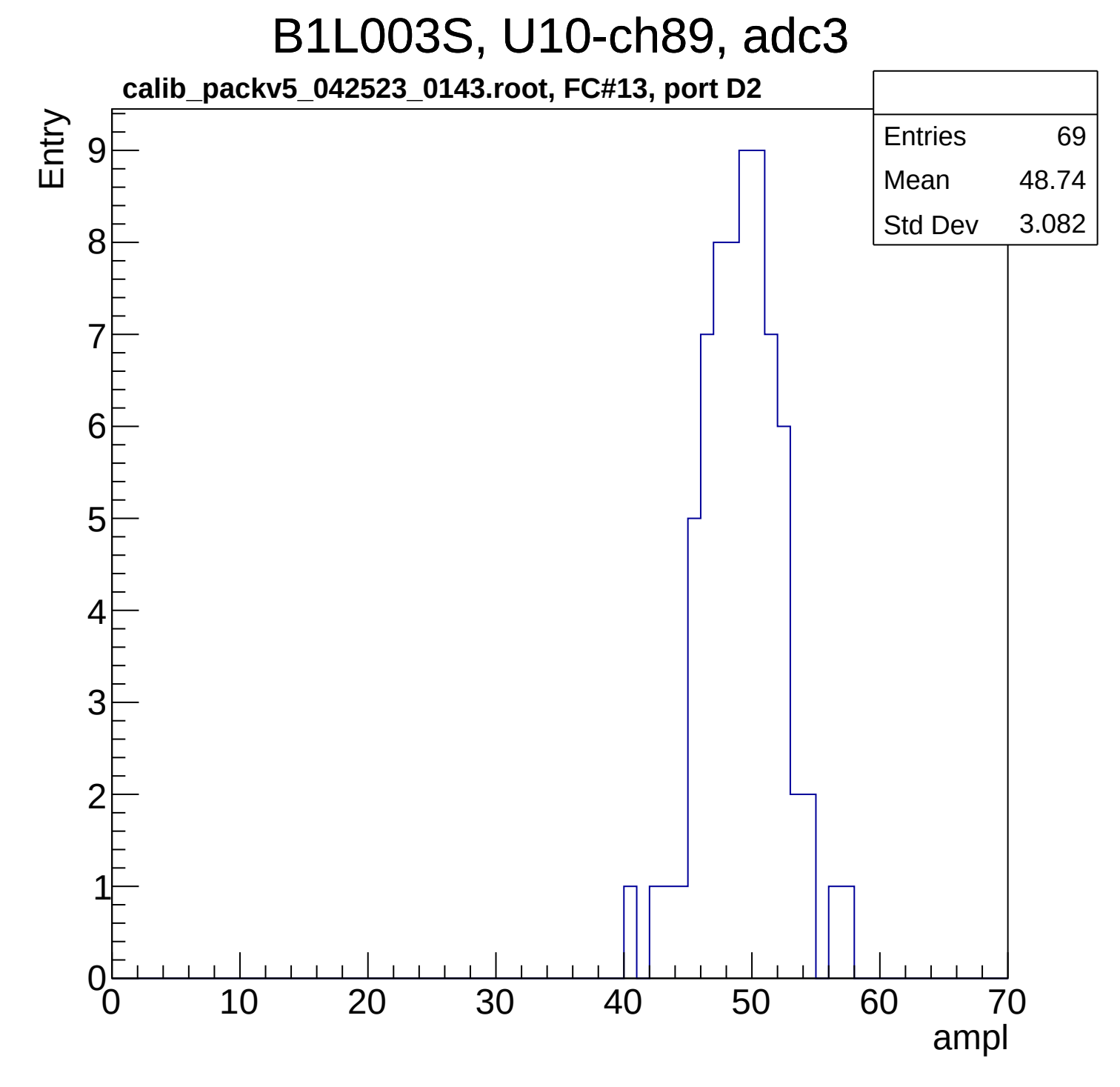
B1L003S, U10-ch89, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.74
Std Dev 3.082
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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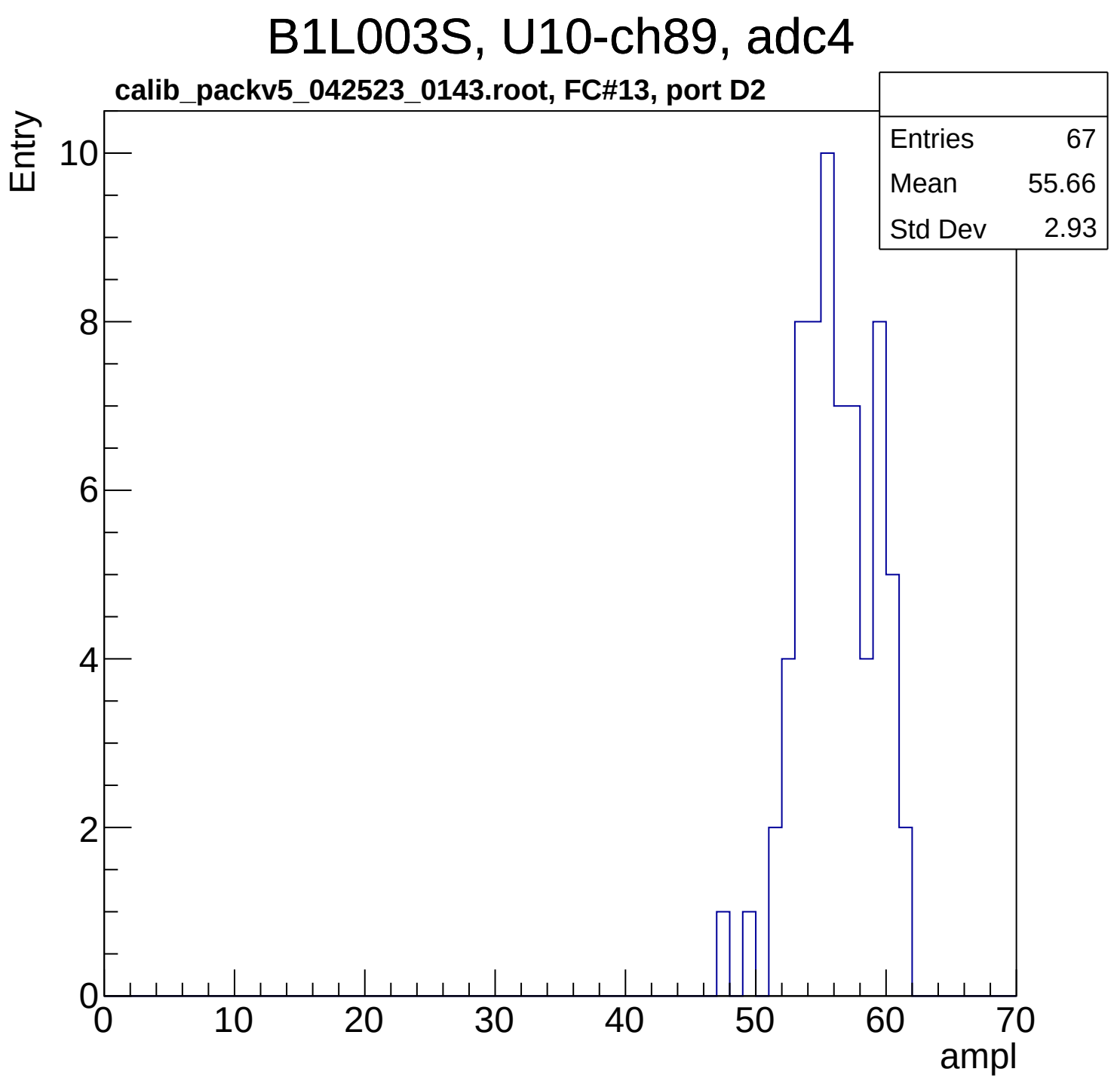
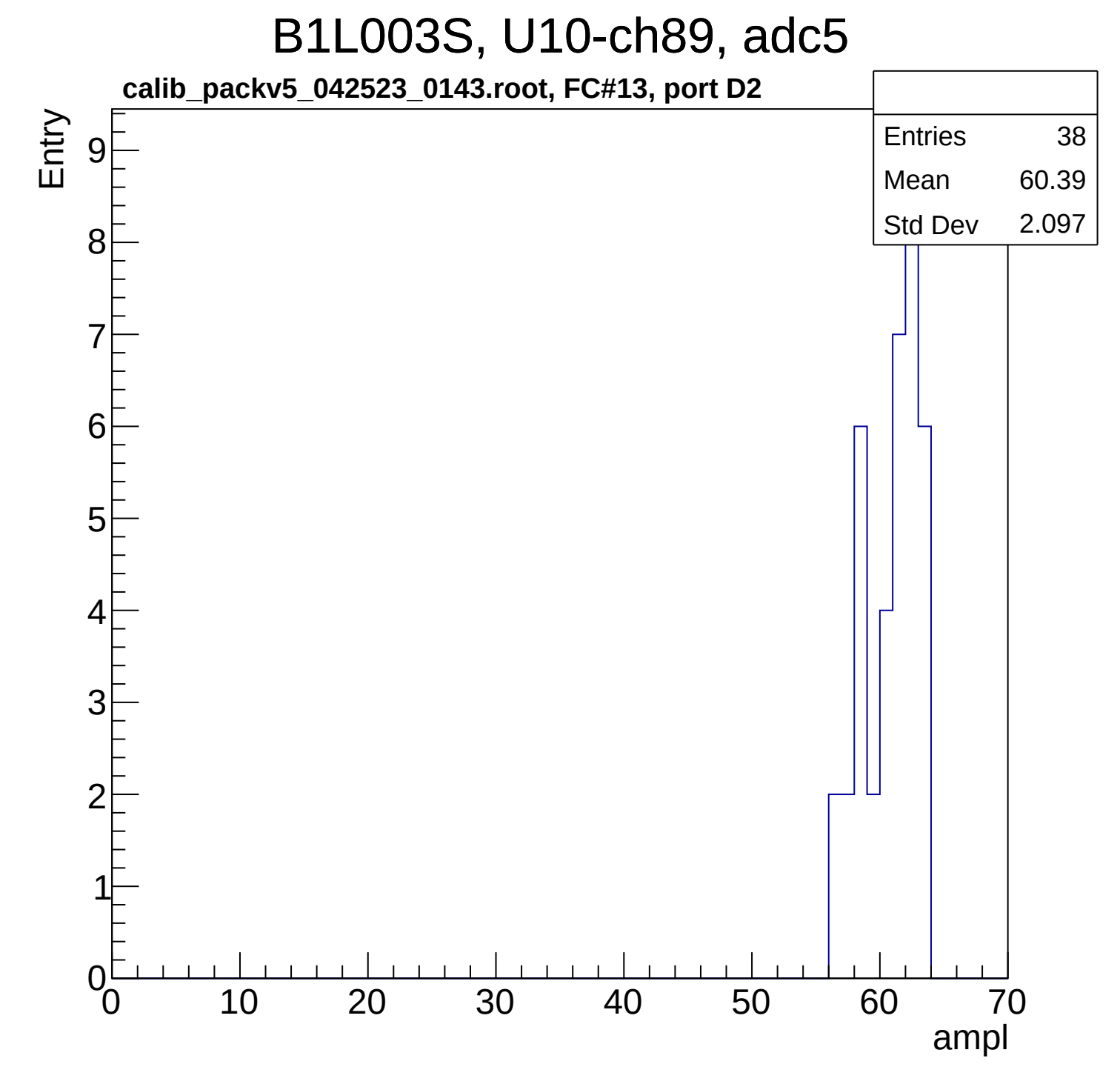

B1L003S, U10-ch89, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 55.66
Std Dev 2.93
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch89, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 60.39
Std Dev 2.097
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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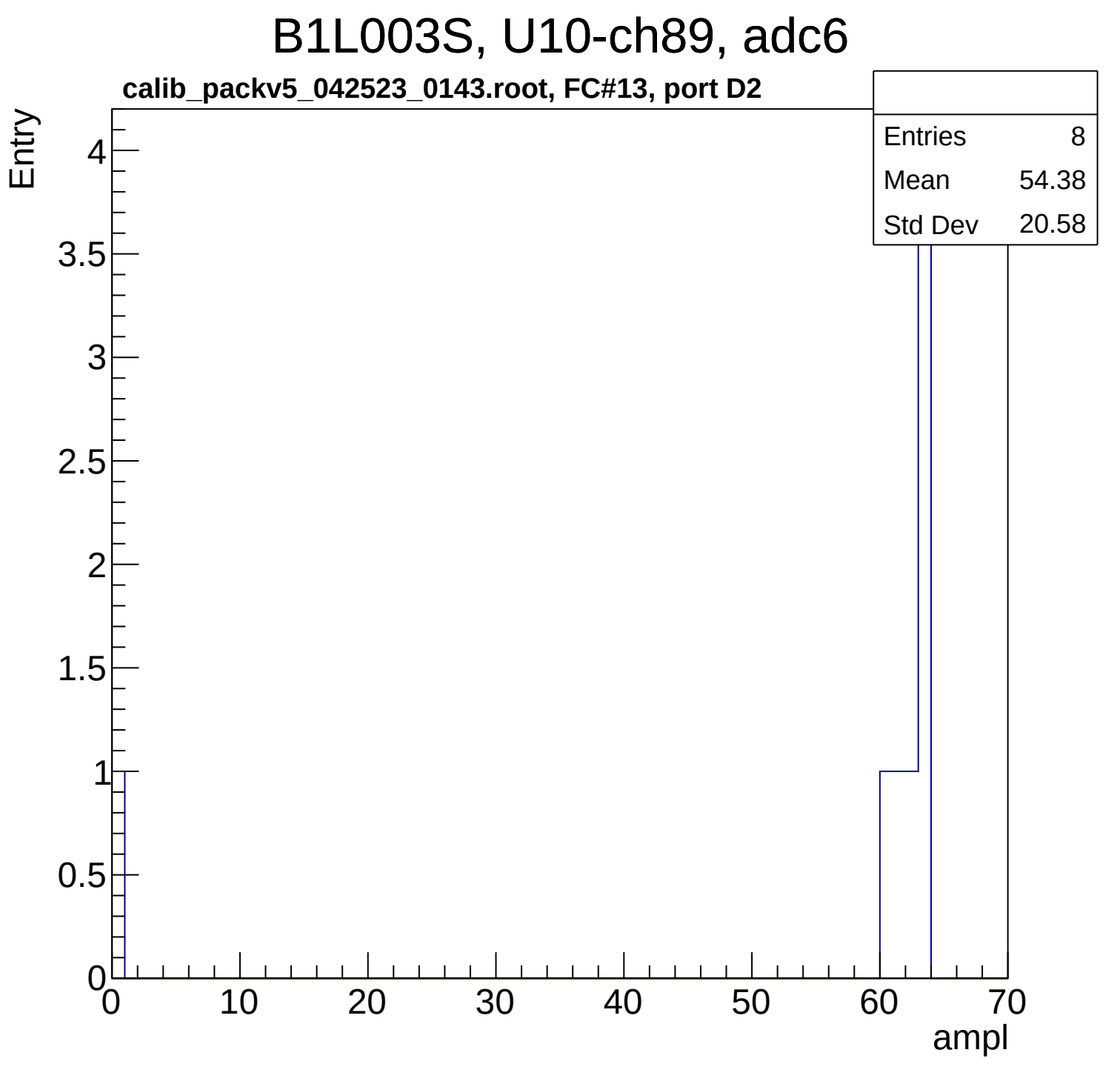
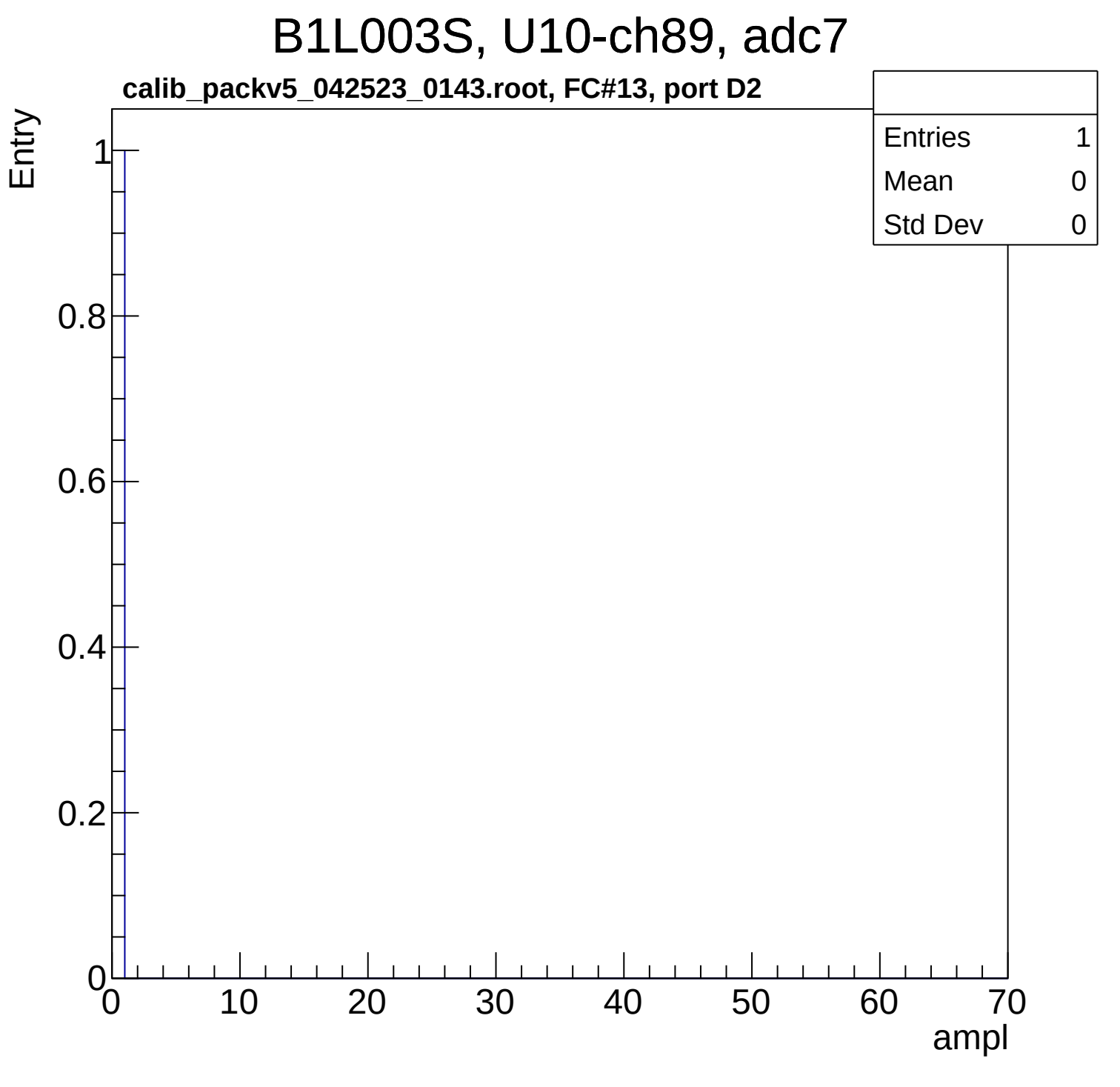
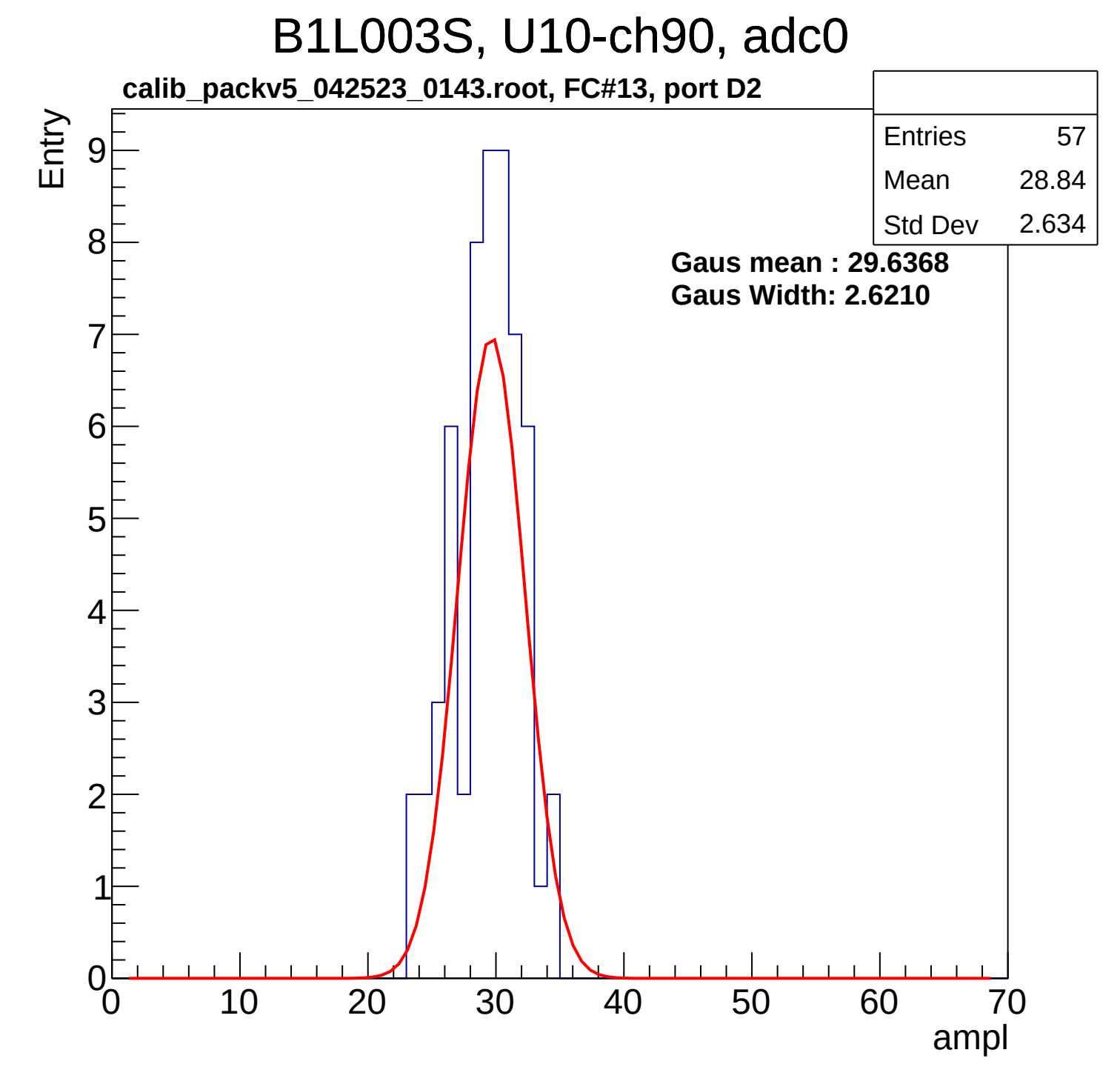

B1L003S, U10-ch89, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.38
Std Dev 20.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch89, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch90, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.84
Std Dev 2.634
Gaus mean : 29.6368
Gaus Width: 2.6210
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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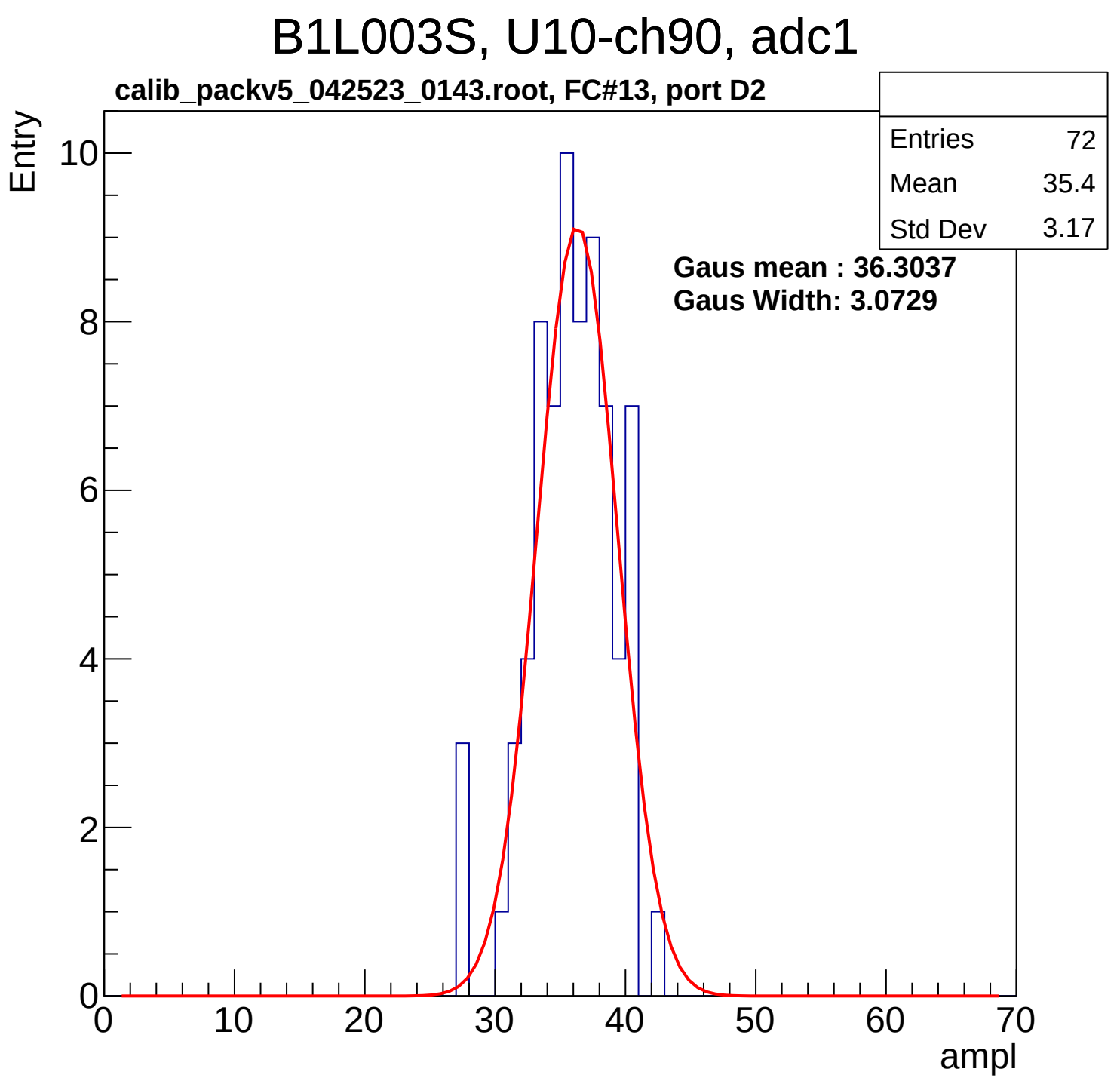
B1L003S, U10-ch90, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 35.4
Std Dev 3.17
Gaus mean : 36.3037
Gaus Width: 3.0729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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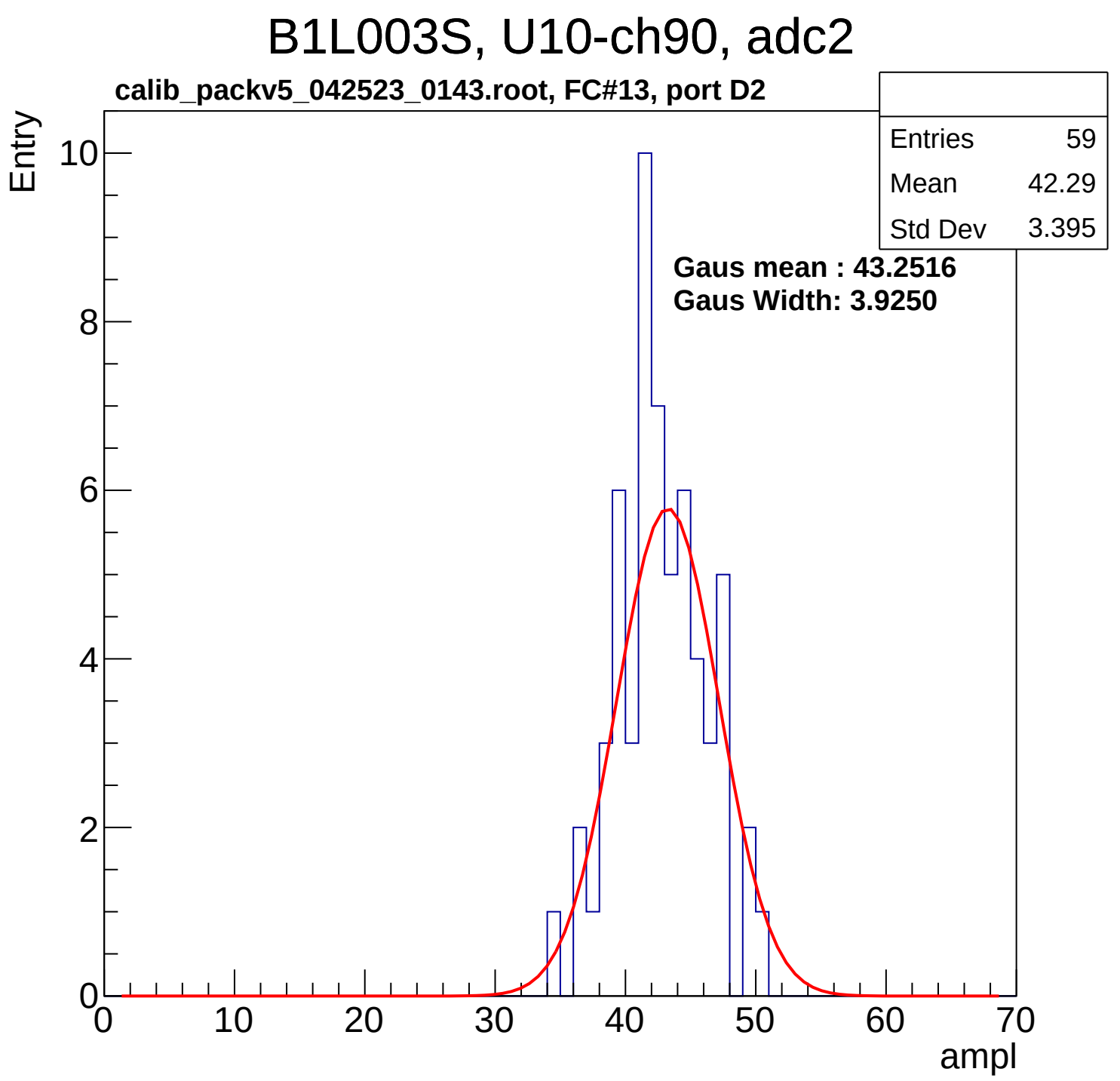
B1L003S, U10-ch90, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 42.29
Std Dev 3.395
Gaus mean : 43.2516
Gaus Width: 3.9250
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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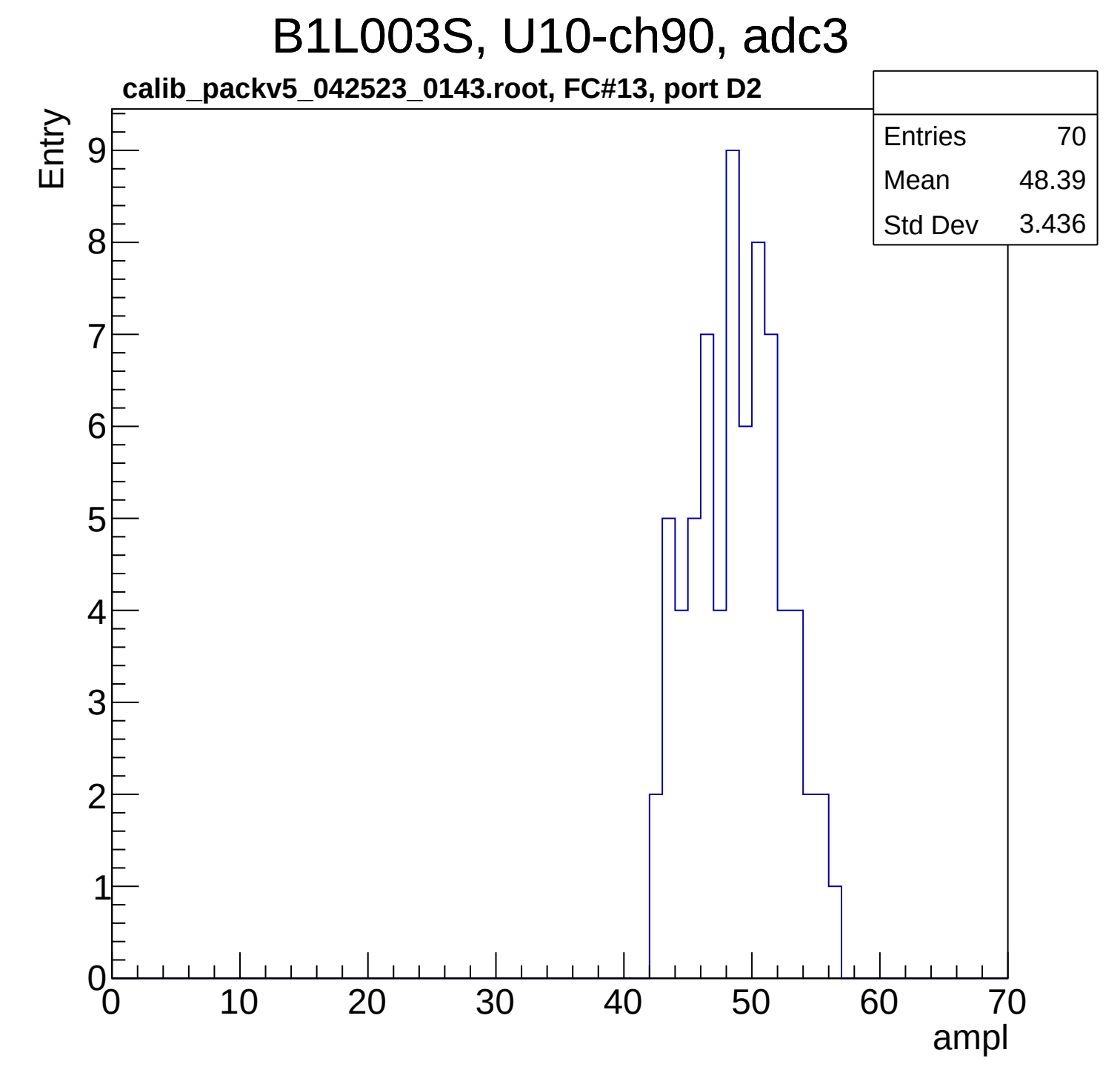
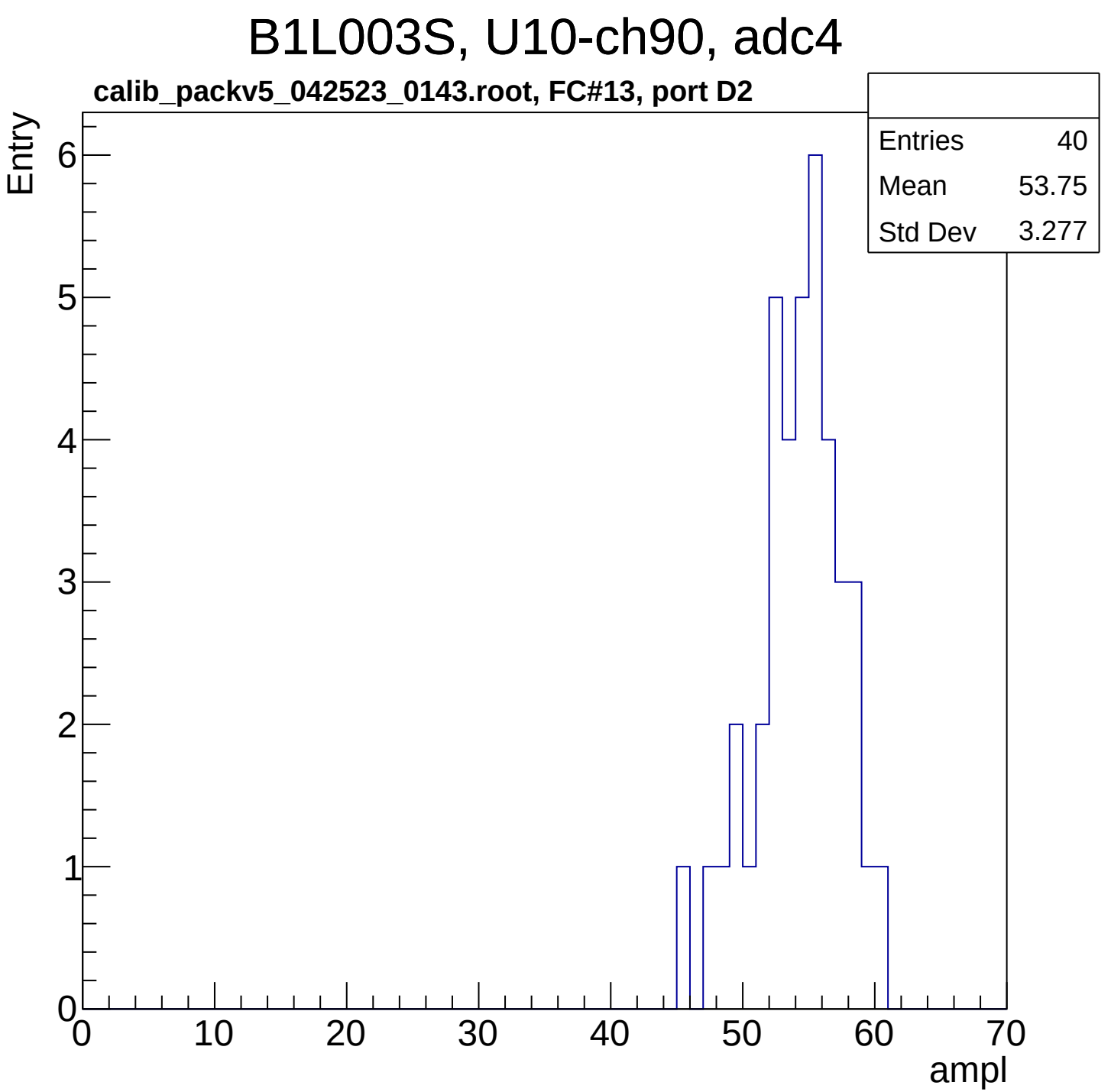
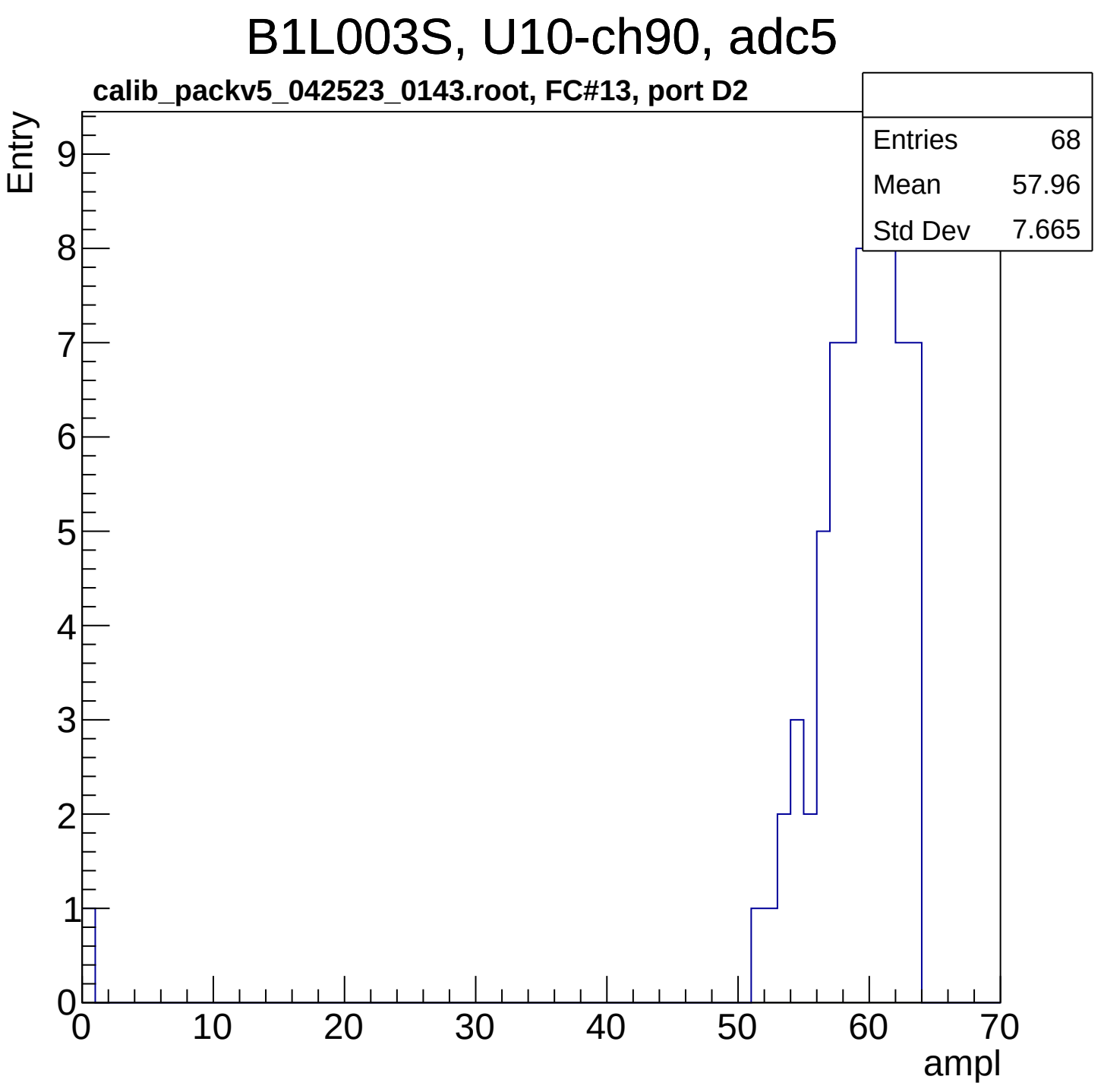
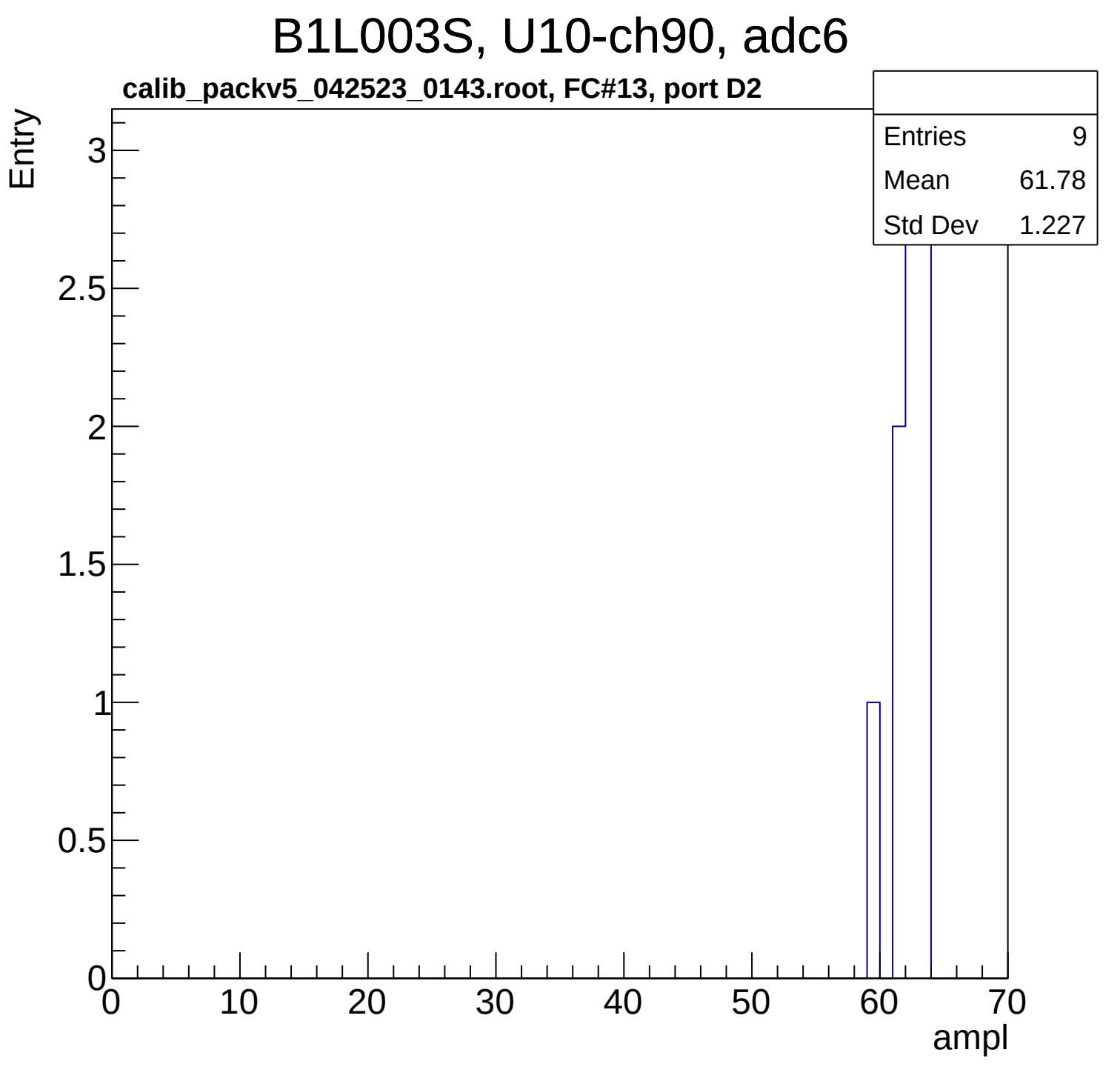

B1L003S, U10-ch90, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 48.39
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch90, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 53.75
Std Dev 3.277
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch90, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 57.96
Std Dev 7.665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch90, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.78
Std Dev 1.227
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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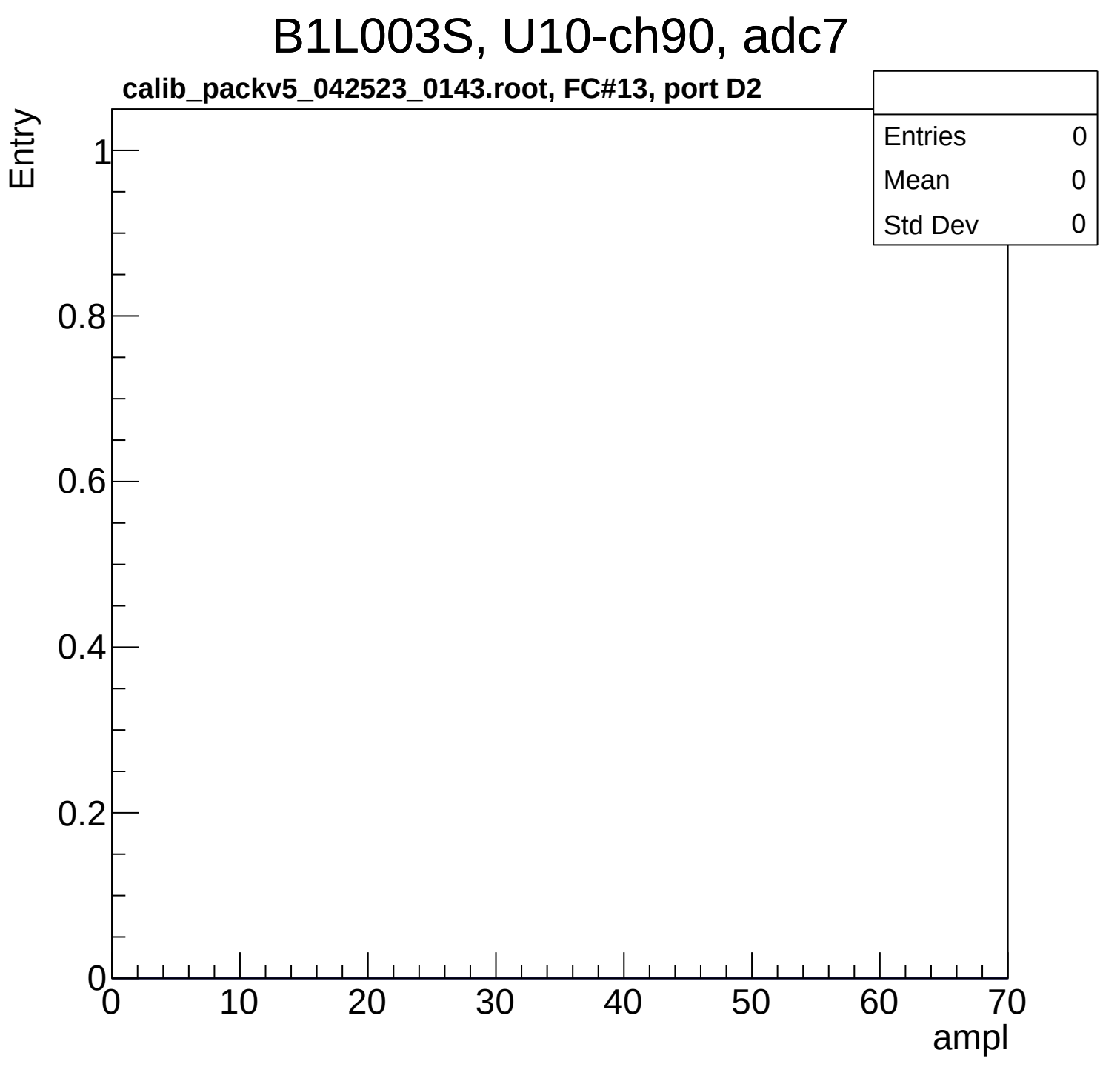
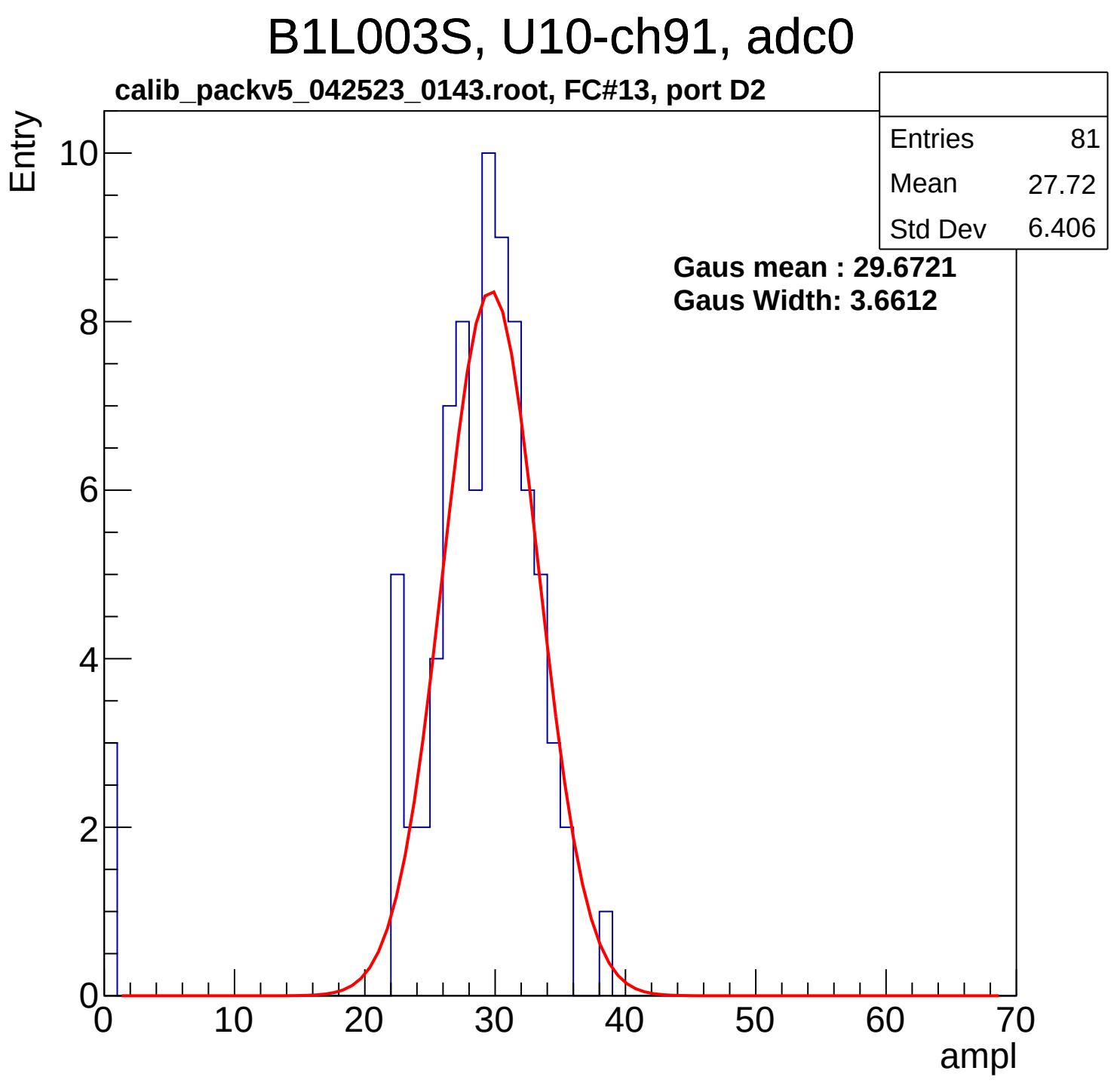

B1L003S, U10-ch90, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch91, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 27.72
Std Dev 6.406
Gaus mean : 29.6721
Gaus Width: 3.6612
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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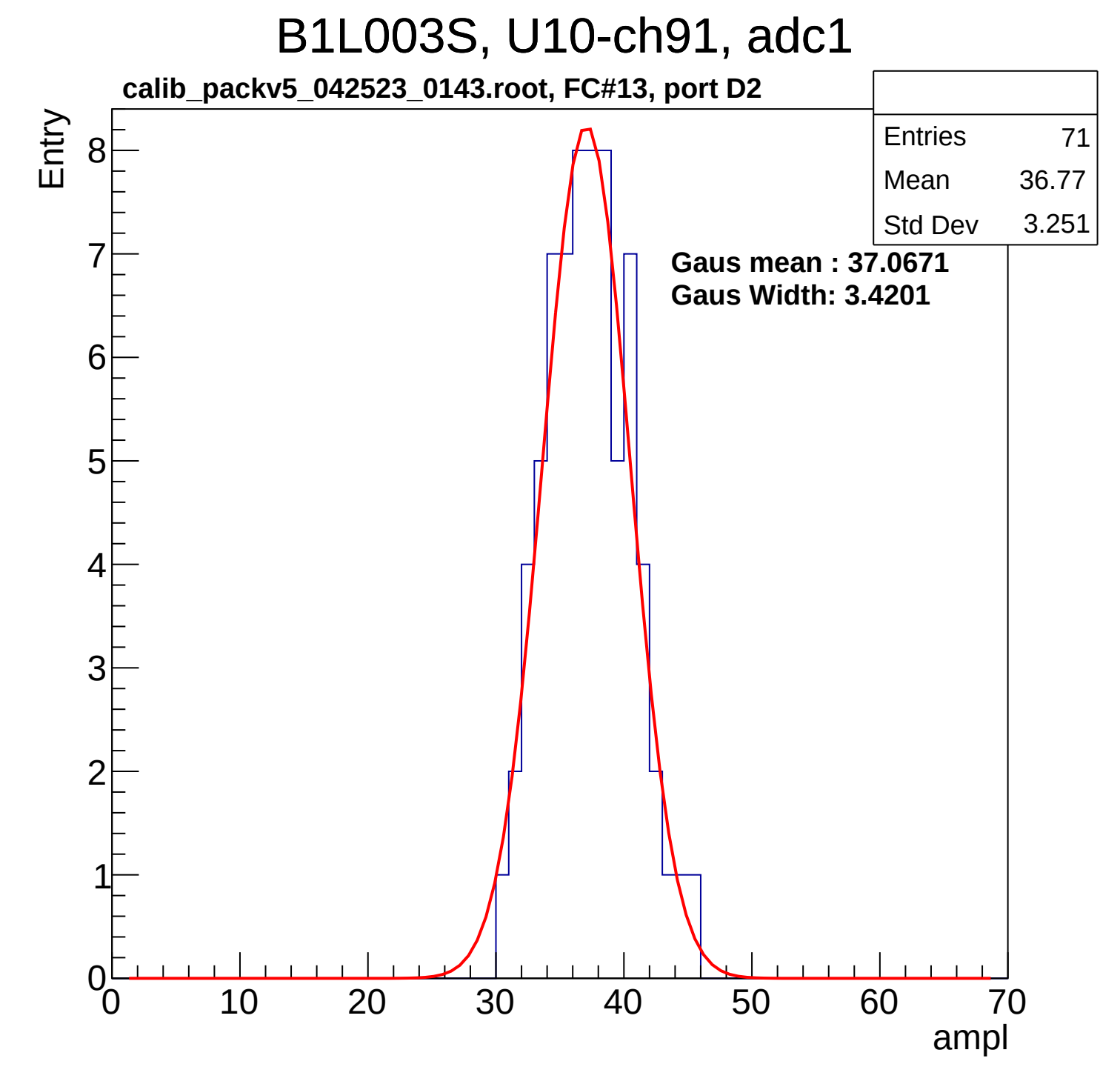
B1L003S, U10-ch91, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.77
Std Dev 3.251
Gaus mean : 37.0671
Gaus Width: 3.4201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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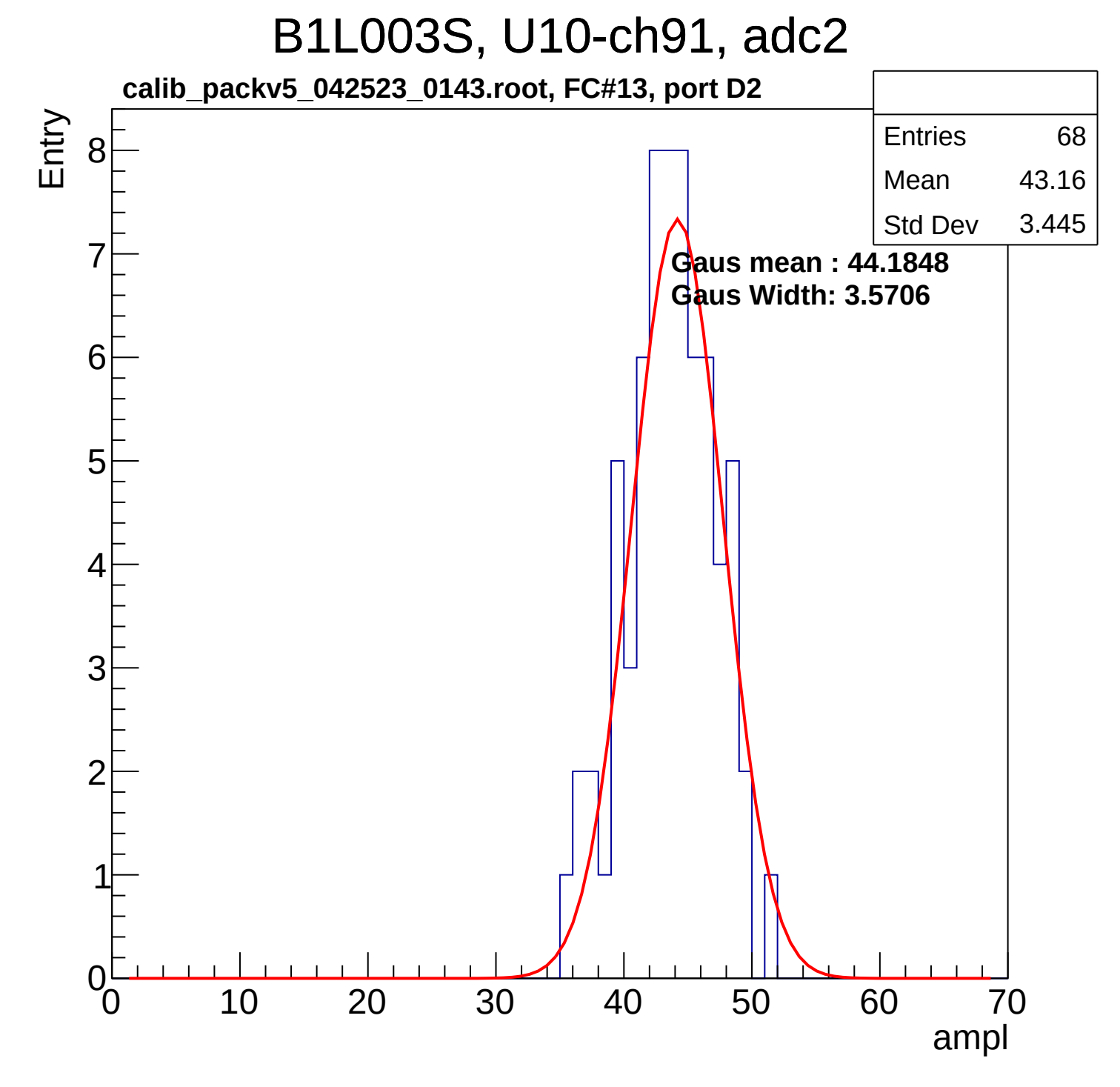
20
30
40
50
60
70


B1L003S, U10-ch91, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 43.16
Std Dev 3.445
Gaus mean : 44.1848
Gaus Width: 3.5706
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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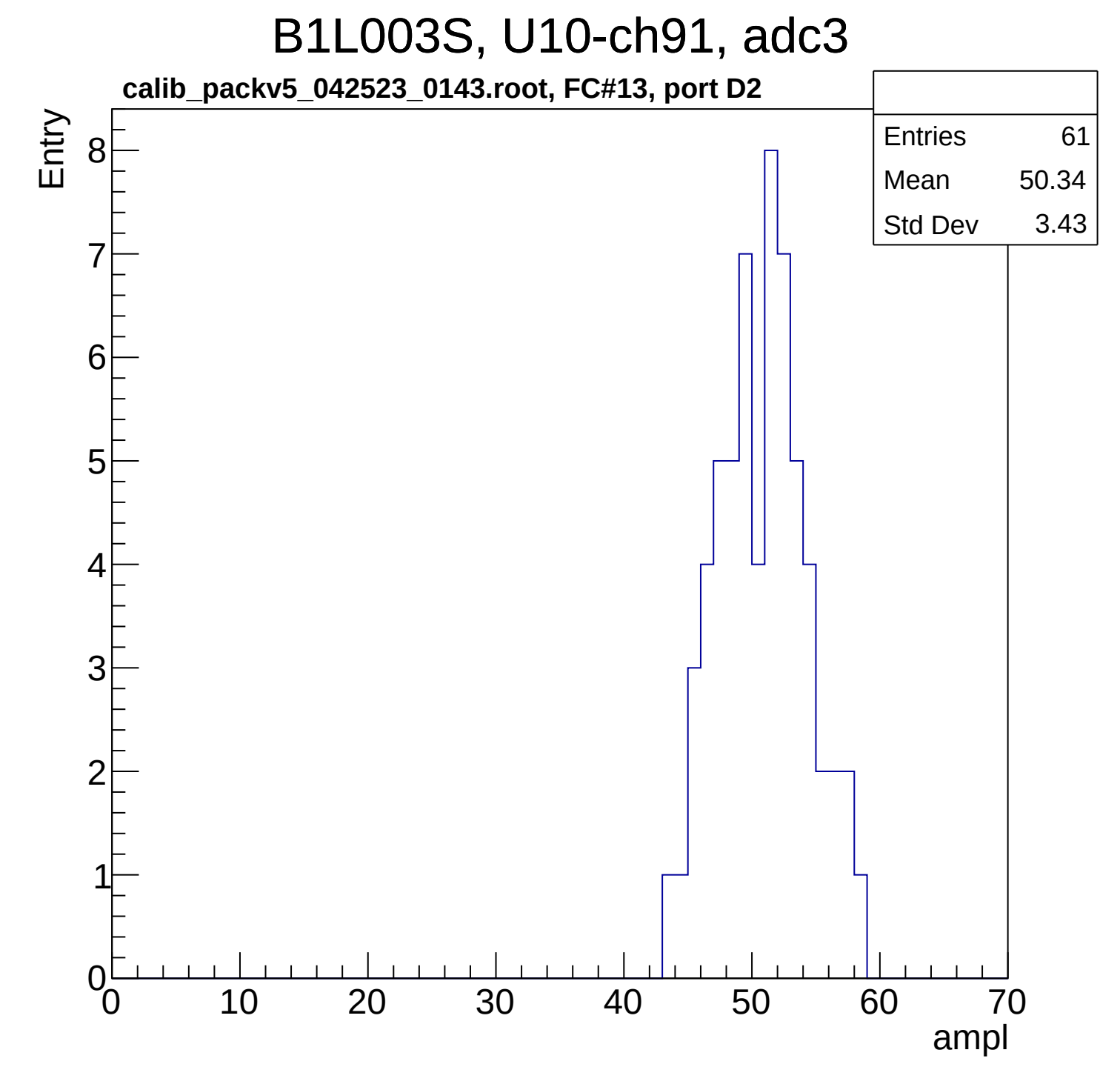
B1L003S, U10-ch91, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.34
Std Dev 3.43
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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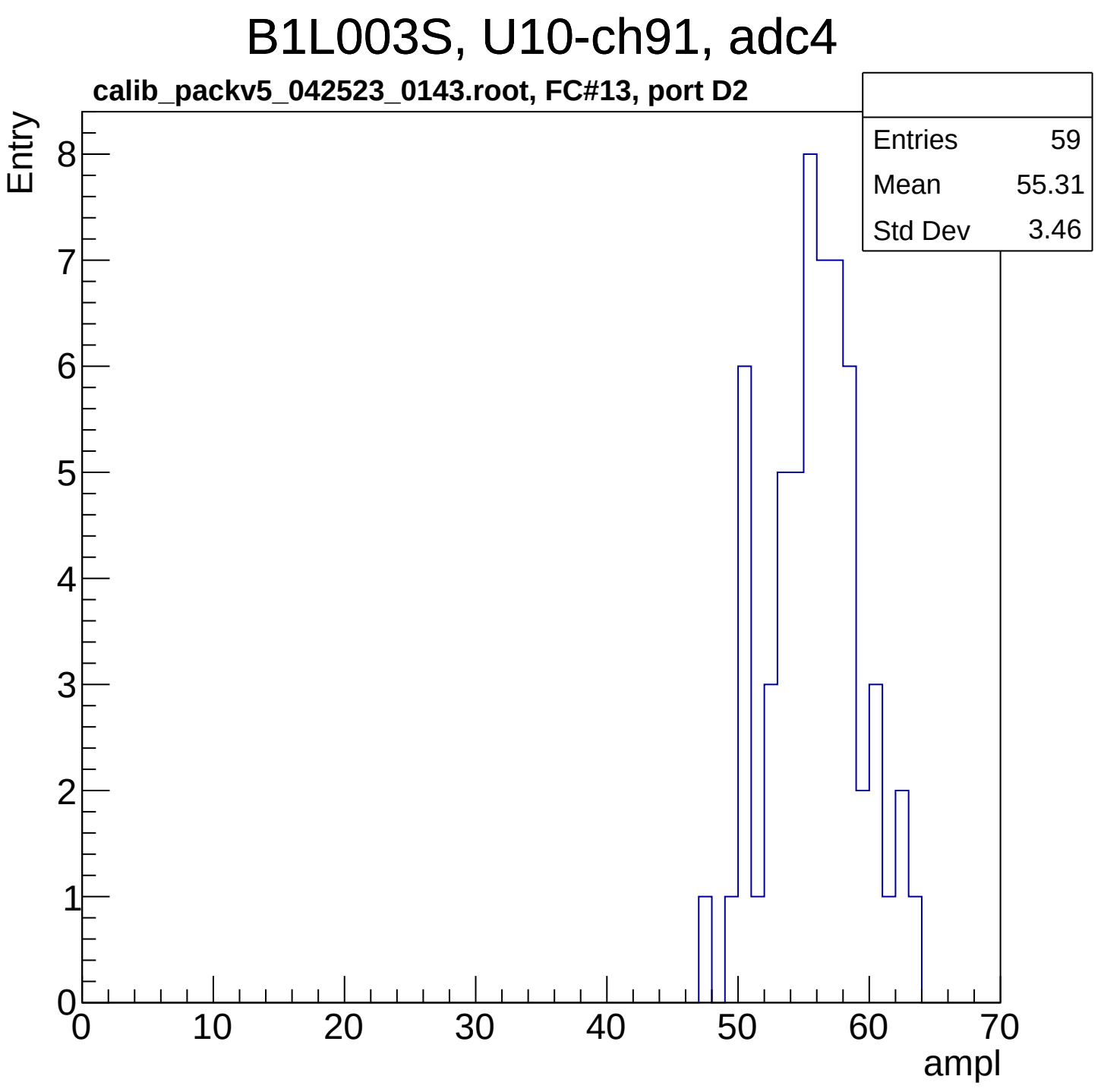
40
50
60
70


B1L003S, U10-ch91, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.31
Std Dev 3.46
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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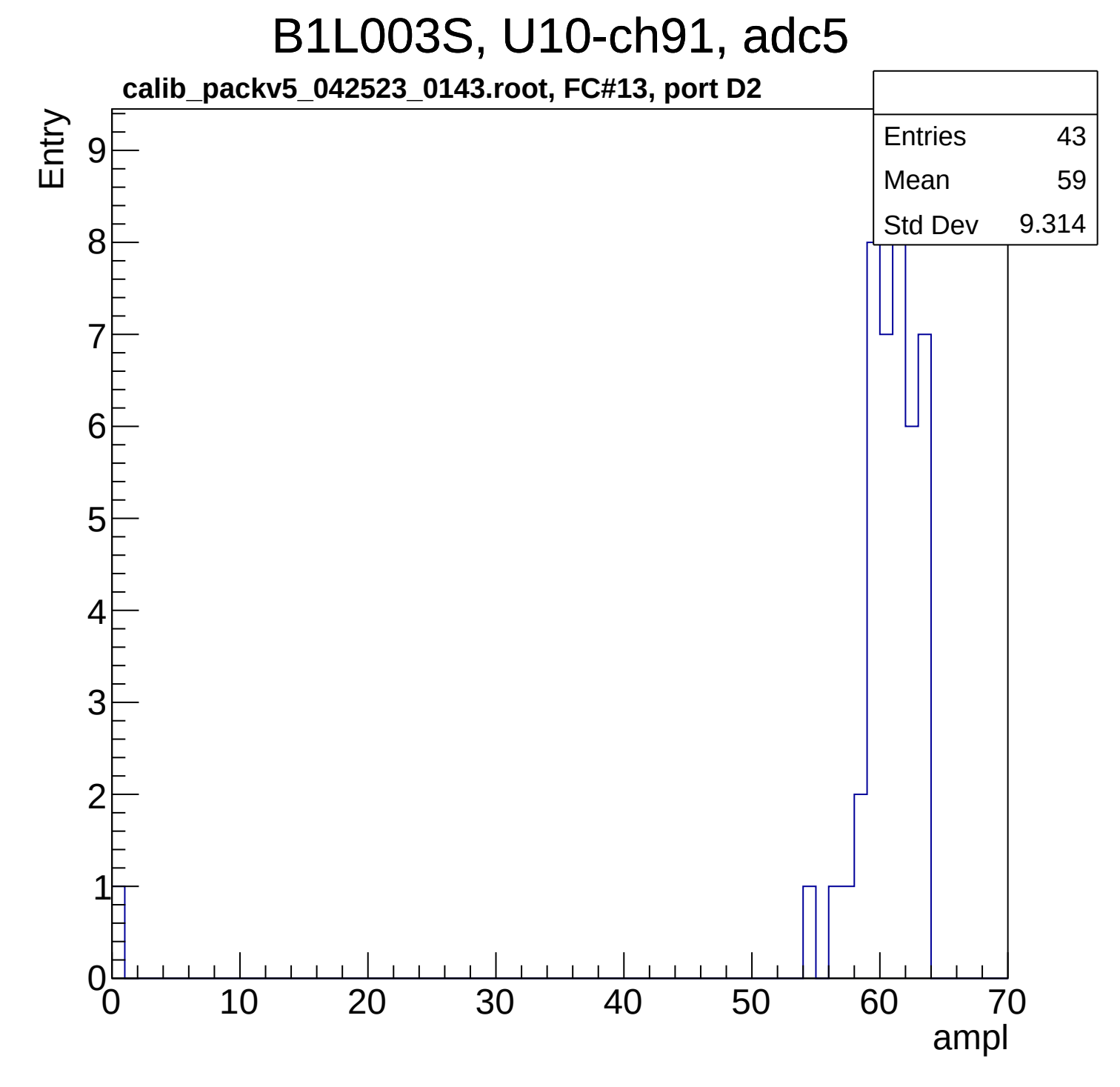
B1L003S, U10-ch91, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59
Std Dev 9.314
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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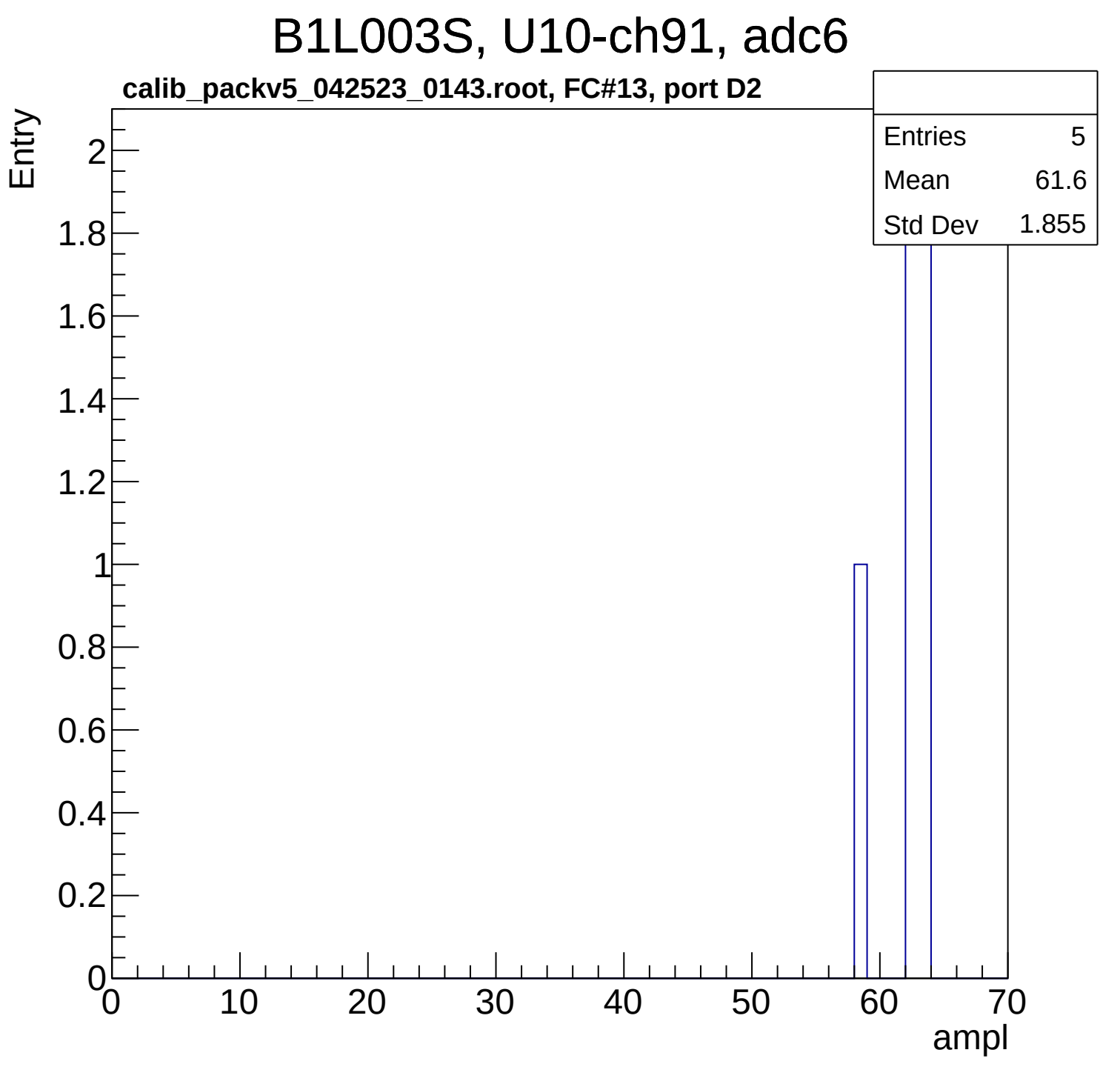
B1L003S, U10-ch91, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.6
Std Dev 1.855
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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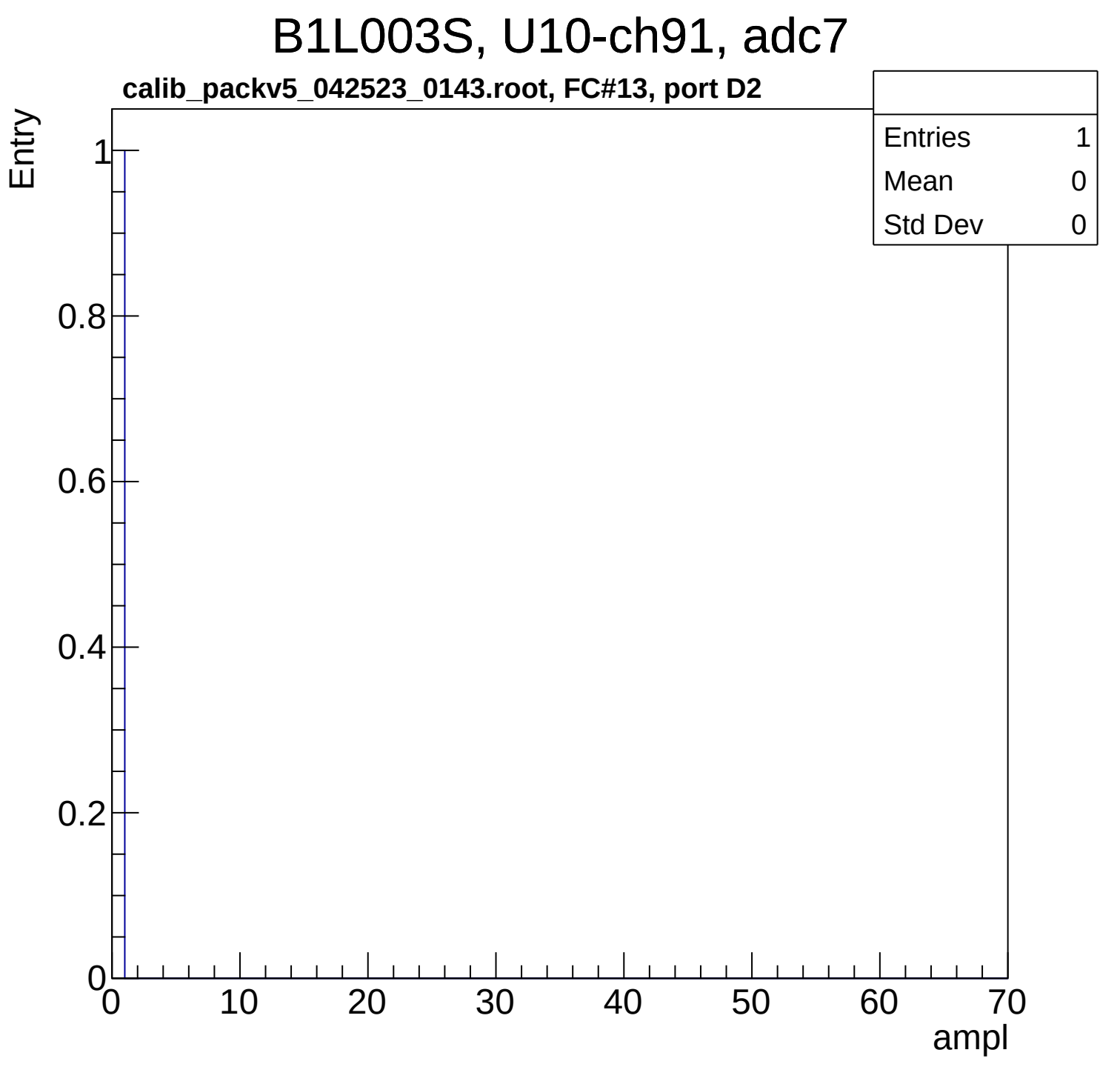
B1L003S, U10-ch91, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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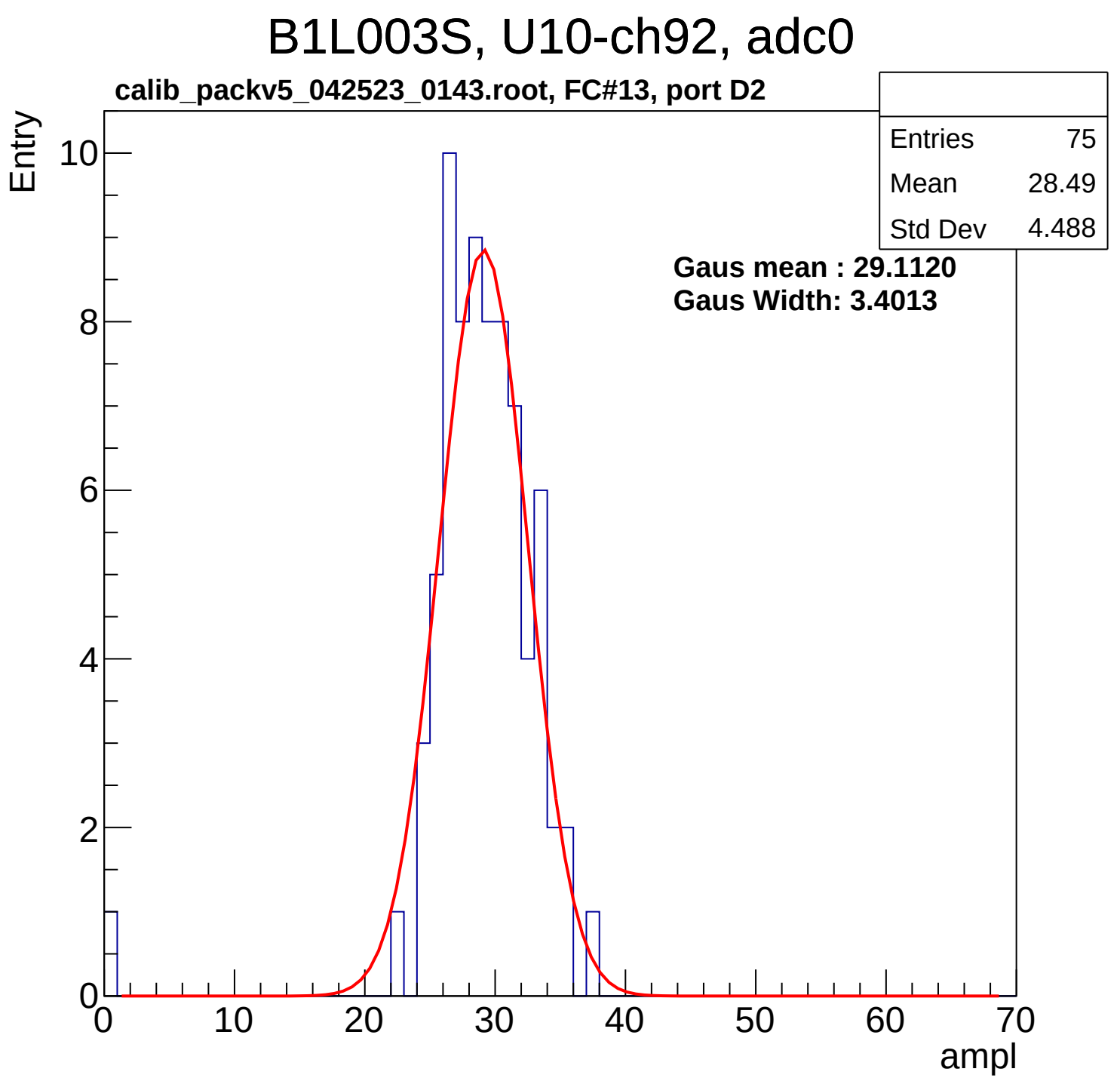
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch92, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.49
Std Dev 4.488
Gaus mean : 29.1120
Gaus Width: 3.4013
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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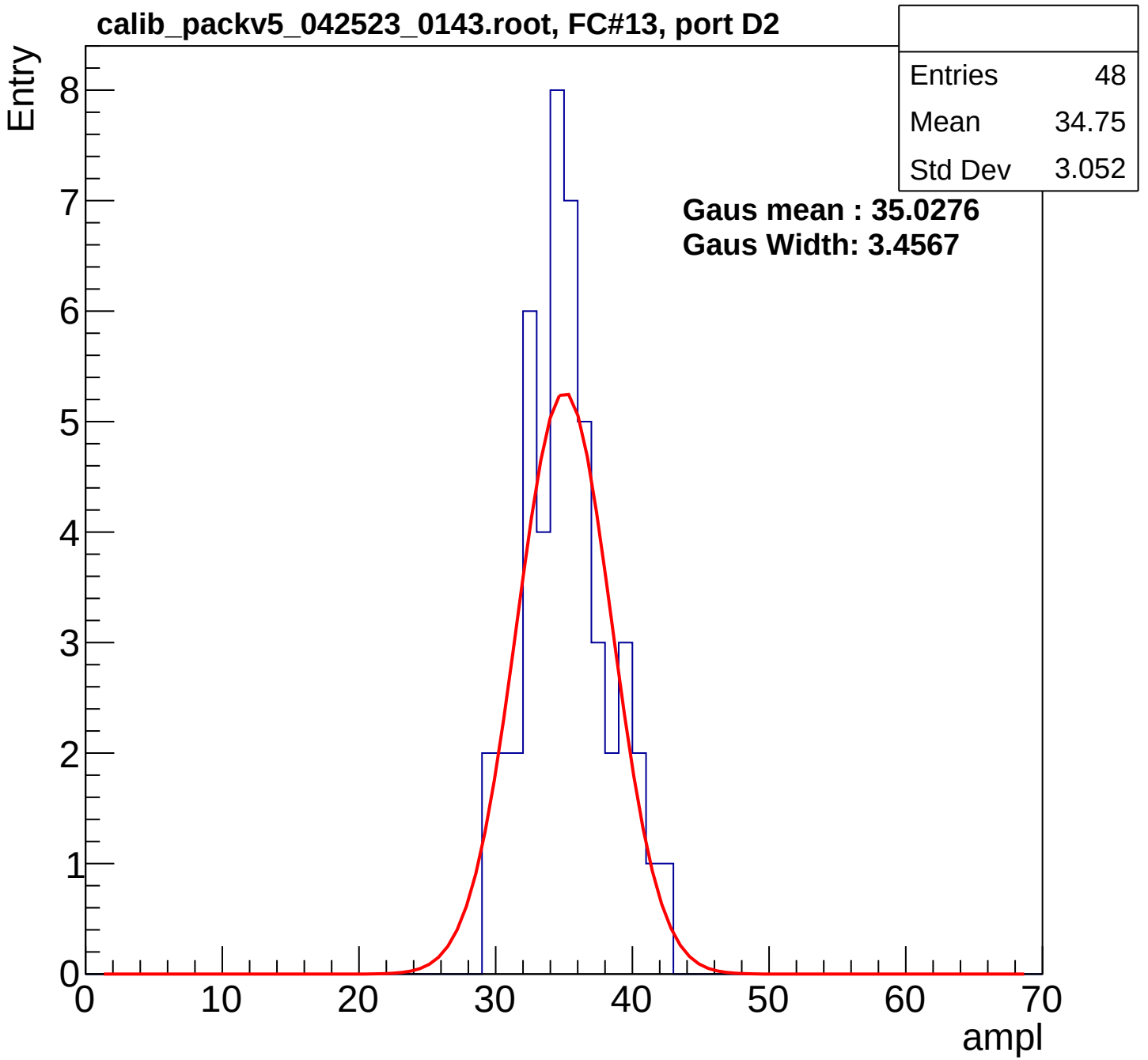
B1L003S, U10-ch92, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 34.75
Std Dev 3.052
Gaus mean : 35.0276
Gaus Width: 3.4567
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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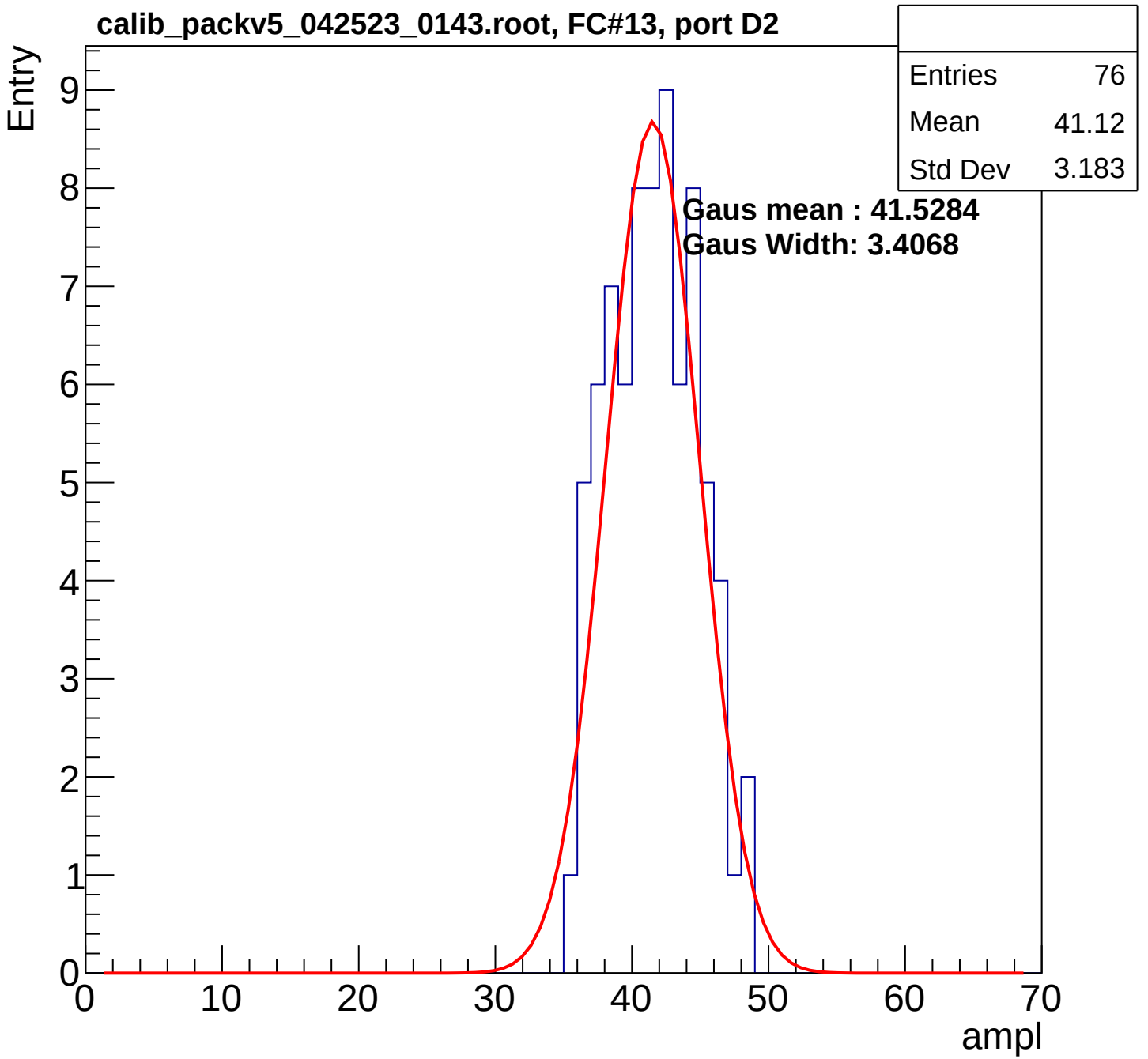
B1L003S, U10-ch92, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 41.12
Std Dev 3.183
Gaus mean : 41.5284
Gaus Width: 3.4068
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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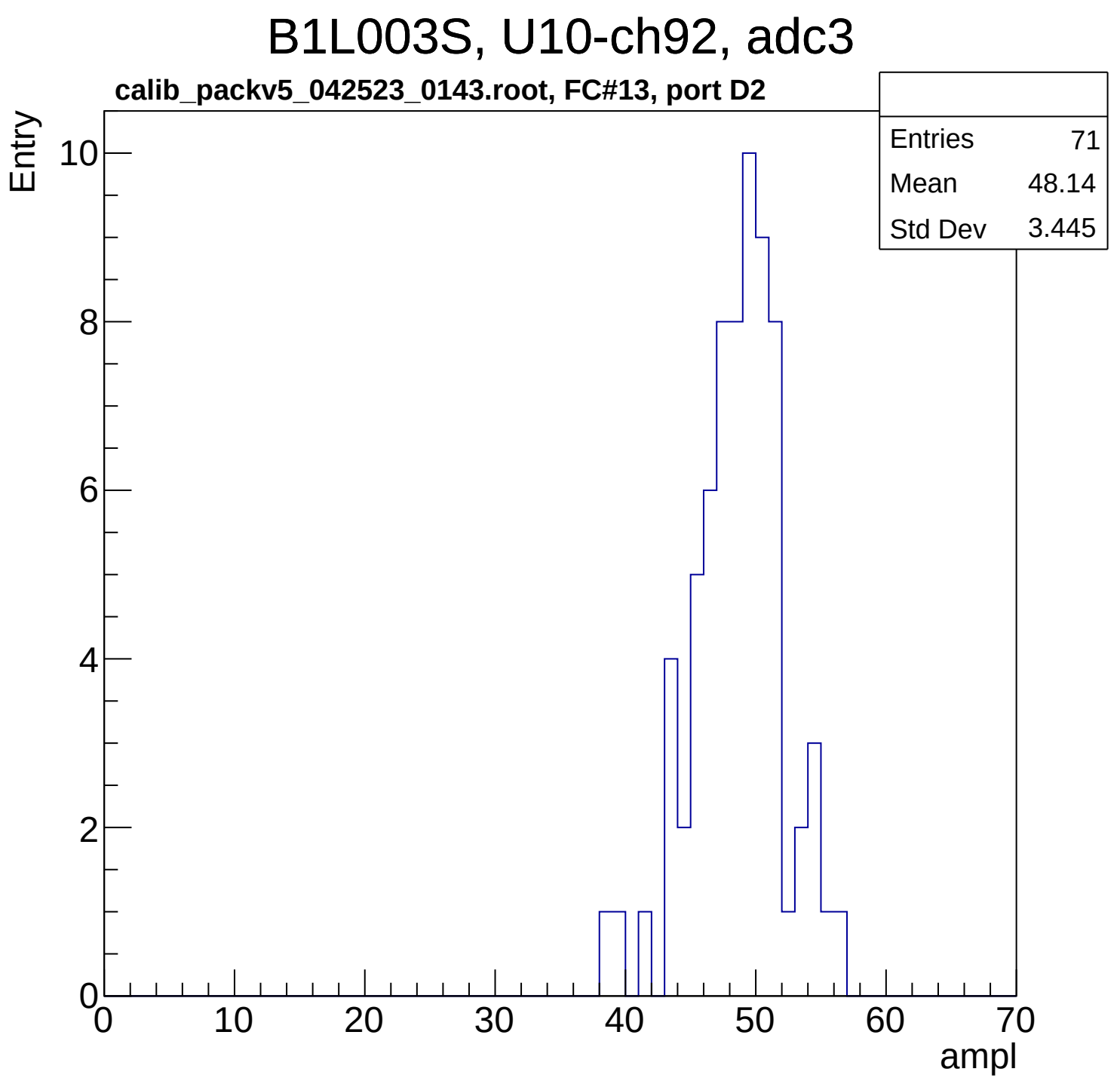
B1L003S, U10-ch92, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 48.14
Std Dev 3.445
Entry
ampl
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4
6
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0
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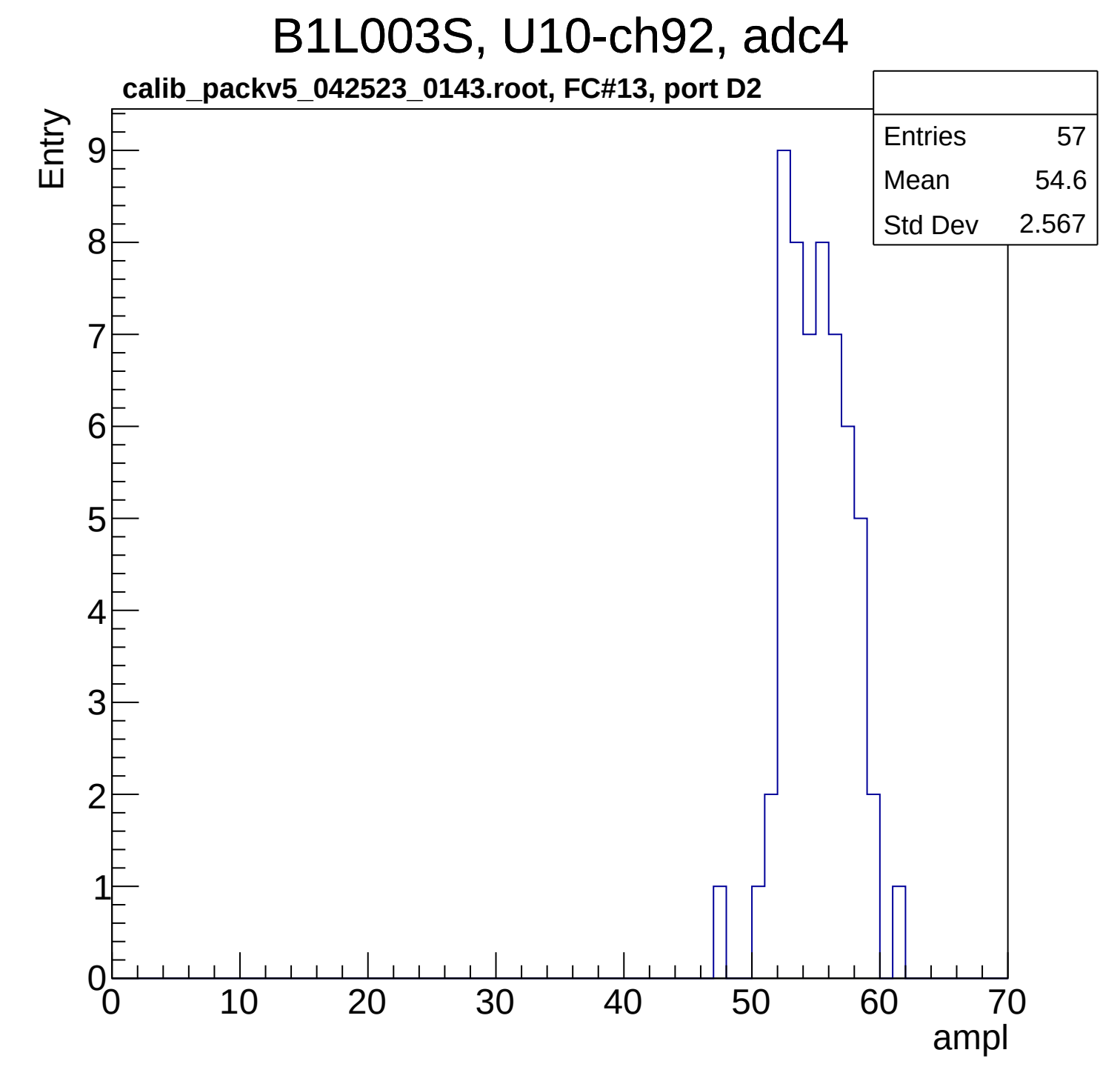
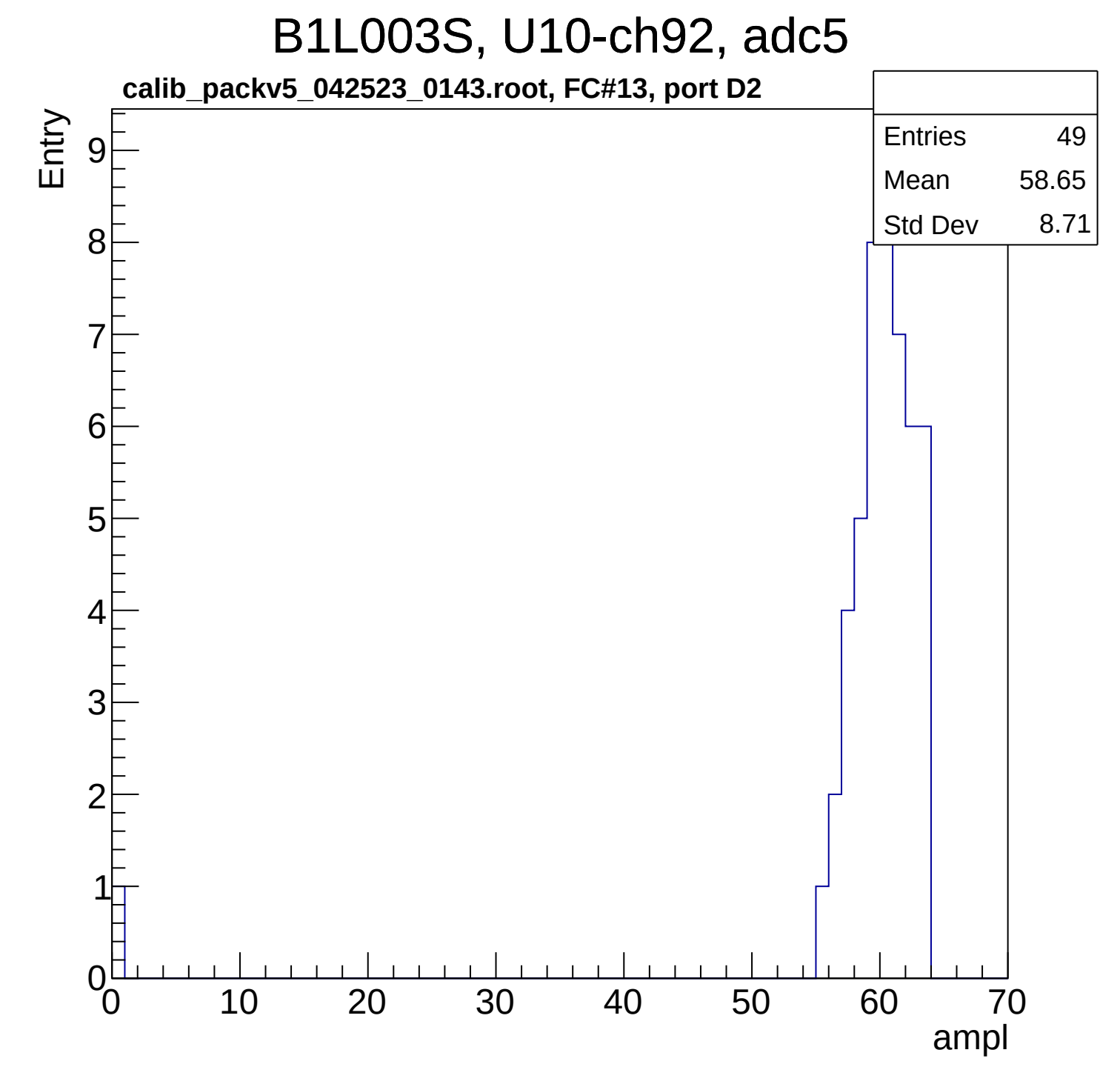

B1L003S, U10-ch92, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.6
Std Dev 2.567
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch92, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.65
Std Dev 8.71
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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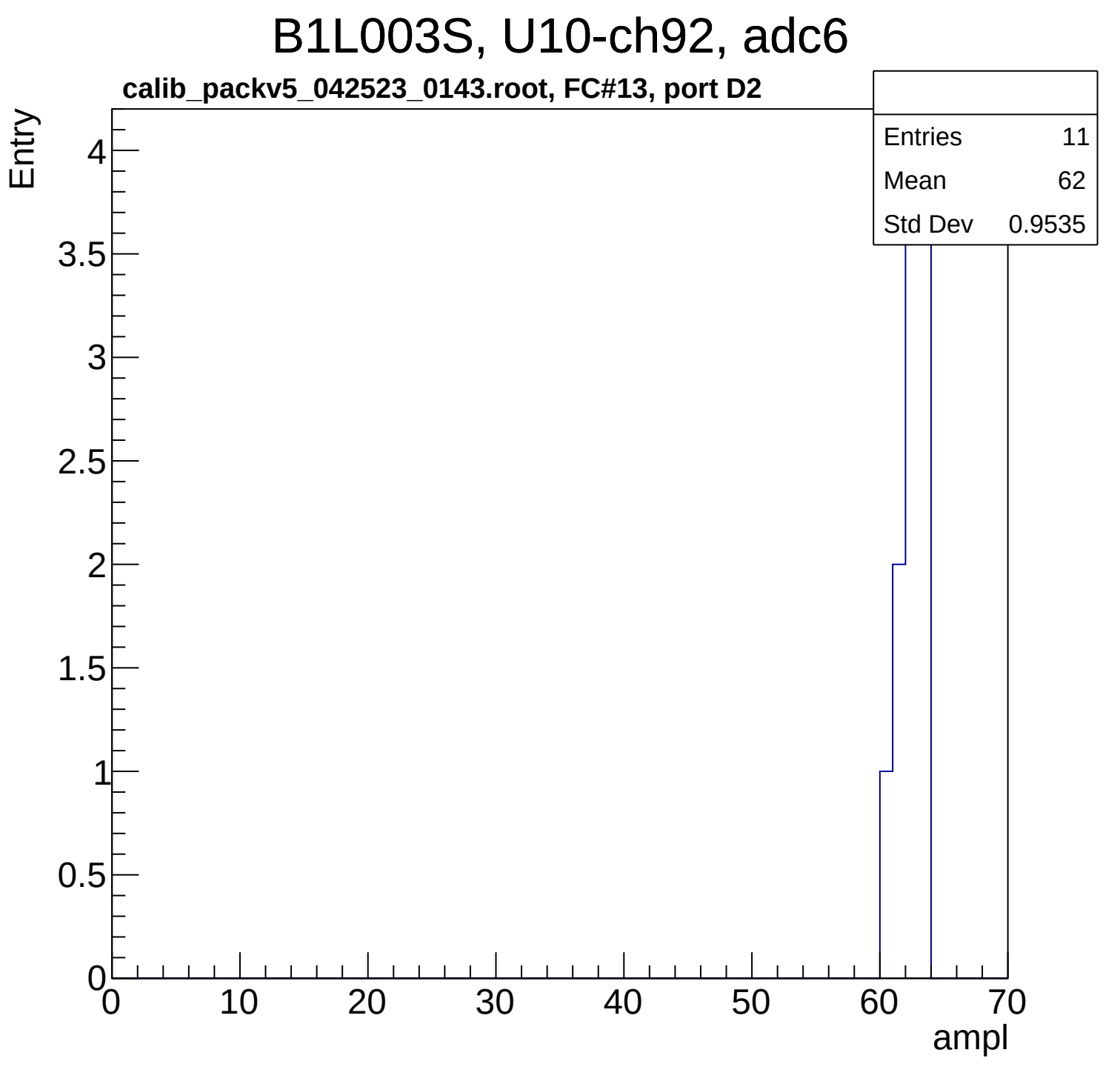
B1L003S, U10-ch92, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 62
Std Dev 0.9535
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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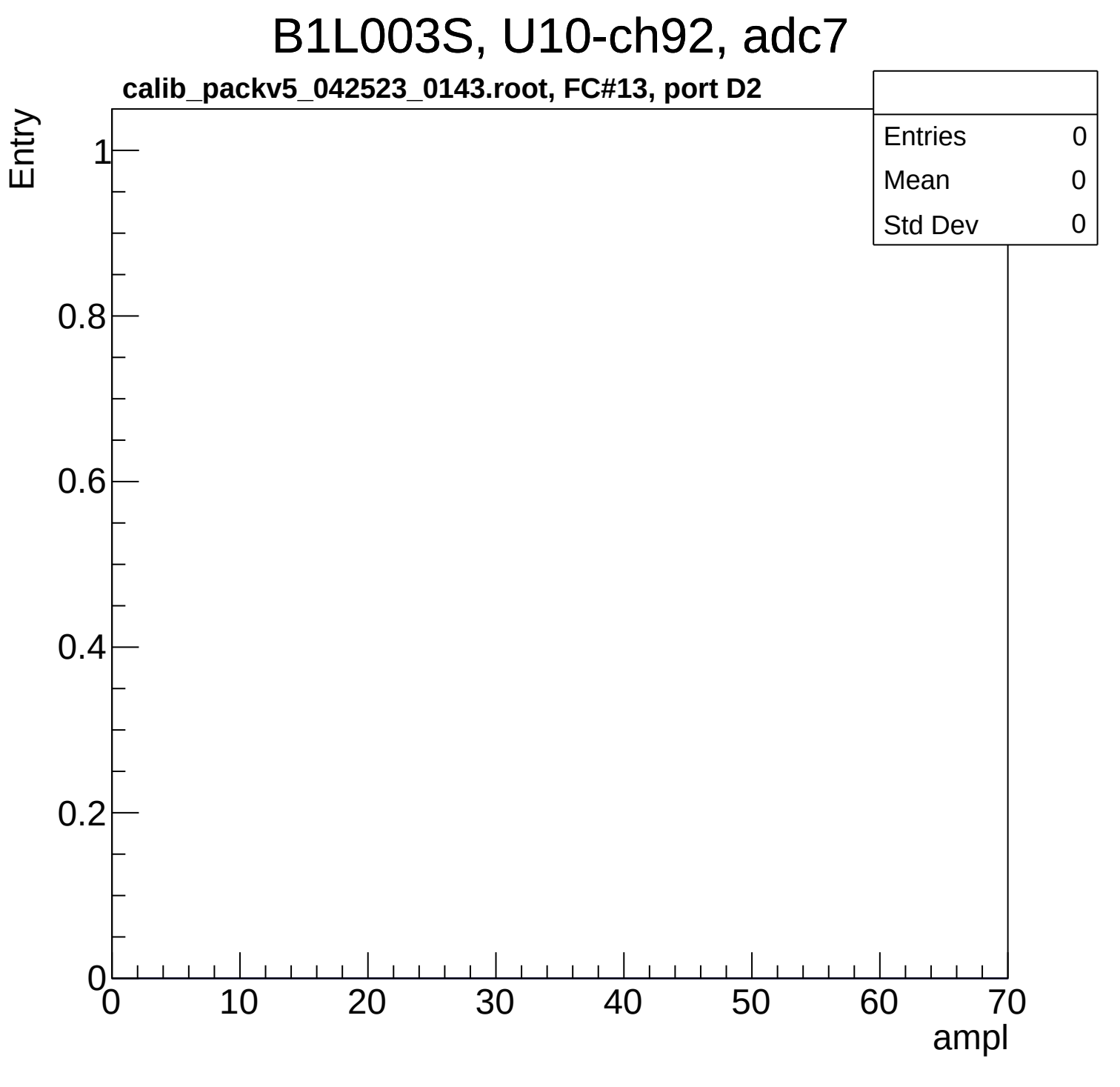
B1L003S, U10-ch92, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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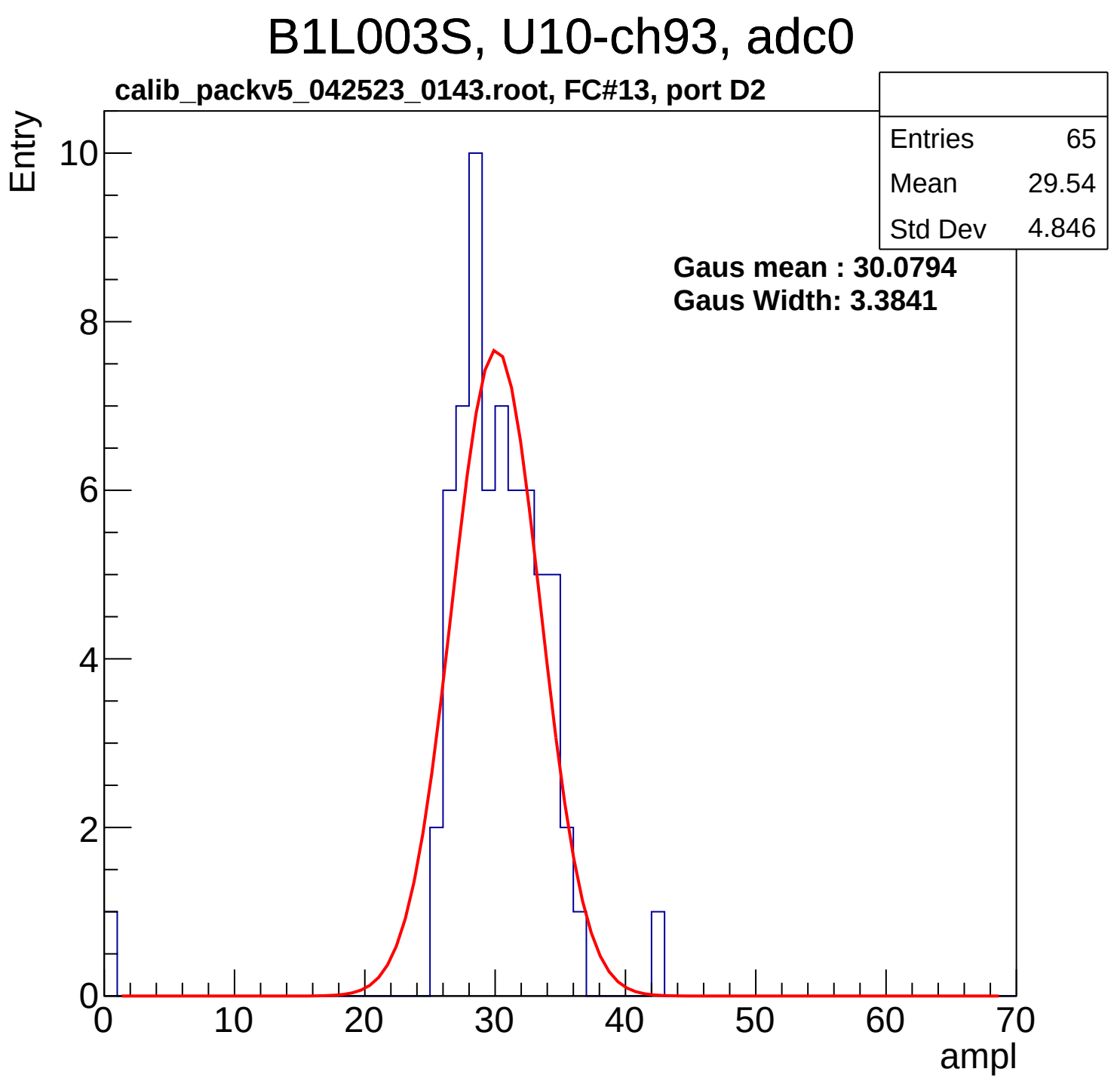
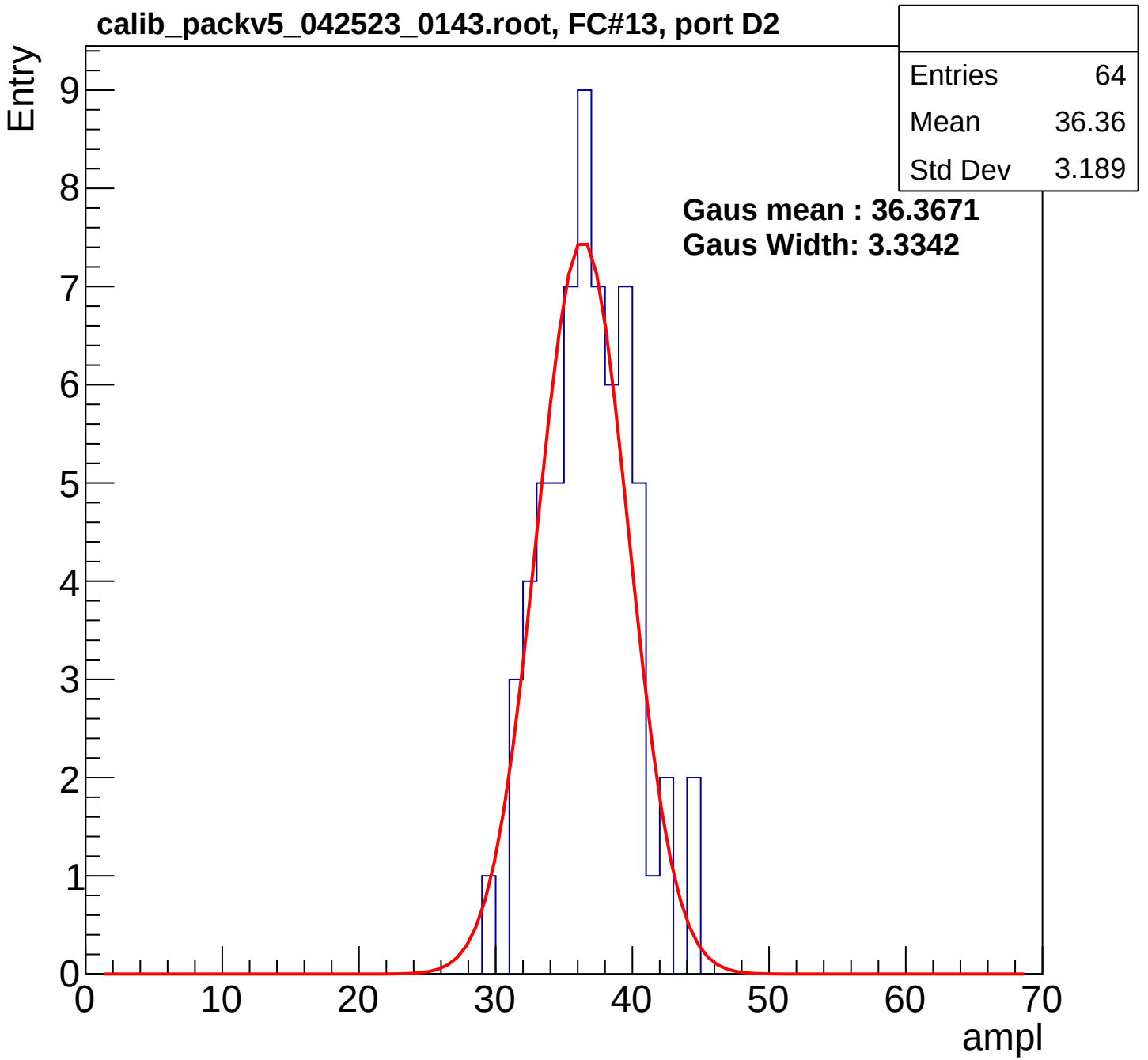
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch93, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.54
Std Dev 4.846
Gaus mean : 30.0794
Gaus Width: 3.3841
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch93, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.36
Std Dev 3.189
Gaus mean : 36.3671
Gaus Width: 3.3342
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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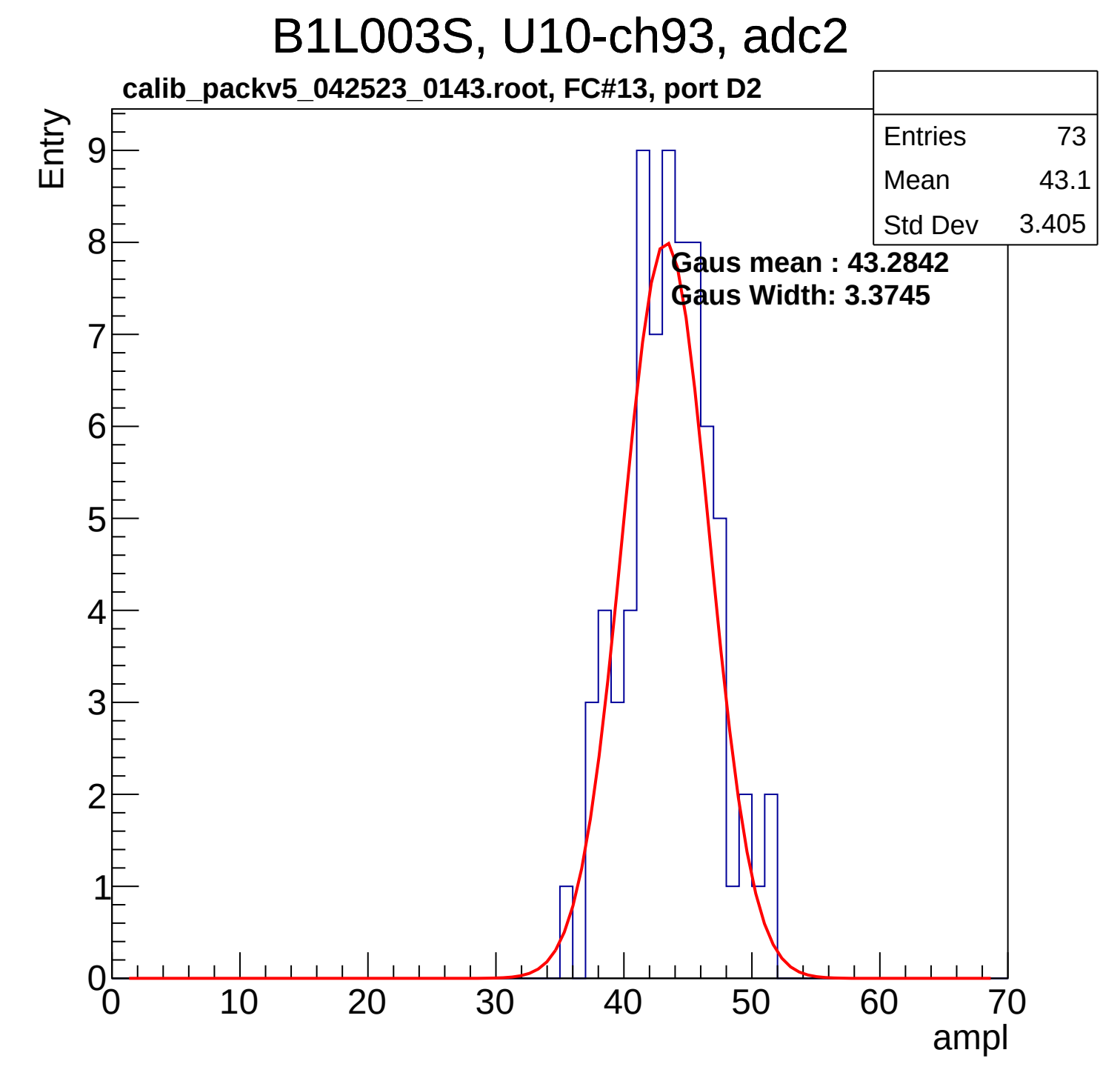
B1L003S, U10-ch93, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 43.1
Std Dev 3.405
Gaus mean : 43.2842
Gaus Width: 3.3745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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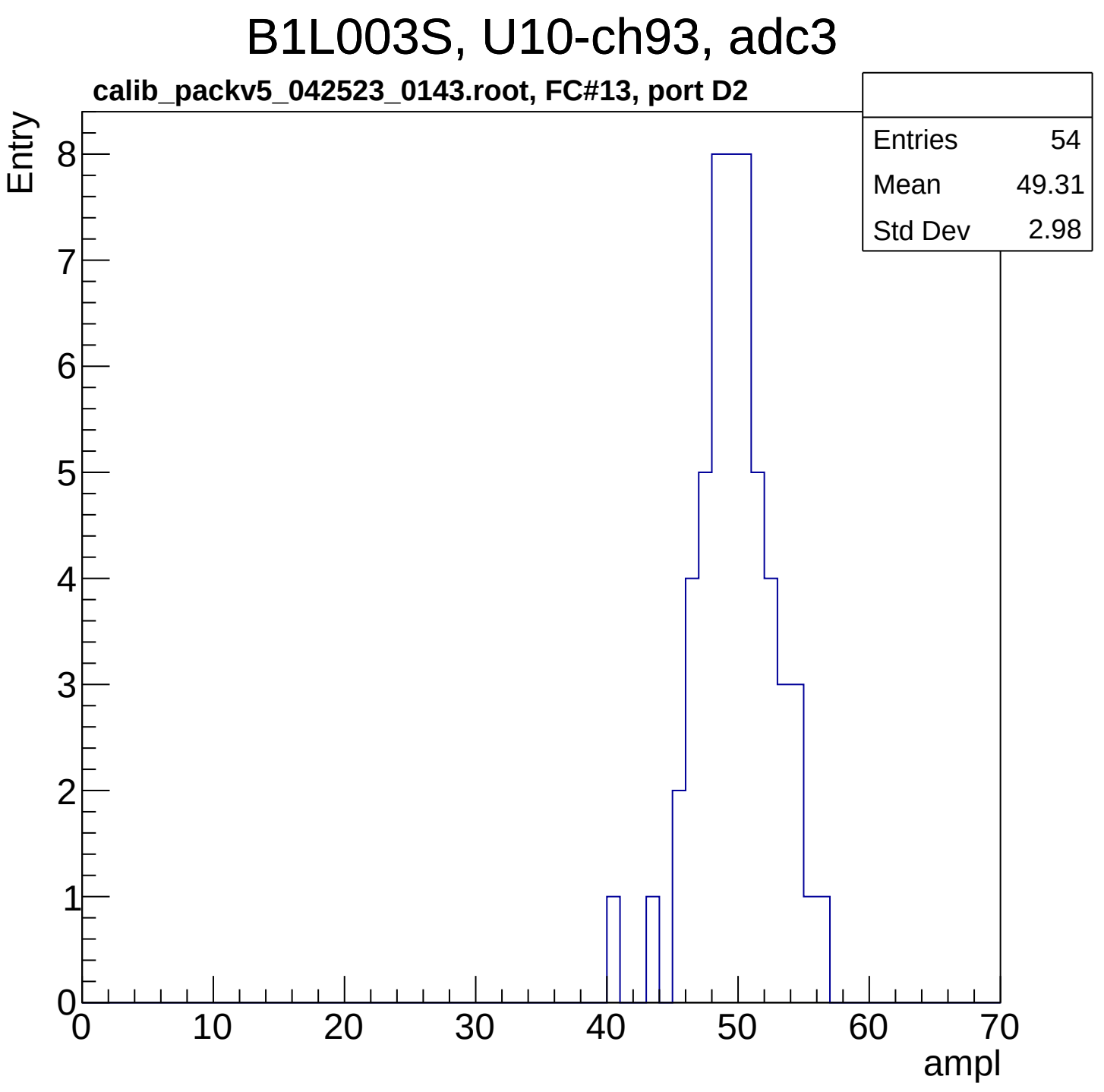
B1L003S, U10-ch93, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.31
Std Dev 2.98
Entry
ampl
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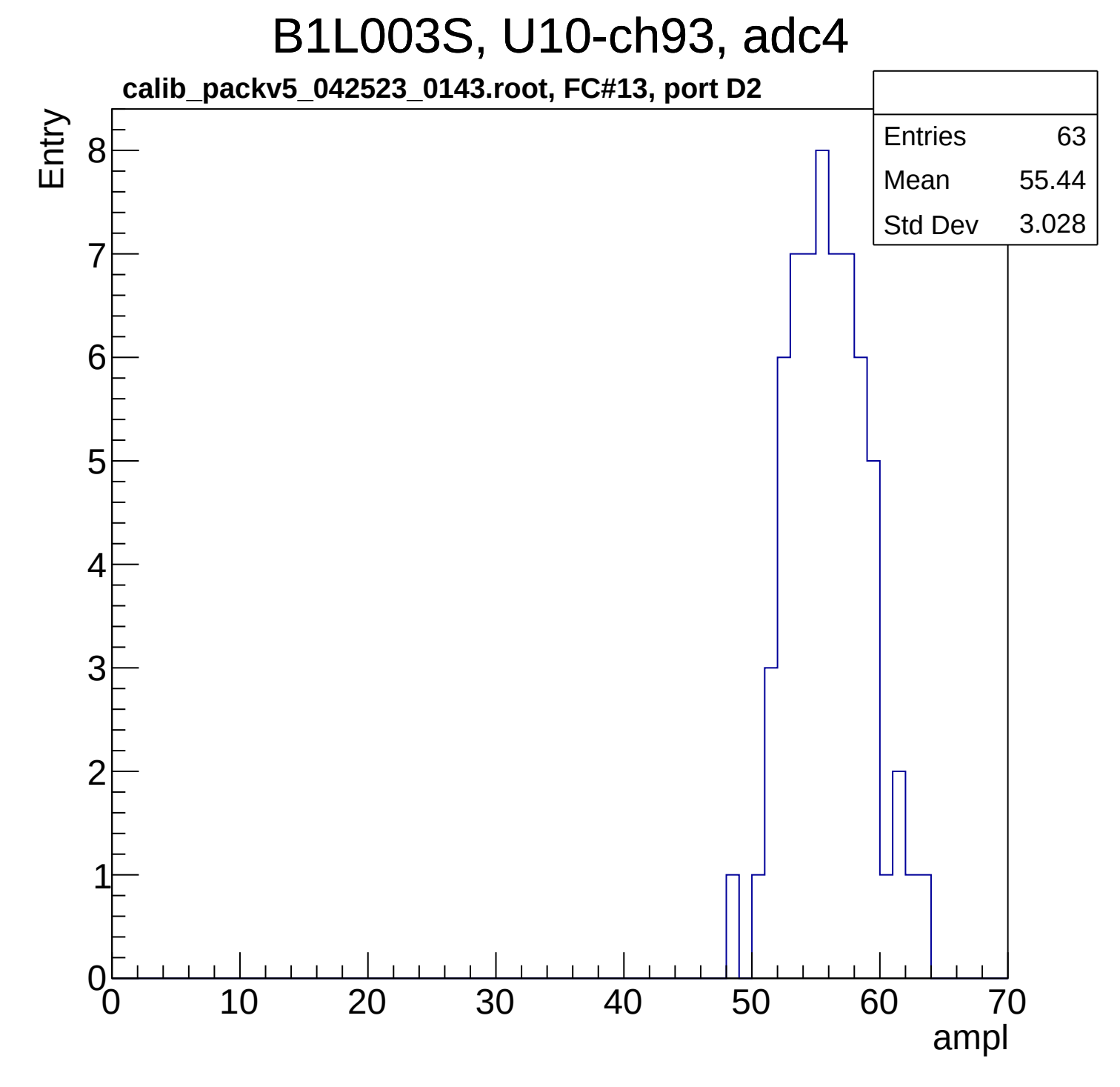
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch93, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.44
Std Dev 3.028
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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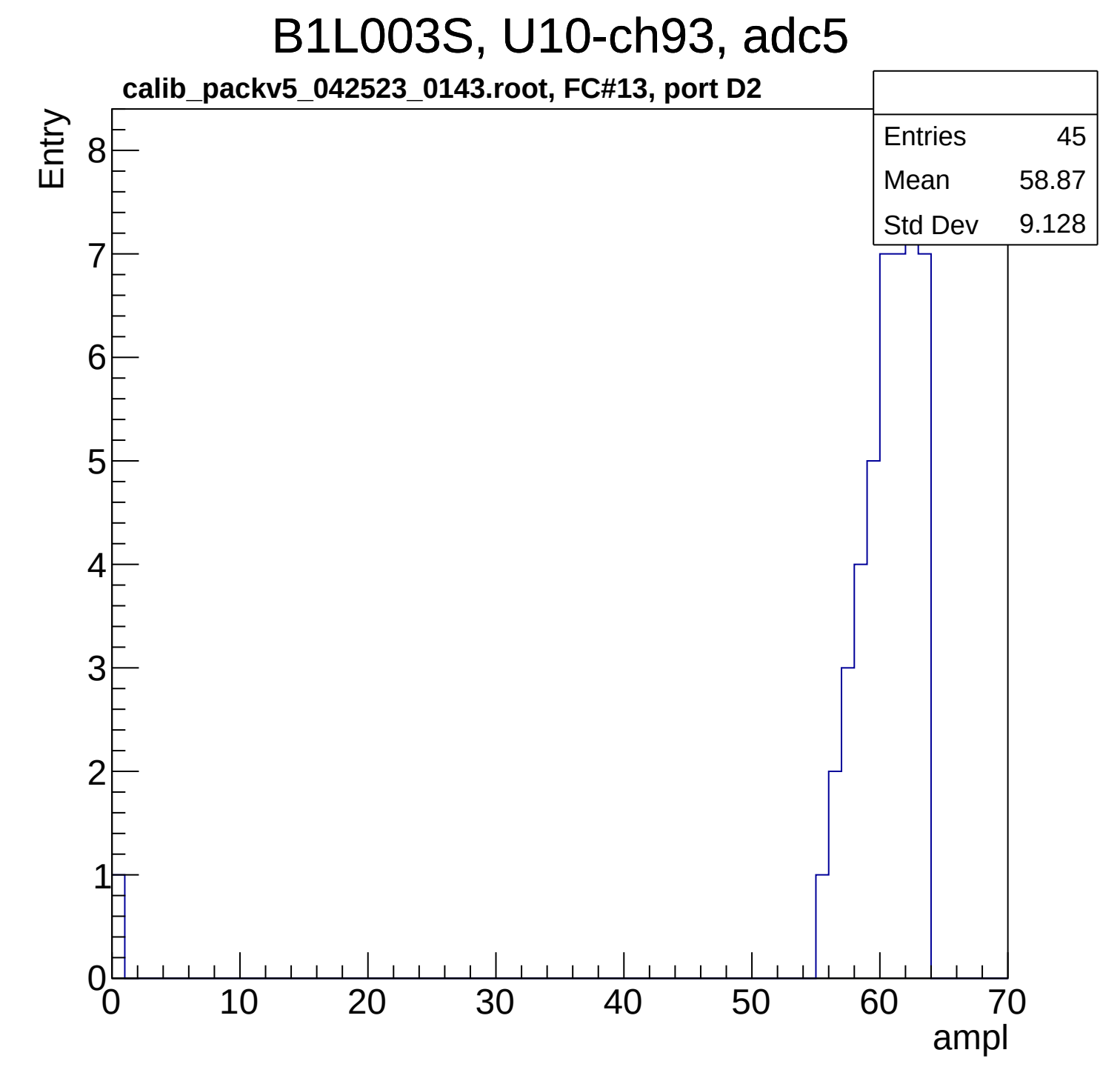
40
50
60
70


B1L003S, U10-ch93, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.87
Std Dev 9.128
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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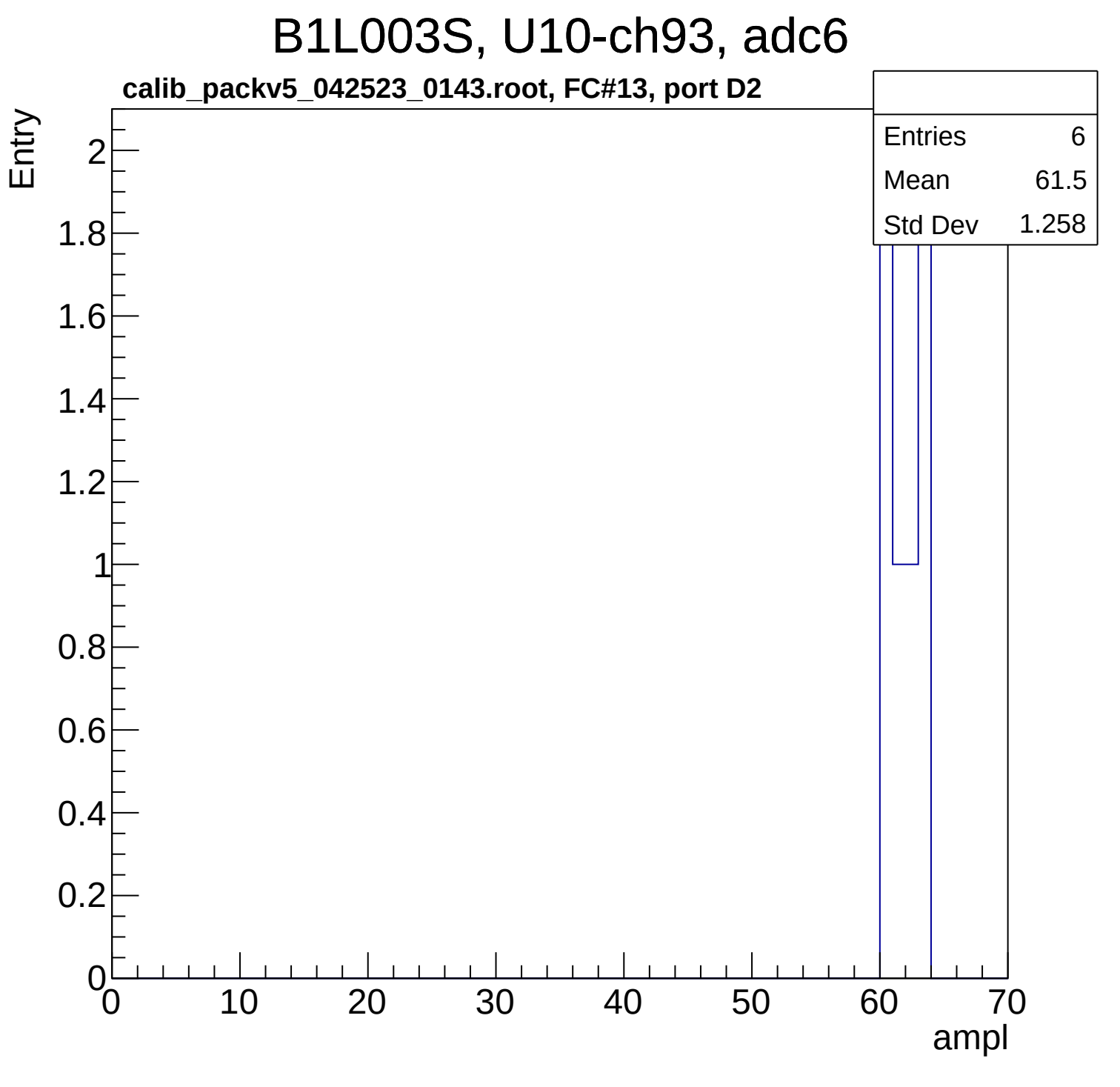
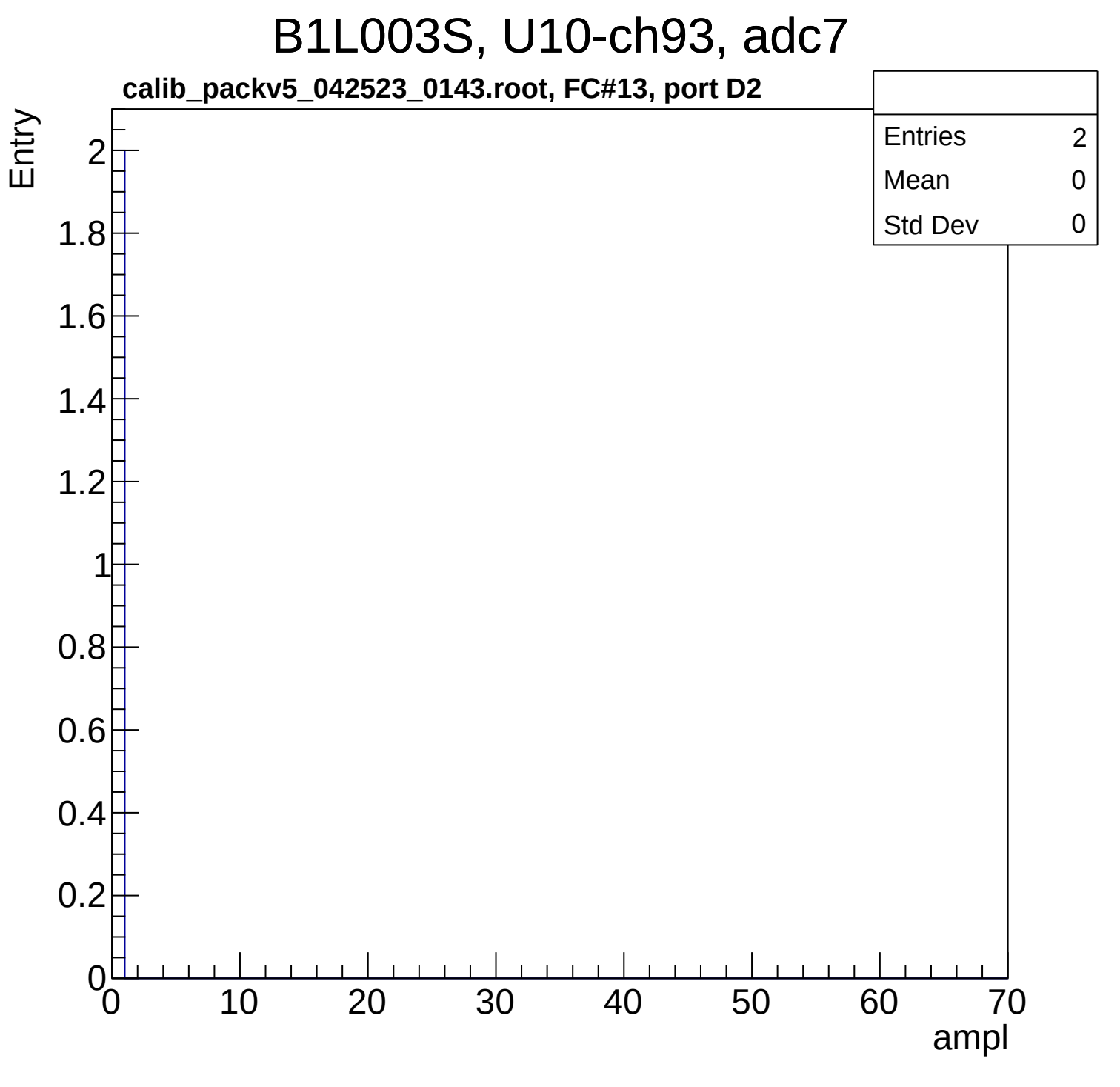

B1L003S, U10-ch93, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.5
Std Dev 1.258
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch93, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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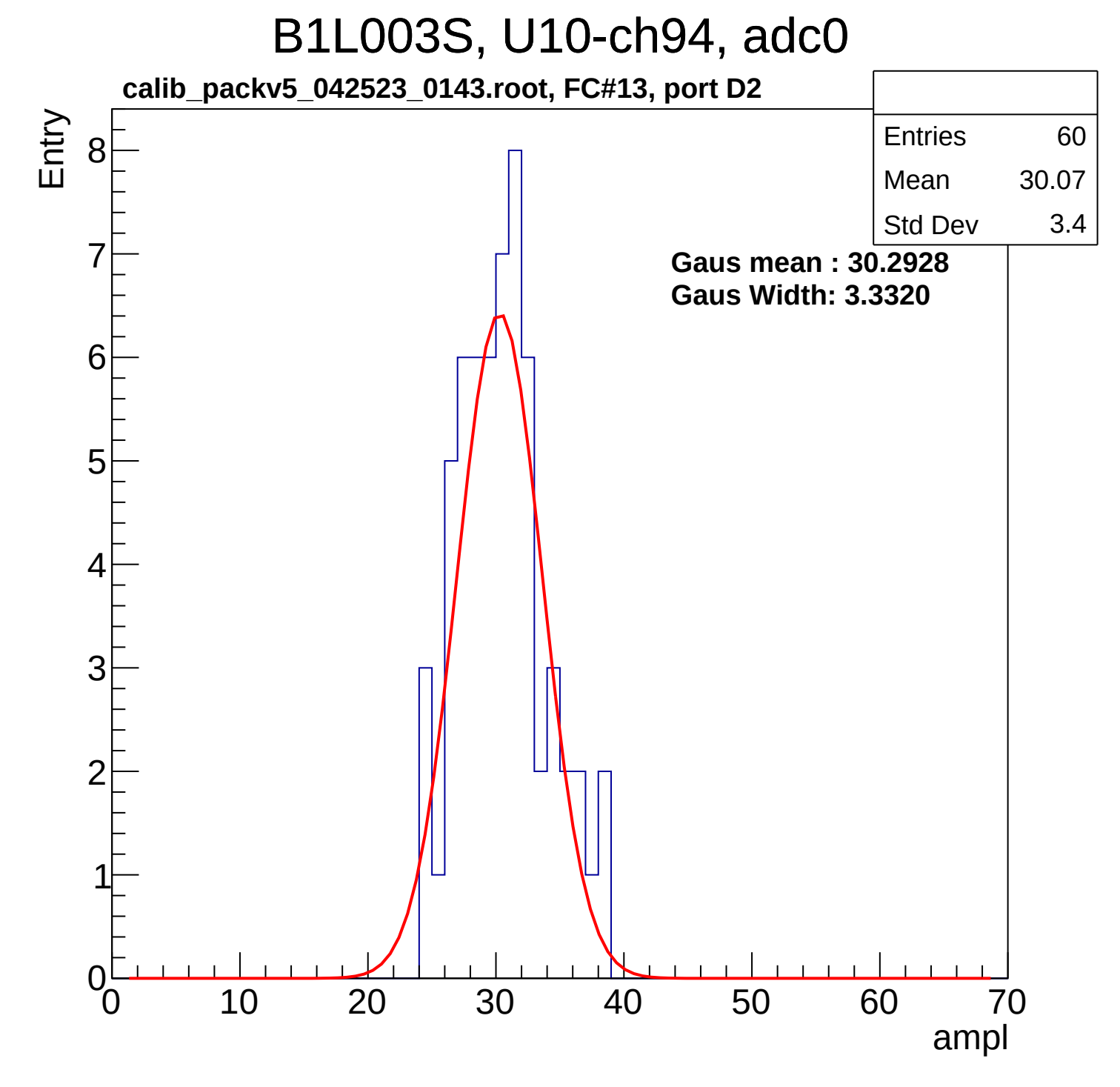
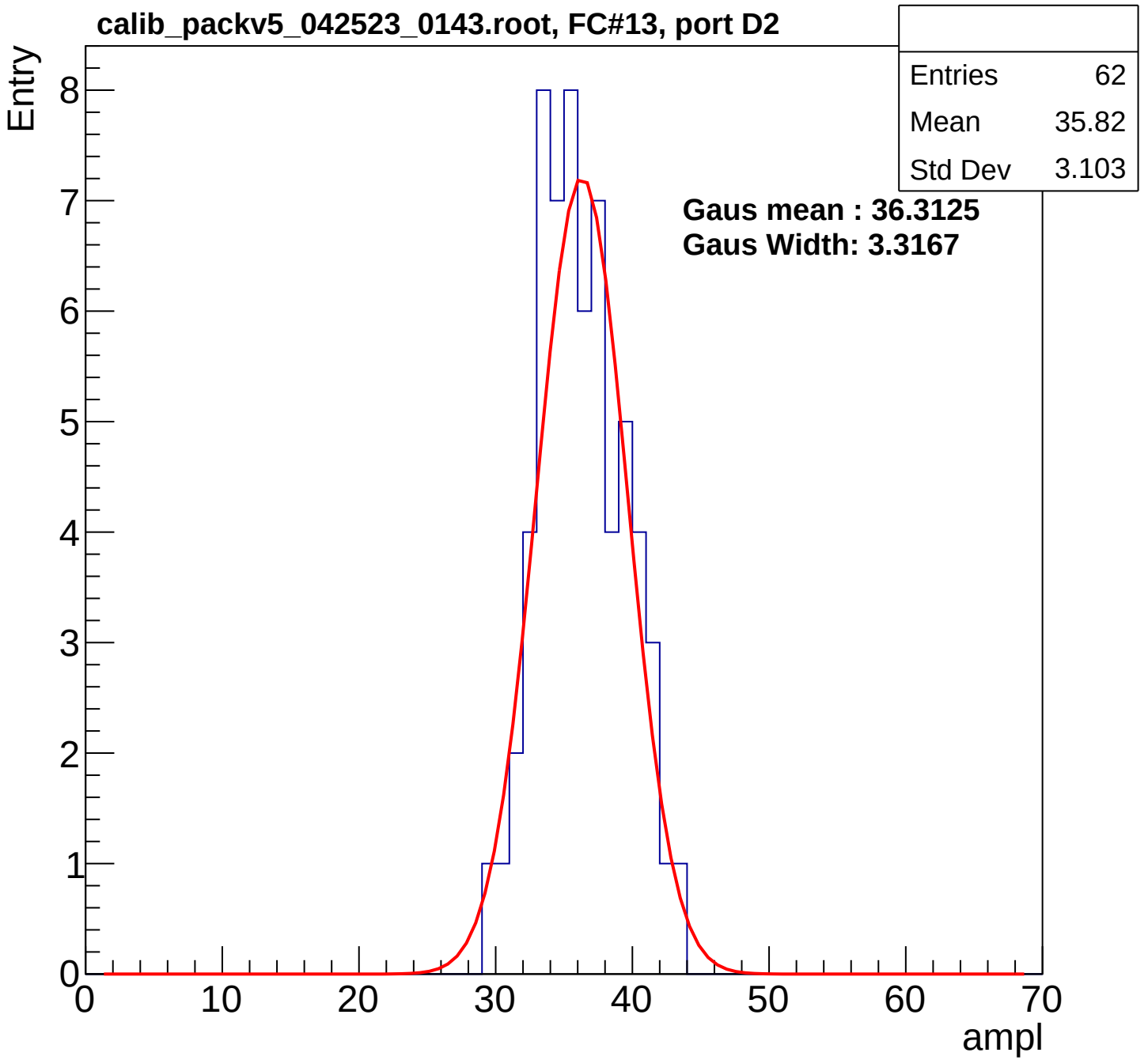

B1L003S, U10-ch94, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 30.07
Std Dev 3.4
Gaus mean : 30.2928
Gaus Width: 3.3320
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch94, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.82
Std Dev 3.103
Gaus mean : 36.3125
Gaus Width: 3.3167
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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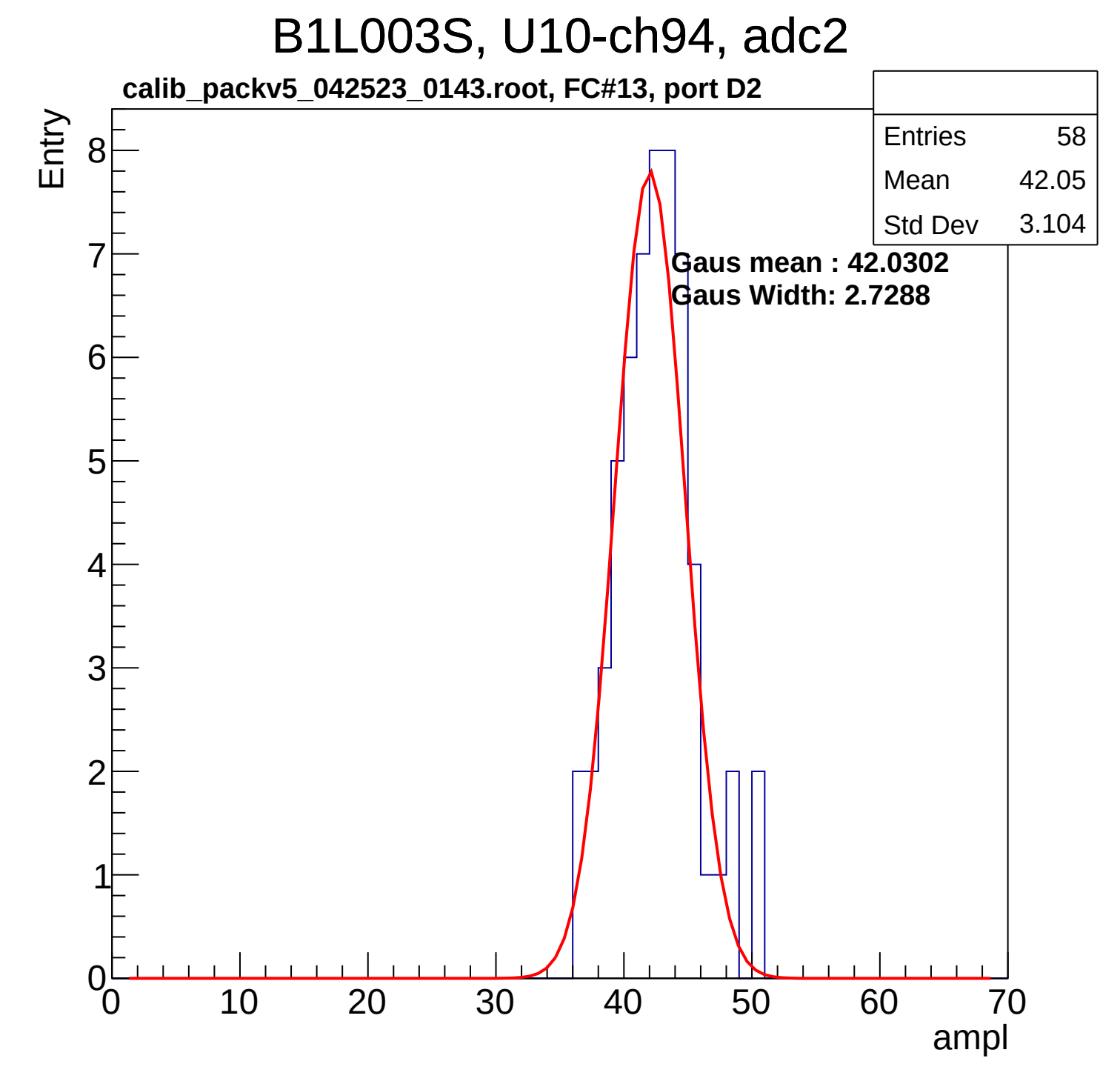
B1L003S, U10-ch94, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.05
Std Dev 3.104
Gaus mean : 42.0302
Gaus Width: 2.7288
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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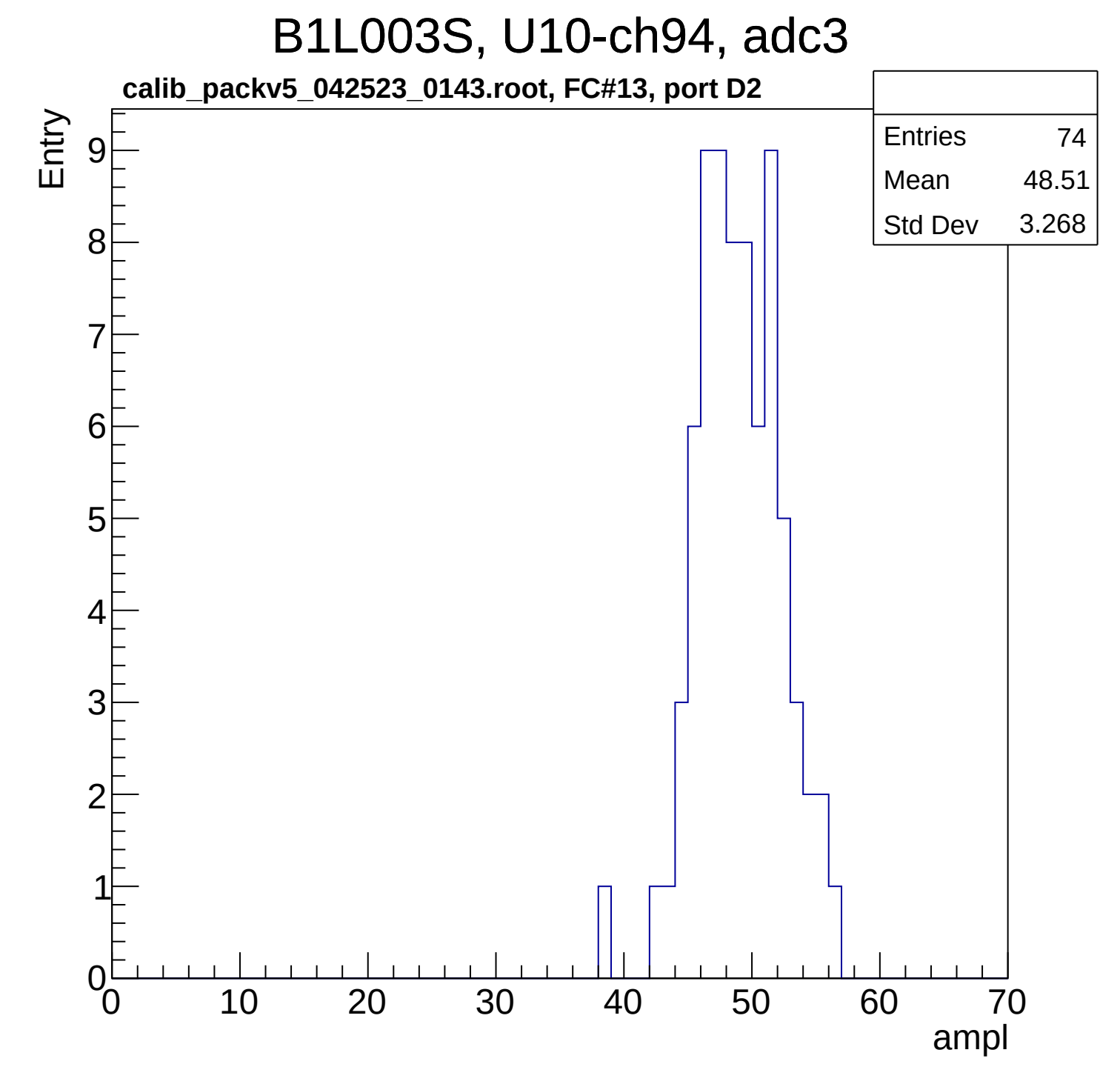
B1L003S, U10-ch94, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.51
Std Dev 3.268
Entry
ampl
0
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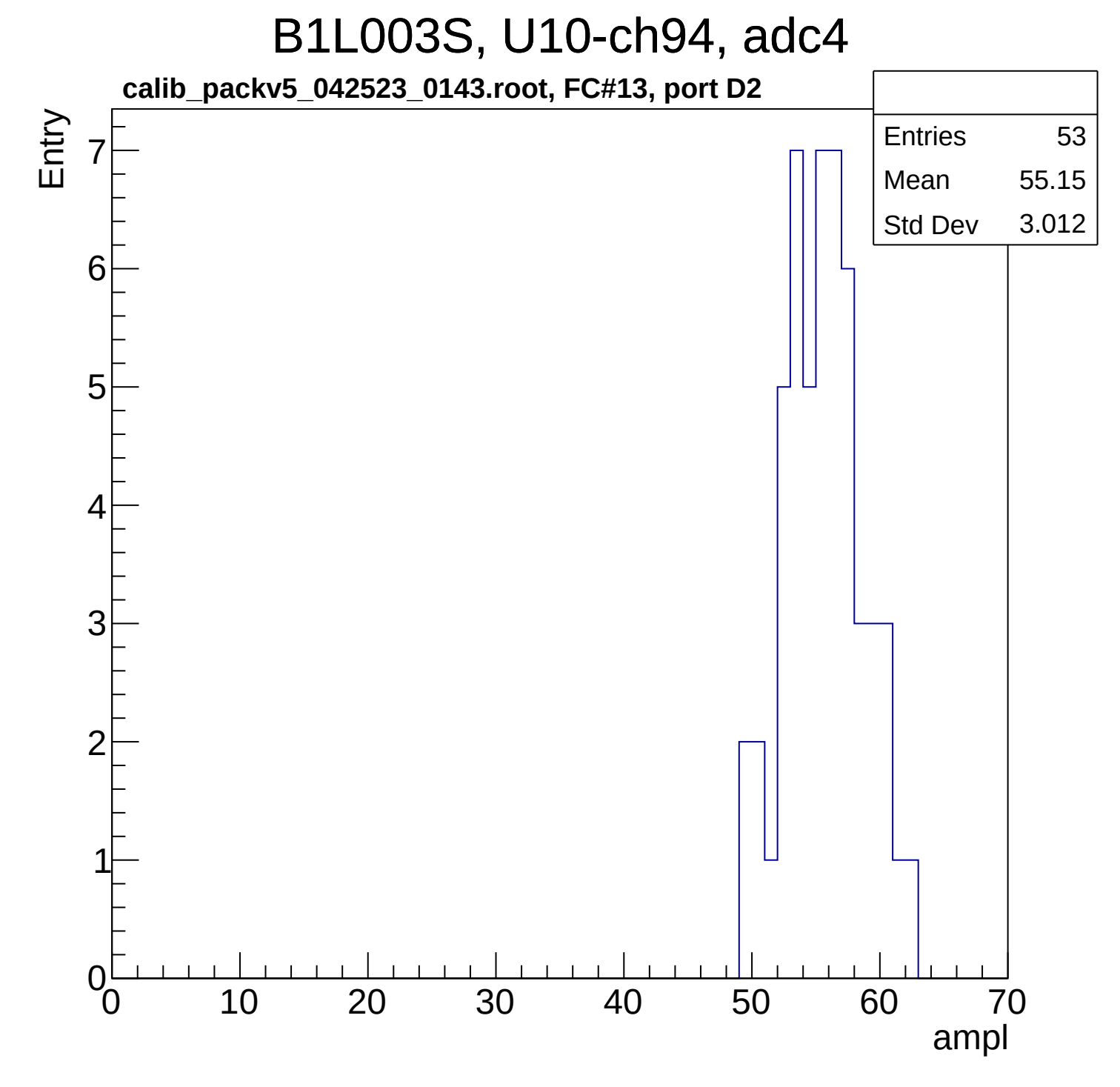
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch94, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.15
Std Dev 3.012
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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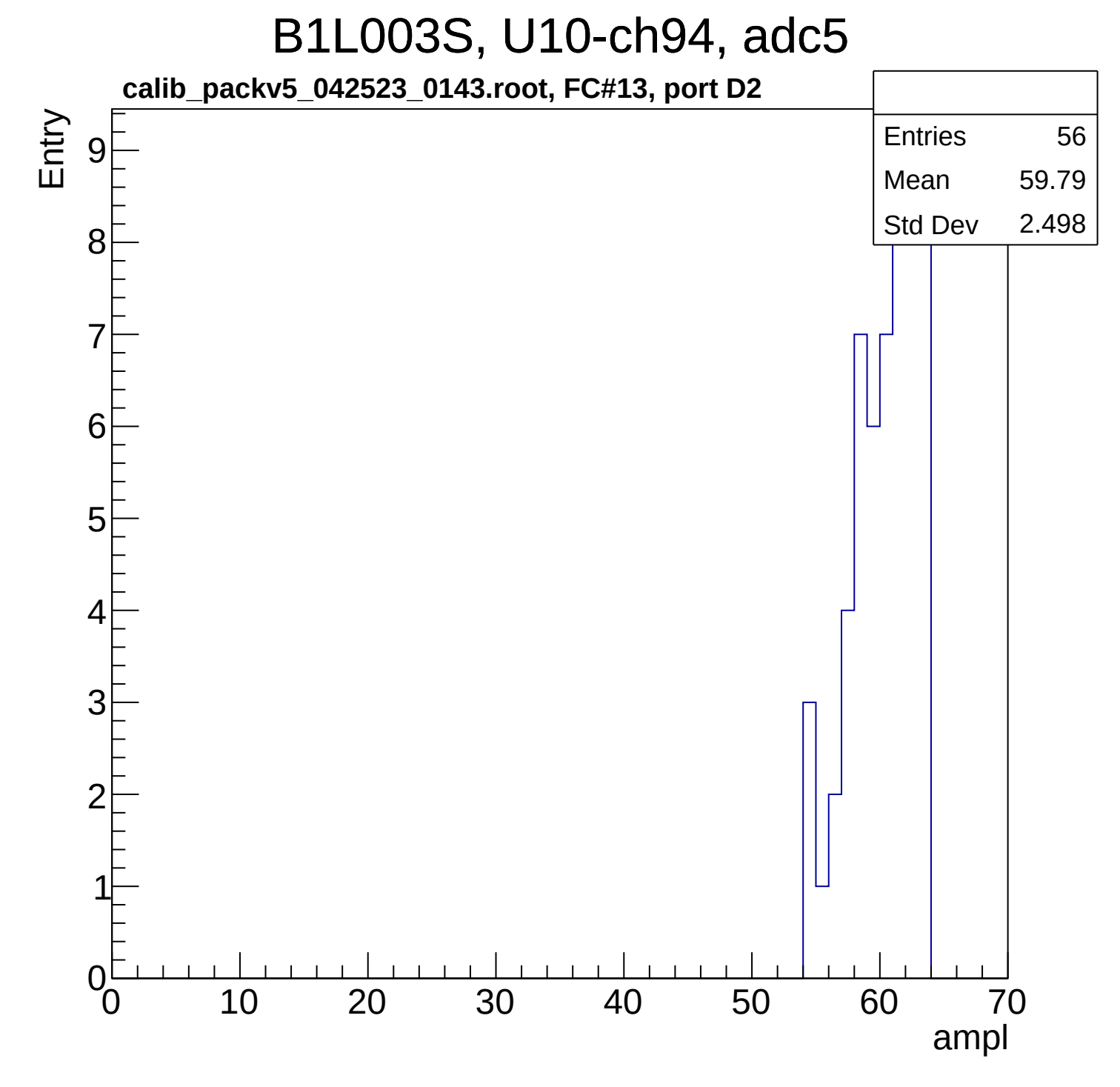
B1L003S, U10-ch94, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.79
Std Dev 2.498
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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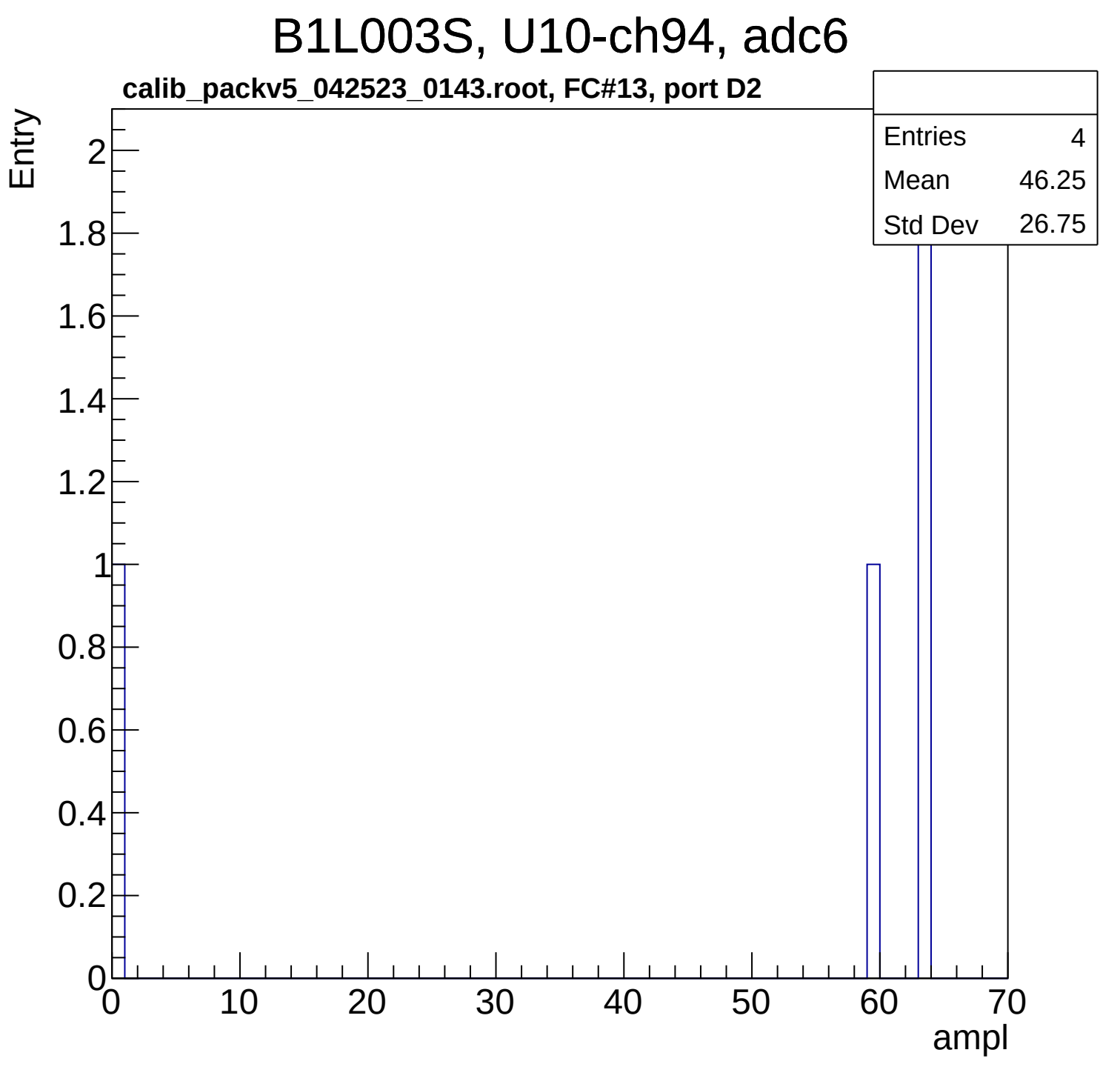
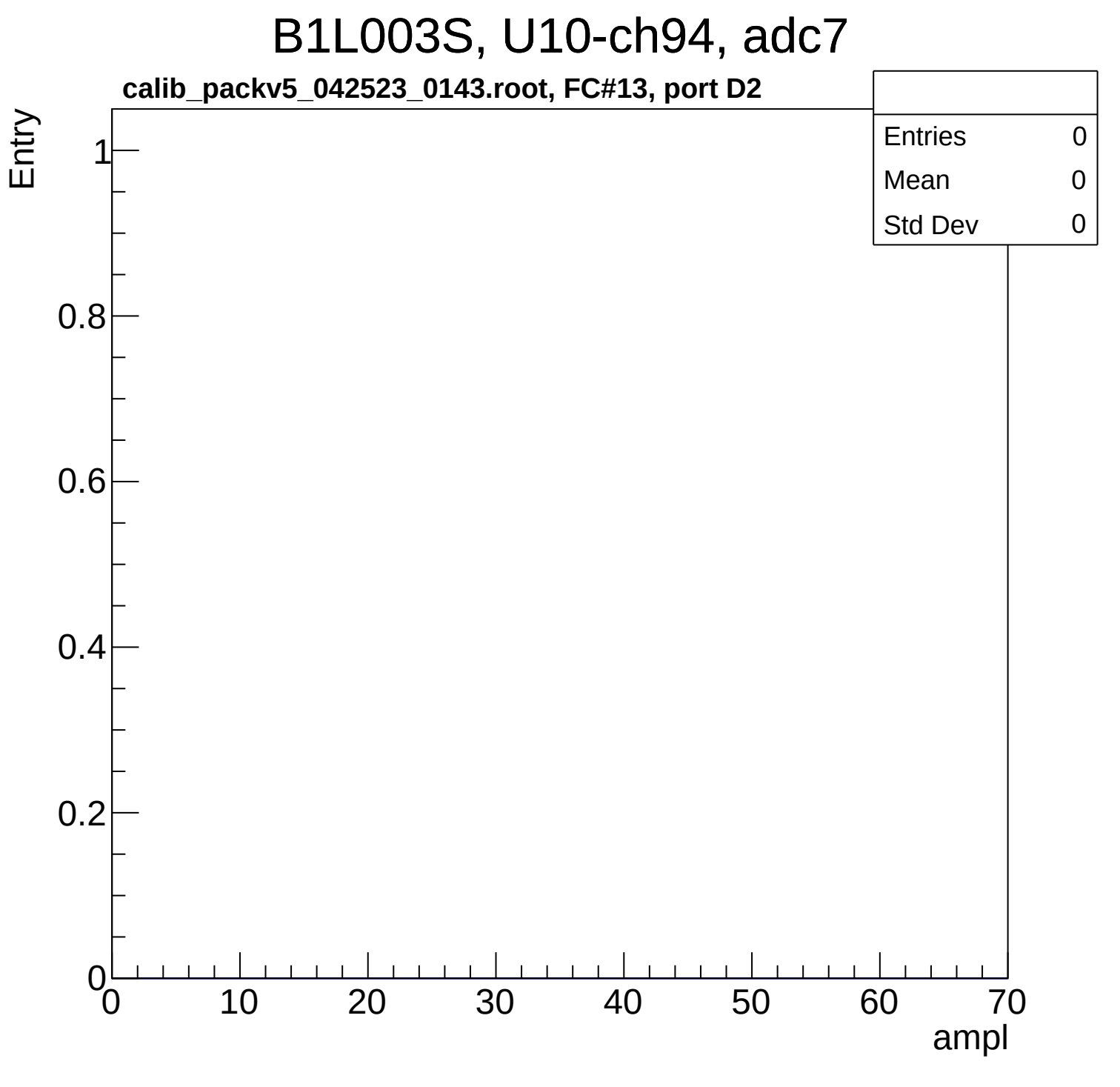
40
50
60
70


B1L003S, U10-ch94, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 46.25
Std Dev 26.75
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch94, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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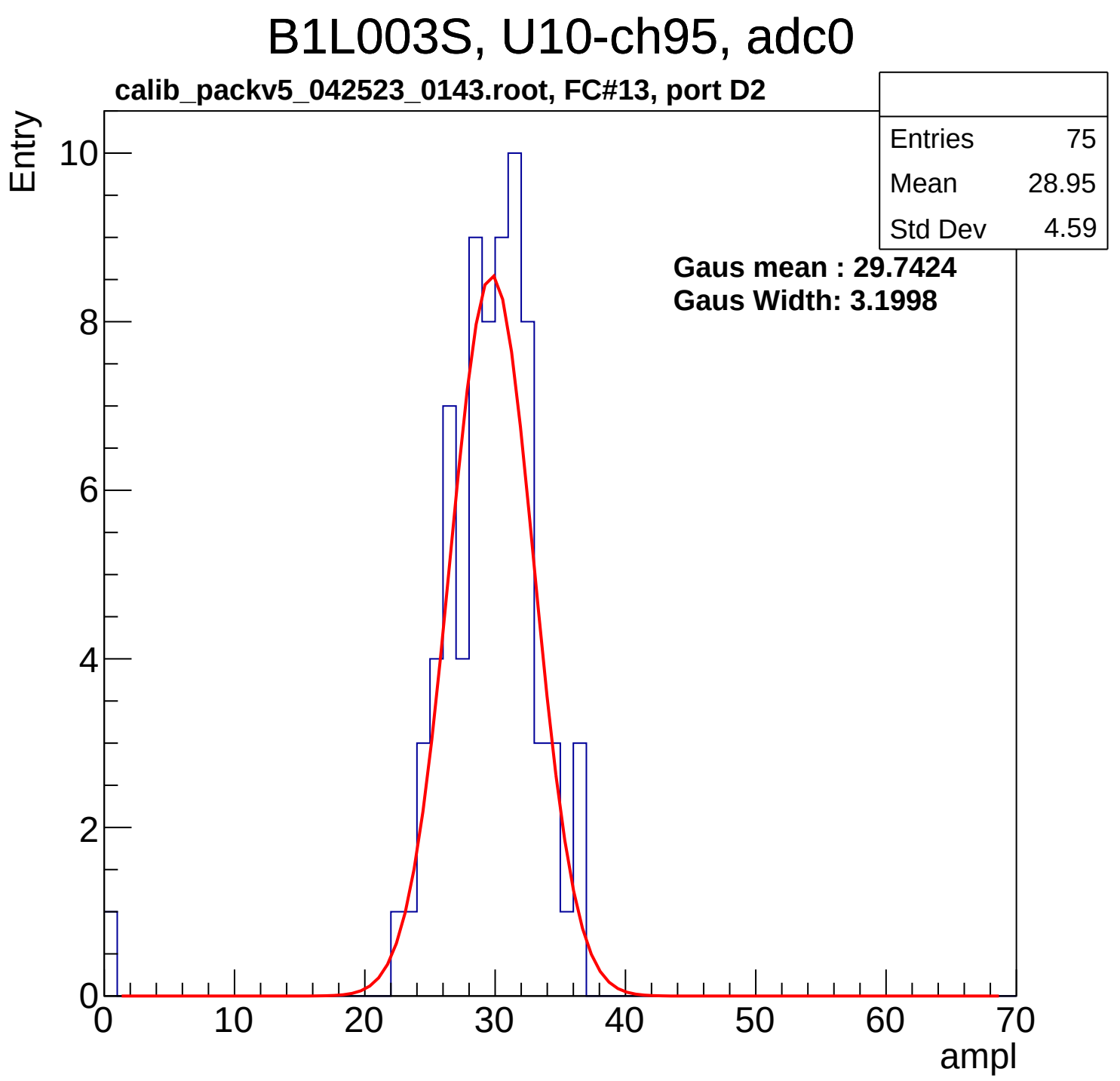
40
50
60
70
ampl


B1L003S, U10-ch95, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.95
Std Dev 4.59
Gaus mean : 29.7424
Gaus Width: 3.1998
Entry
ampl
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2
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8
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40
50
60
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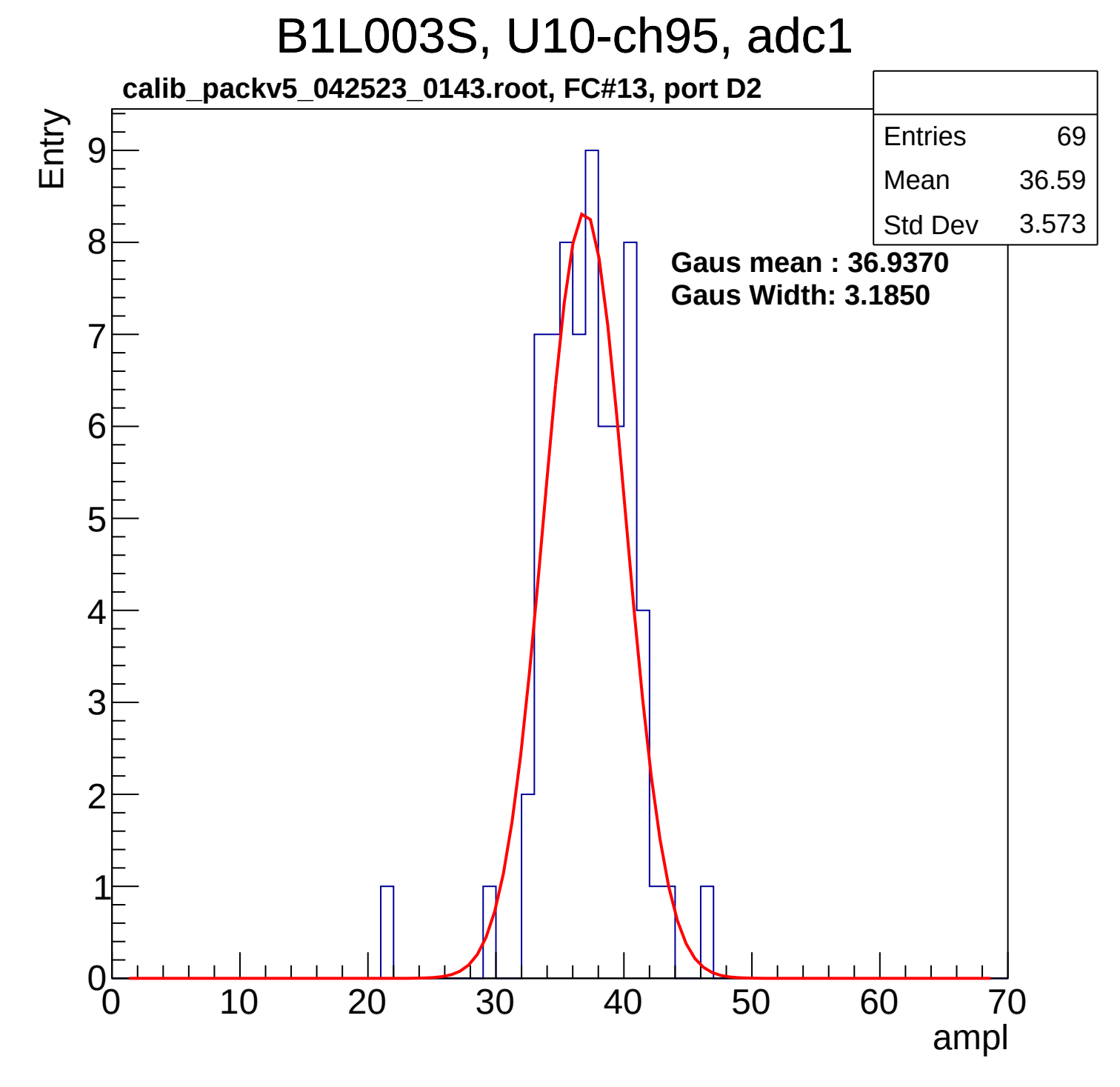
B1L003S, U10-ch95, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36.59
Std Dev 3.573
Gaus mean : 36.9370
Gaus Width: 3.1850
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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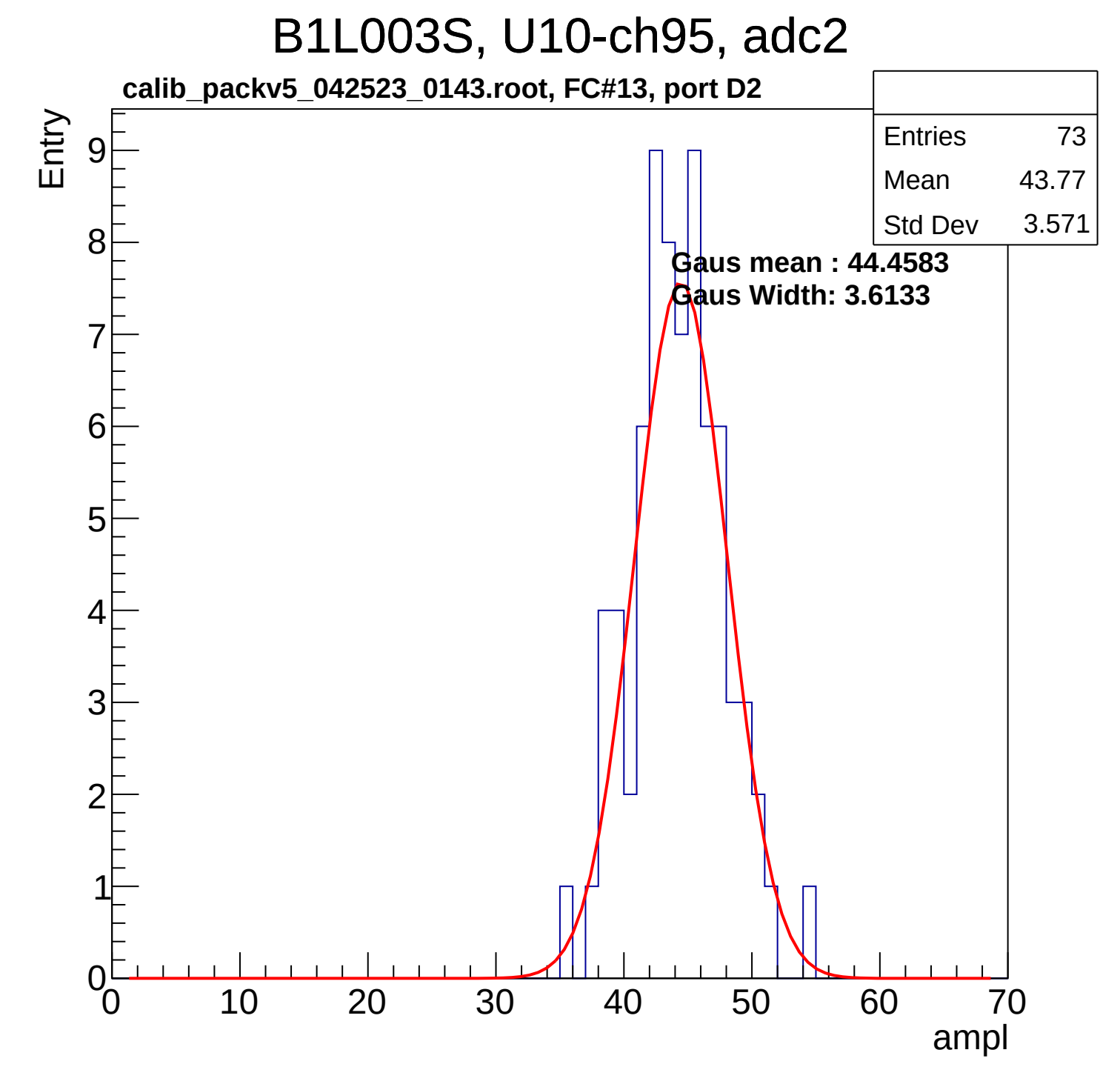
B1L003S, U10-ch95, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 43.77
Std Dev 3.571
Gaus mean : 44.4583
Gaus Width: 3.6133
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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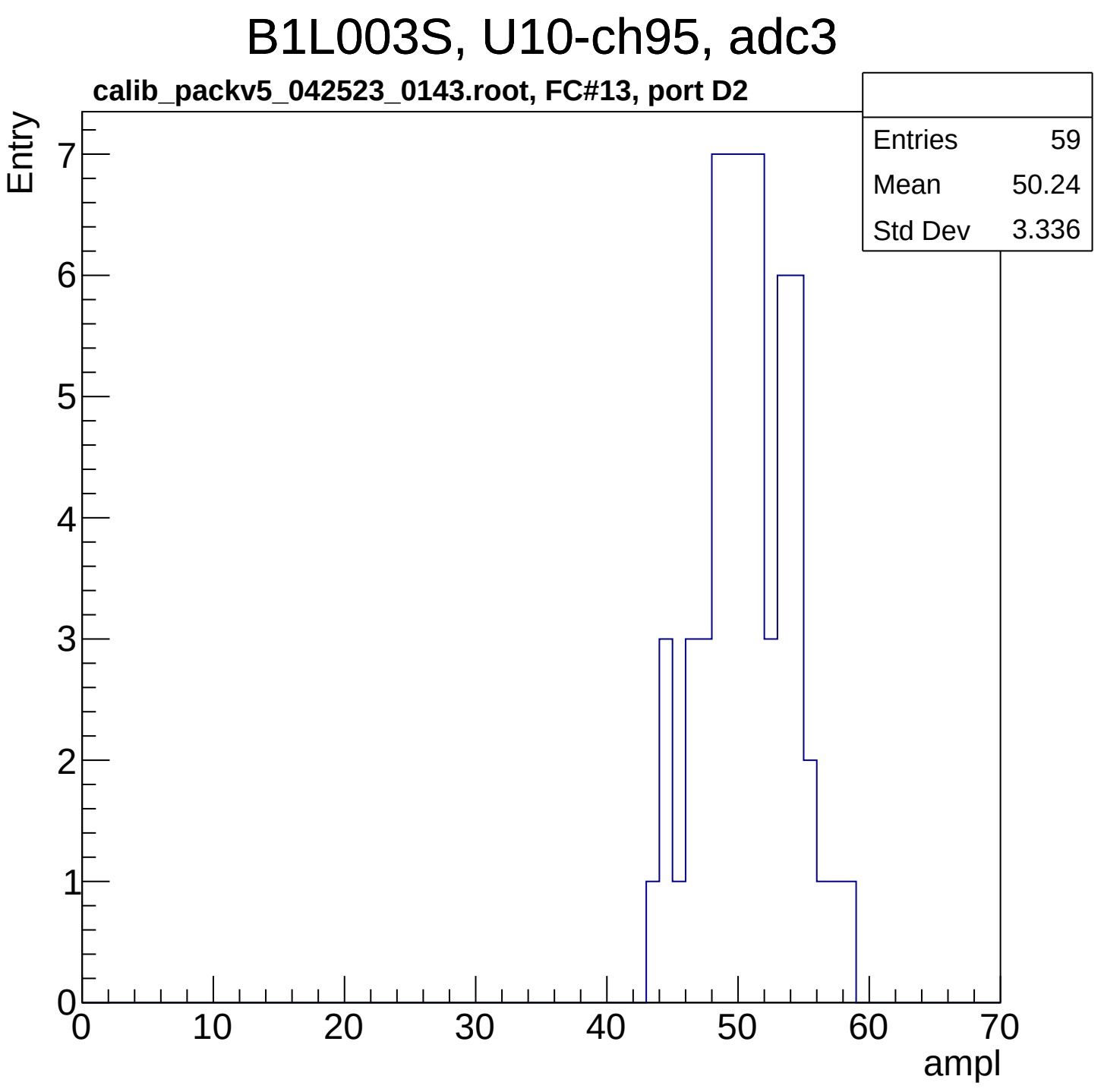
B1L003S, U10-ch95, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 50.24
Std Dev 3.336
Entry
ampl
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1
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4
5
6
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0
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20
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40
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60
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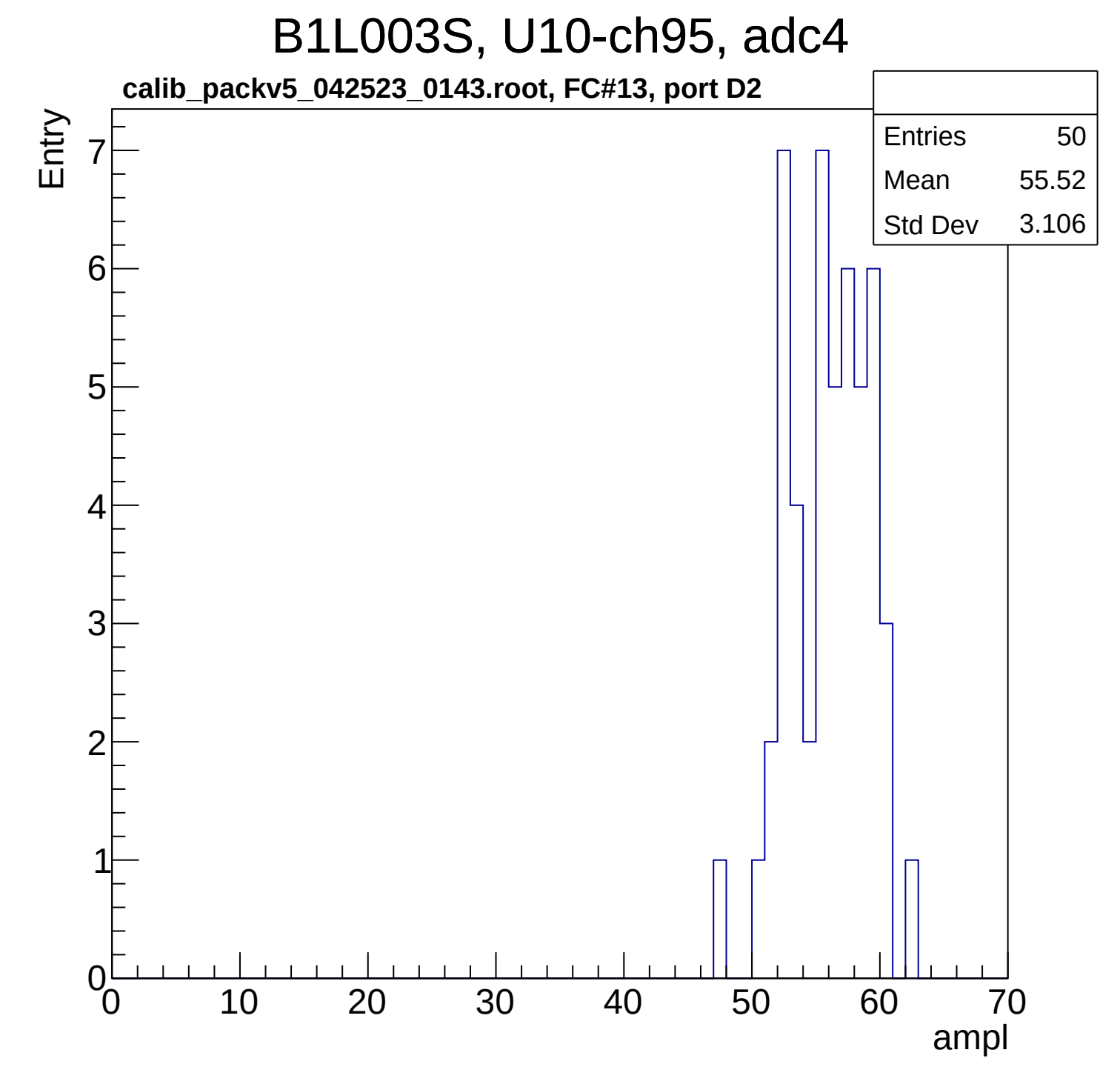
B1L003S, U10-ch95, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 55.52
Std Dev 3.106
Entry
ampl
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4
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0
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60
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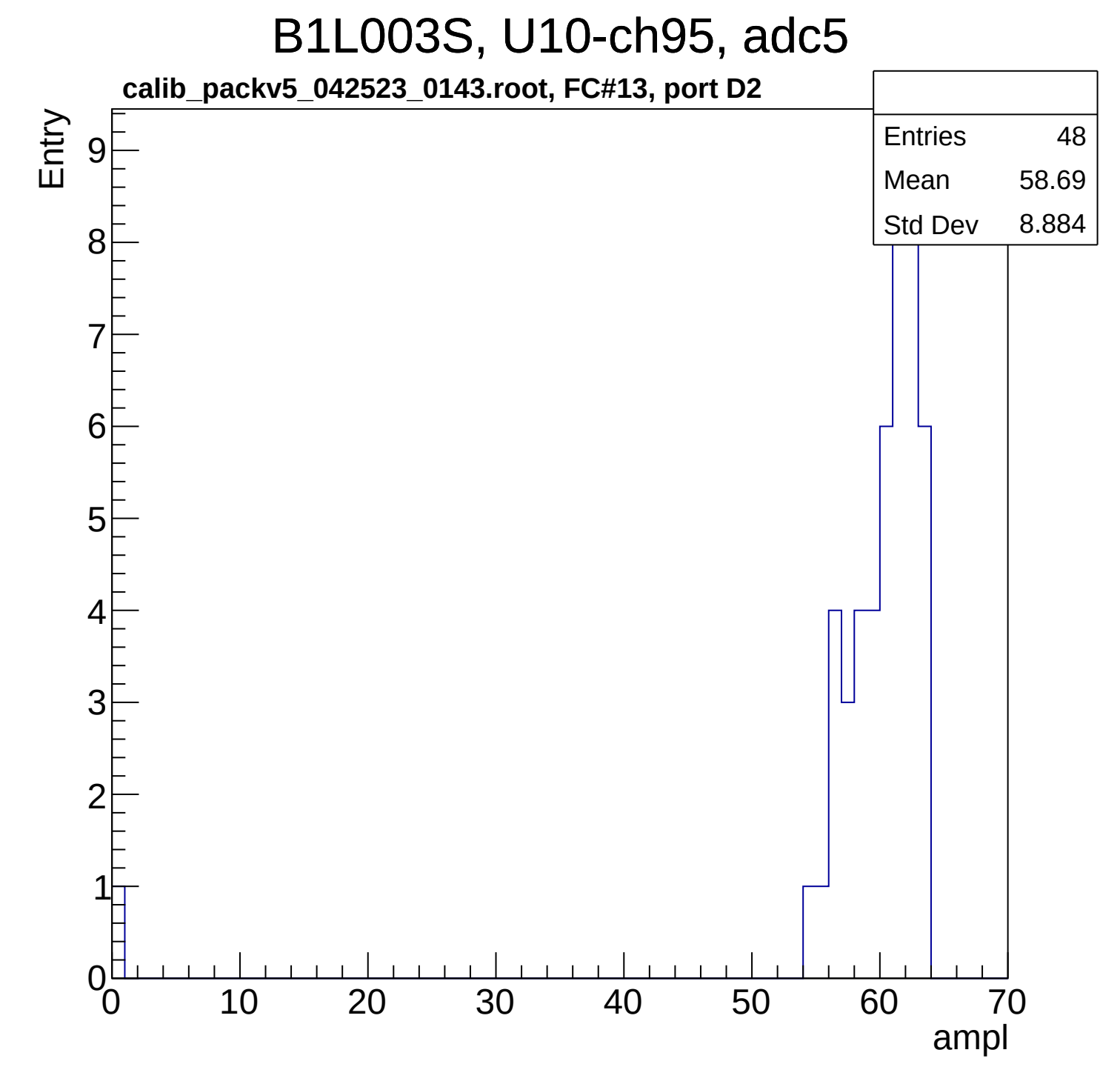
B1L003S, U10-ch95, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.69
Std Dev 8.884
Entry
ampl
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2
3
4
5
6
7
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0
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50
60
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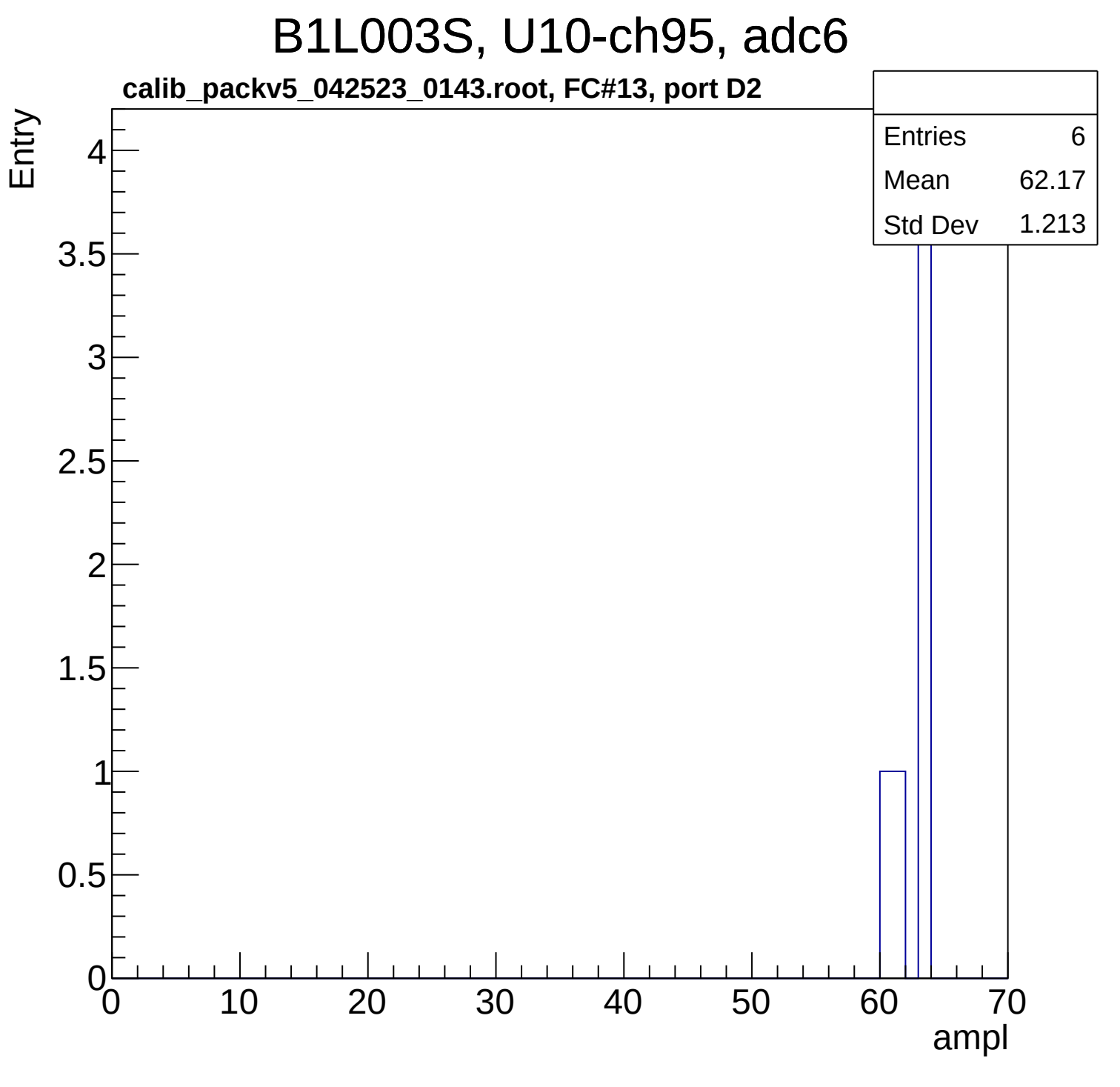
B1L003S, U10-ch95, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.17
Std Dev 1.213
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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50
60
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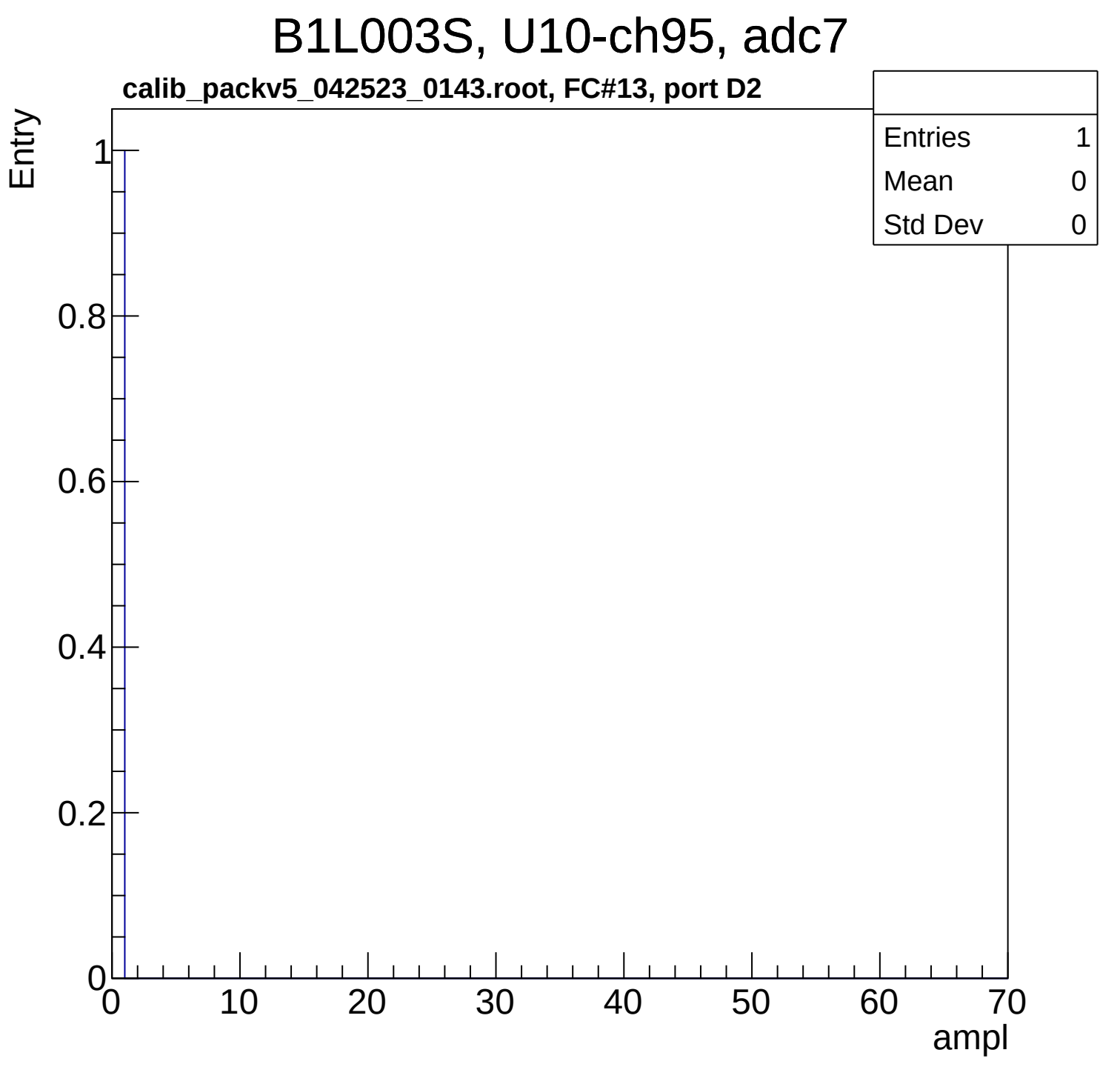
B1L003S, U10-ch95, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.2
0
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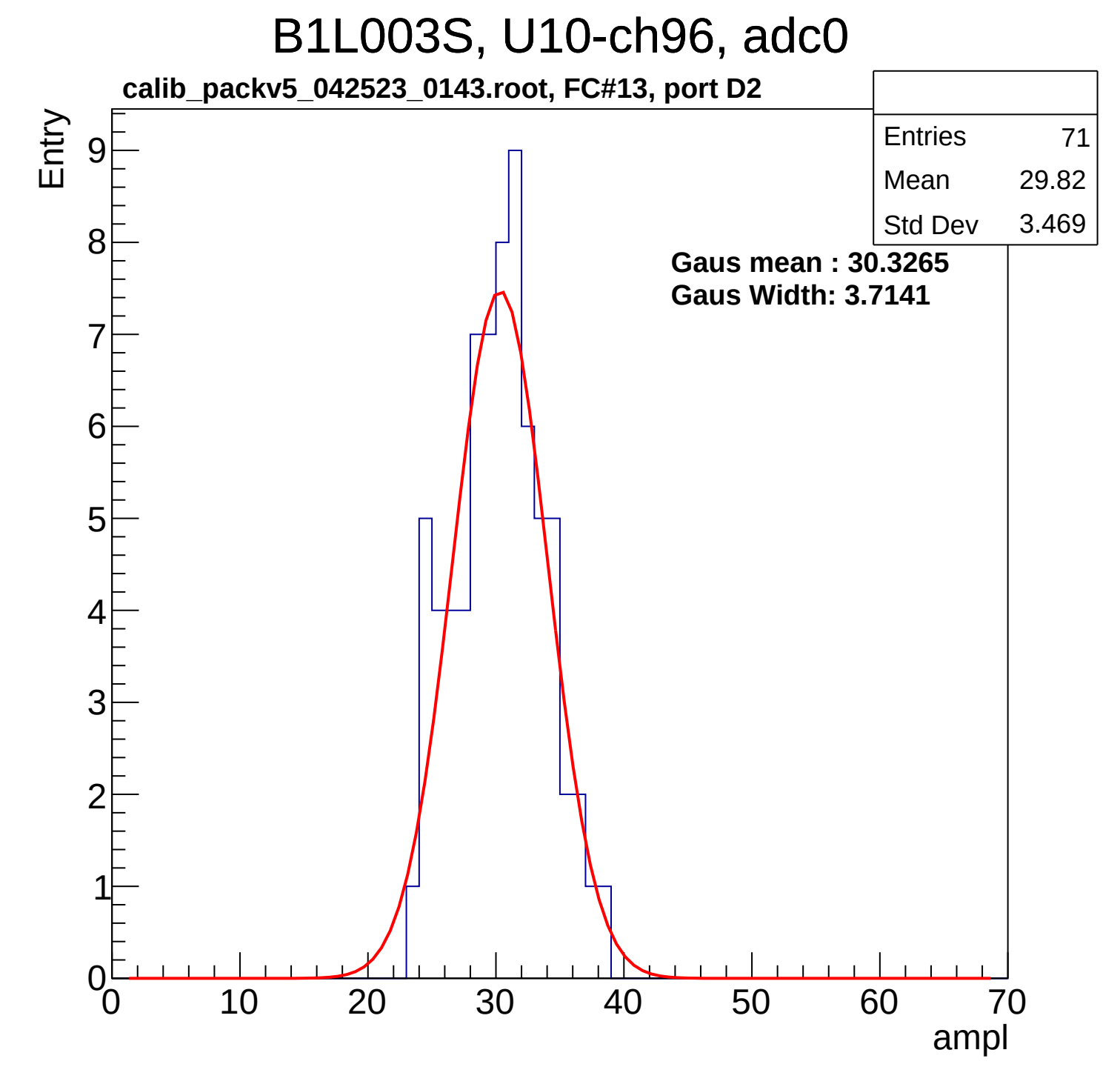
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch96, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.82
Std Dev 3.469
Gaus mean : 30.3265
Gaus Width: 3.7141
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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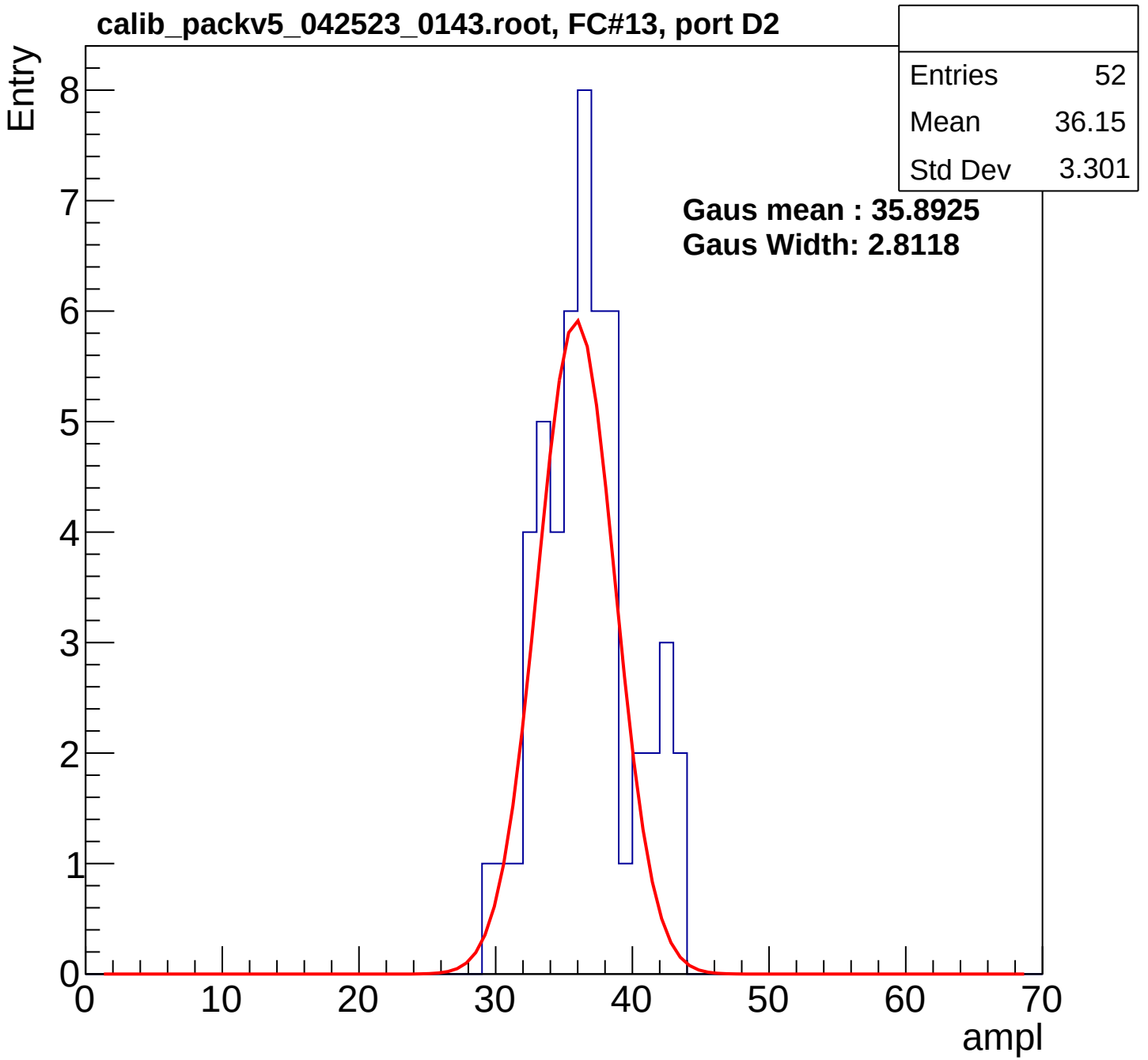
B1L003S, U10-ch96, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 36.15
Std Dev 3.301
Gaus mean : 35.8925
Gaus Width: 2.8118
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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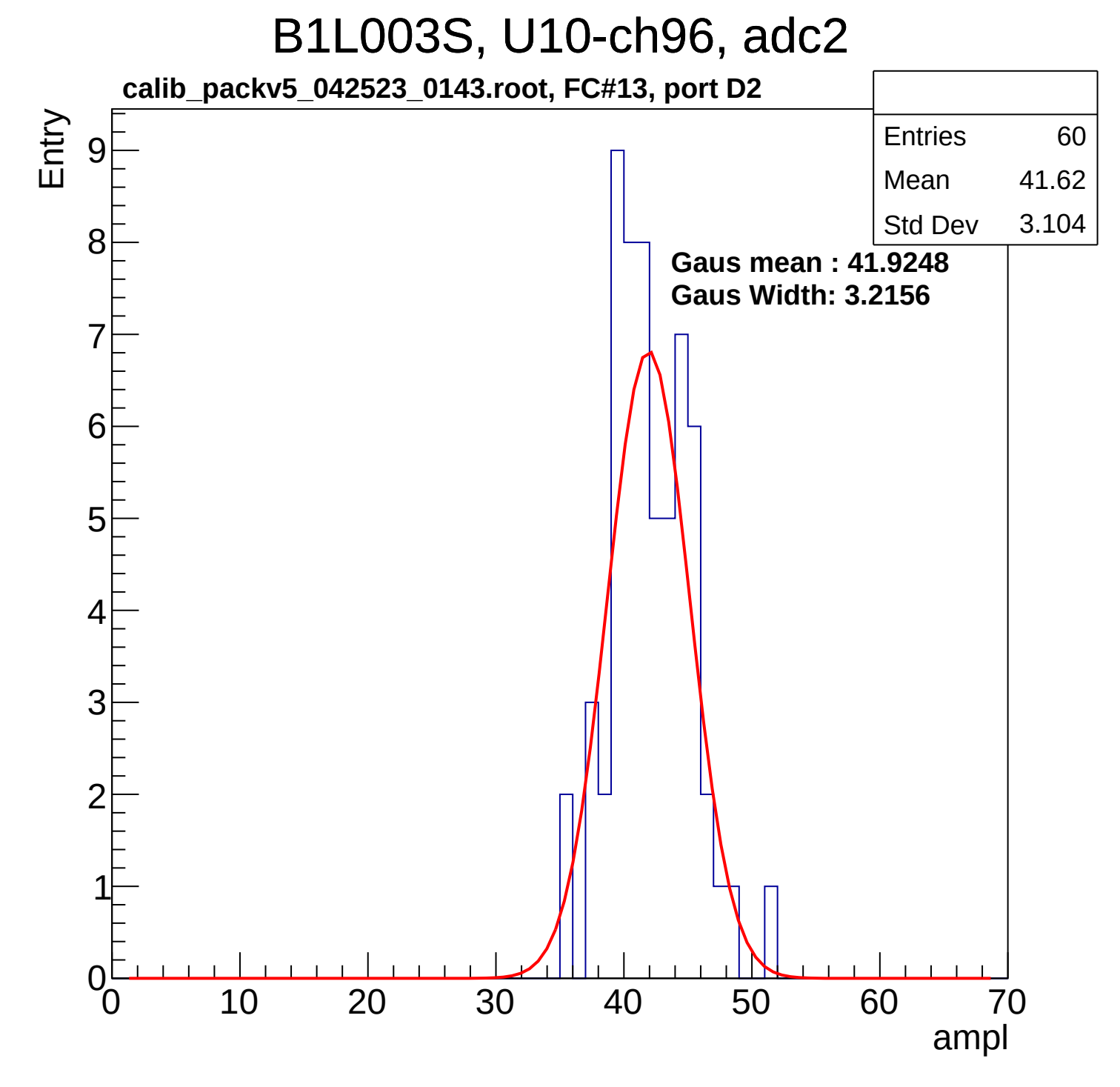
B1L003S, U10-ch96, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.62
Std Dev 3.104
Gaus mean : 41.9248
Gaus Width: 3.2156
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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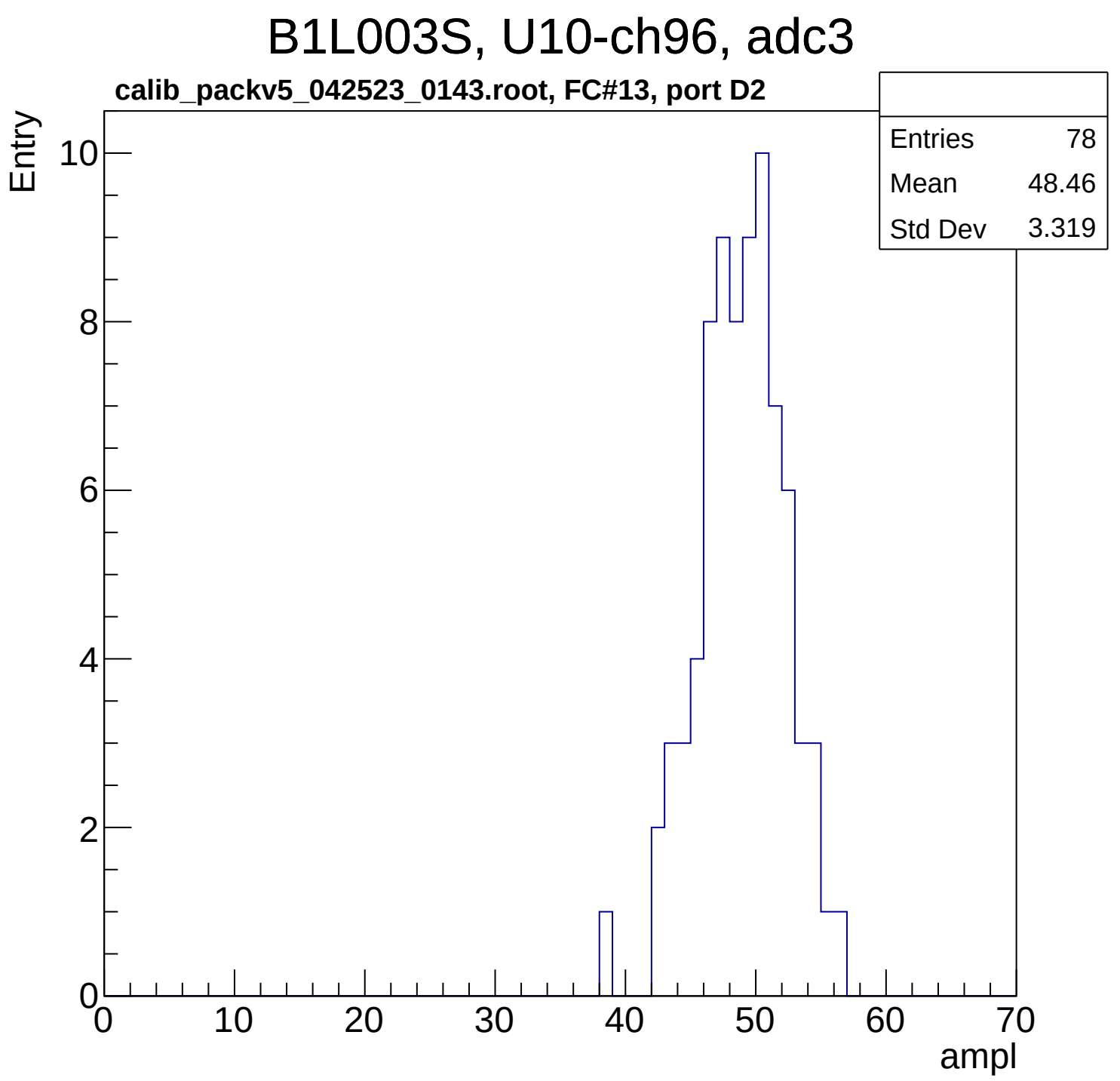
B1L003S, U10-ch96, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 48.46
Std Dev 3.319
Entry
ampl
0
2
4
6
8
10
0
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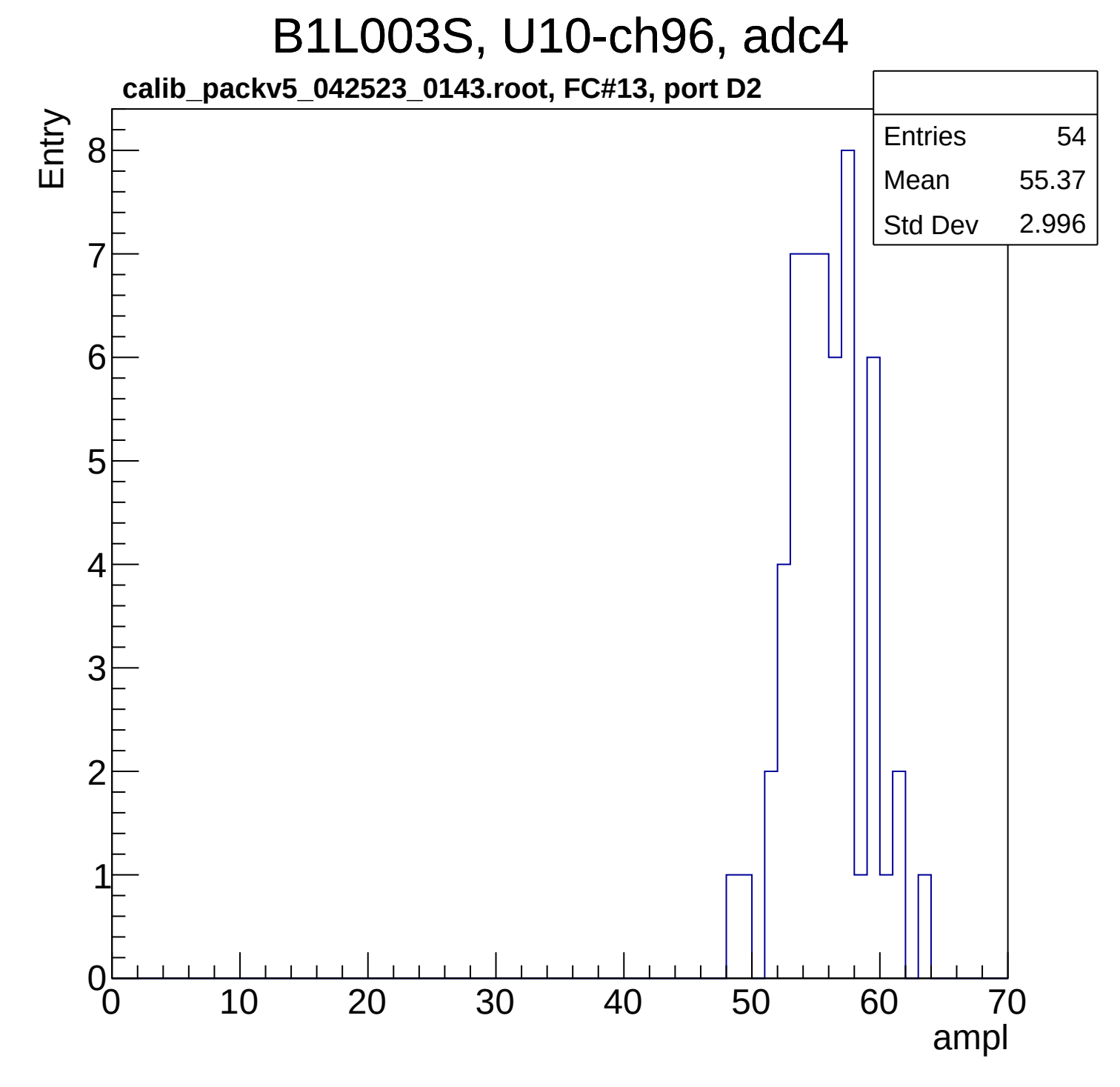
20
30
40
50
60
70


B1L003S, U10-ch96, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.37
Std Dev 2.996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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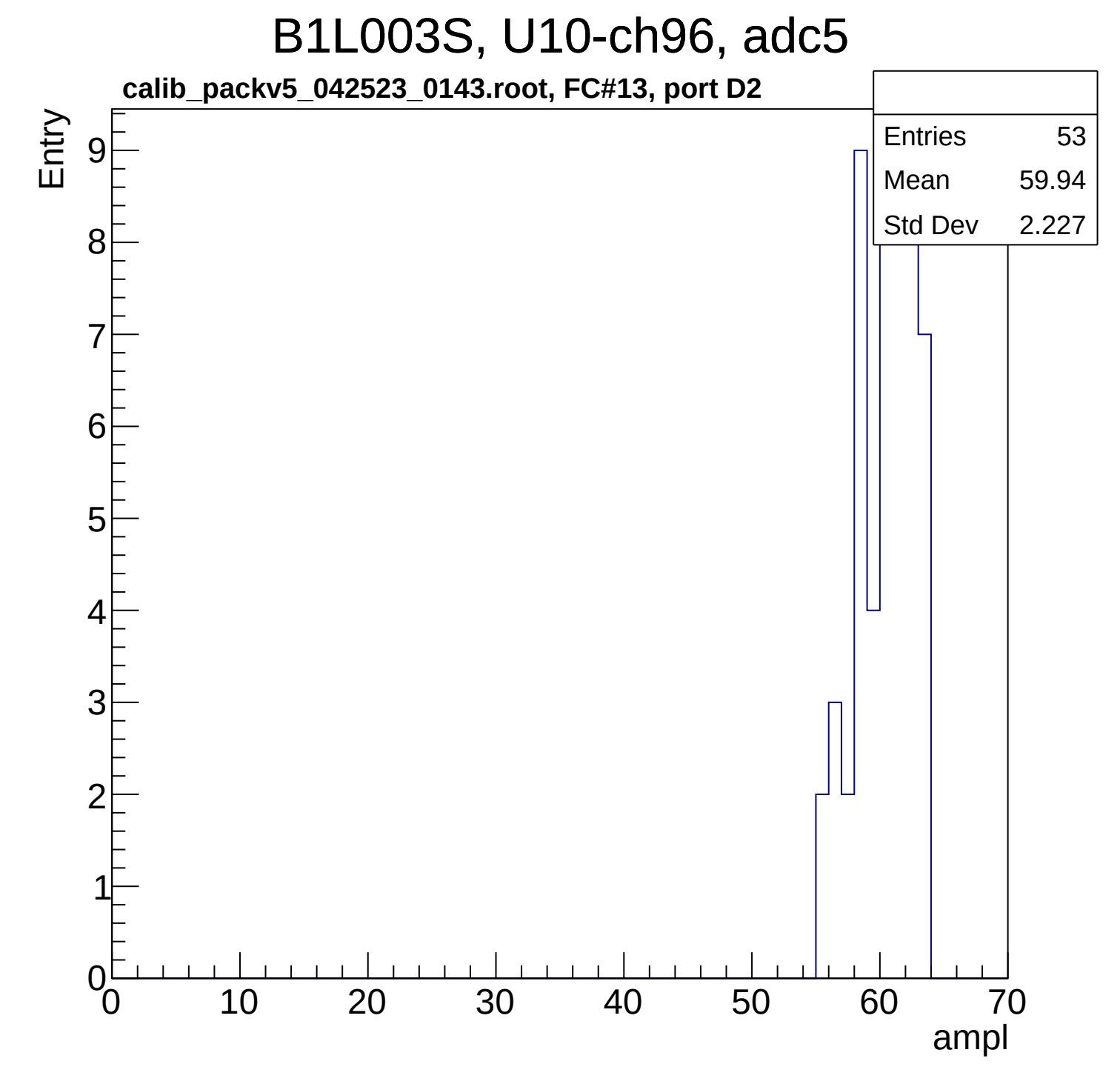
B1L003S, U10-ch96, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.94
Std Dev 2.227
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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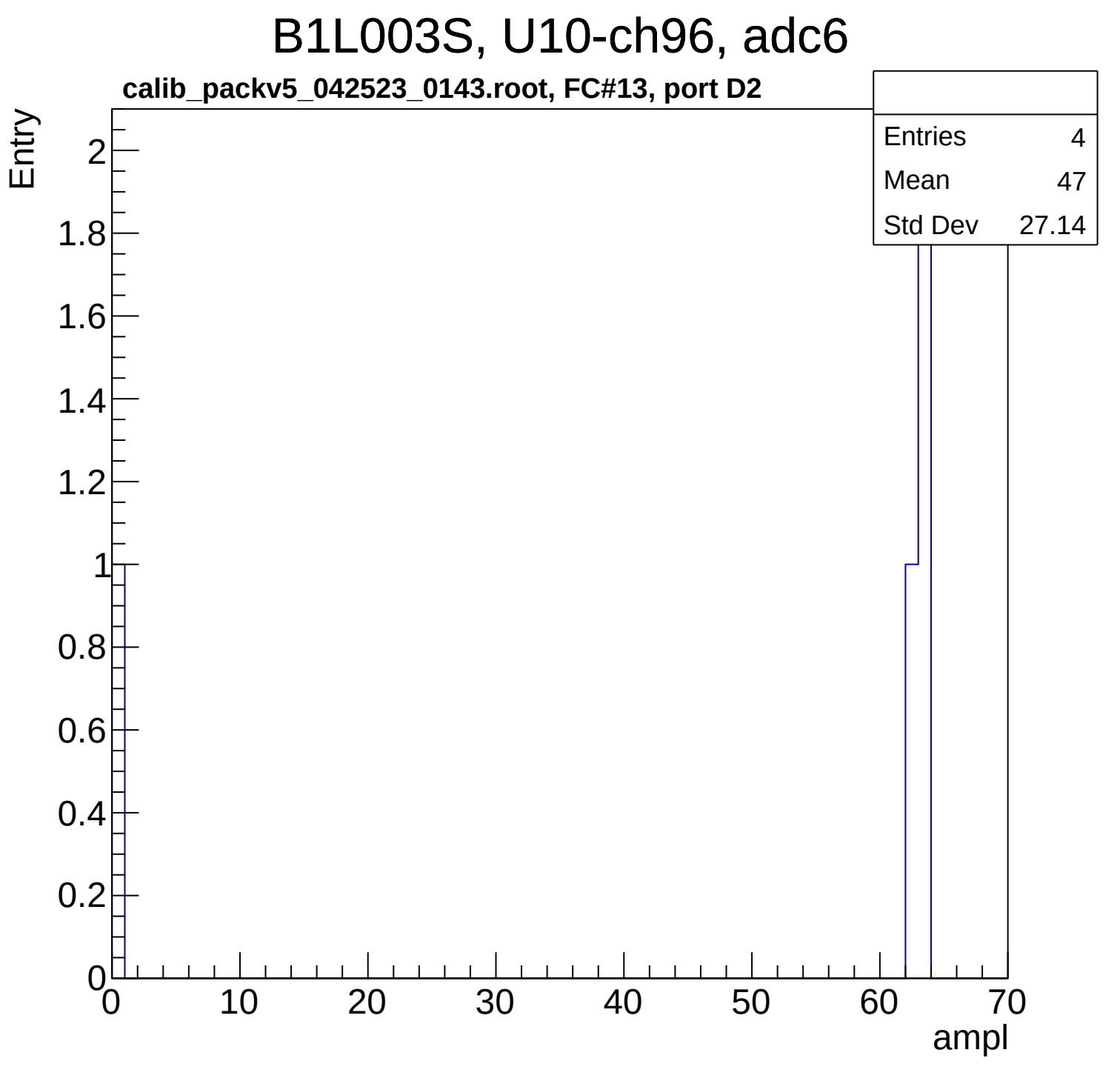
B1L003S, U10-ch96, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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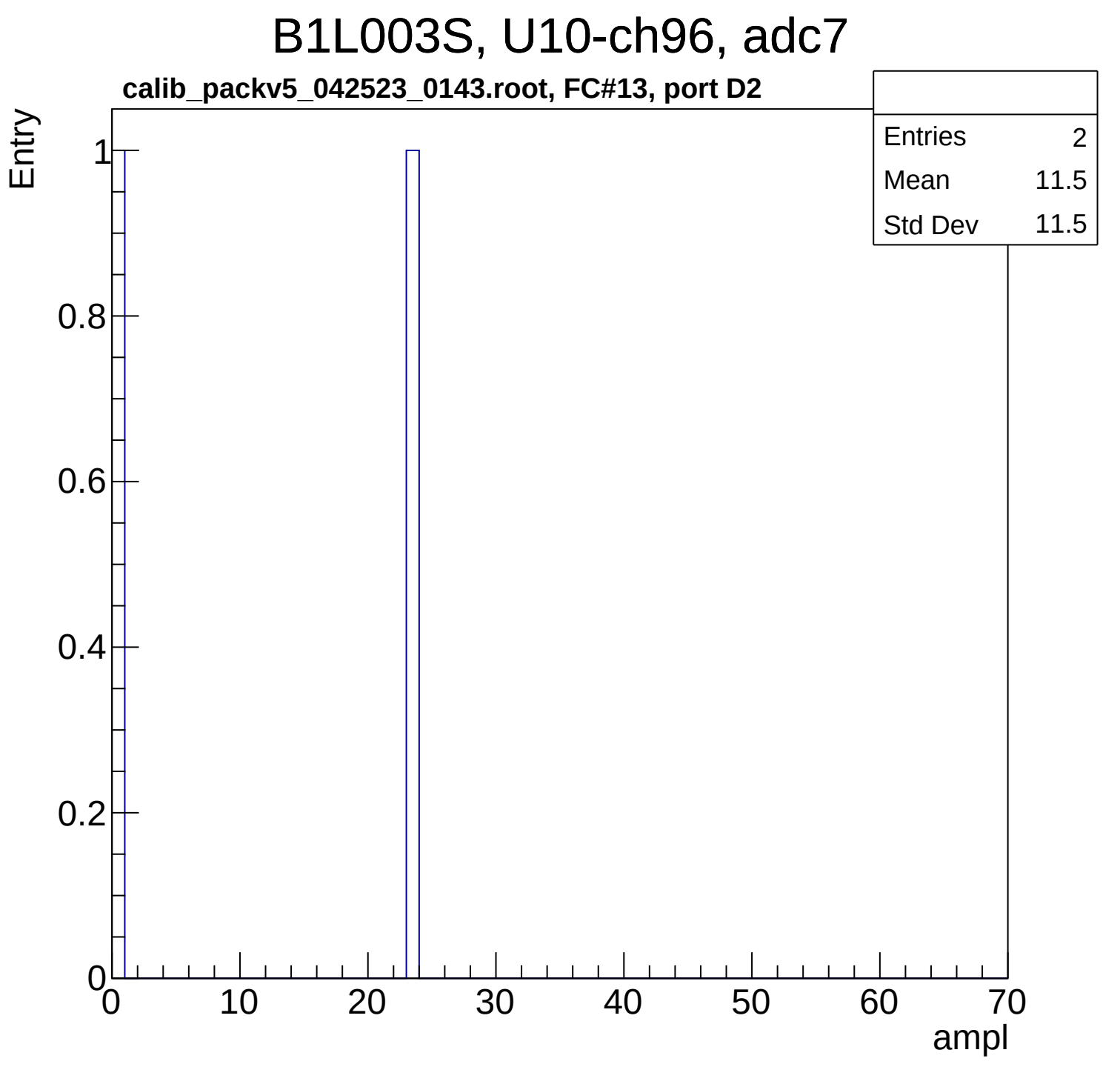
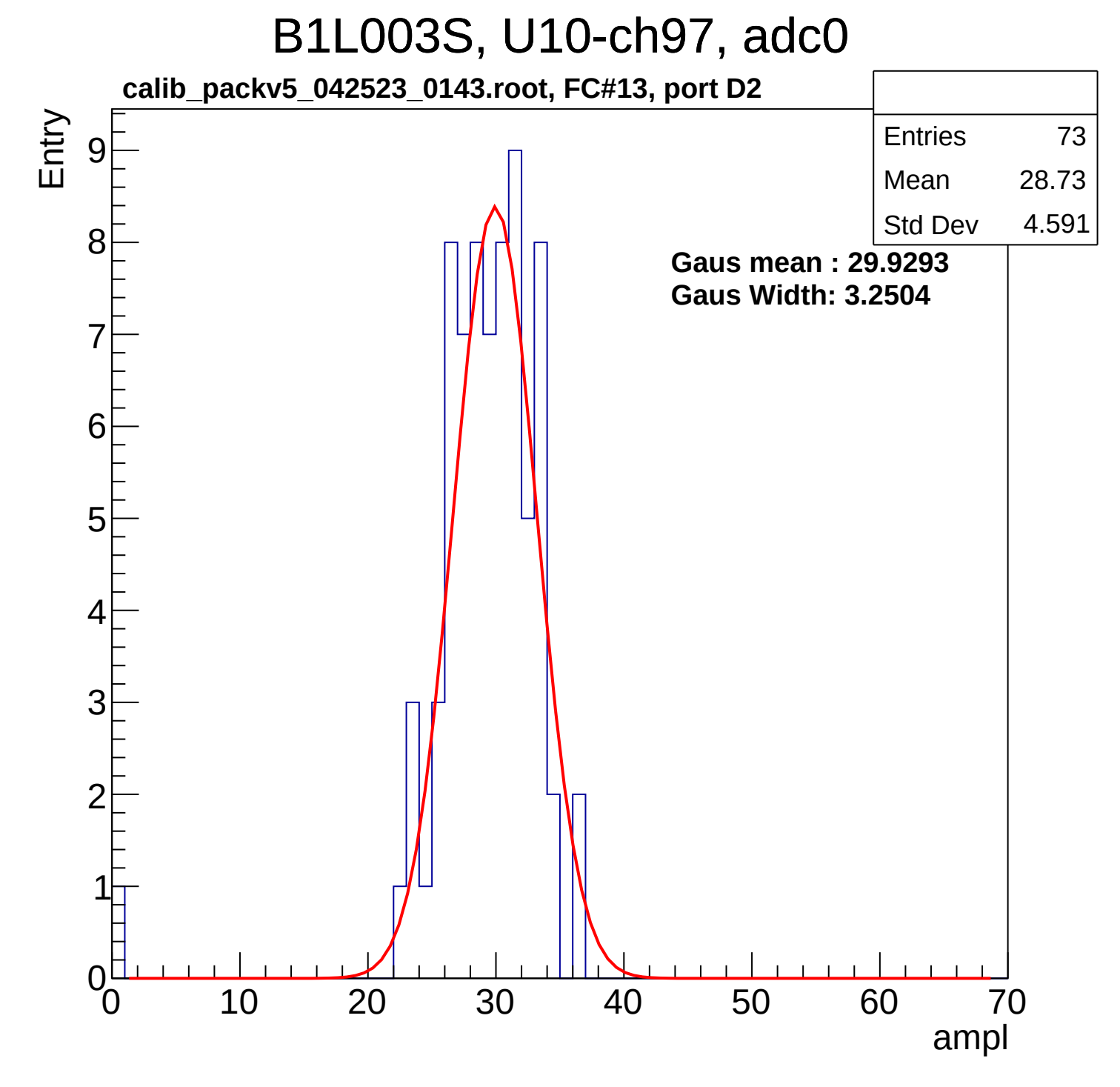

B1L003S, U10-ch96, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch97, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 28.73
Std Dev 4.591
Gaus mean : 29.9293
Gaus Width: 3.2504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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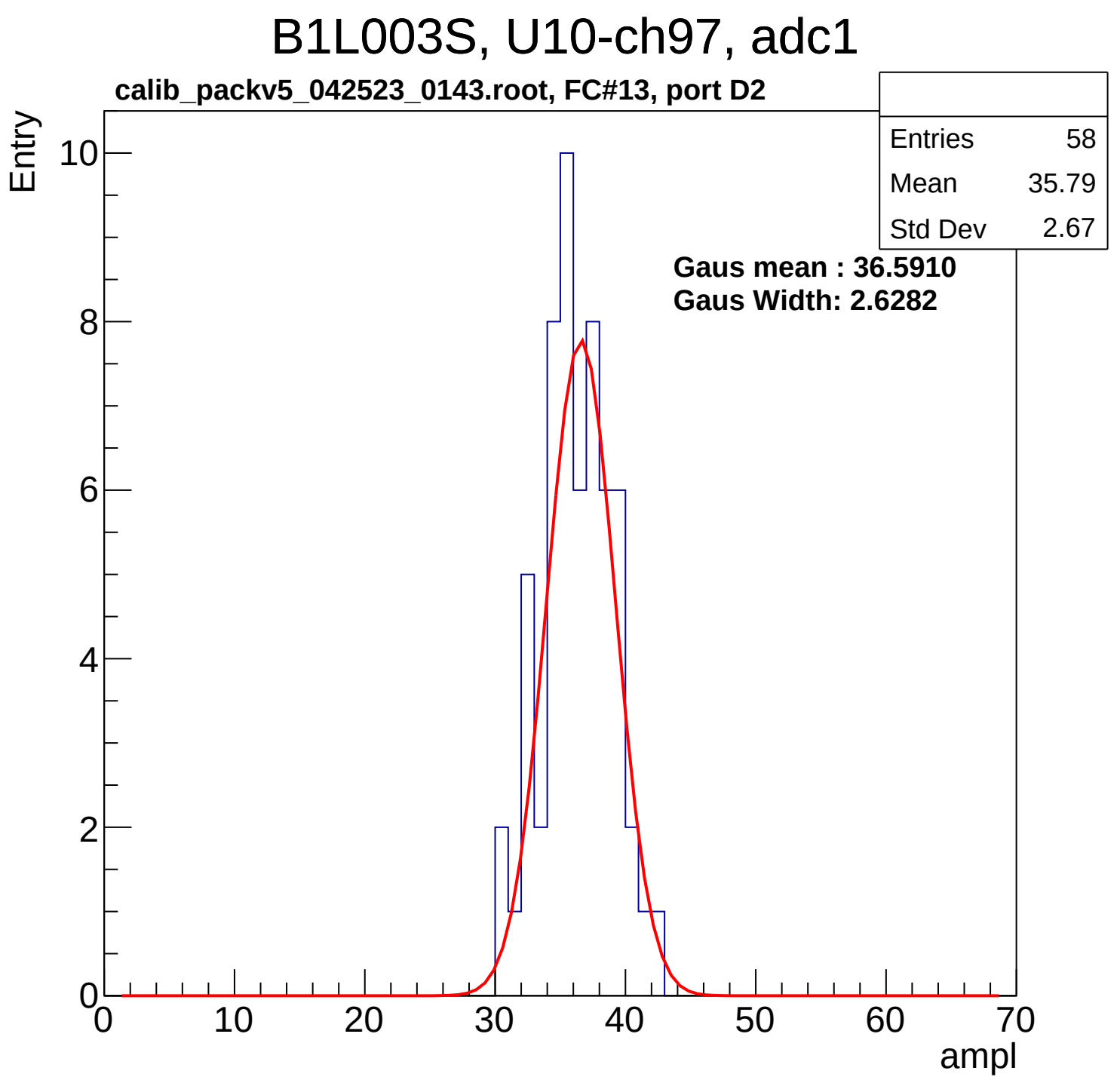
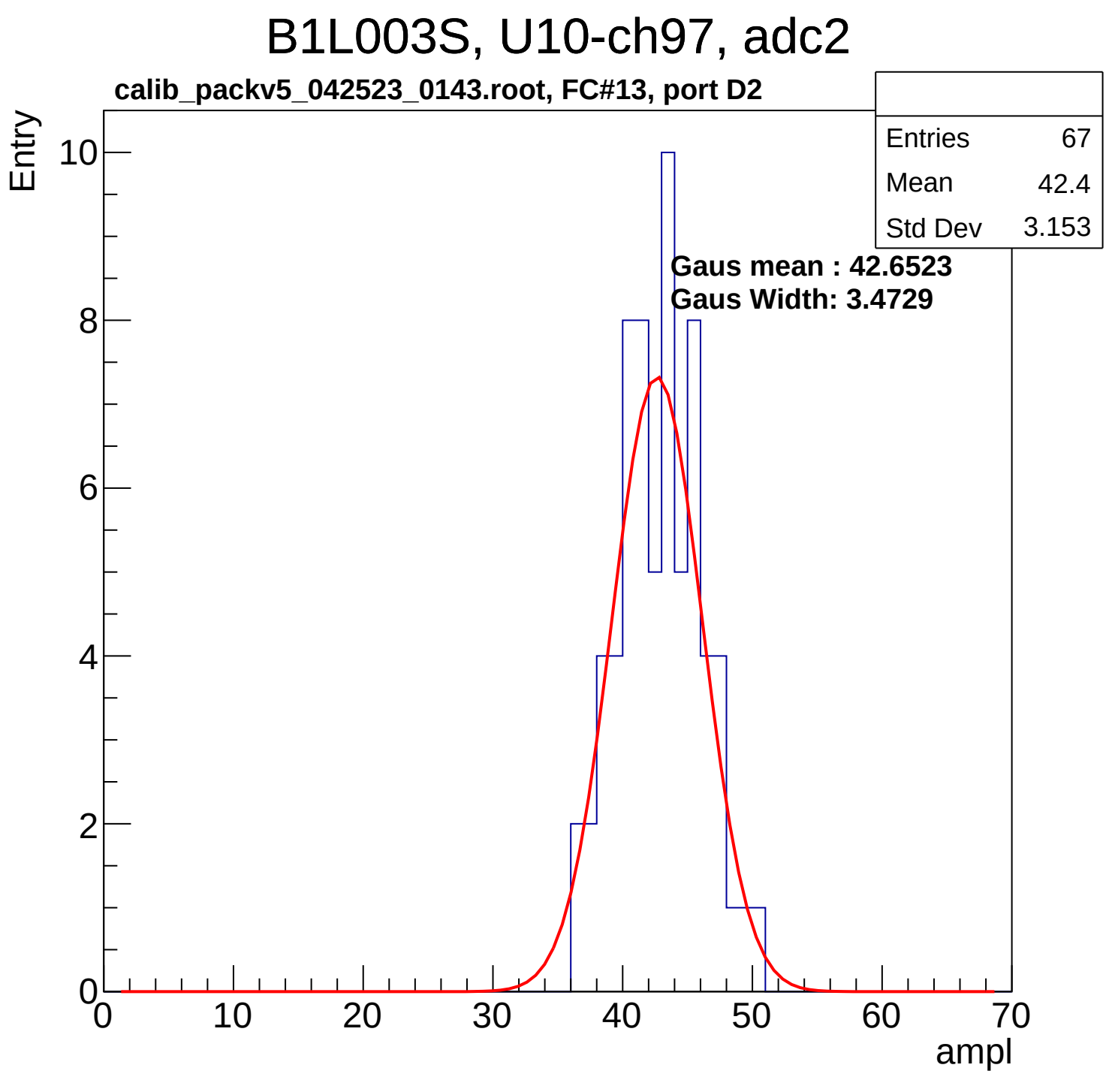
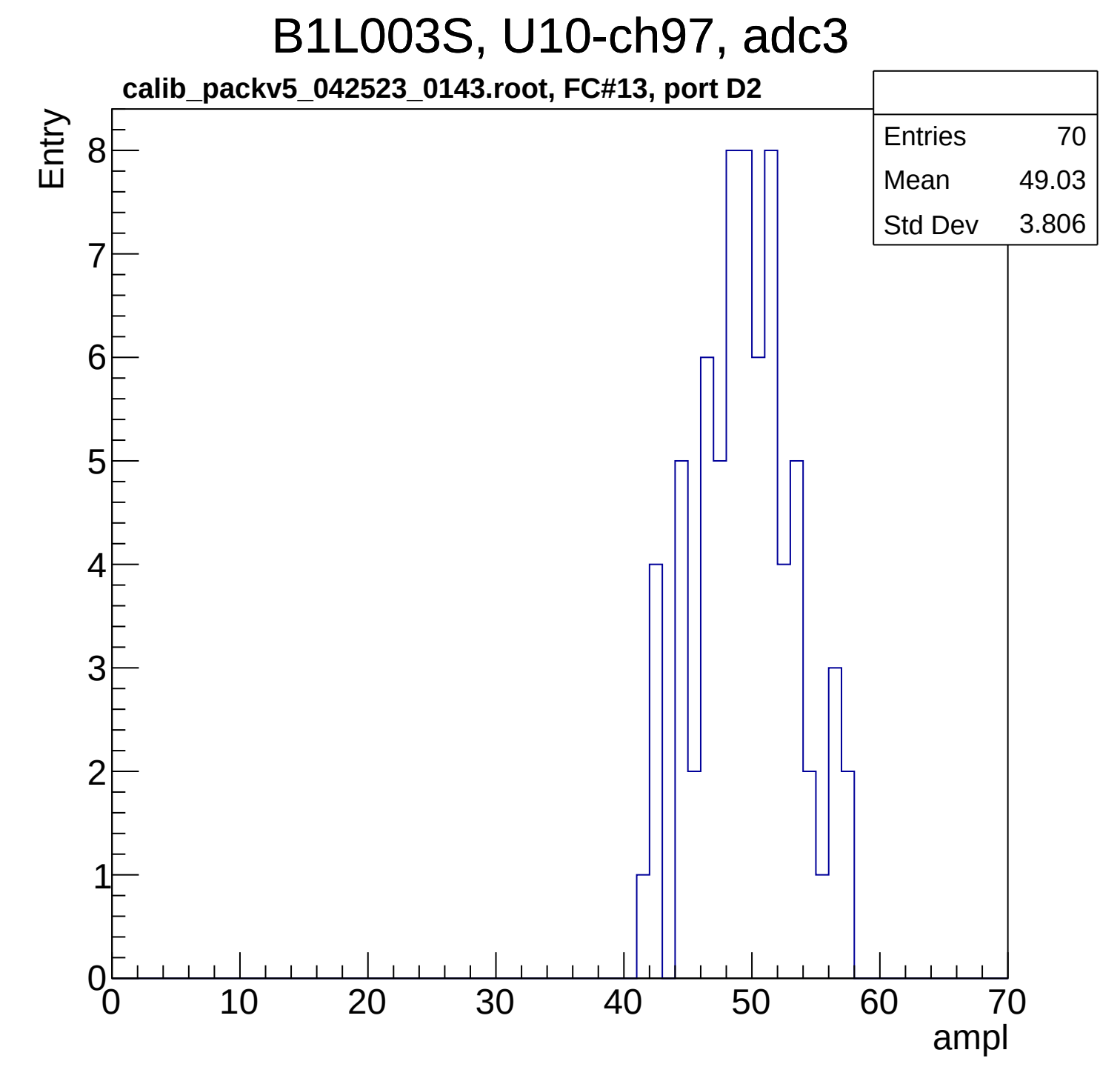
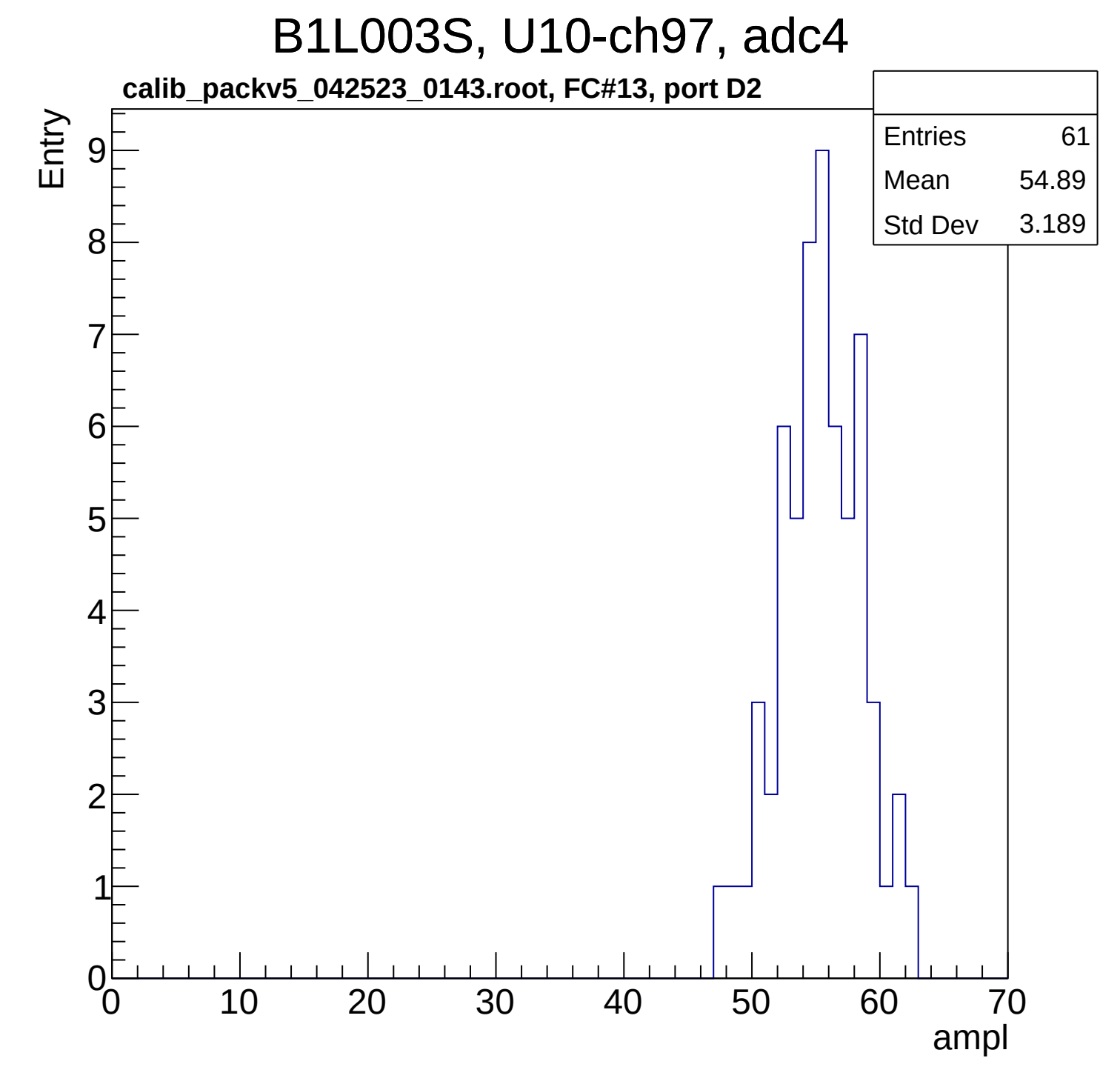
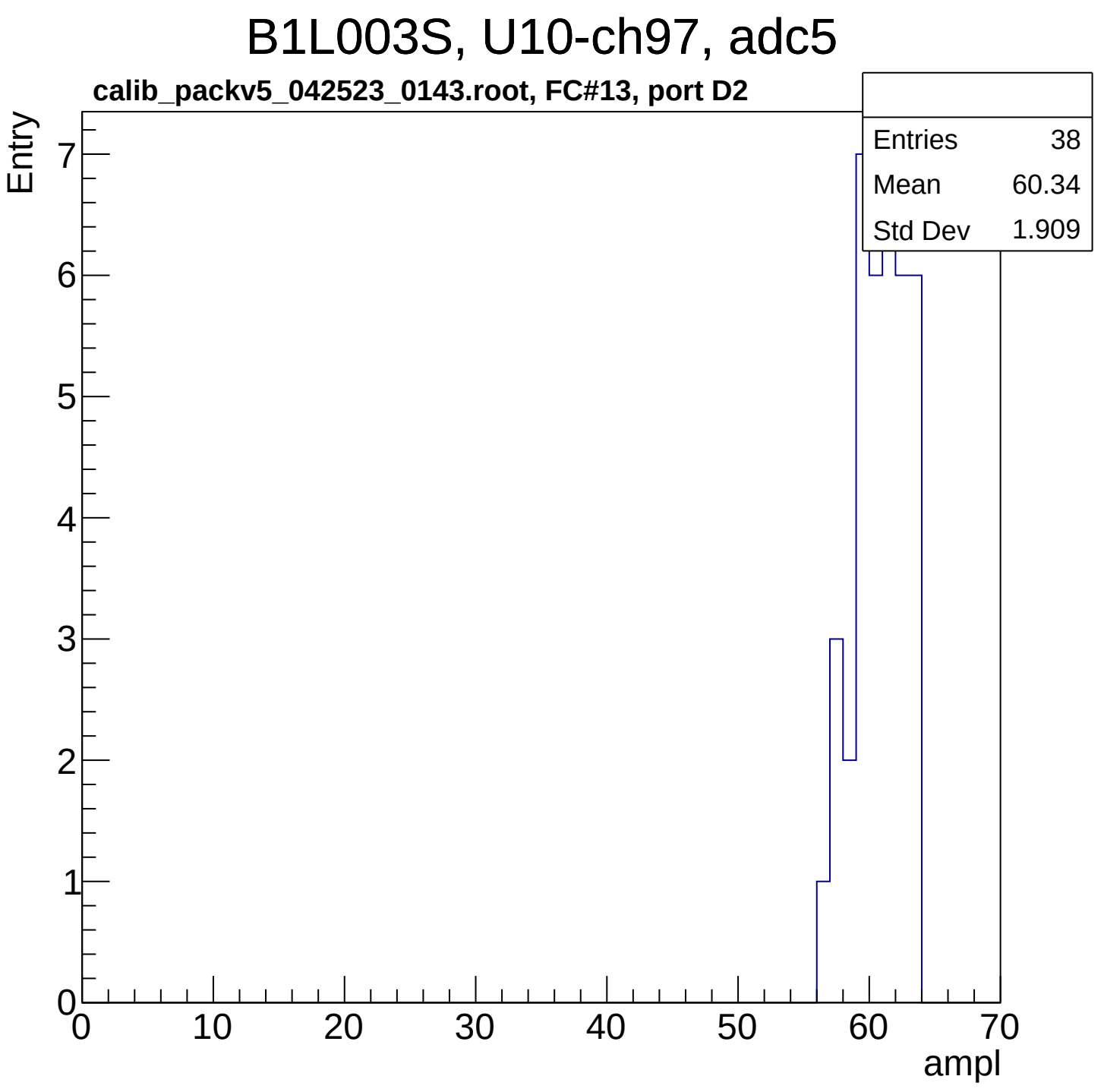
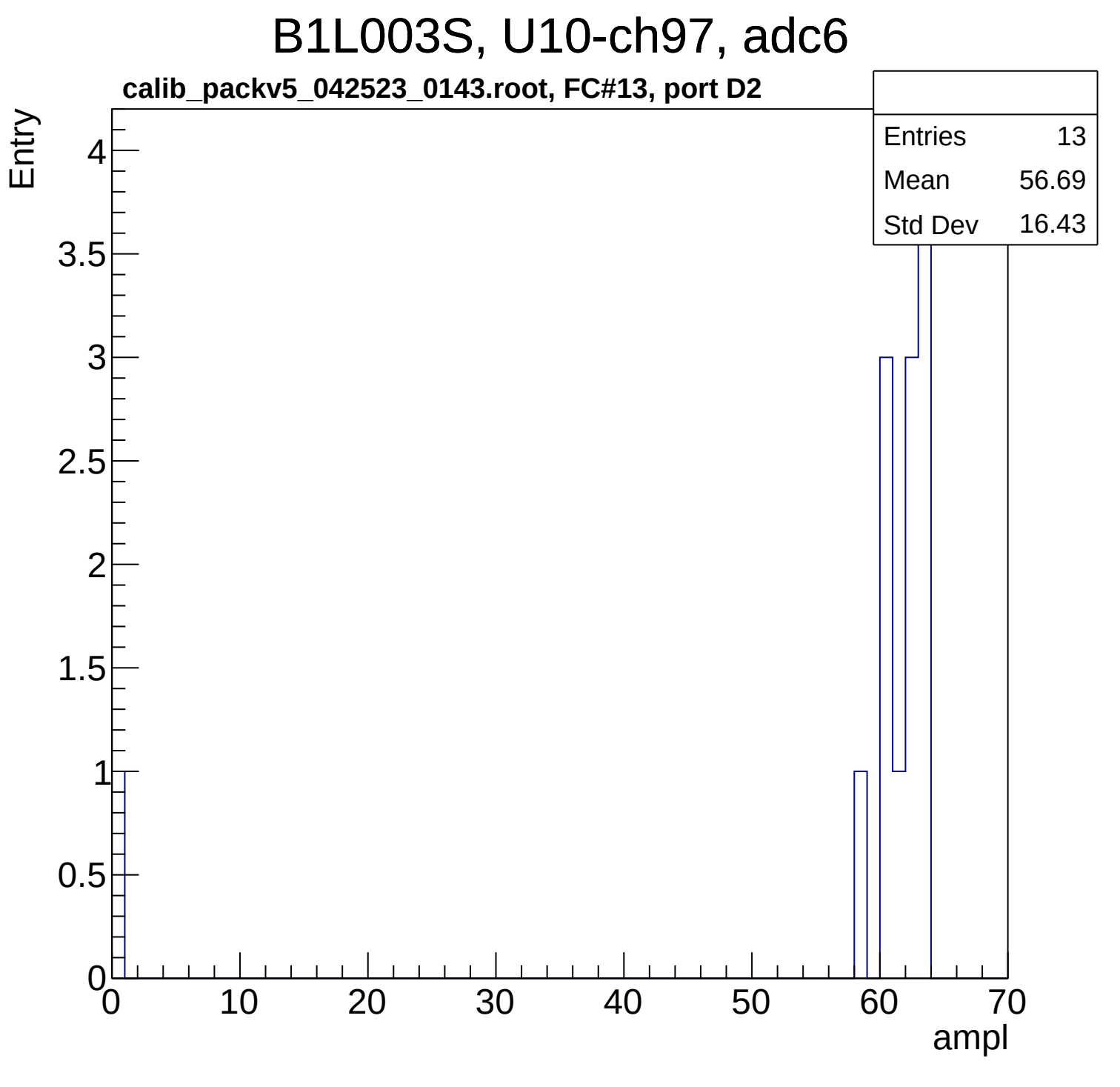

B1L003S, U10-ch97, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 35.79
Std Dev 2.67
Gaus mean : 36.5910
Gaus Width: 2.6282
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

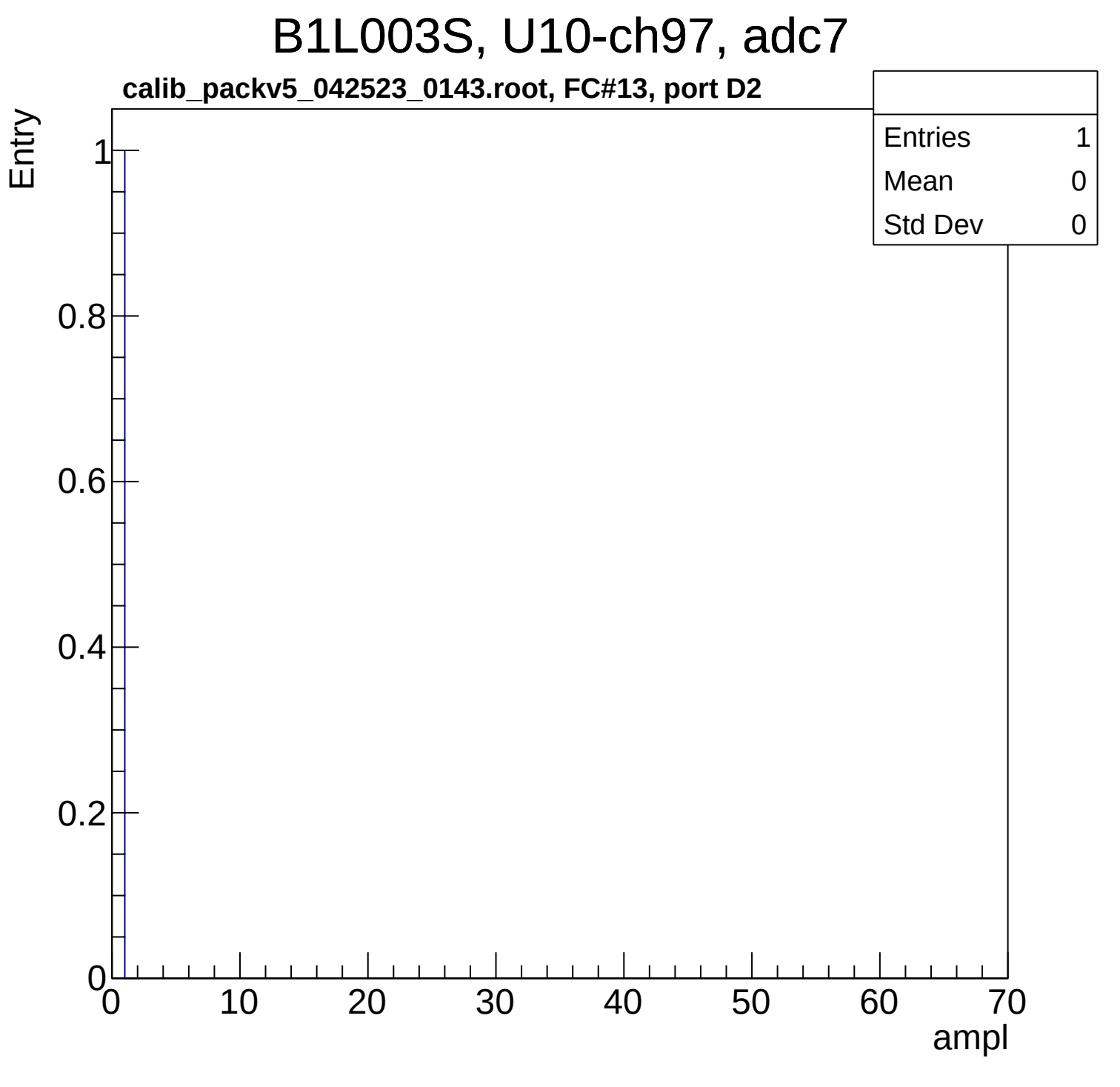

B1L003S, U10-ch97, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.4
Std Dev 3.153
Gaus mean : 42.6523
Gaus Width: 3.4729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

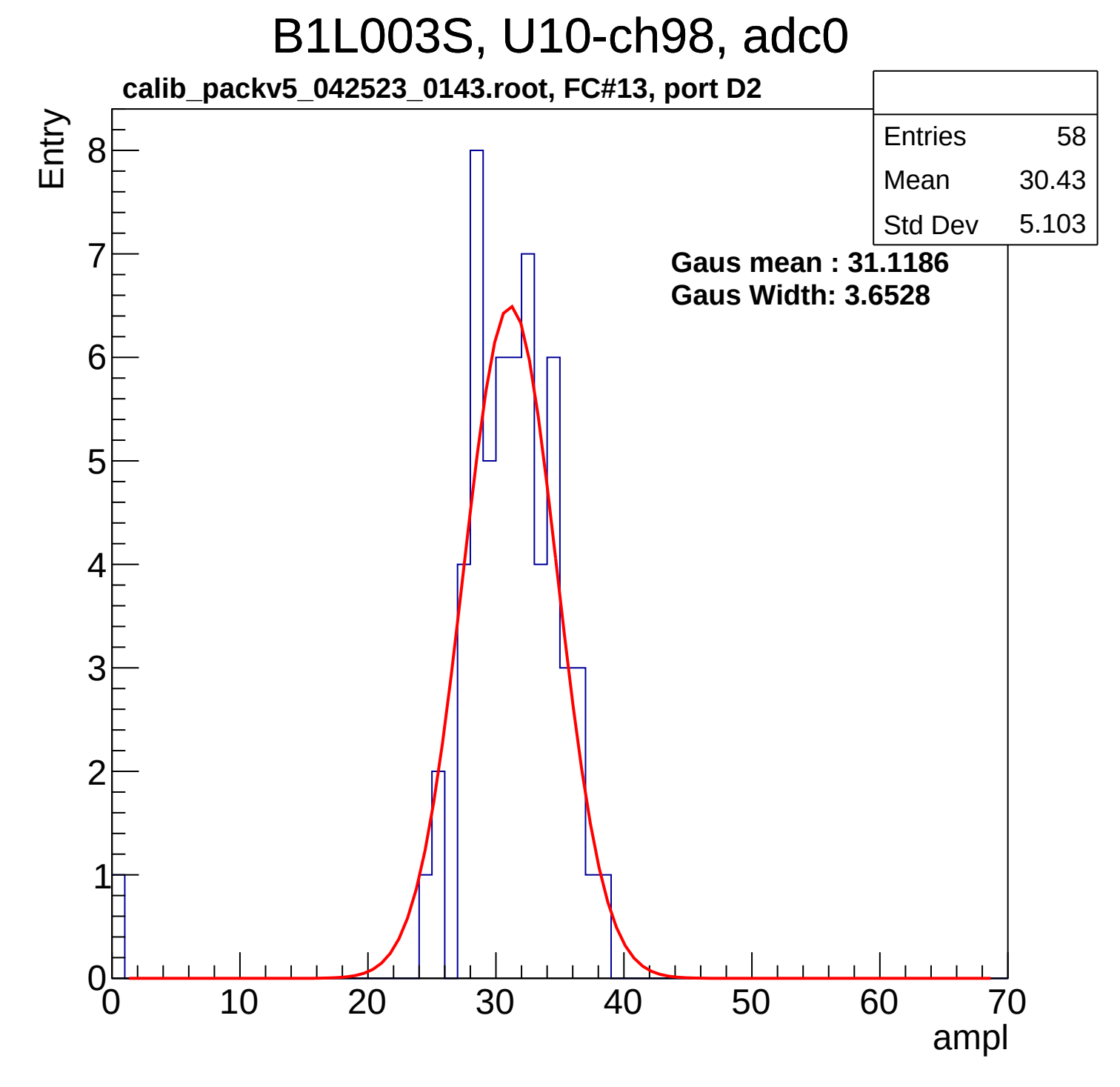

B1L003S, U10-ch97, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 49.03
Std Dev 3.806
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

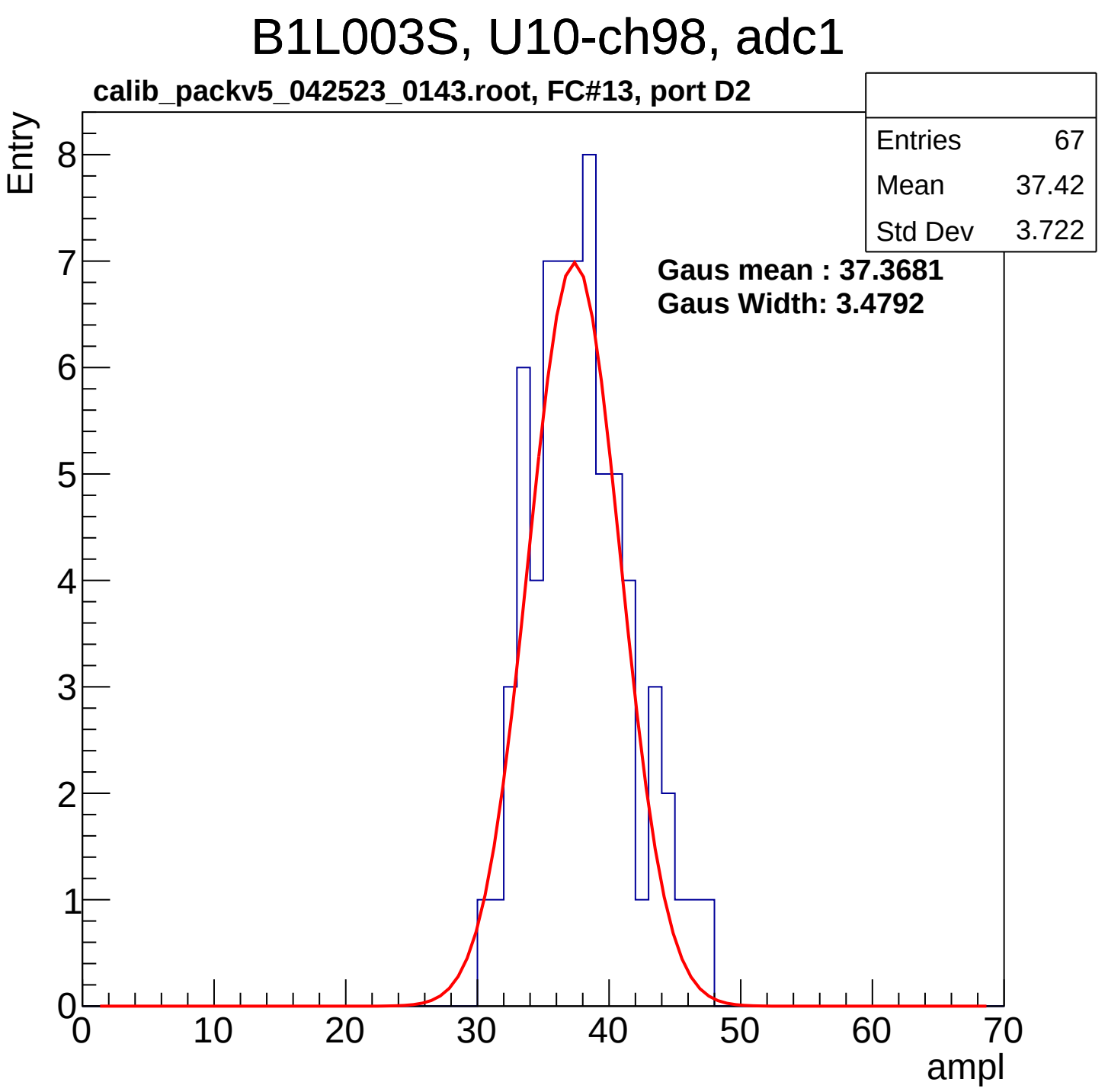

B1L003S, U10-ch97, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.89
Std Dev 3.189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

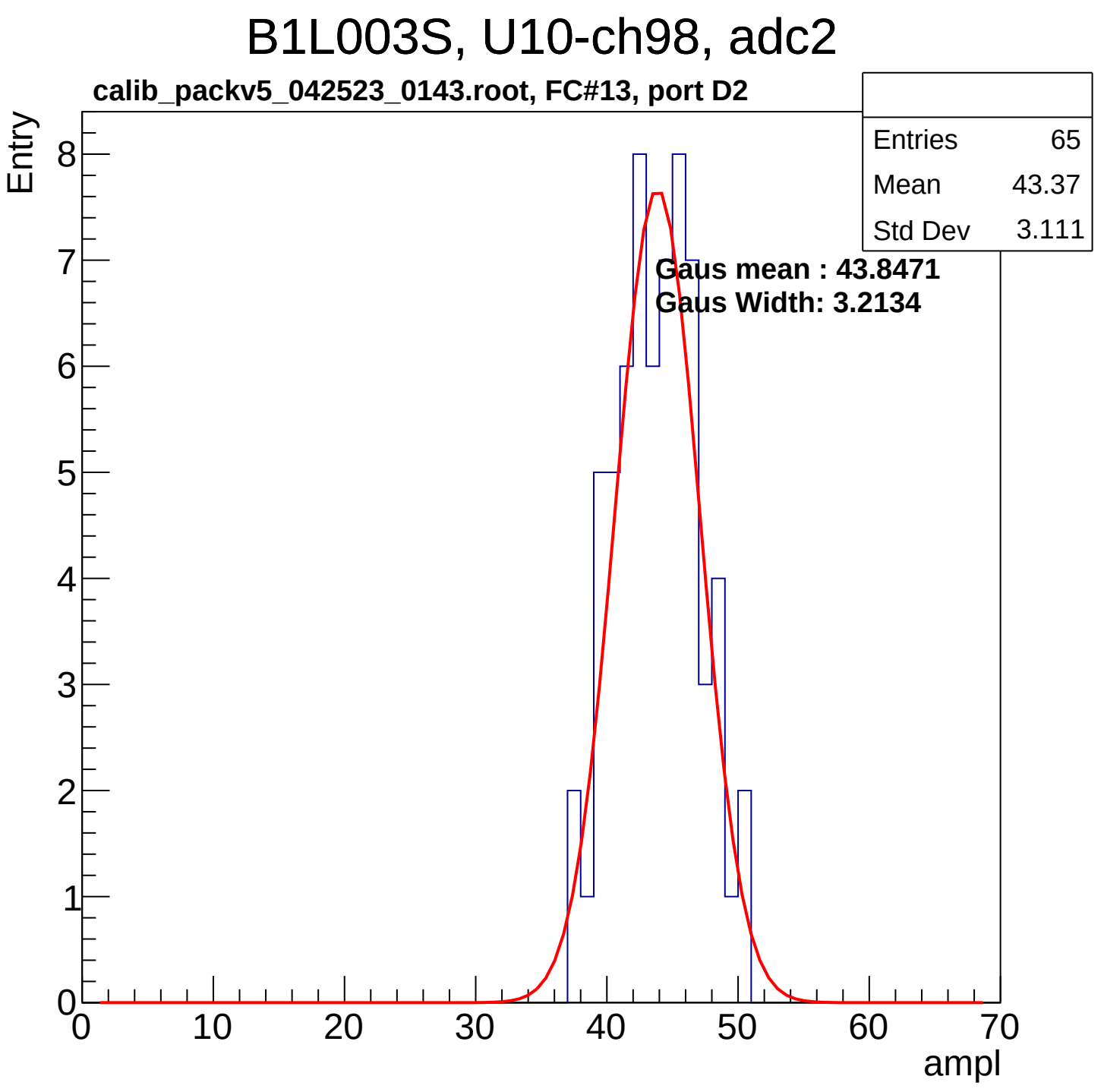

B1L003S, U10-ch97, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 60.34
Std Dev 1.909
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

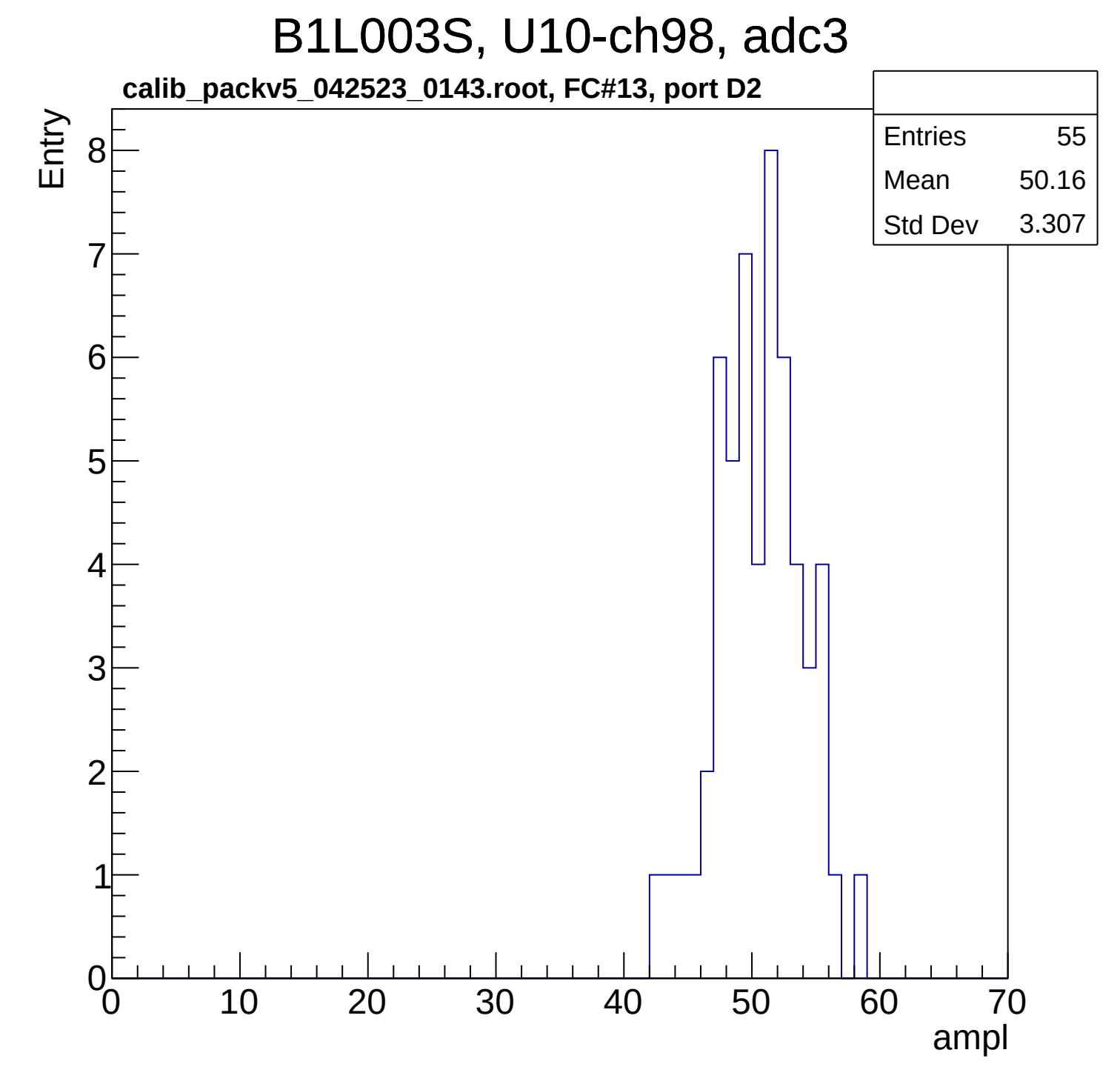

B1L003S, U10-ch97, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.69
Std Dev 16.43
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

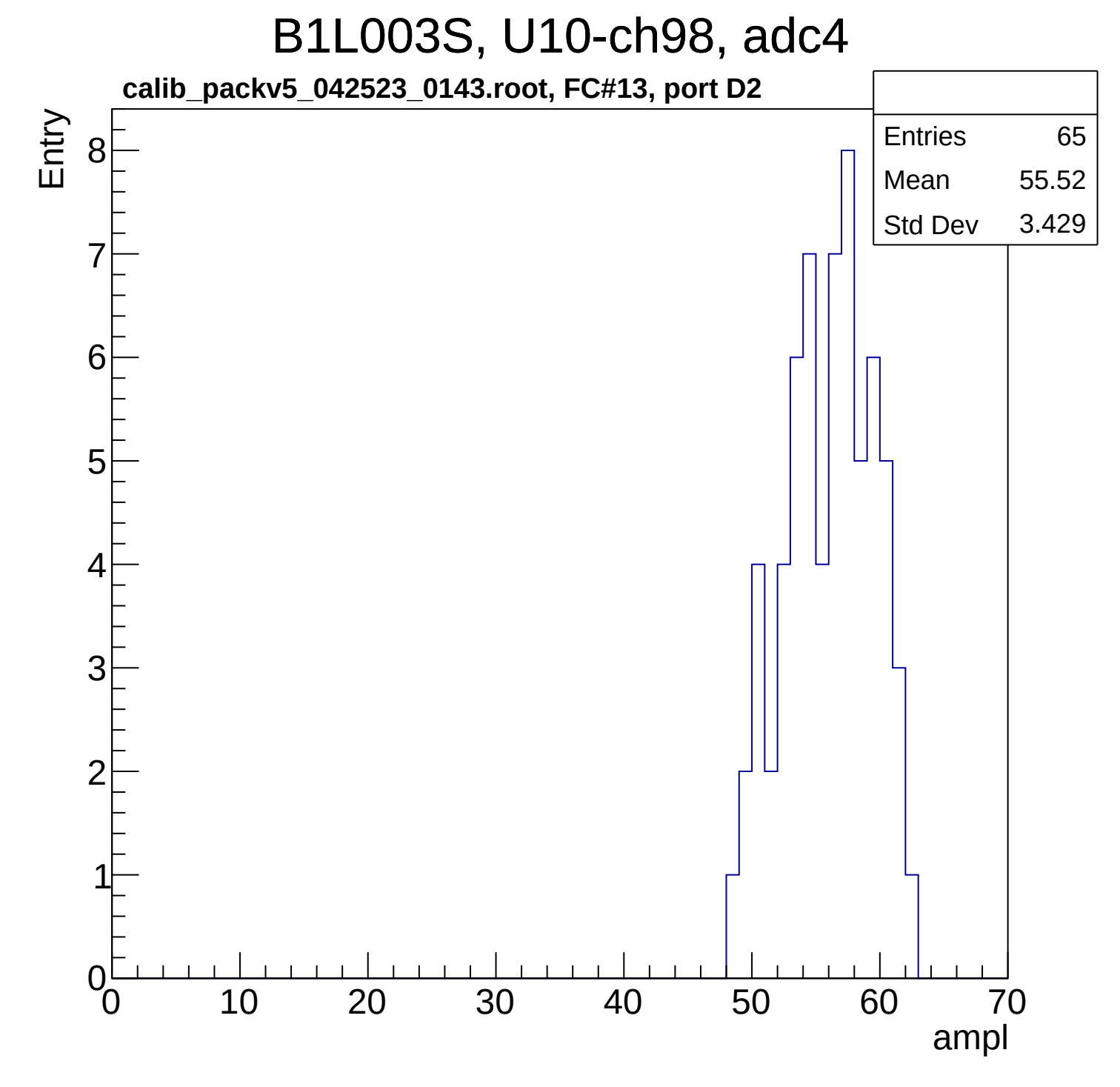

B1L003S, U10-ch97, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch98, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 30.43
Std Dev 5.103
Gaus mean : 31.1186
Gaus Width: 3.6528
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch98, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.42
Std Dev 3.722
Gaus mean : 37.3681
Gaus Width: 3.4792
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch98, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.37
Std Dev 3.111
Gaus mean : 43.8471
Gaus Width: 3.2134
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch98, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 50.16
Std Dev 3.307
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch98, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 55.52
Std Dev 3.429
Entry
ampl
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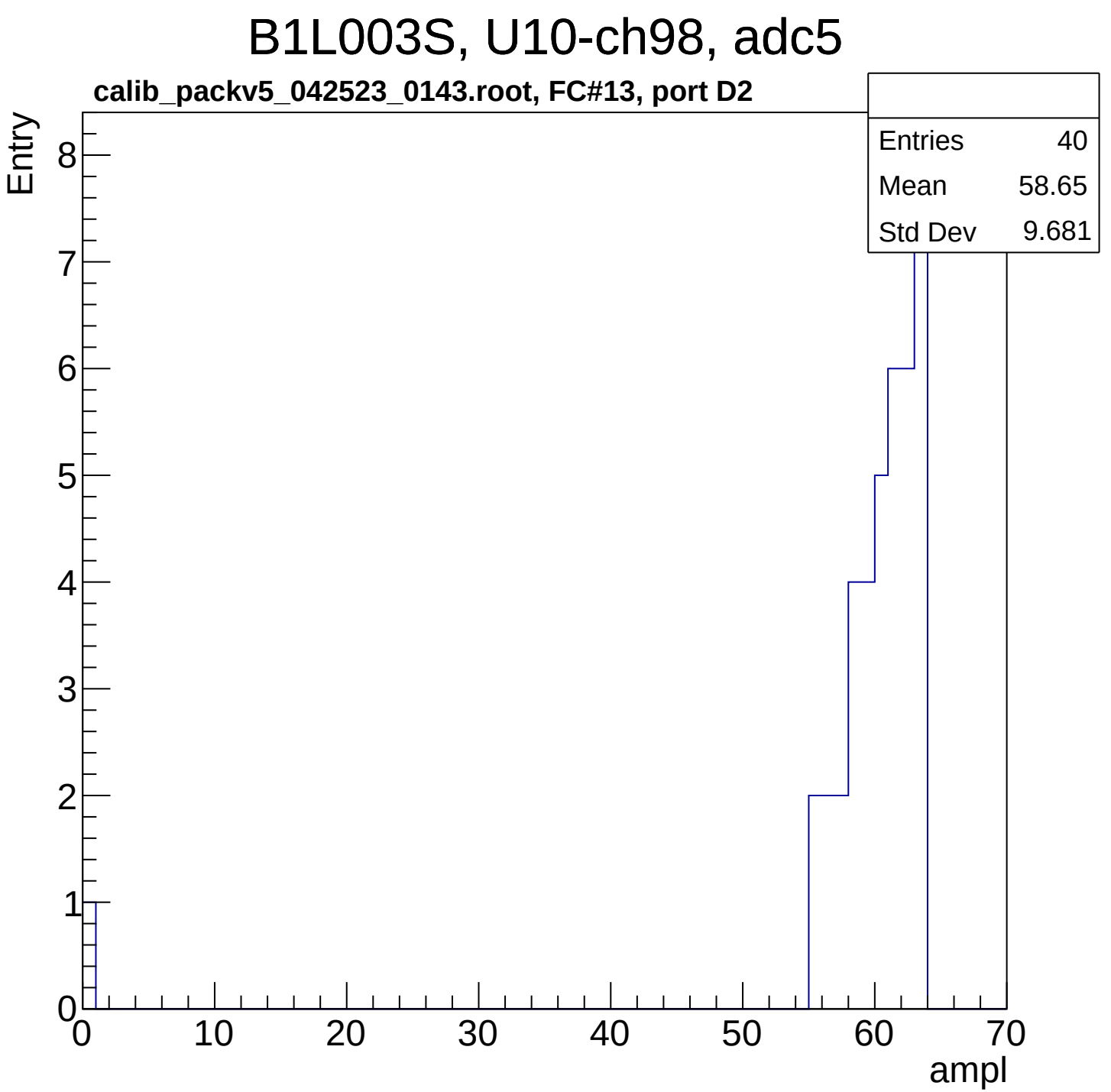
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch98, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.65
Std Dev 9.681
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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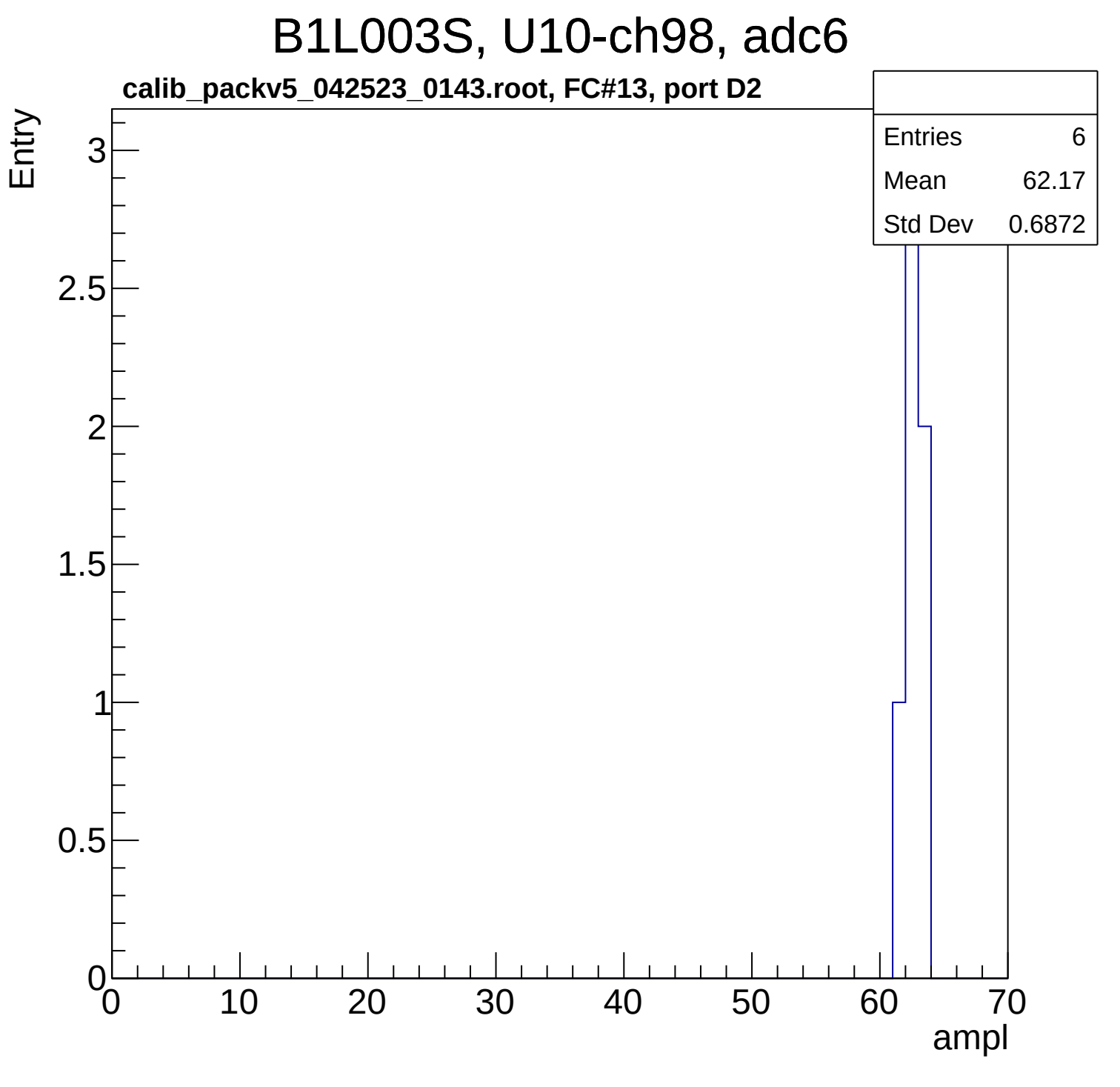
B1L003S, U10-ch98, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
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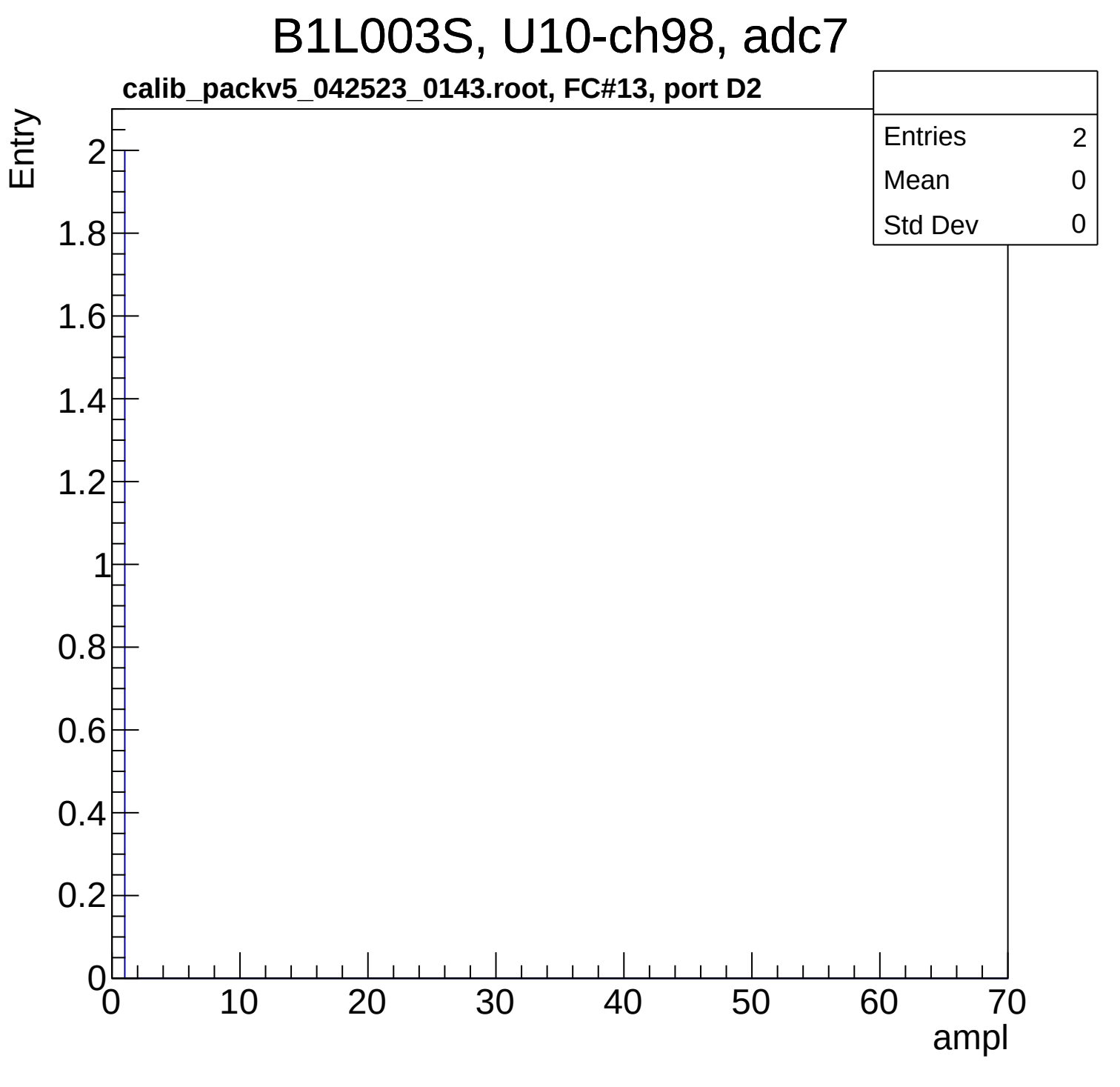
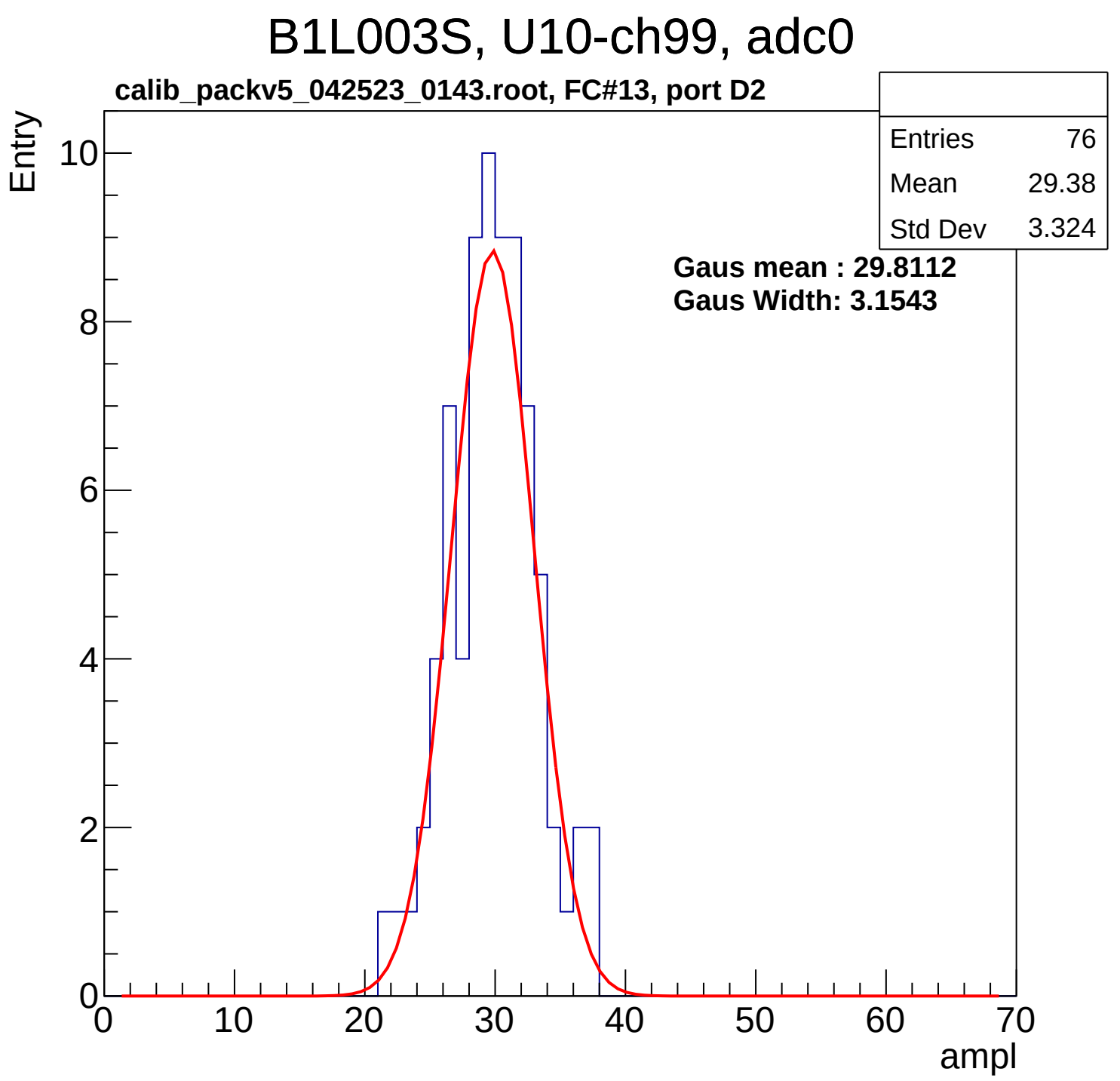
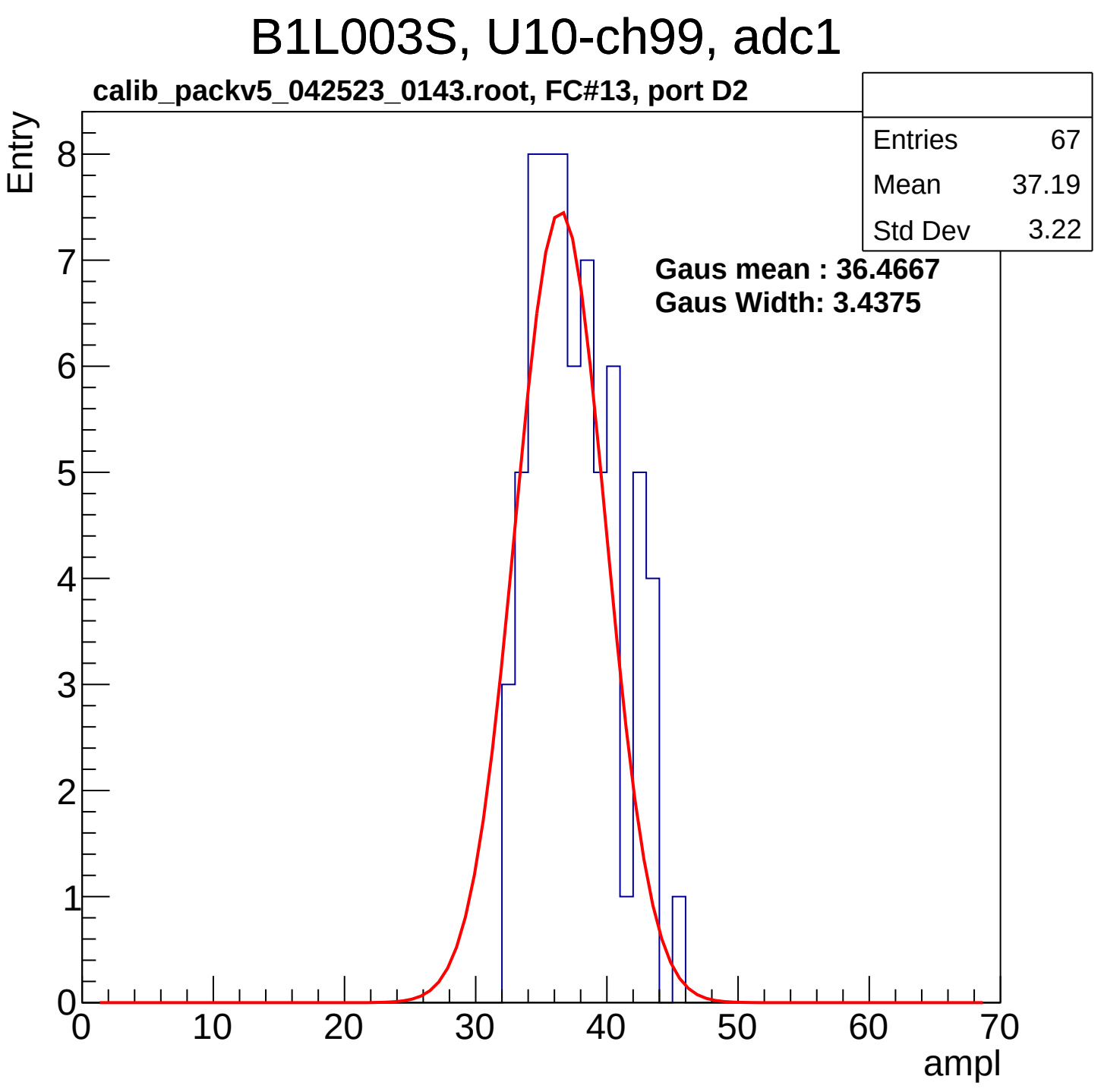
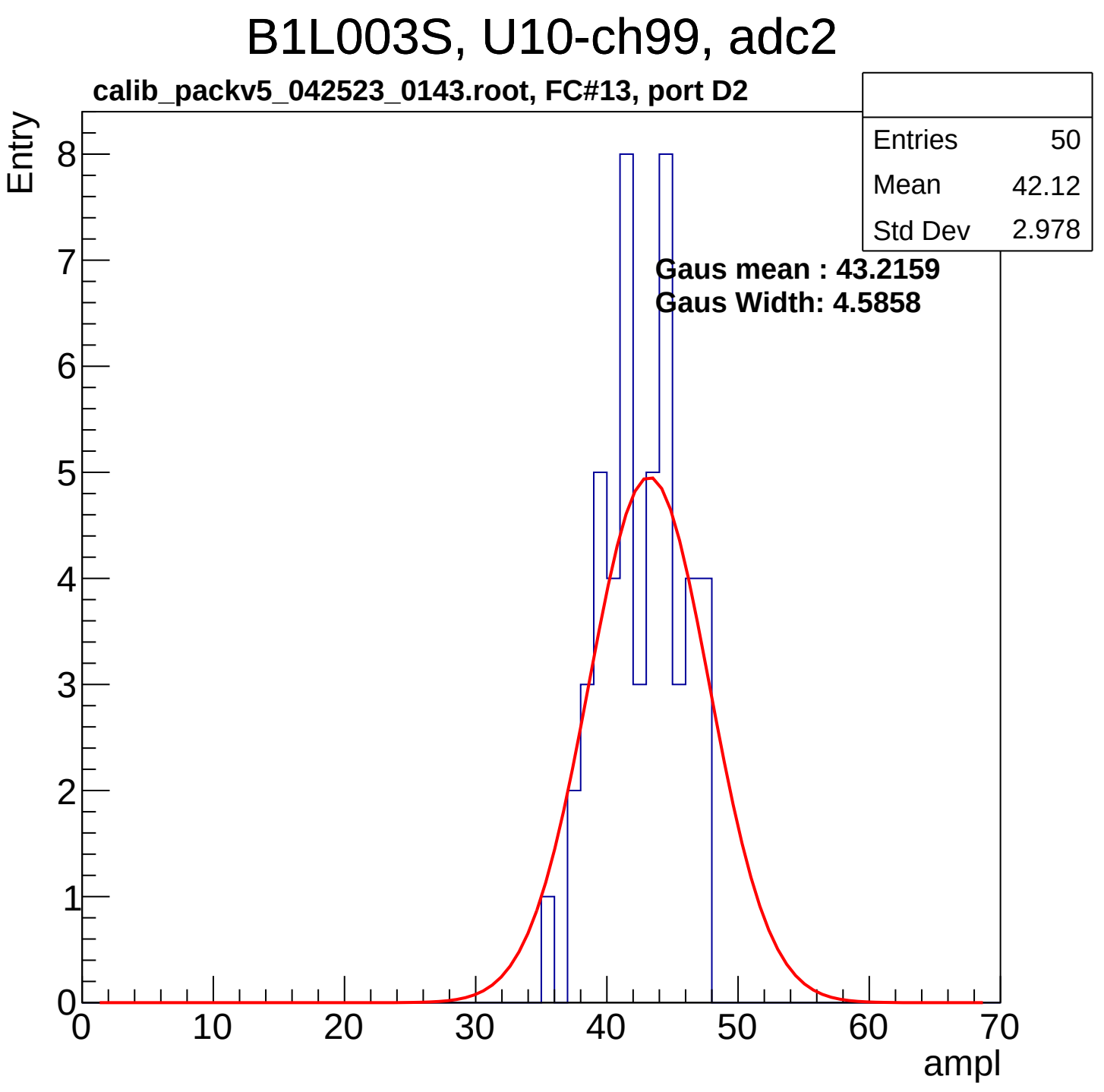
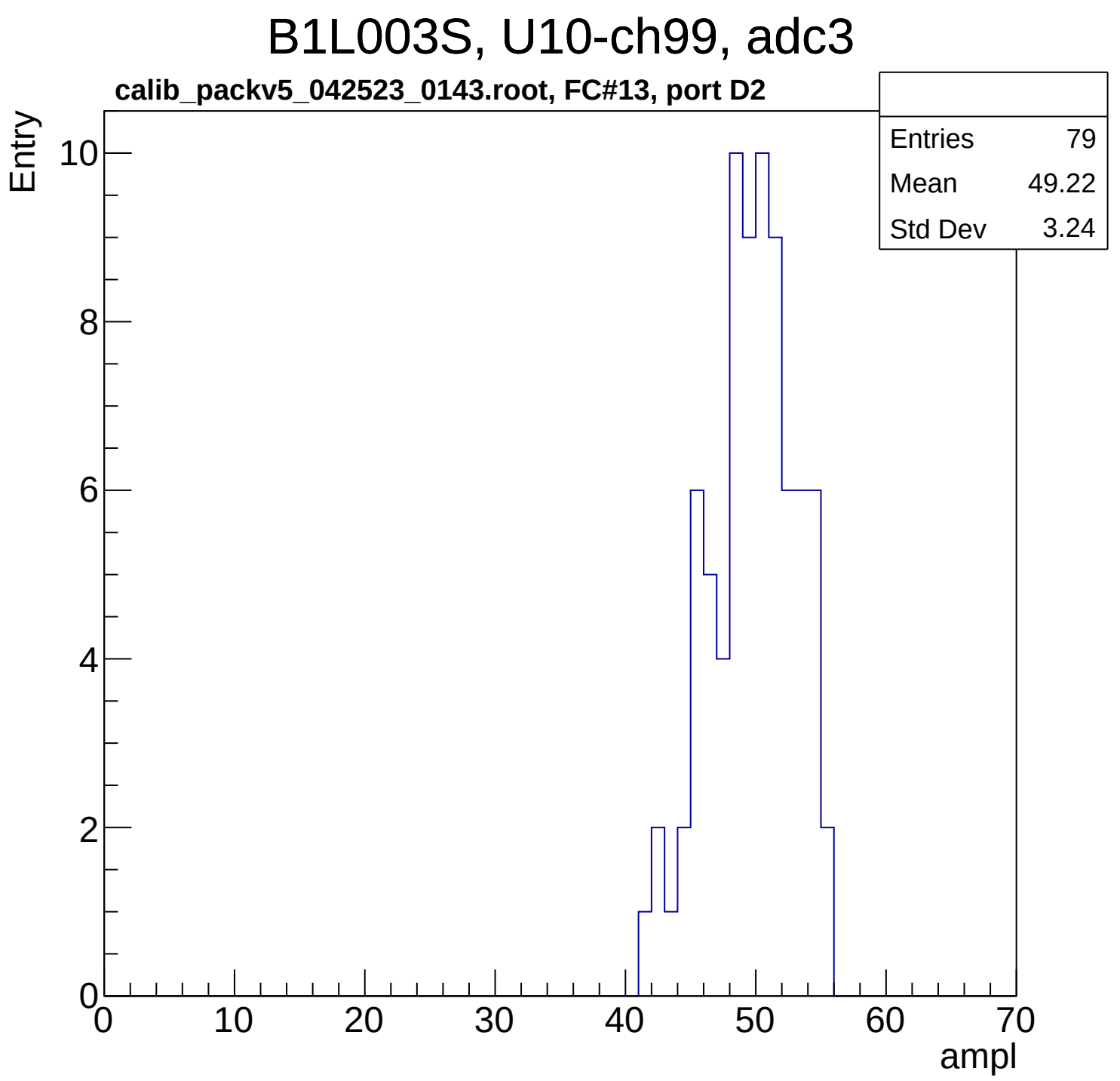
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

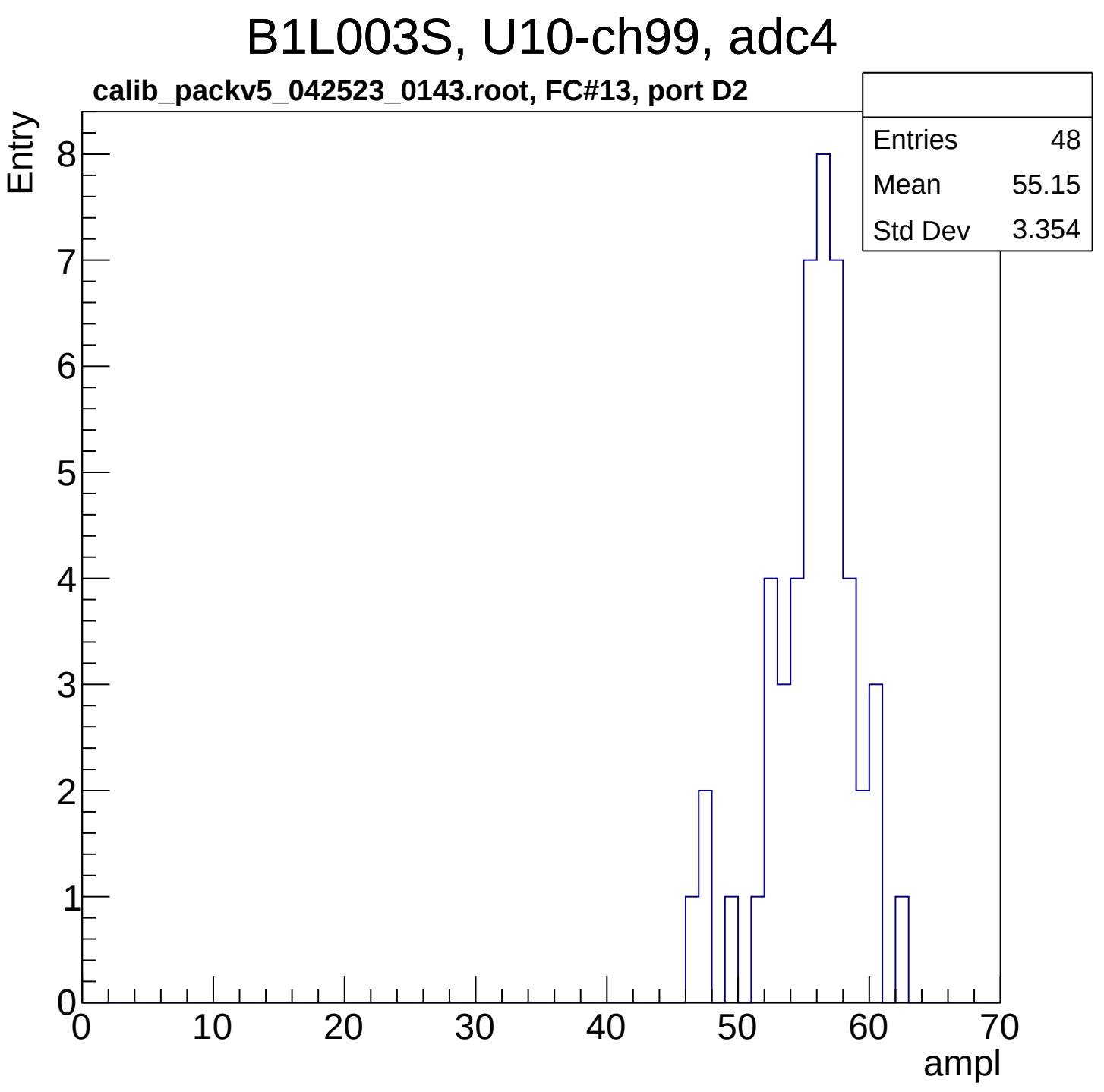

B1L003S, U10-ch98, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch99, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 29.38
Std Dev 3.324
Gaus mean : 29.8112
Gaus Width: 3.1543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch99, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.19
Std Dev 3.22
Gaus mean : 36.4667
Gaus Width: 3.4375
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch99, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 42.12
Std Dev 2.978
Gaus mean : 43.2159
Gaus Width: 4.5858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch99, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 49.22
Std Dev 3.24
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch99, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.15
Std Dev 3.354
Entry
ampl
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1
2
3
4
5
6
7
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0
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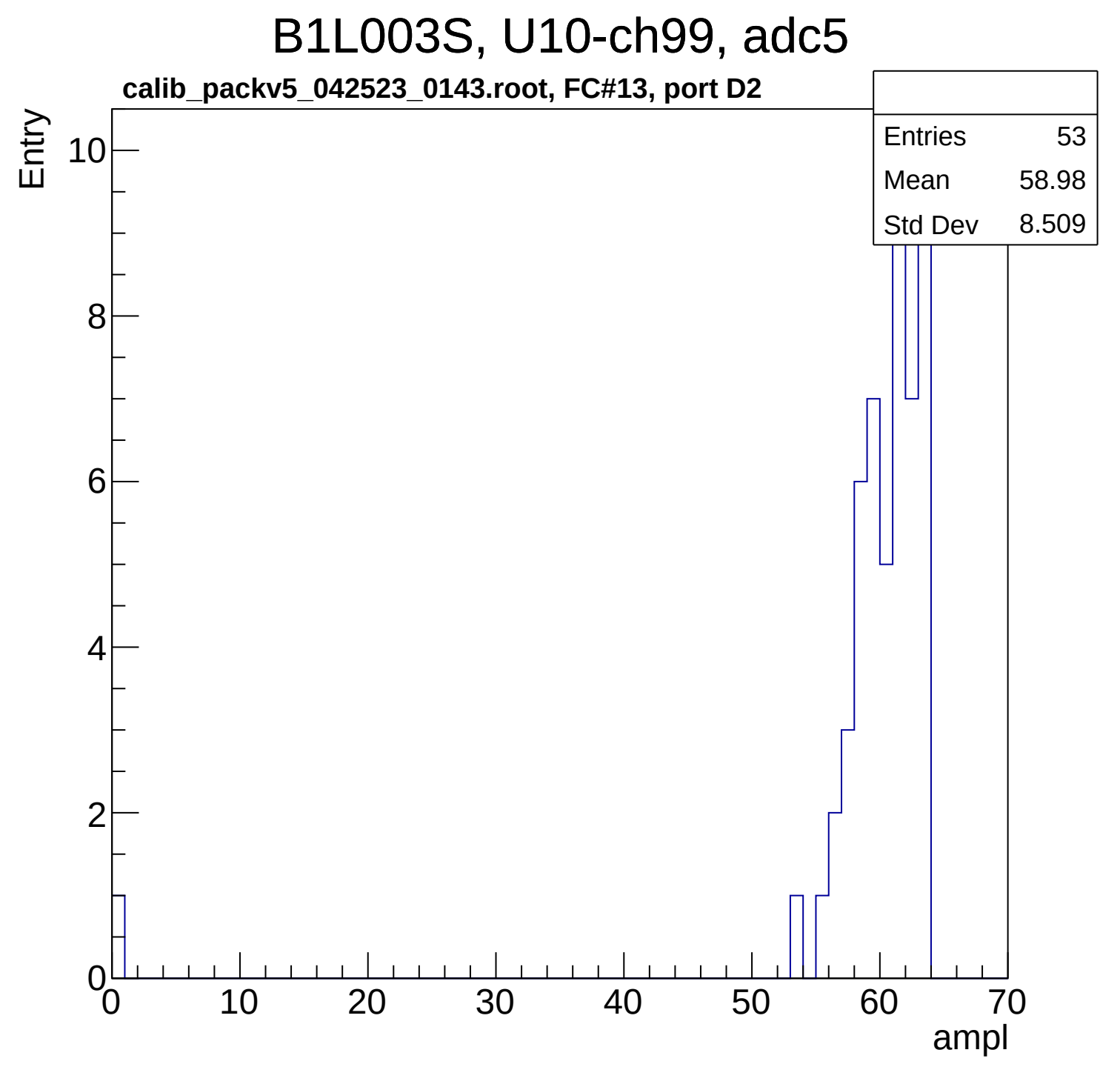
20
30
40
50
60
70


B1L003S, U10-ch99, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.98
Std Dev 8.509
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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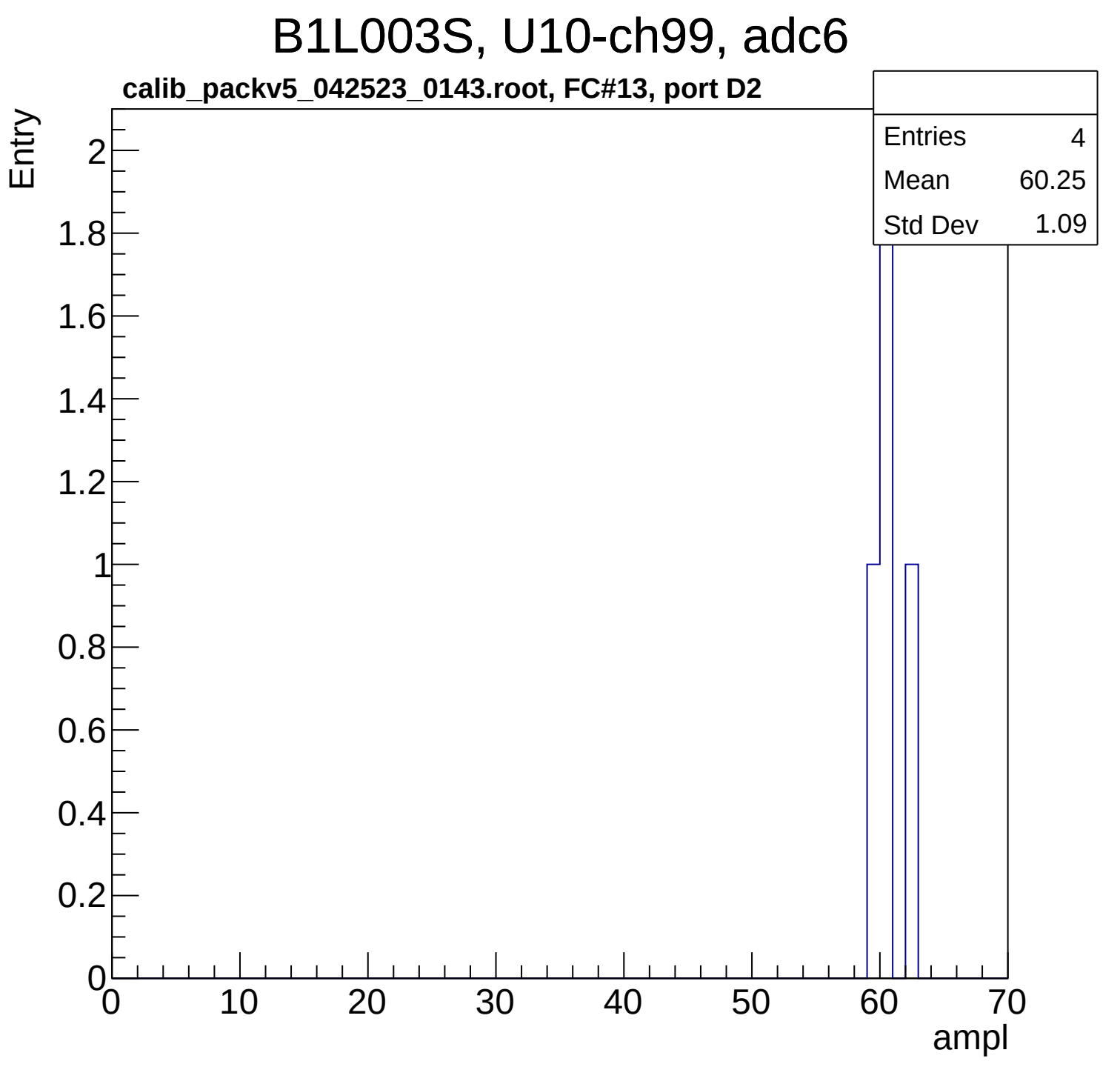
B1L003S, U10-ch99, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 60.25
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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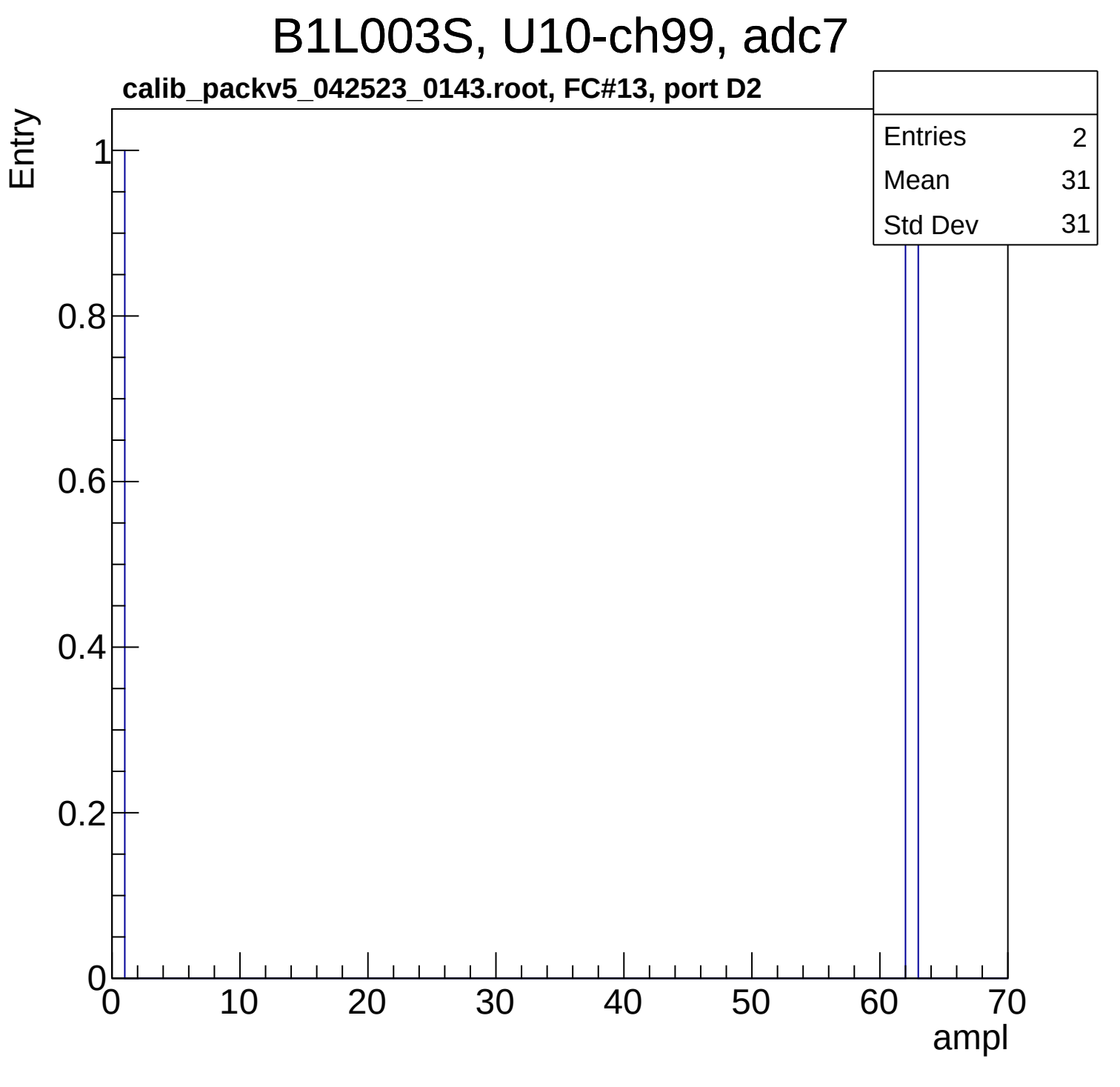
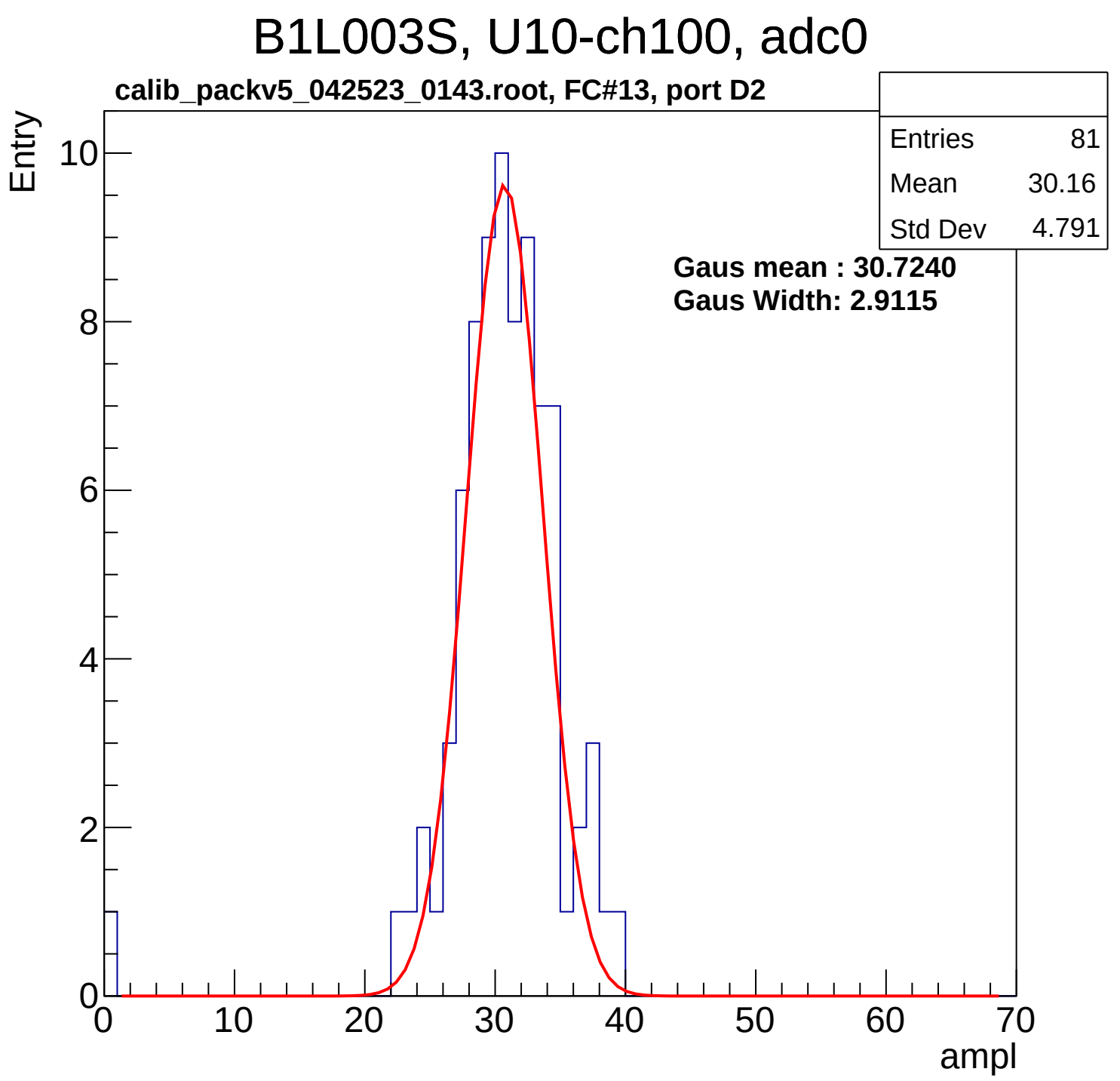
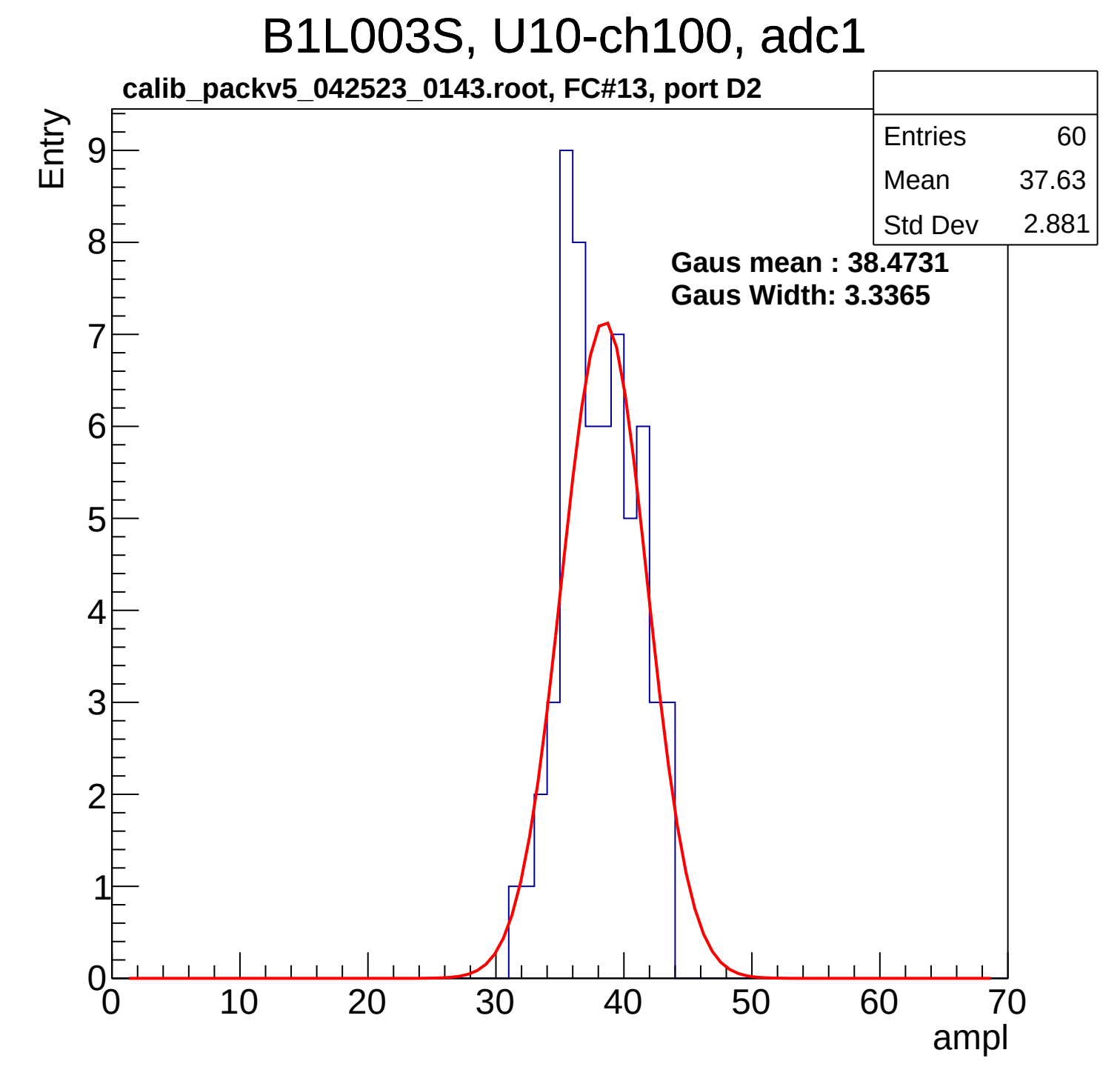

B1L003S, U10-ch99, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch100, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 30.16
Std Dev 4.791
Gaus mean : 30.7240
Gaus Width: 2.9115
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch100, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 37.63
Std Dev 2.881
Gaus mean : 38.4731
Gaus Width: 3.3365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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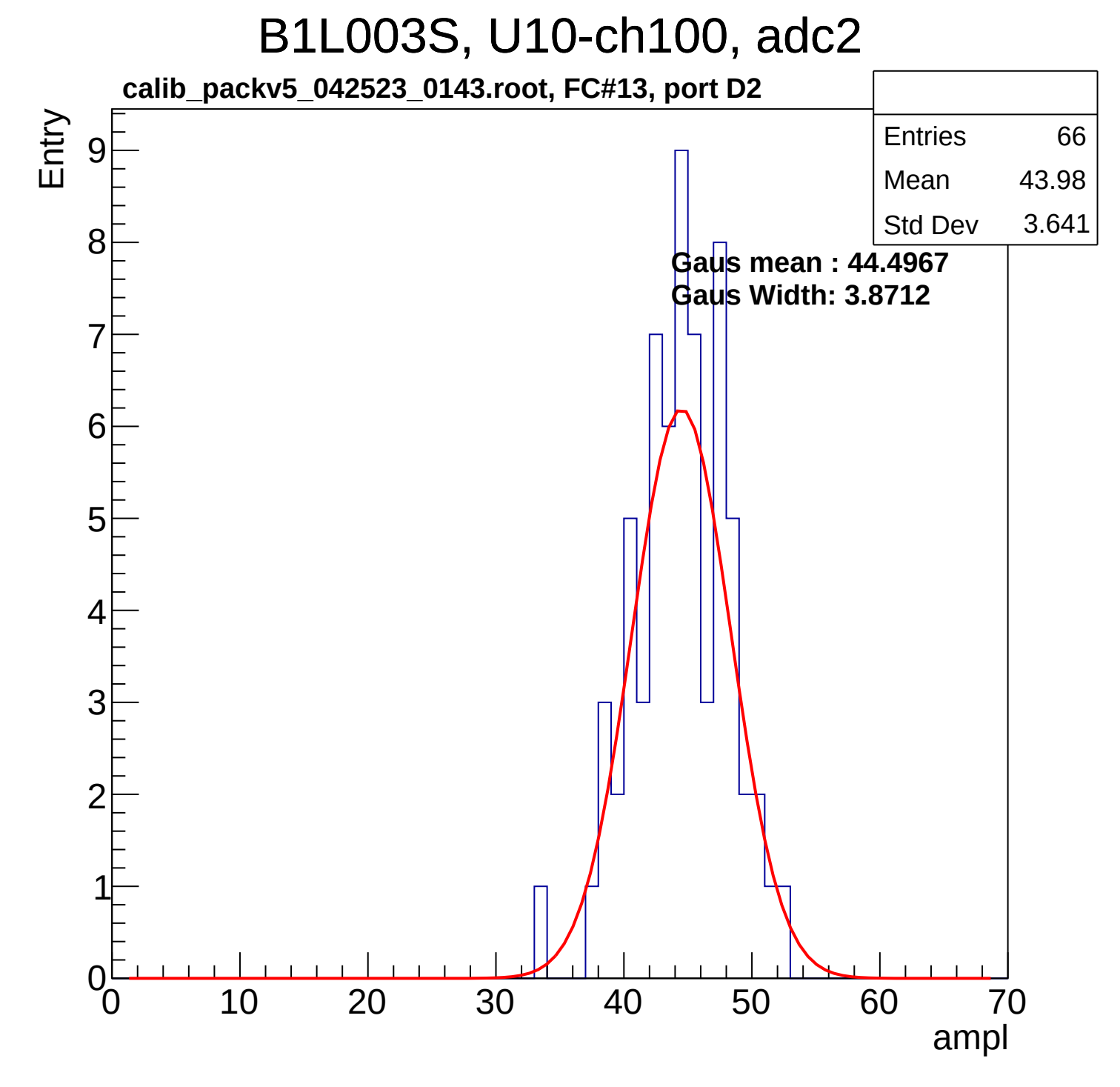
B1L003S, U10-ch100, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.98
Std Dev 3.641
Gaus mean : 44.4967
Gaus Width: 3.8712
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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60
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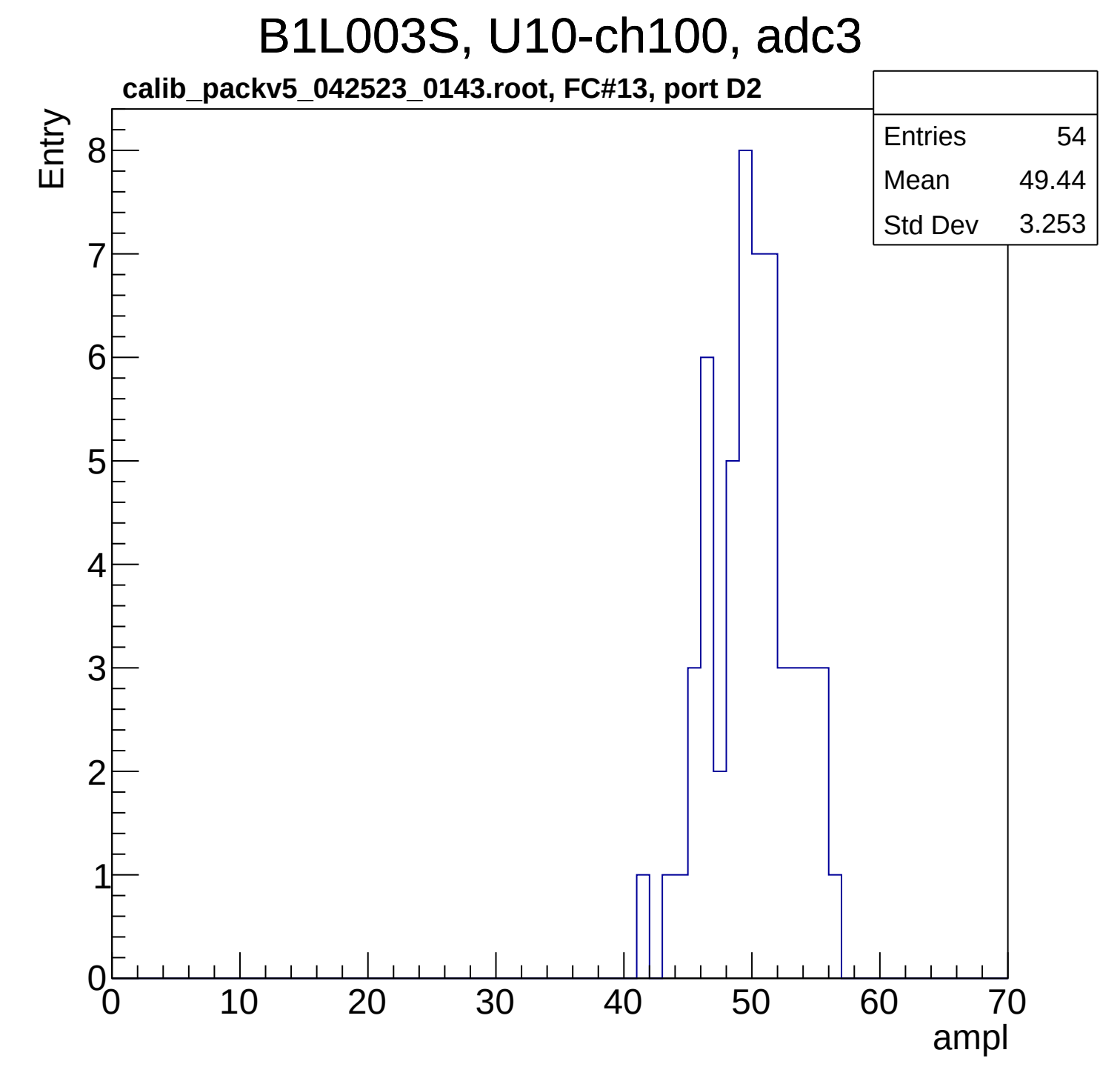
B1L003S, U10-ch100, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.44
Std Dev 3.253
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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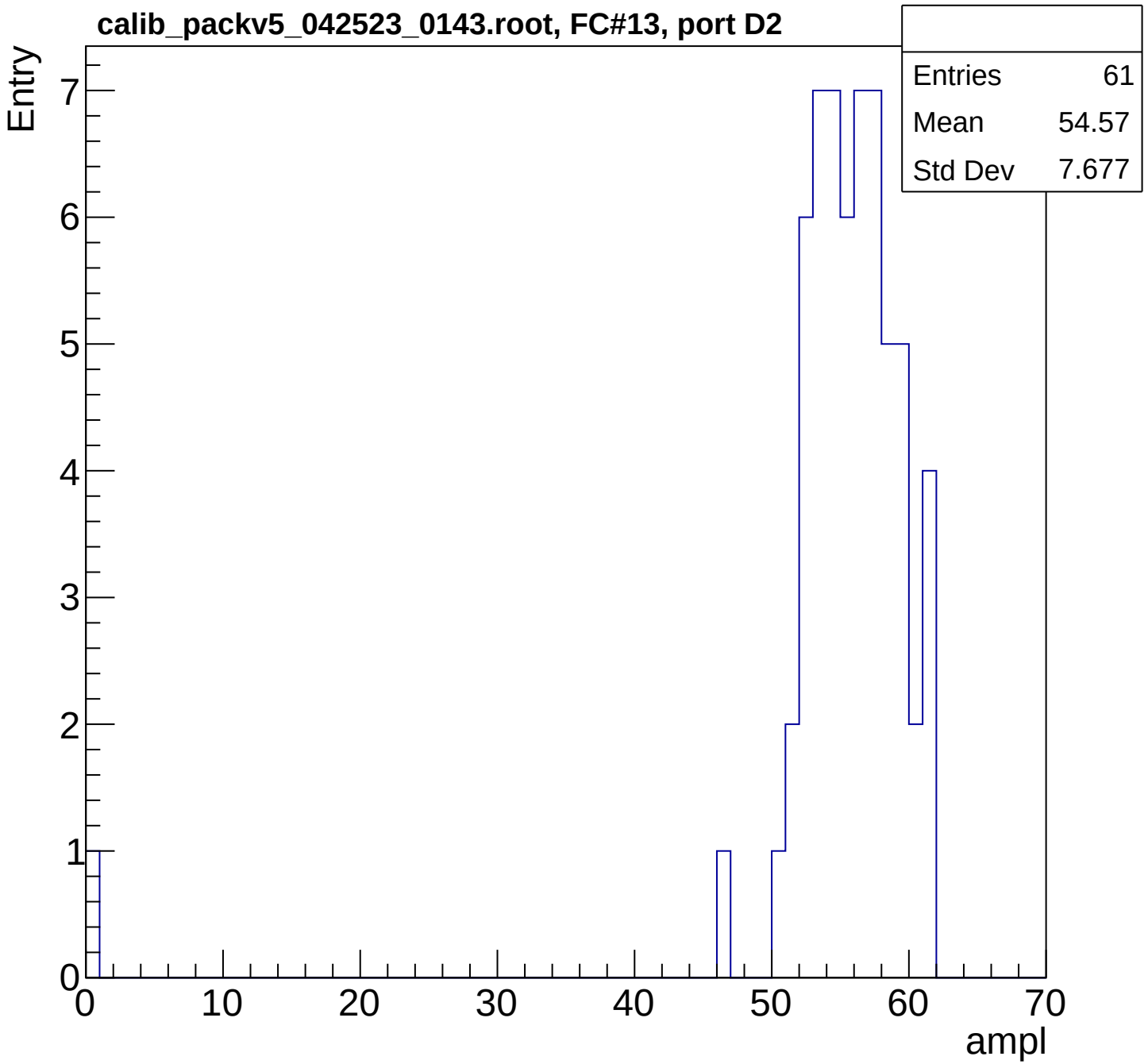
B1L003S, U10-ch100, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.57
Std Dev 7.677
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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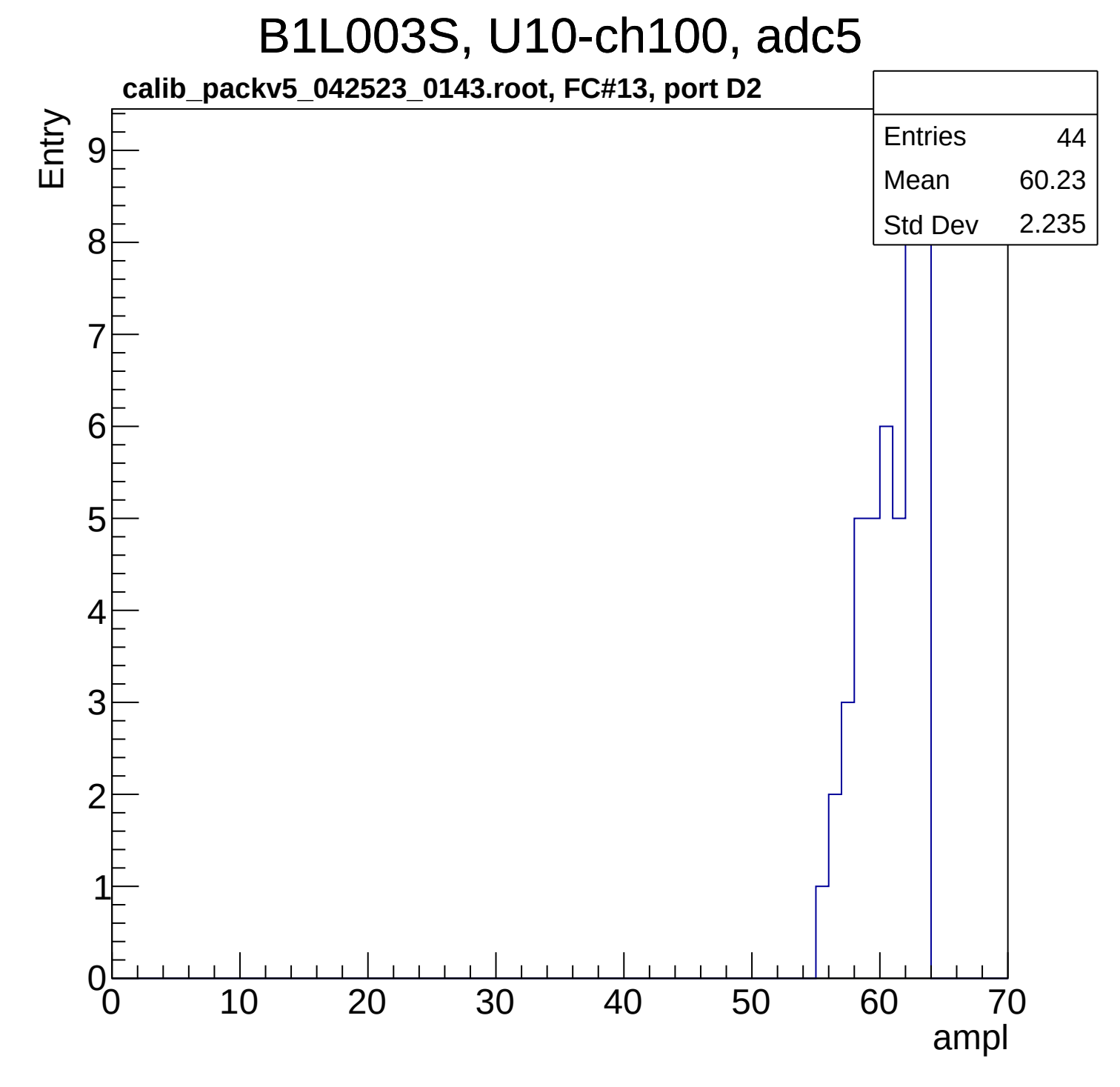
B1L003S, U10-ch100, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 60.23
Std Dev 2.235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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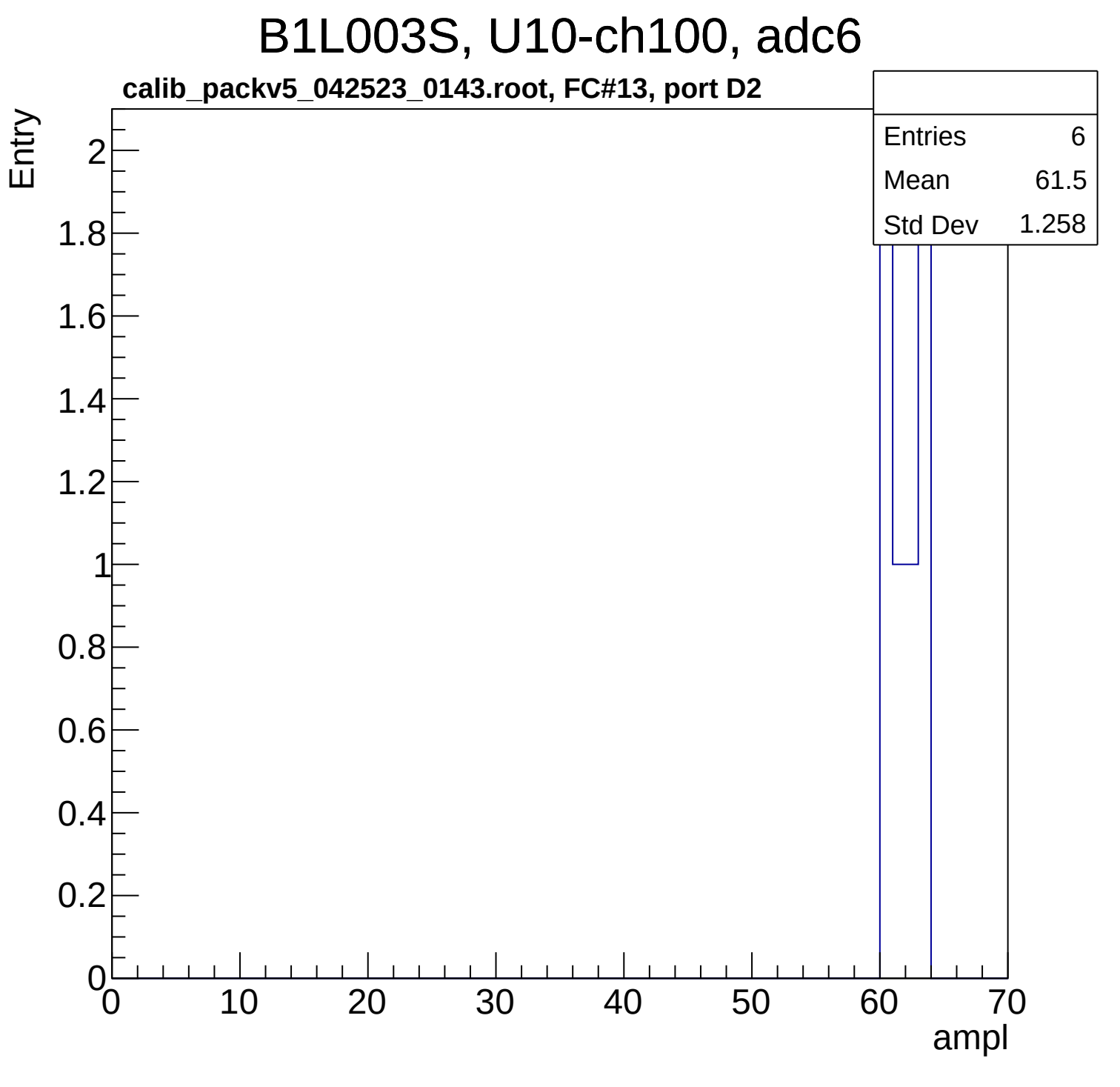
B1L003S, U10-ch100, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.5
Std Dev 1.258
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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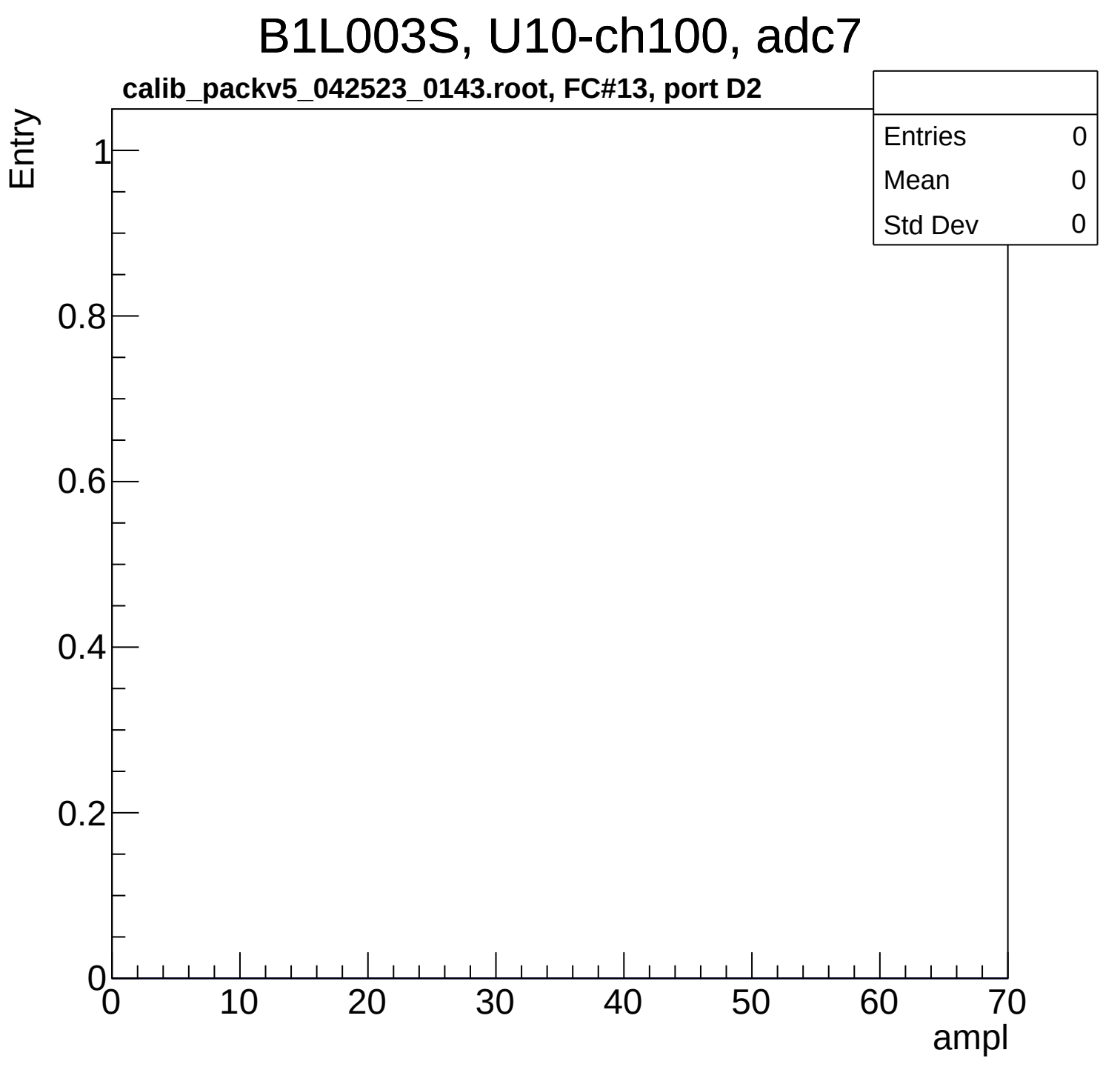
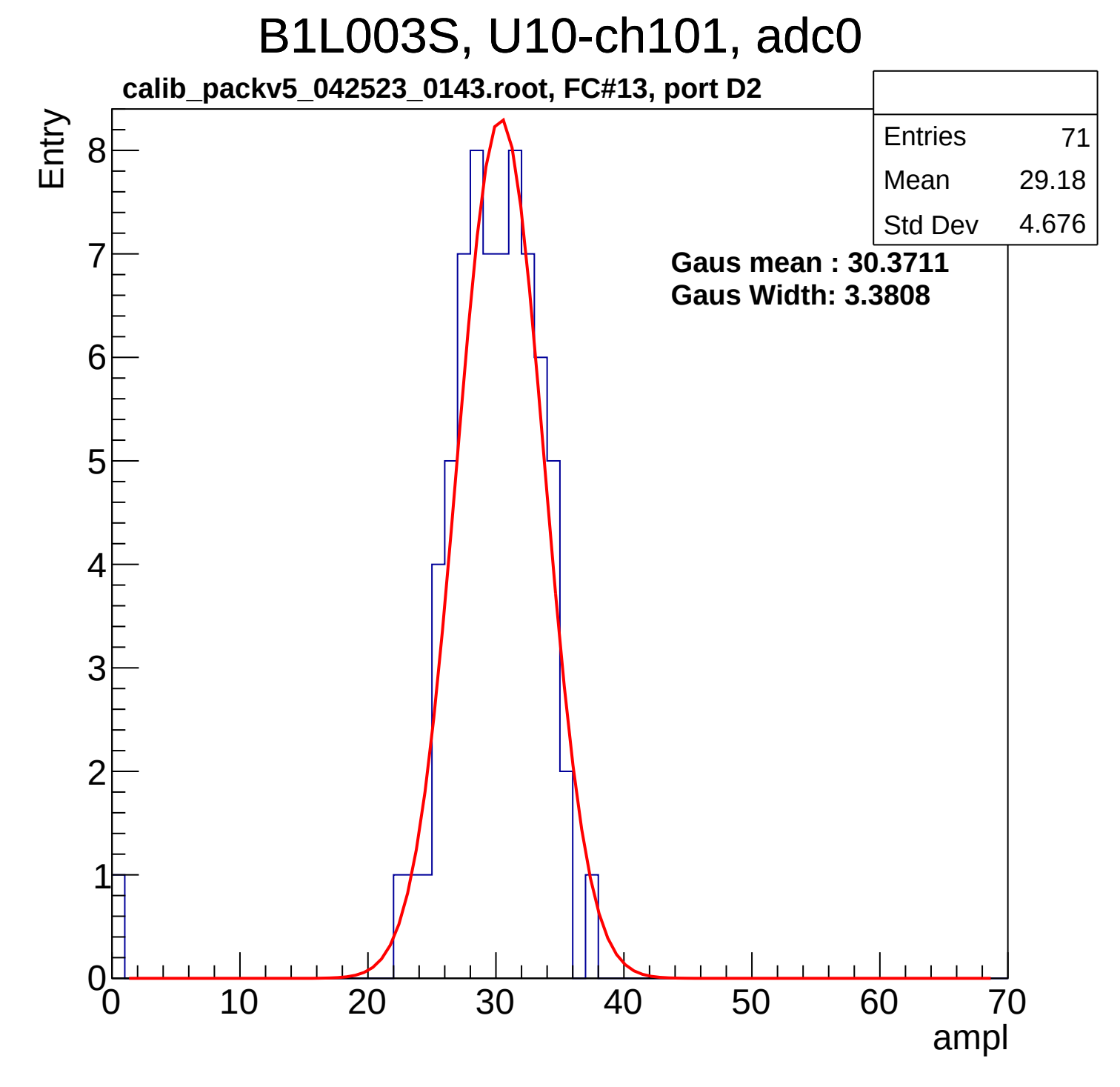
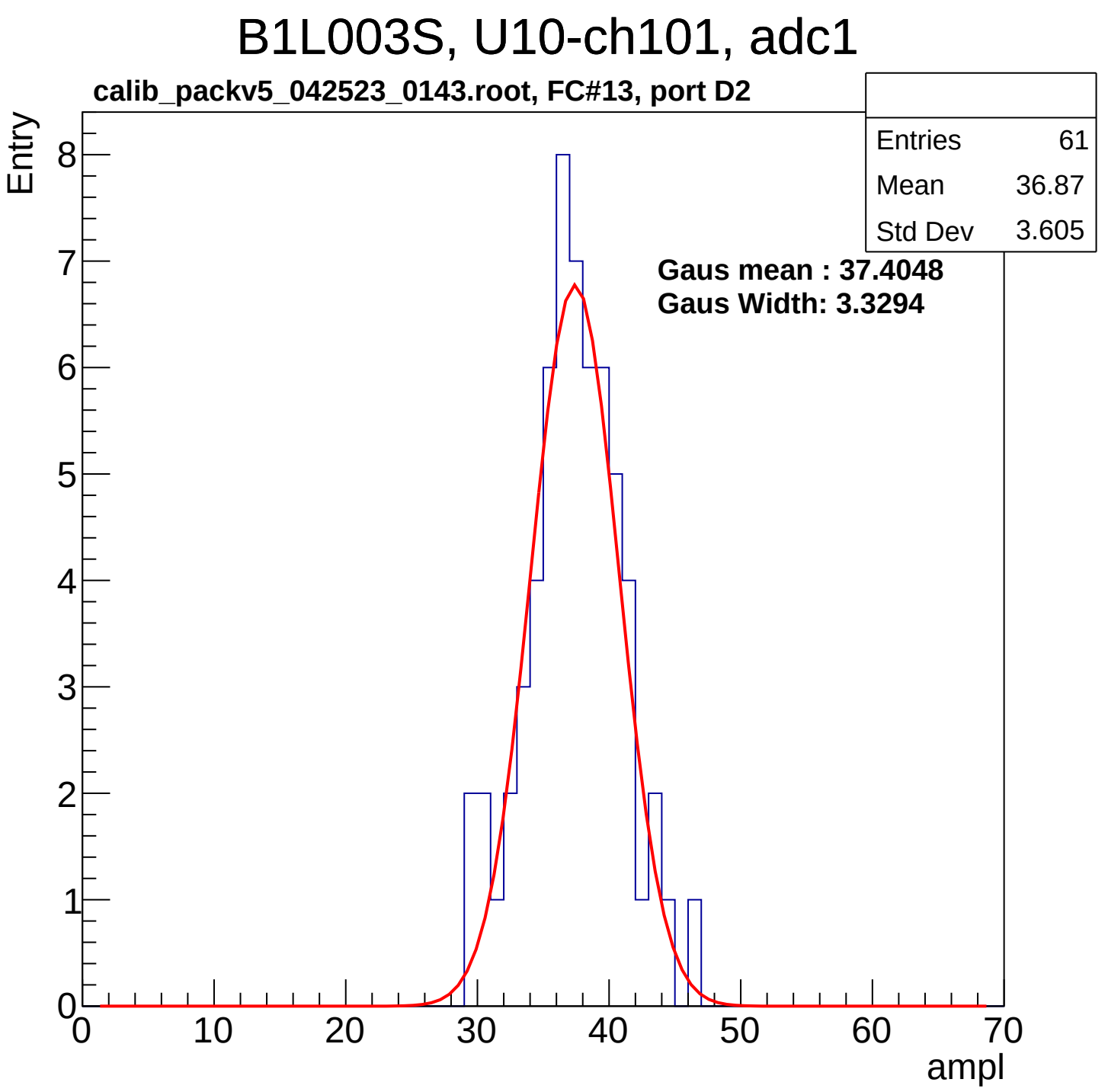
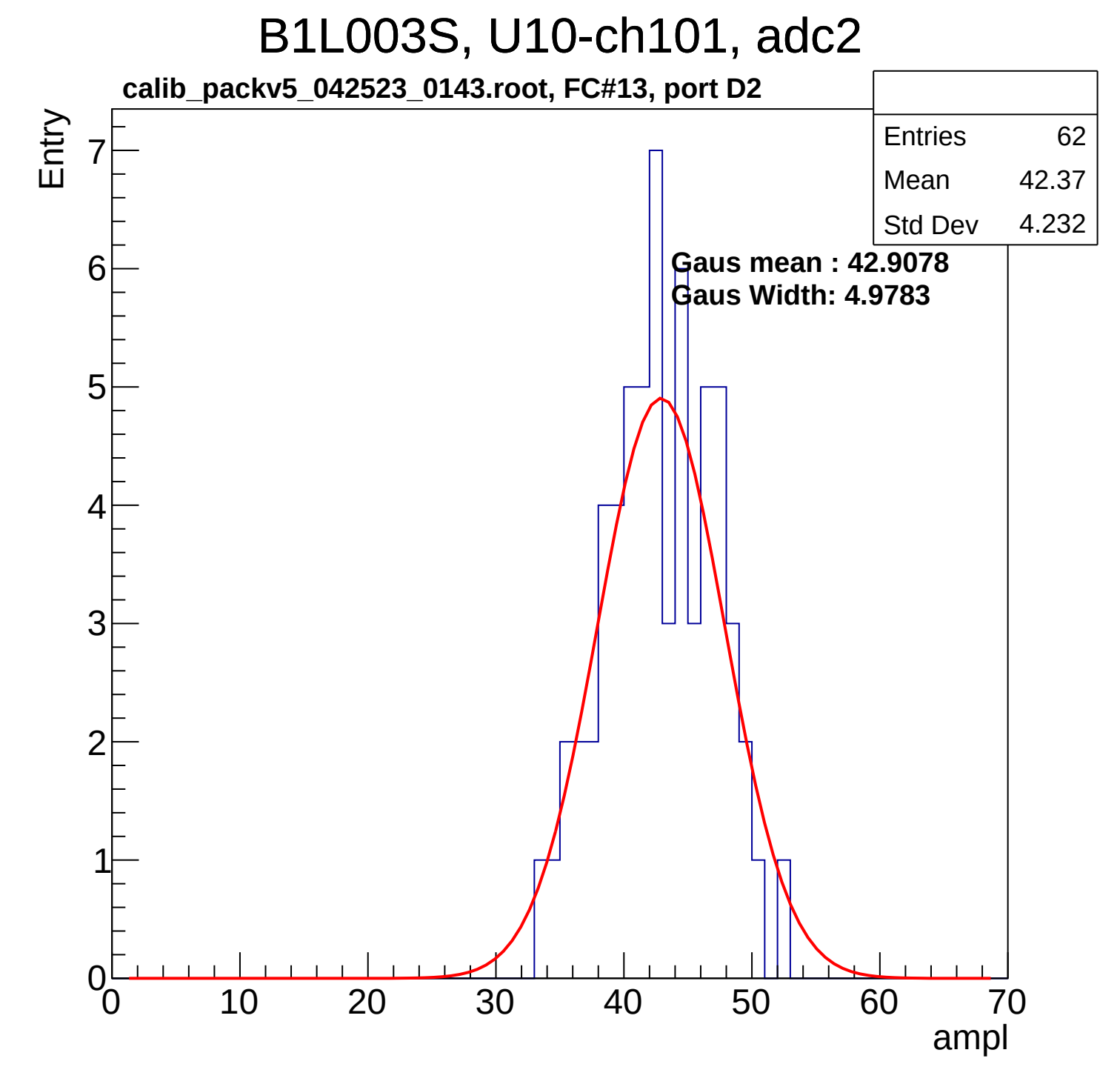
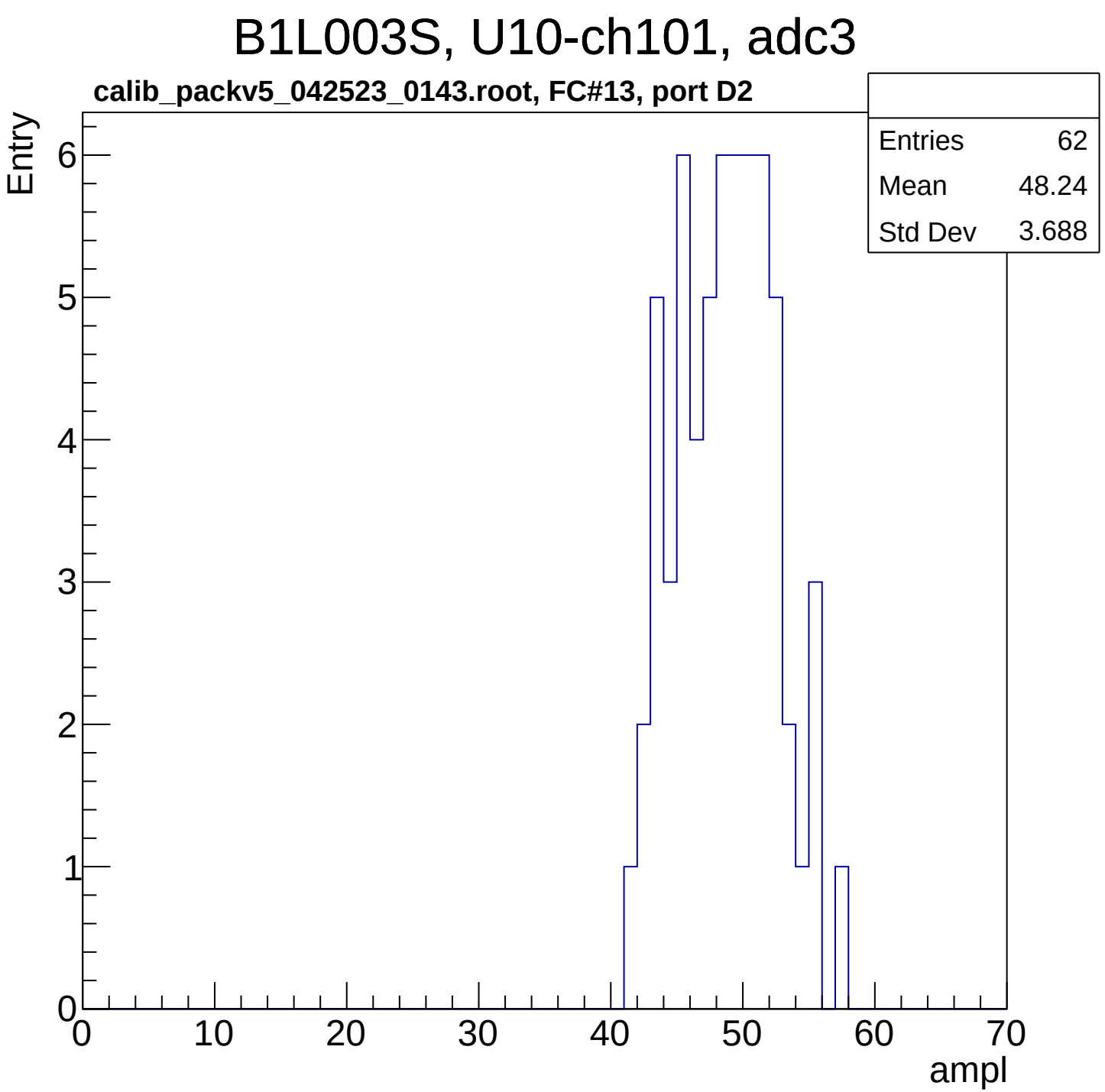
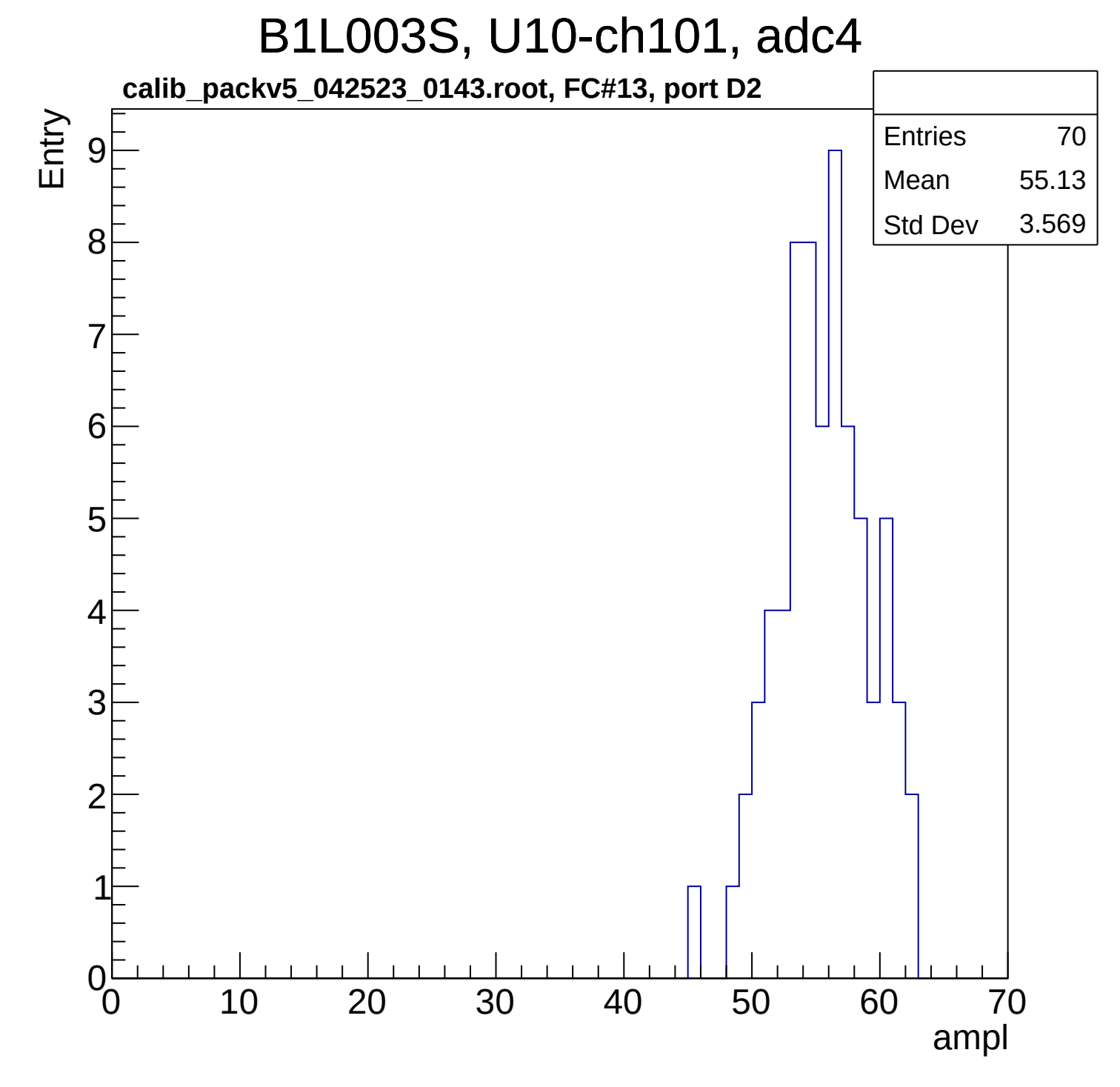

B1L003S, U10-ch100, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

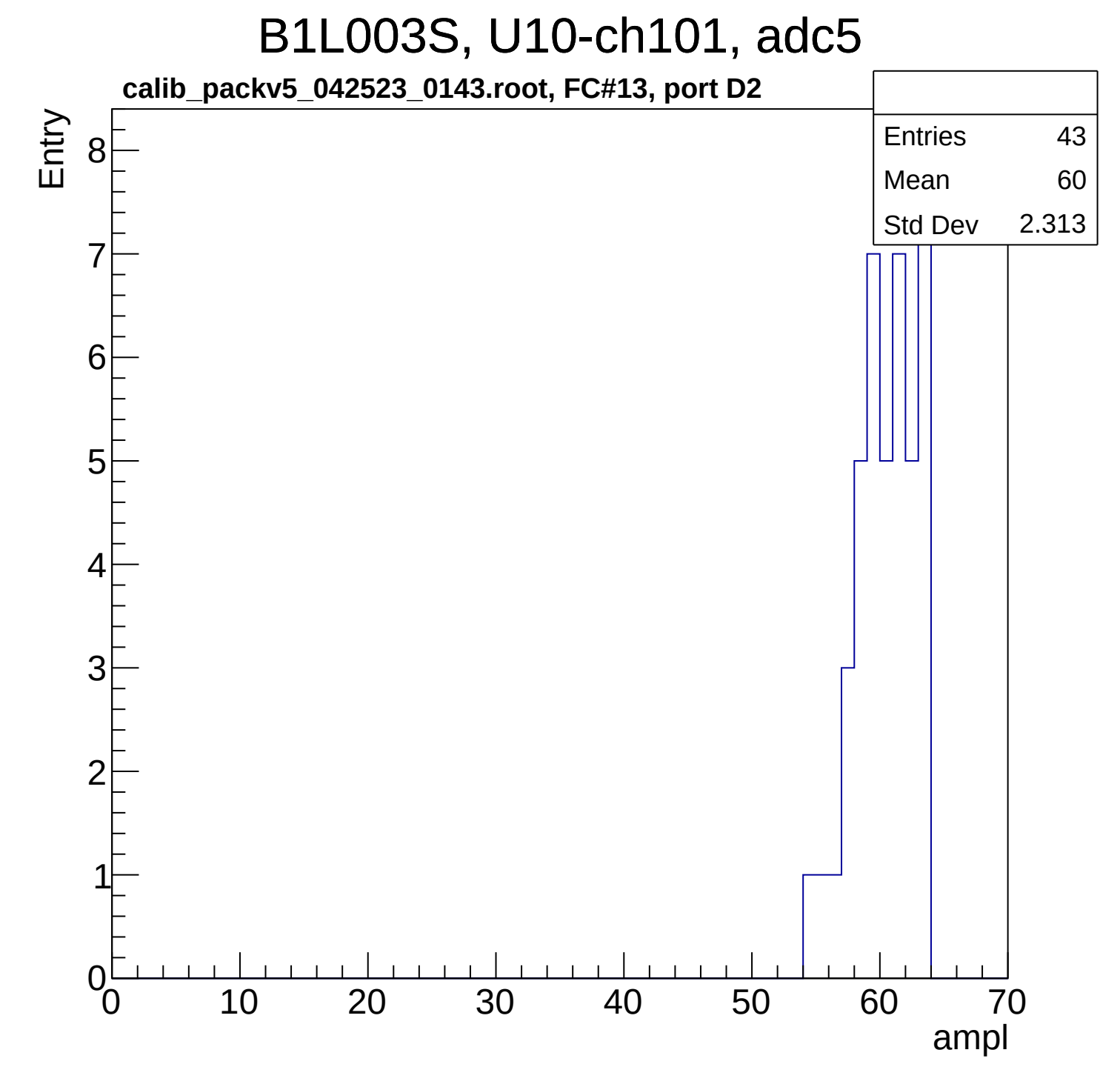

B1L003S, U10-ch101, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.18
Std Dev 4.676
Gaus mean : 30.3711
Gaus Width: 3.3808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

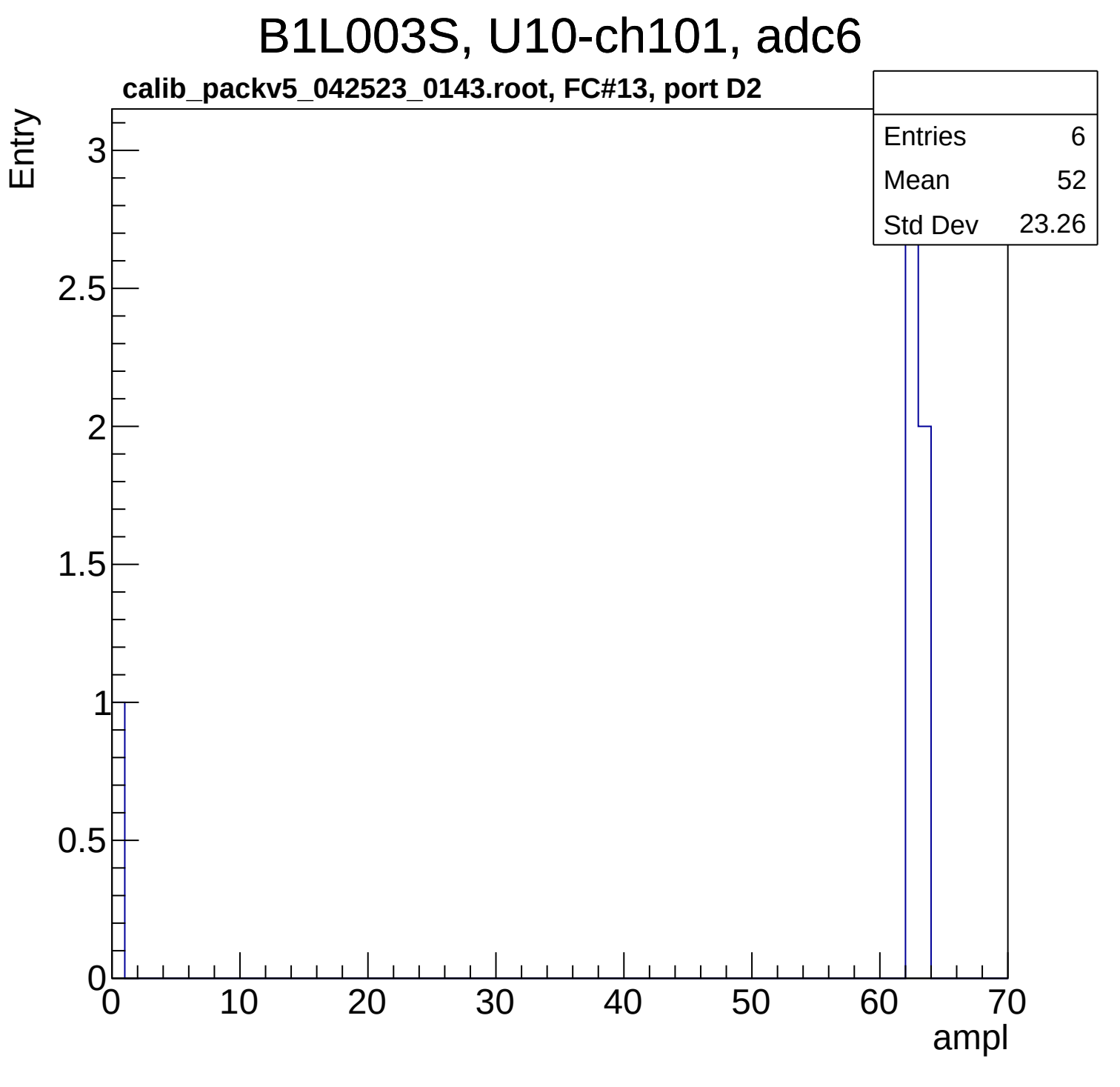

B1L003S, U10-ch101, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.87
Std Dev 3.605
Gaus mean : 37.4048
Gaus Width: 3.3294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

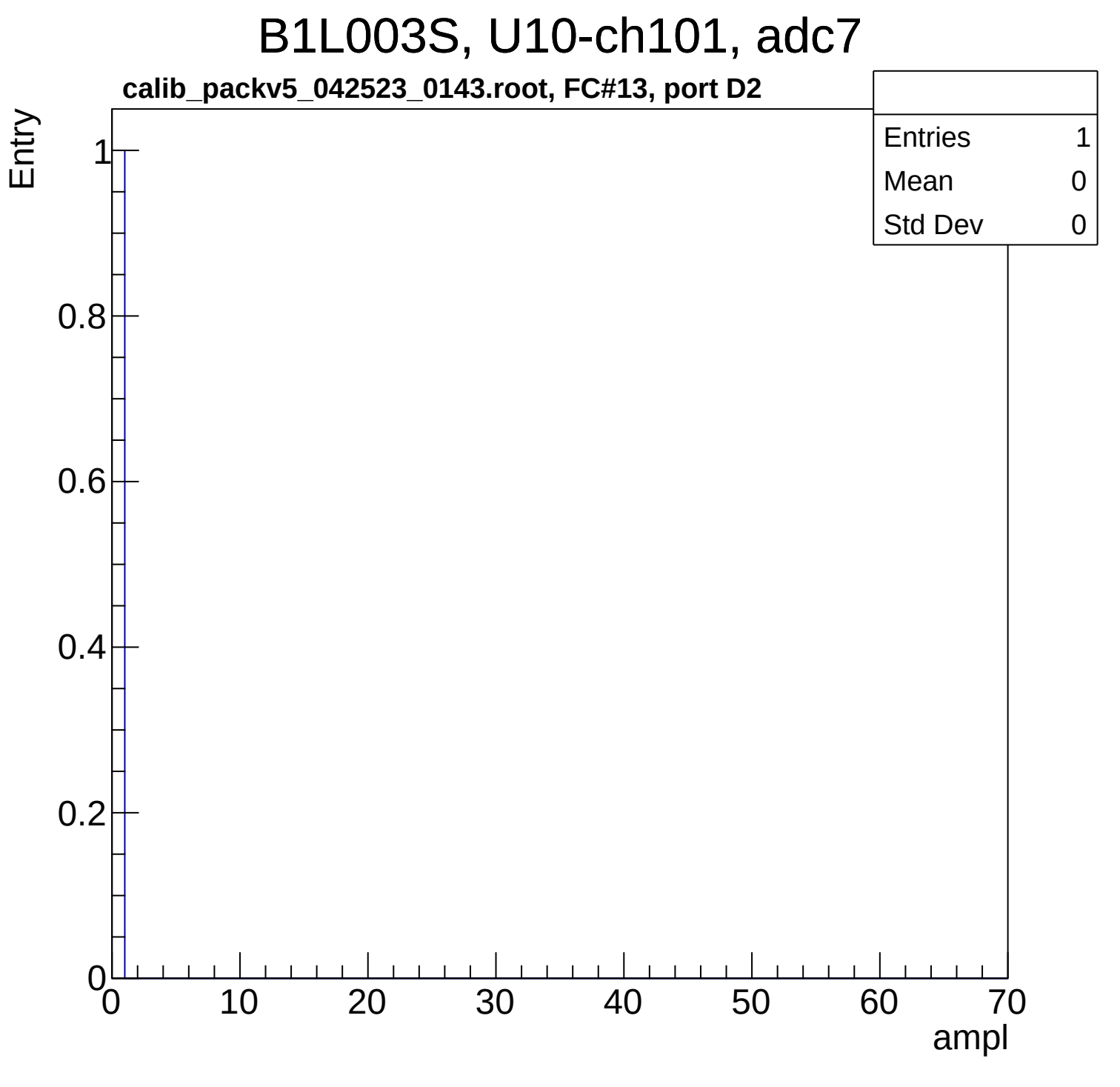

B1L003S, U10-ch101, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.37
Std Dev 4.232
Gaus mean : 42.9078
Gaus Width: 4.9783
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

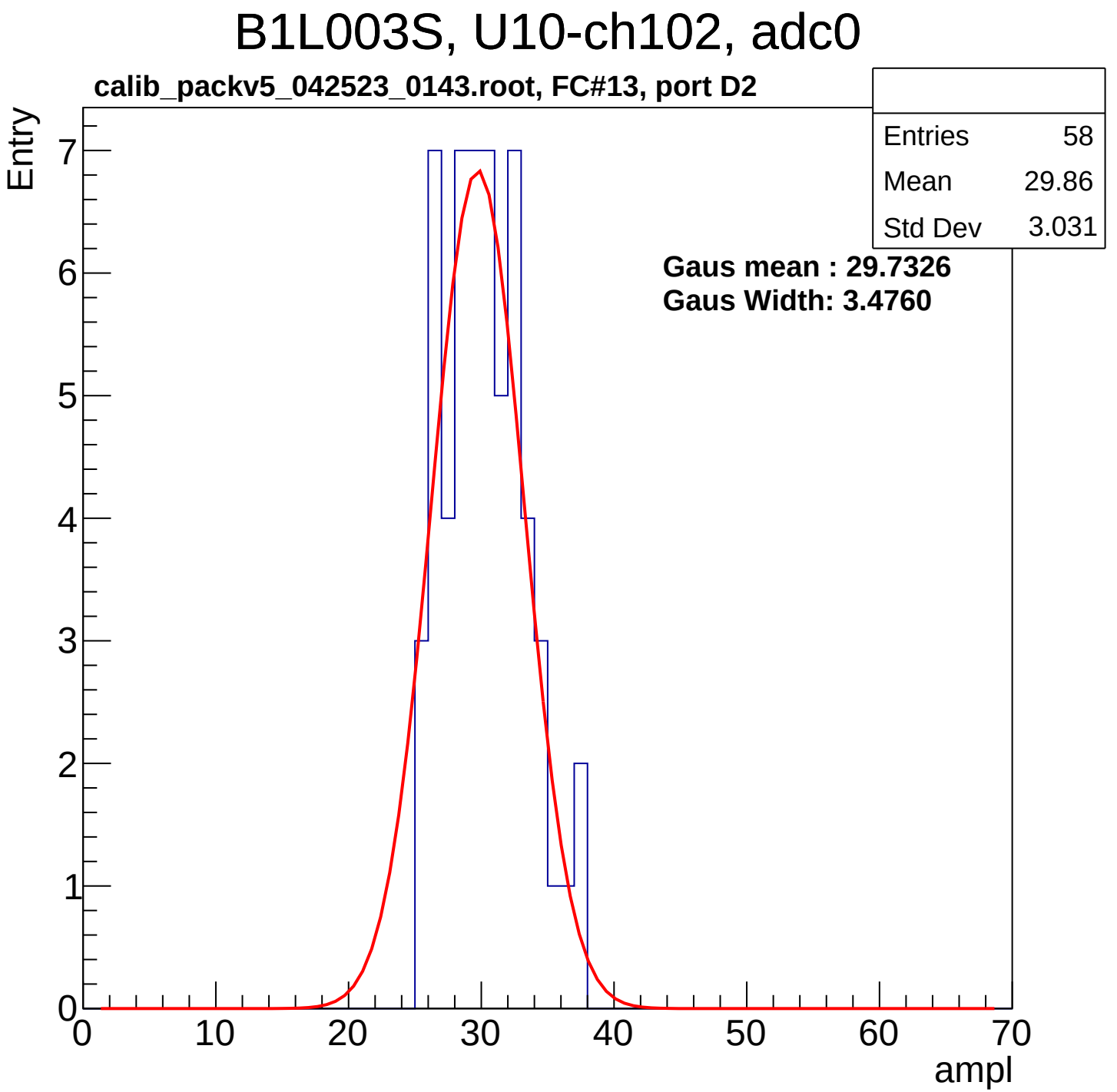

B1L003S, U10-ch101, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.24
Std Dev 3.688
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

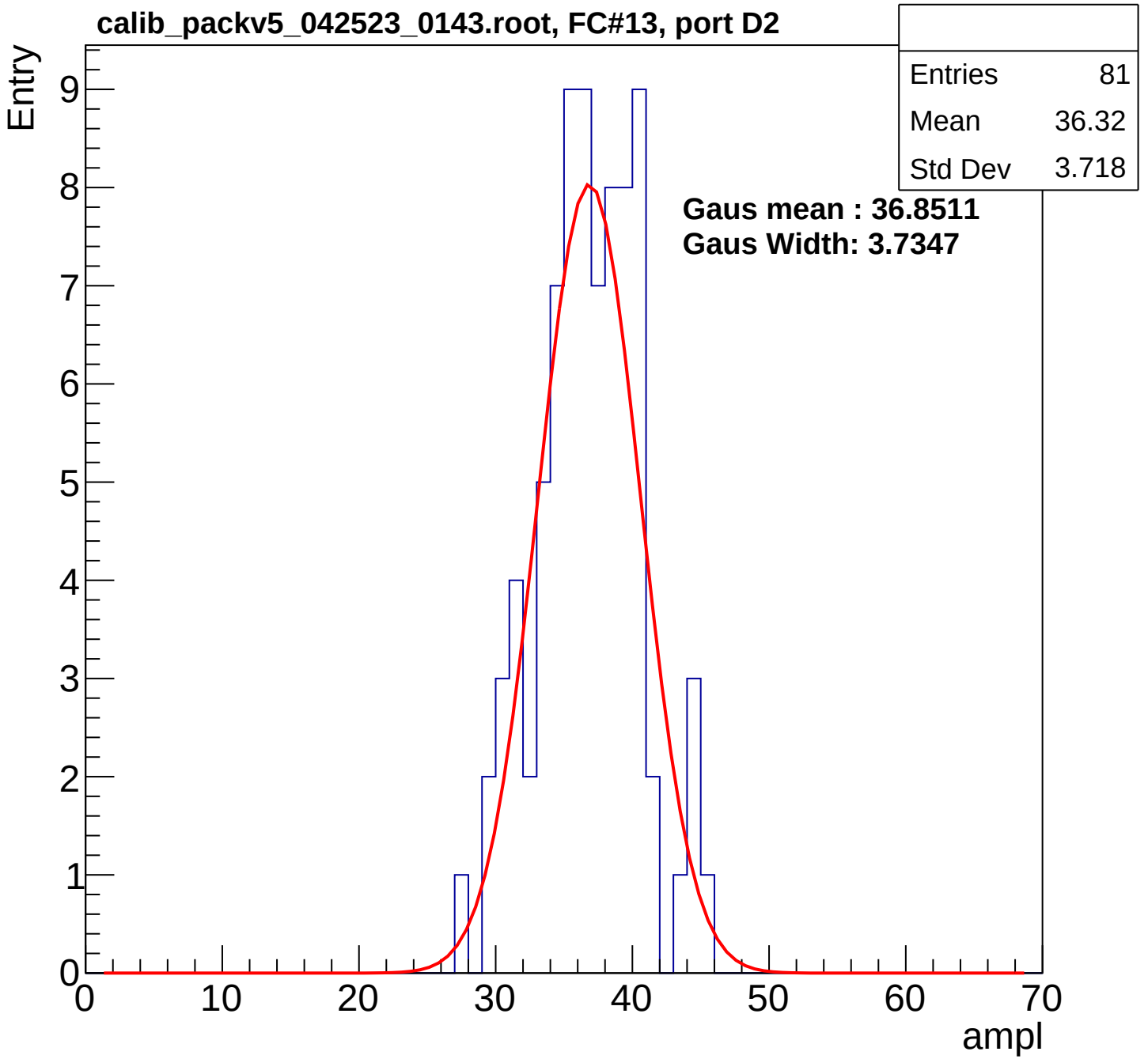

B1L003S, U10-ch101, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 55.13
Std Dev 3.569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch101, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 60
Std Dev 2.313
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch101, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 52
Std Dev 23.26
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch101, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch102, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 29.86
Std Dev 3.031
Gaus mean : 29.7326
Gaus Width: 3.4760
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch102, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 36.32
Std Dev 3.718
Gaus mean : 36.8511
Gaus Width: 3.7347
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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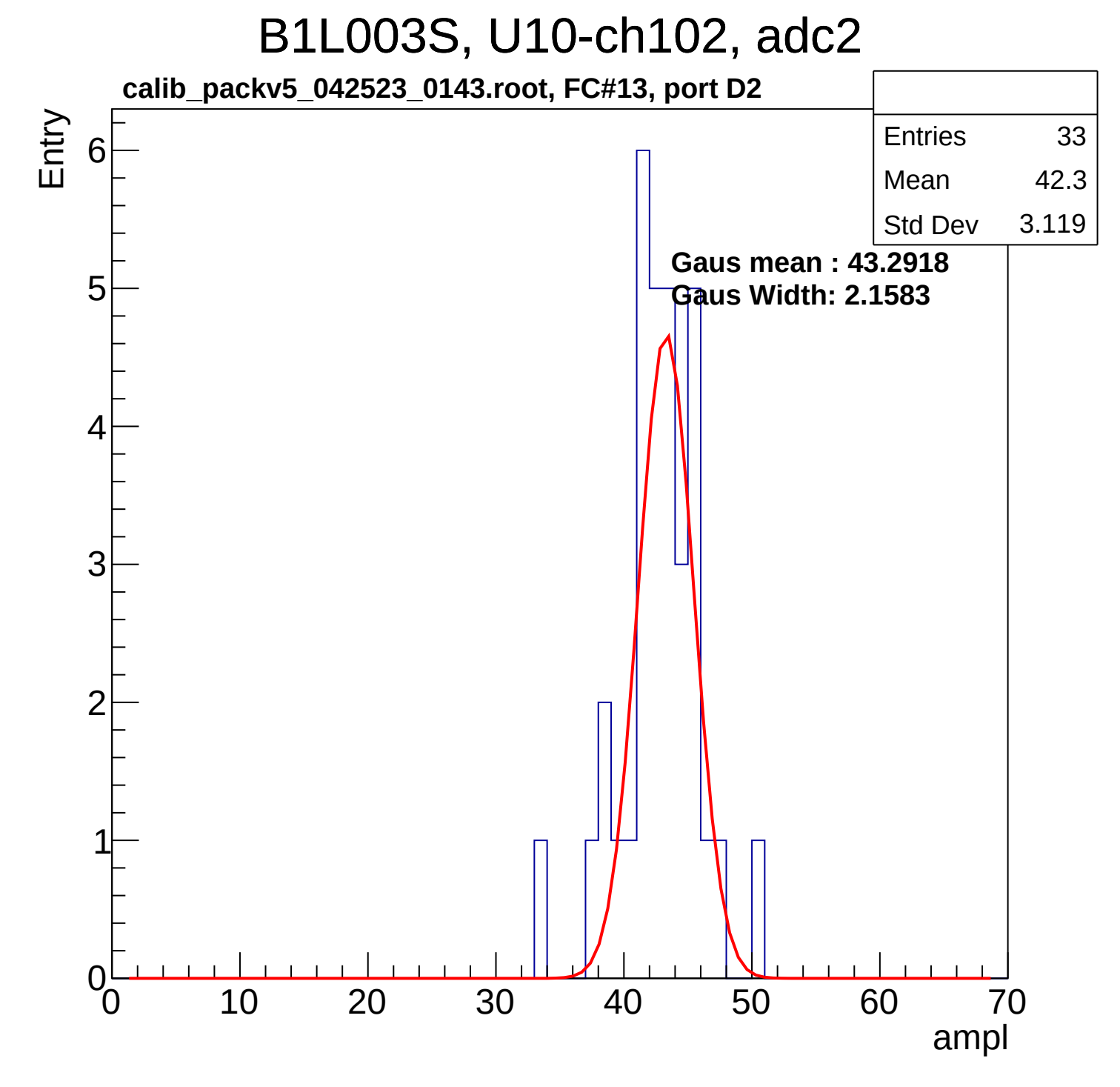
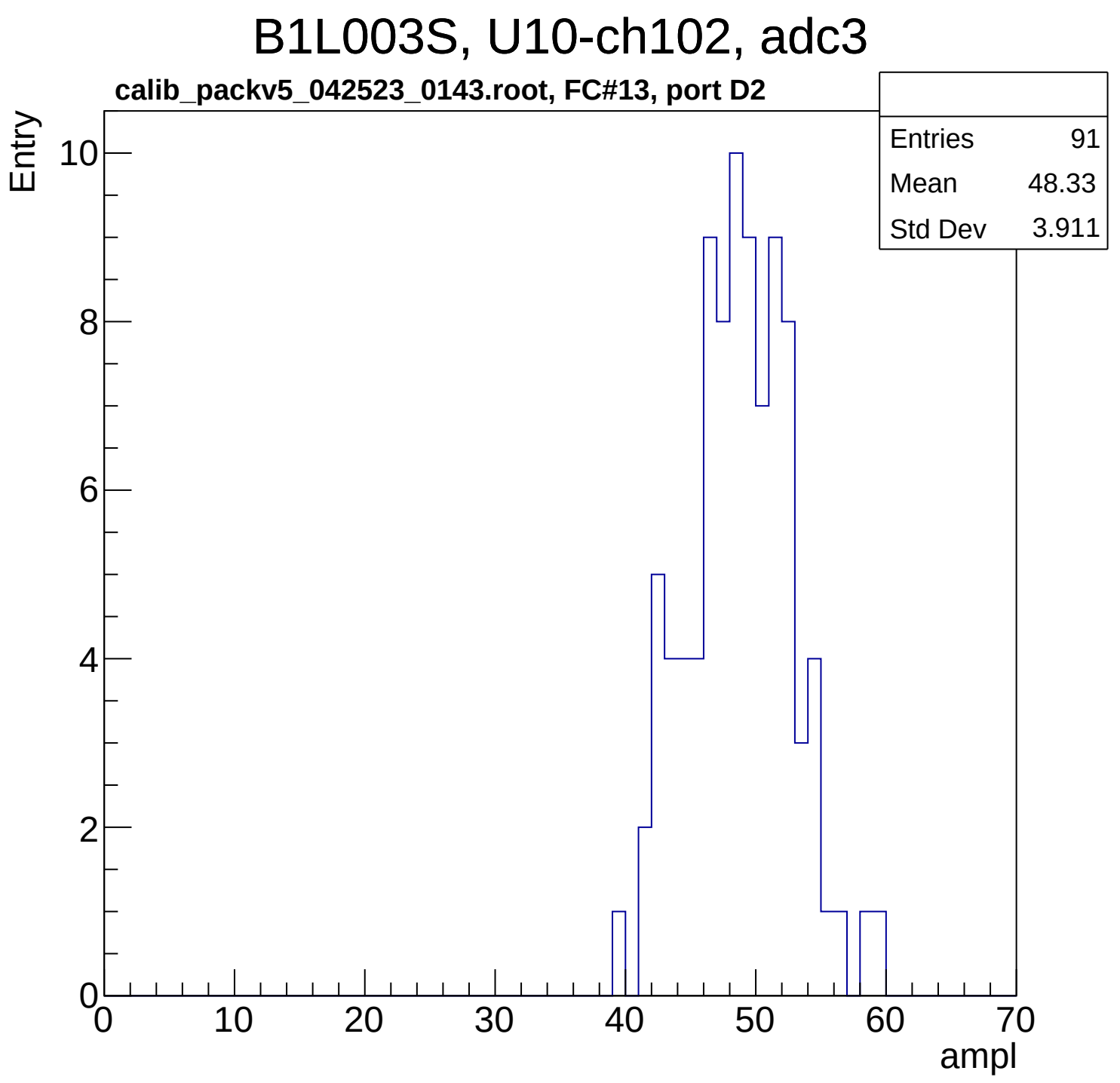
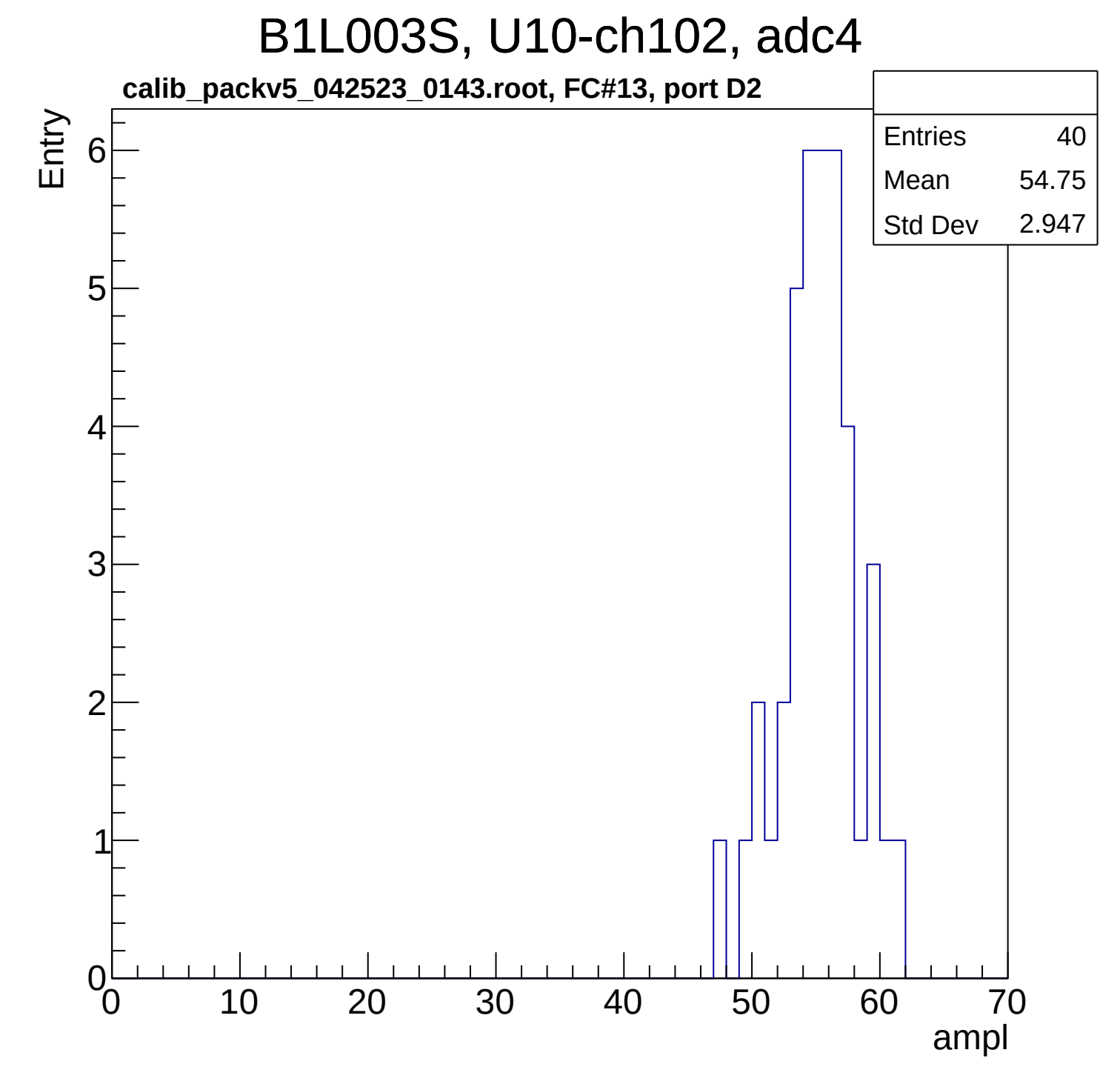
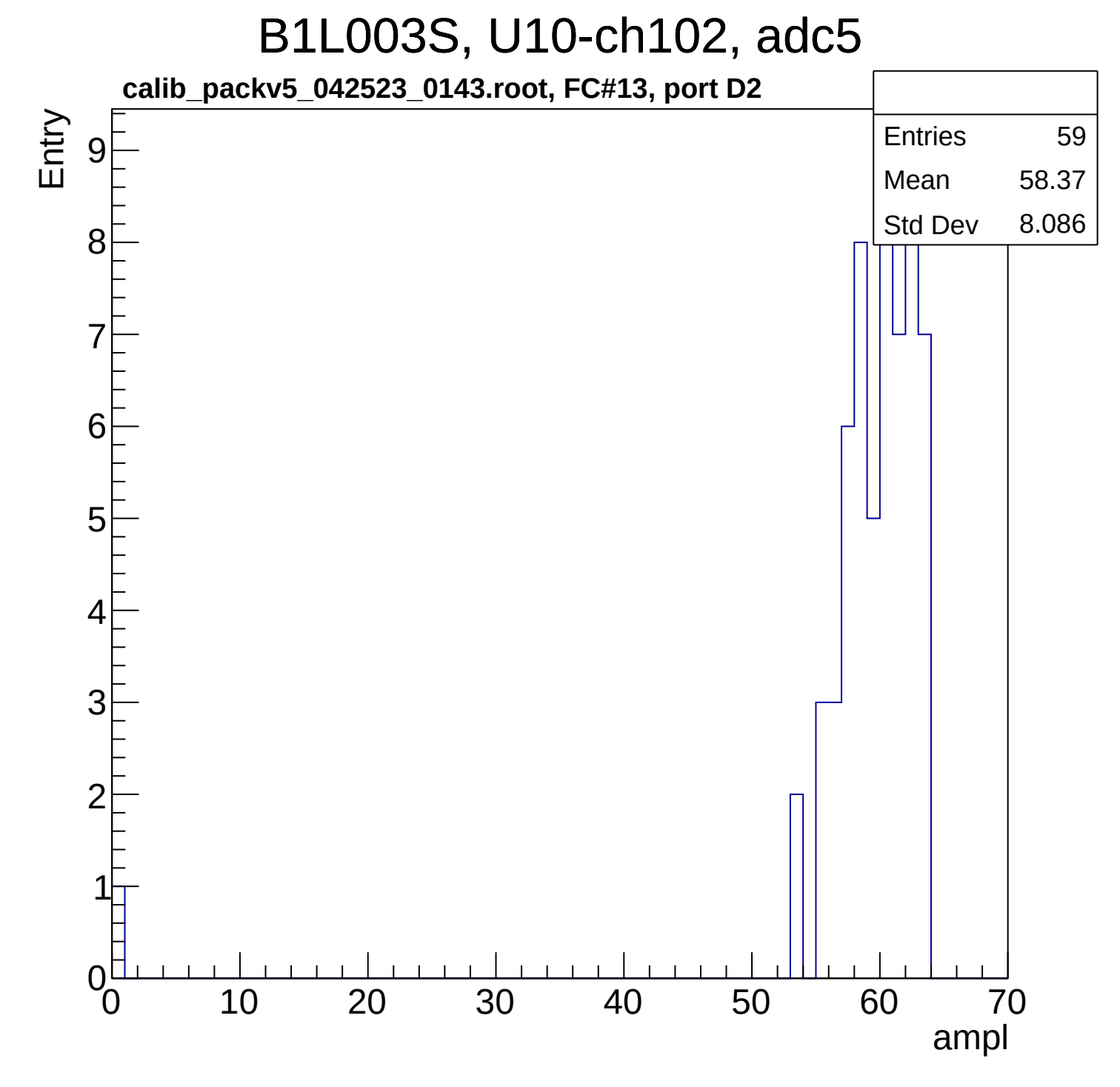

B1L003S, U10-ch102, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 42.3
Std Dev 3.119
Gaus mean : 43.2918
Gaus Width: 2.1583
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U10-ch102, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 91
Mean 48.33
Std Dev 3.911
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch102, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 54.75
Std Dev 2.947
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch102, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.37
Std Dev 8.086
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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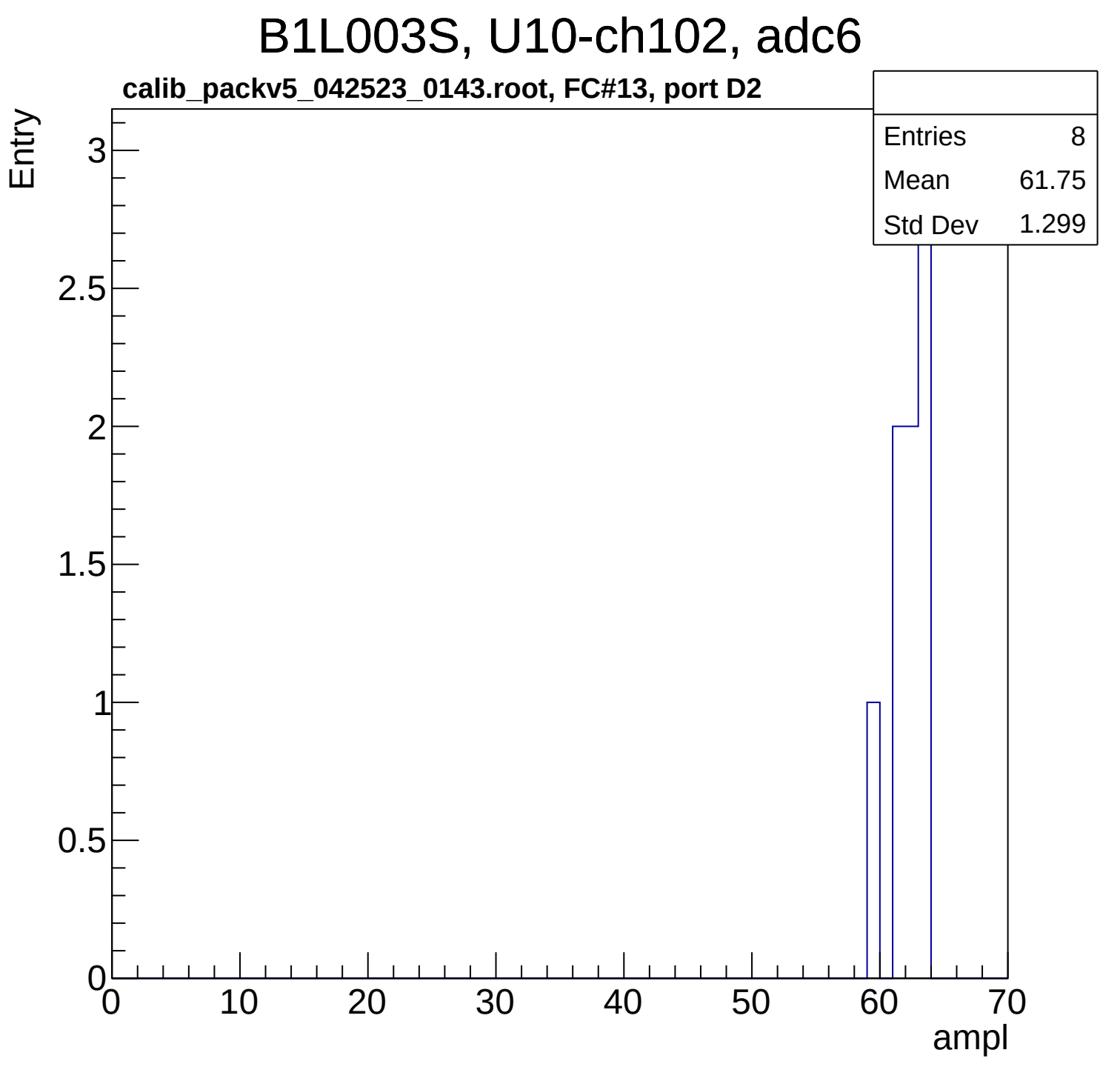
40
50
60
70


B1L003S, U10-ch102, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.75
Std Dev 1.299
Entry
ampl
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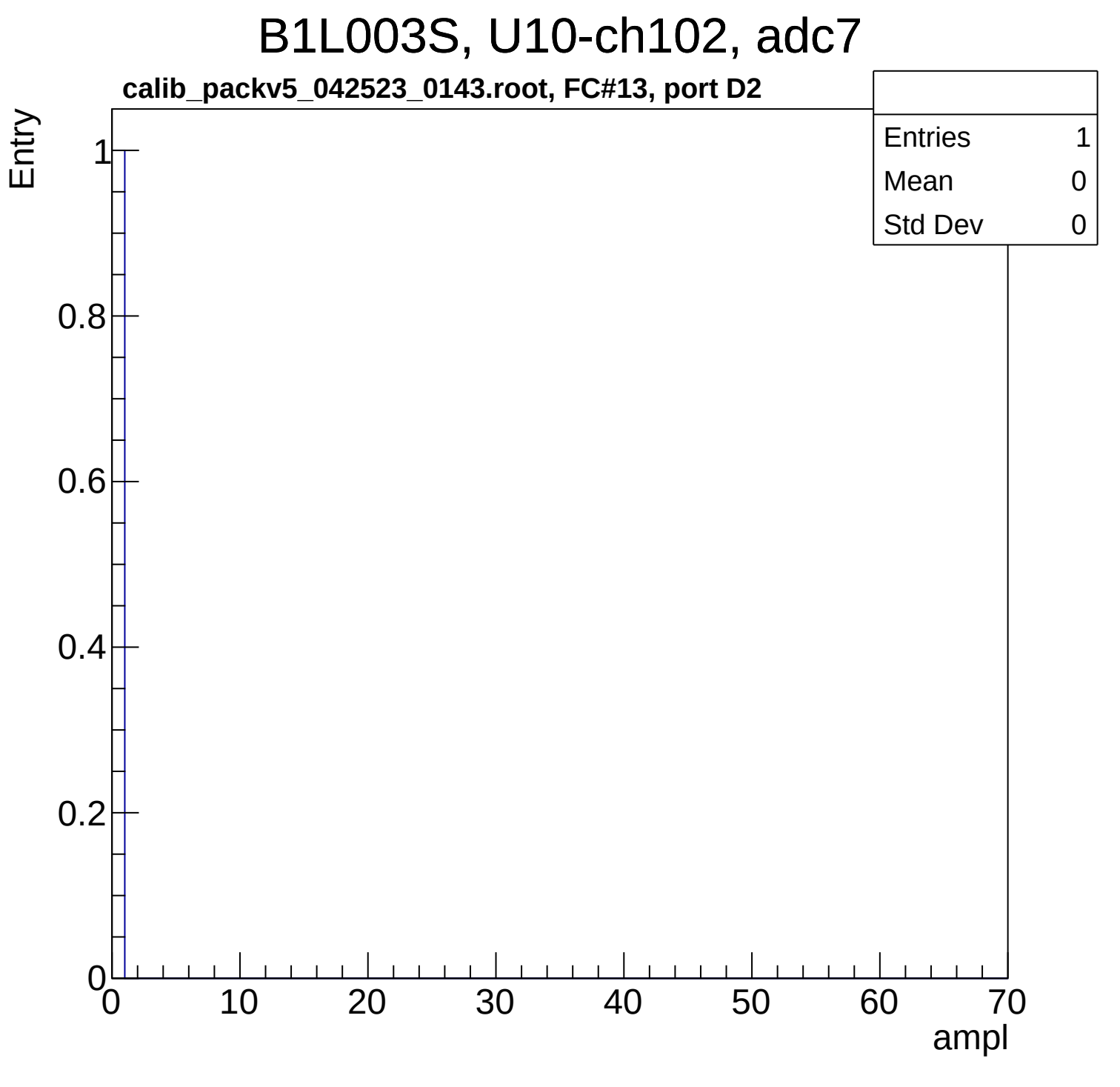
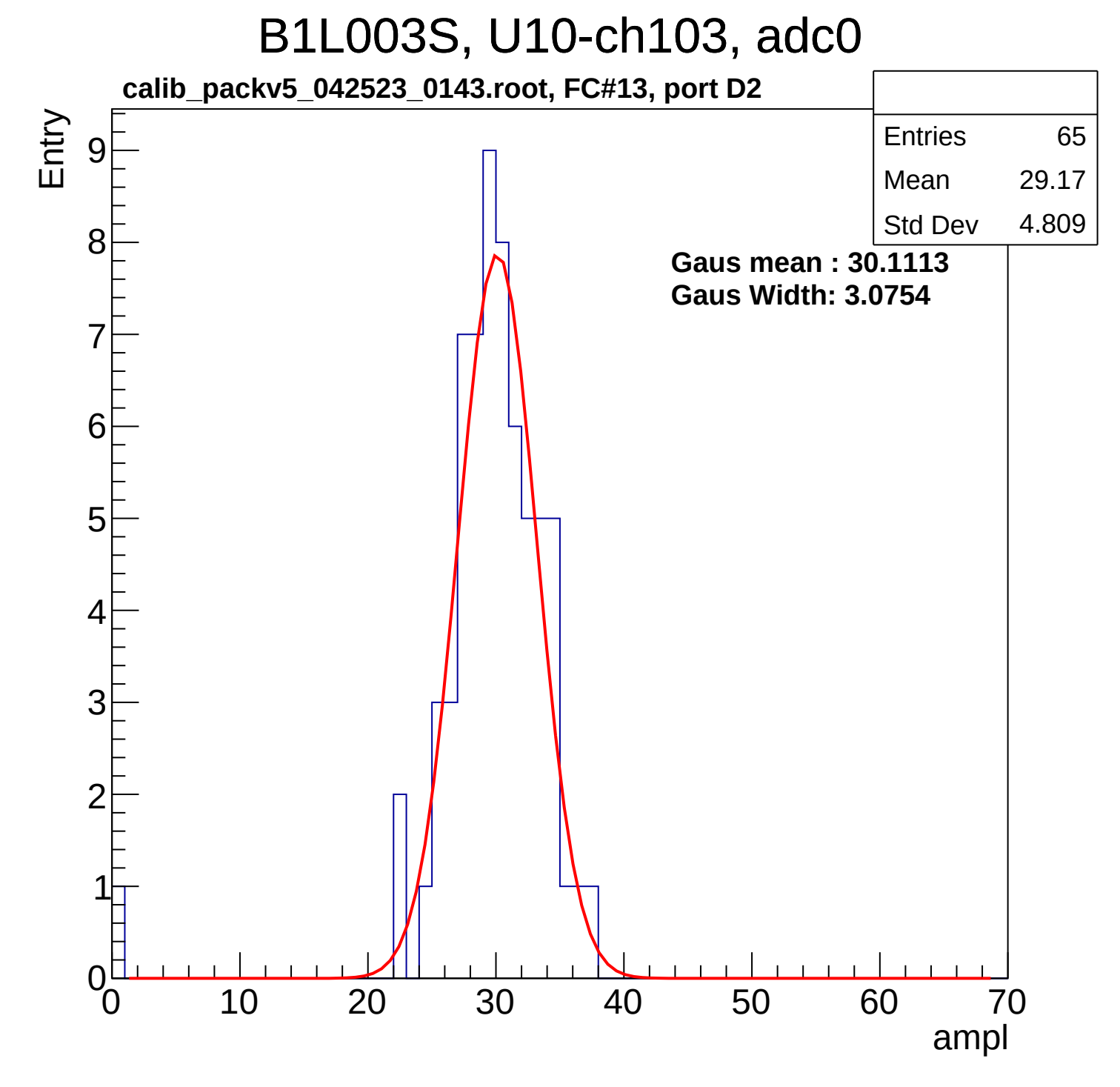
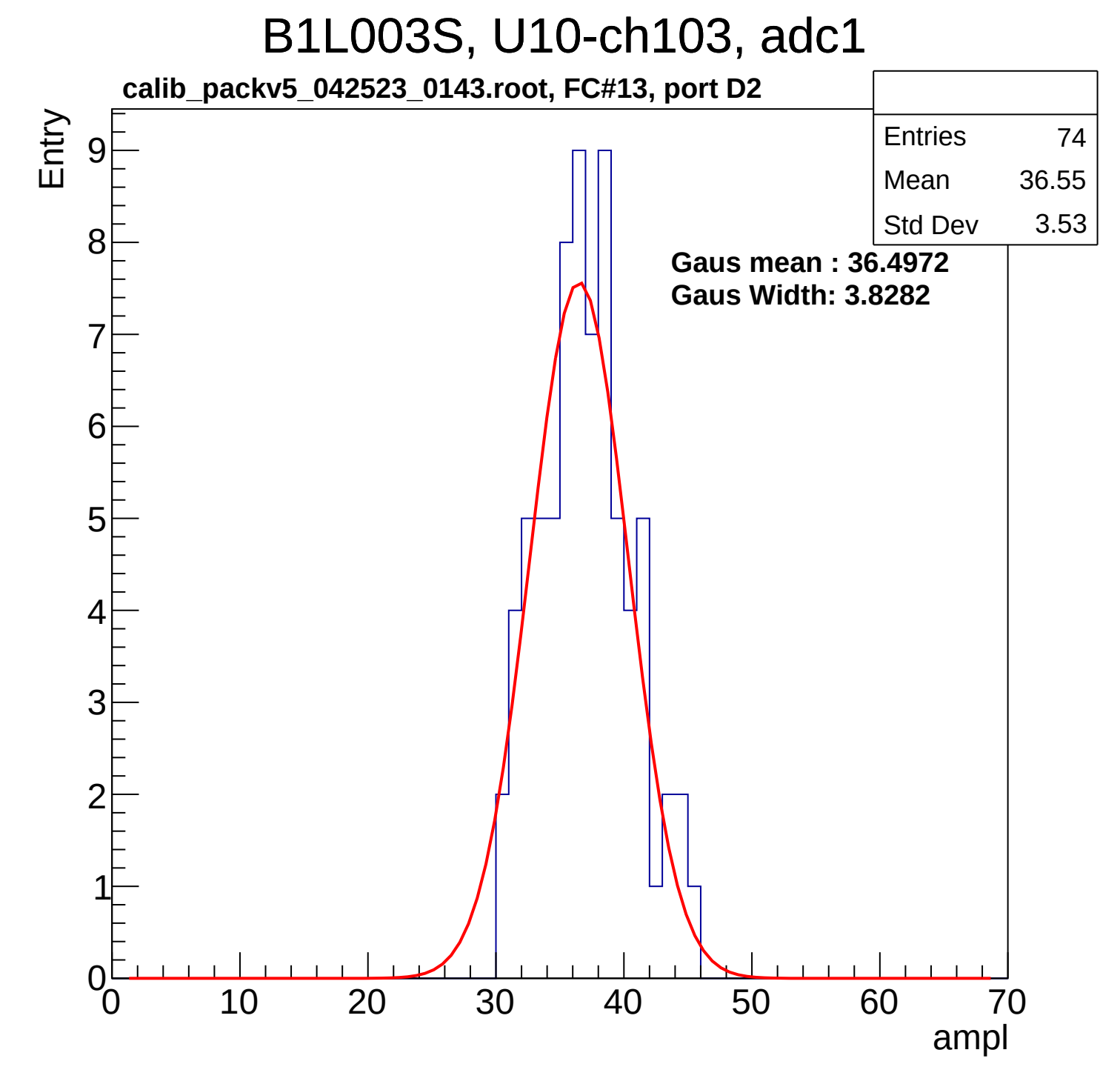
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch102, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch103, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.17
Std Dev 4.809
Gaus mean : 30.1113
Gaus Width: 3.0754
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch103, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.55
Std Dev 3.53
Gaus mean : 36.4972
Gaus Width: 3.8282
Entry
ampl
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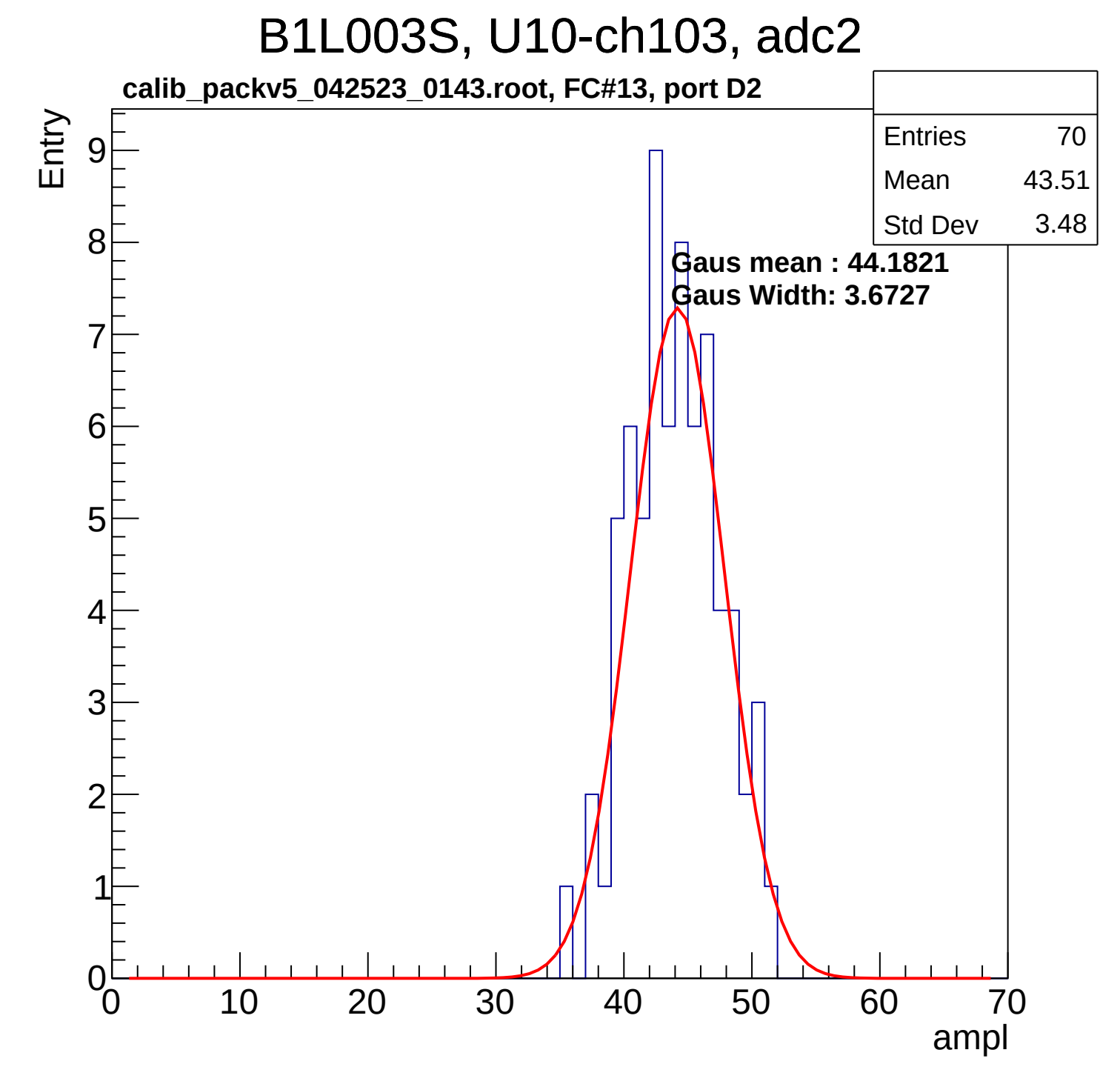
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch103, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 43.51
Std Dev 3.48
Gaus mean : 44.1821
Gaus Width: 3.6727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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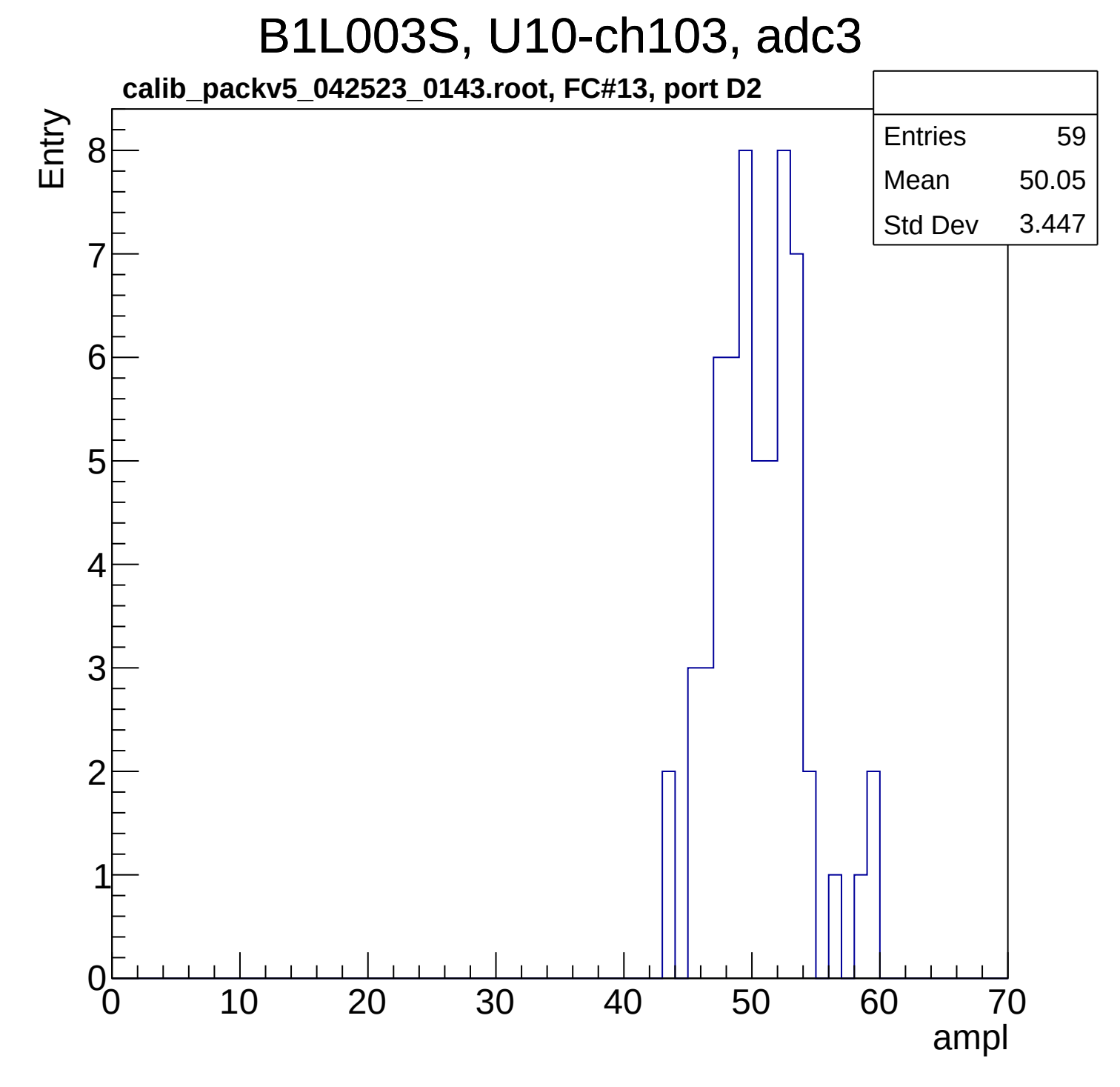
B1L003S, U10-ch103, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 50.05
Std Dev 3.447
Entry
ampl
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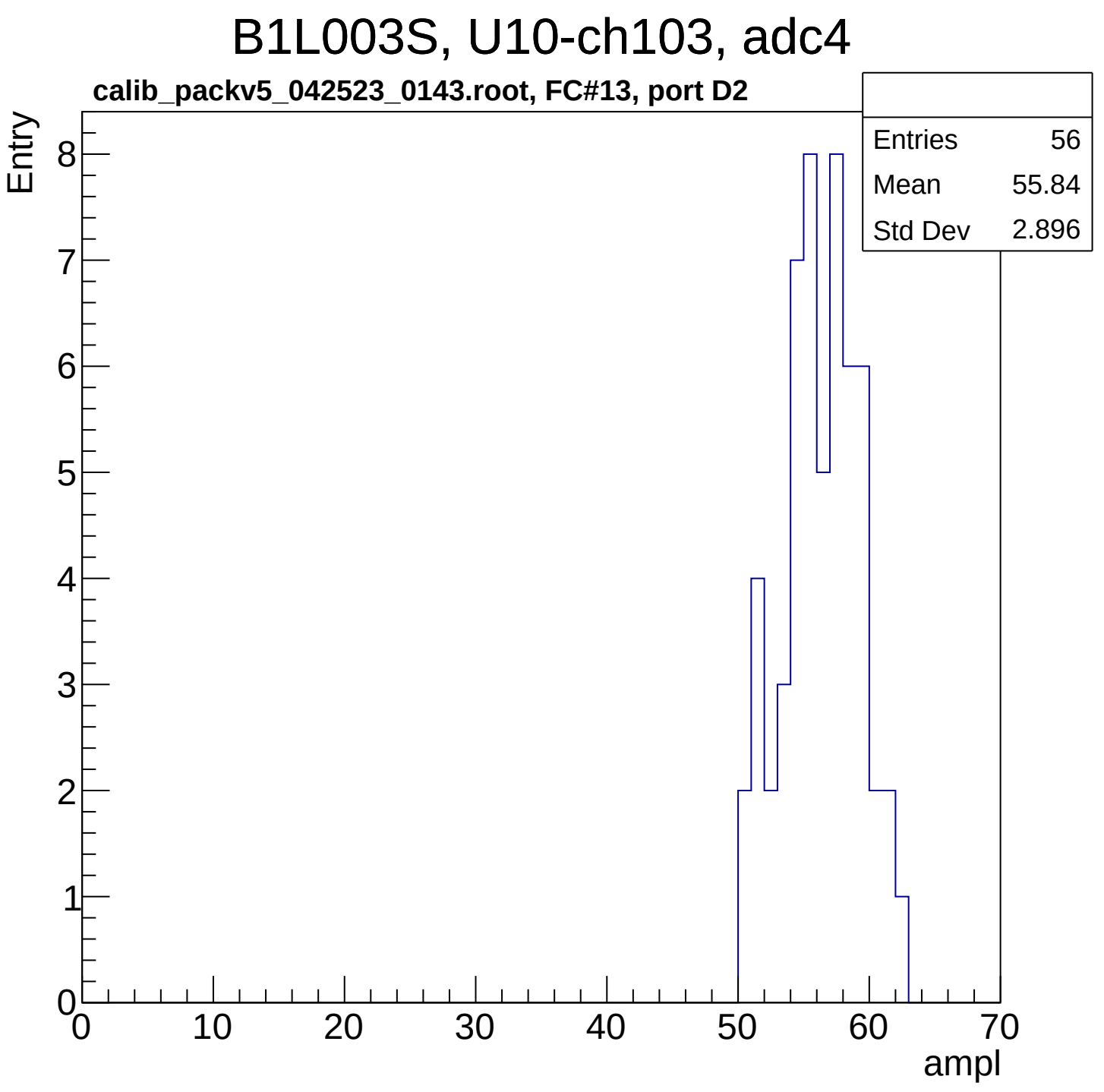
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch103, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.84
Std Dev 2.896
Entry
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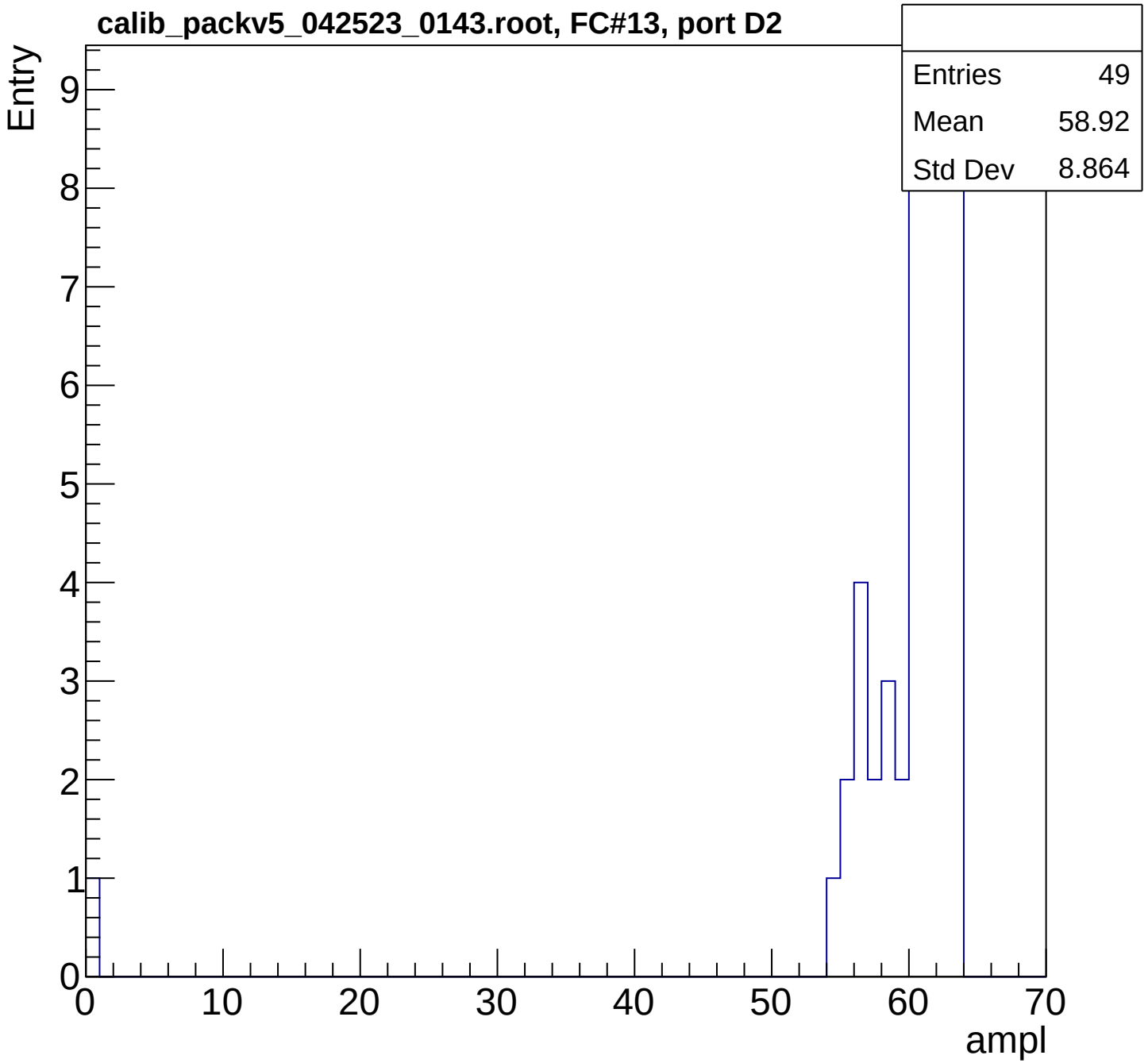
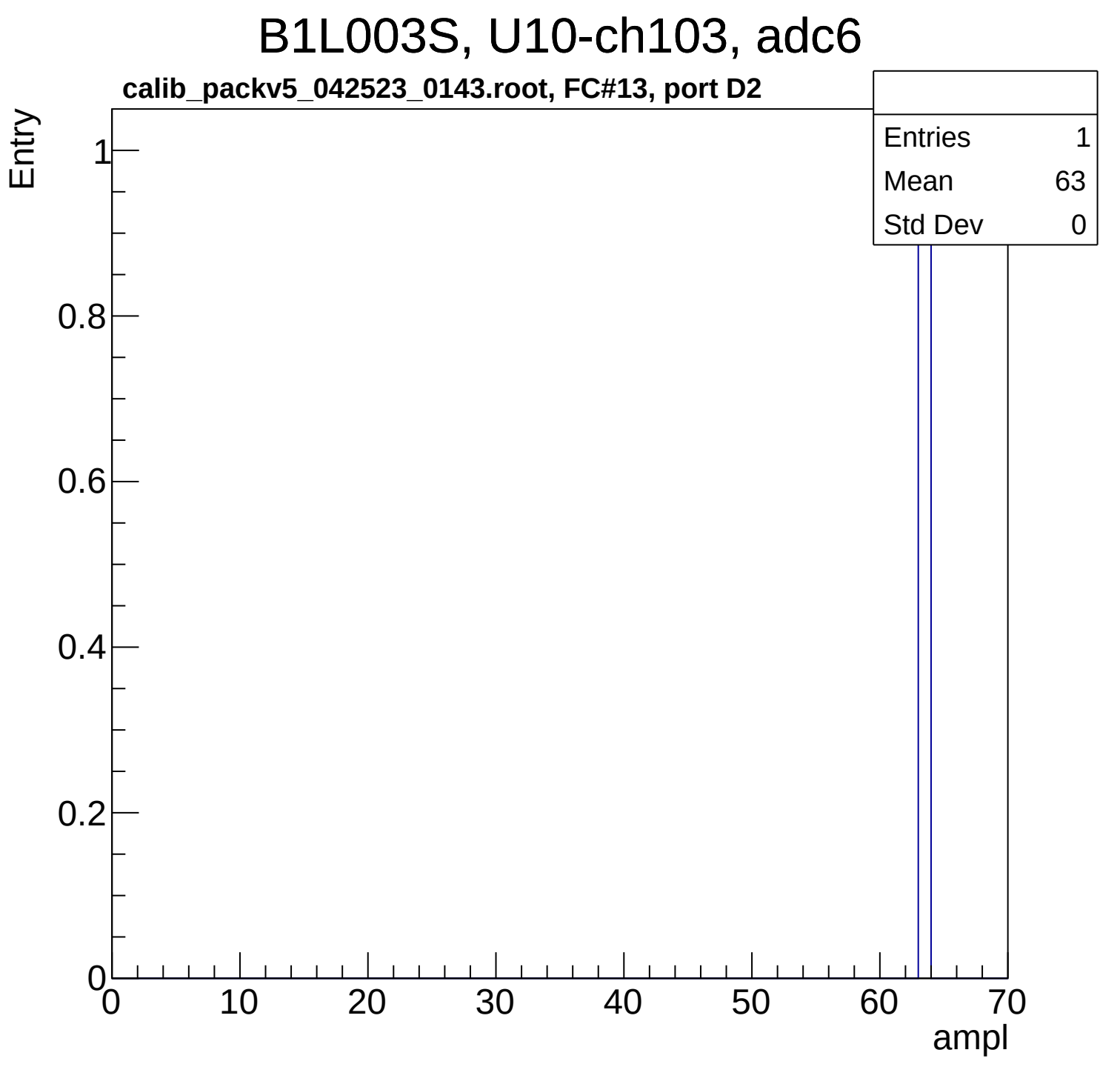
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch103, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.92
Std Dev 8.864
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch103, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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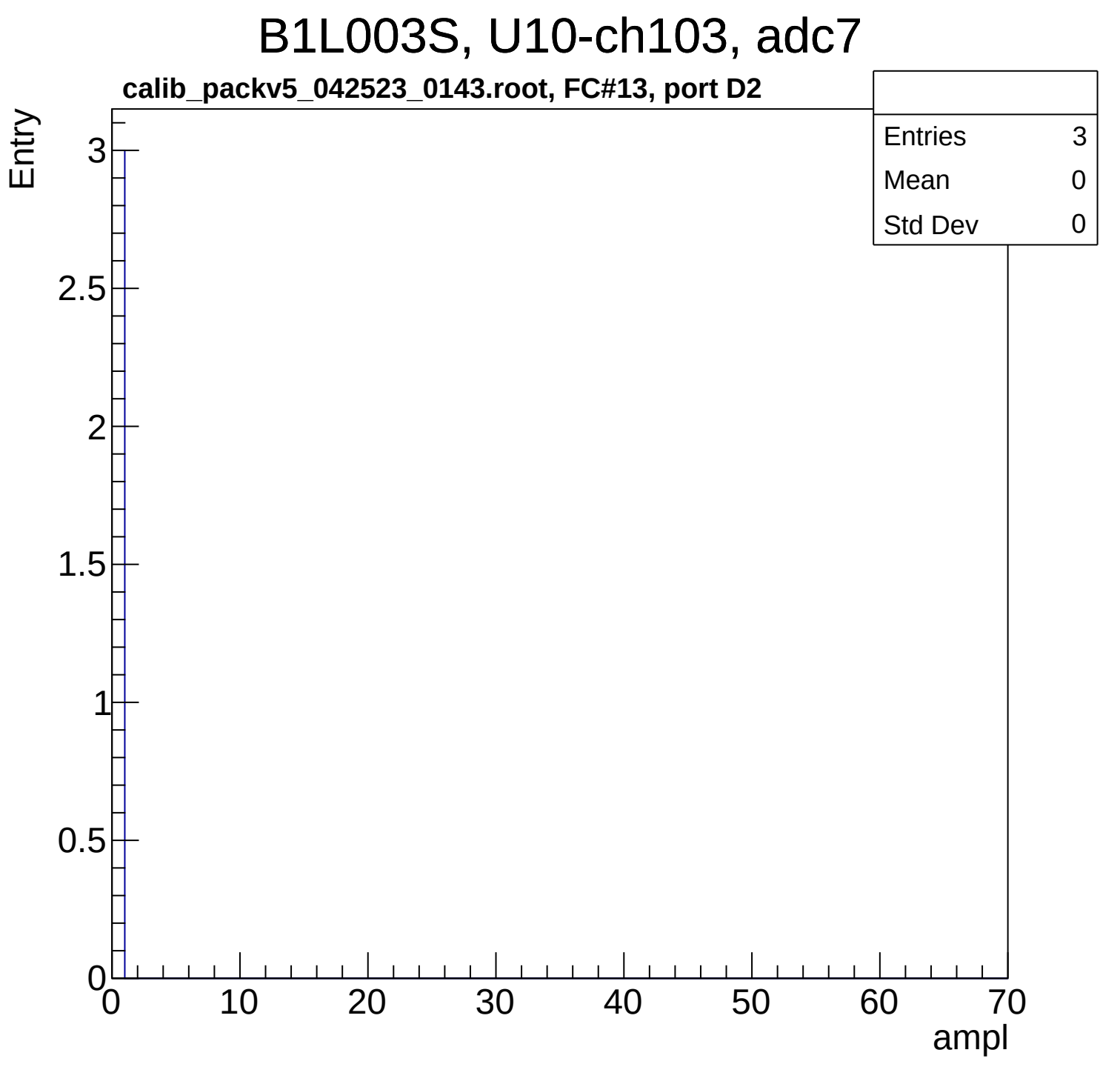
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch103, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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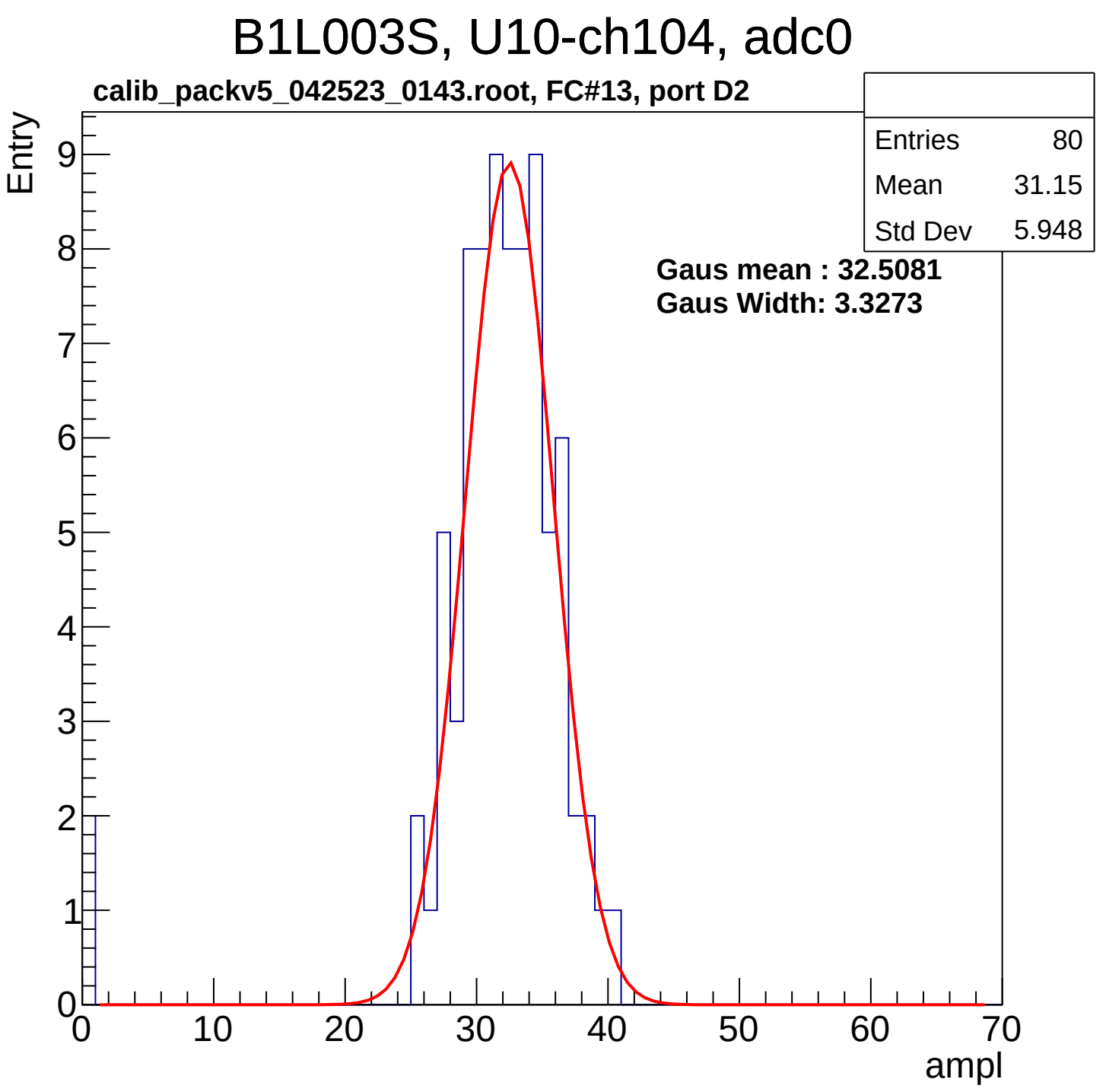
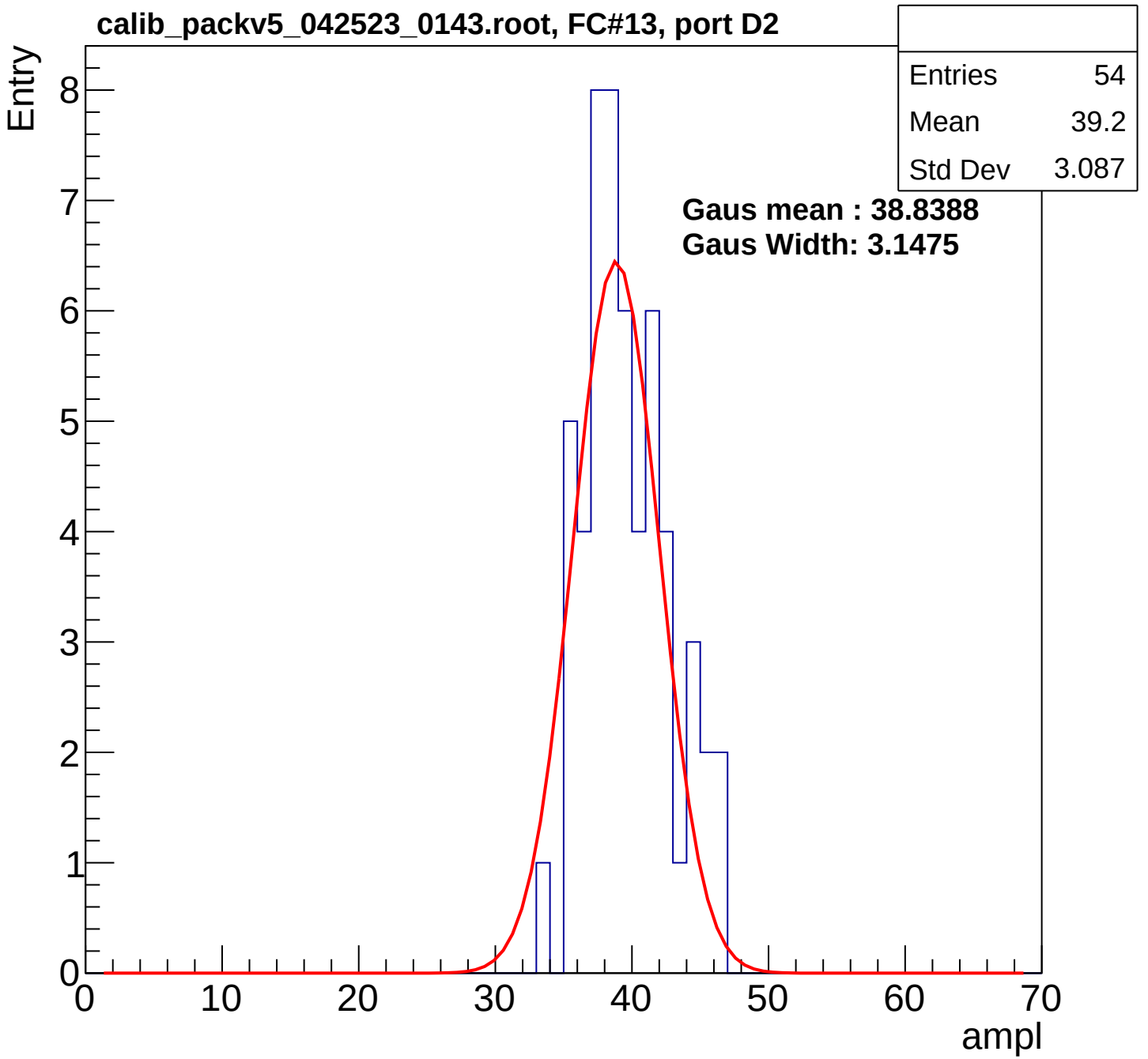
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch104, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 31.15
Std Dev 5.948
Gaus mean : 32.5081
Gaus Width: 3.3273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch104, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 39.2
Std Dev 3.087
Gaus mean : 38.8388
Gaus Width: 3.1475
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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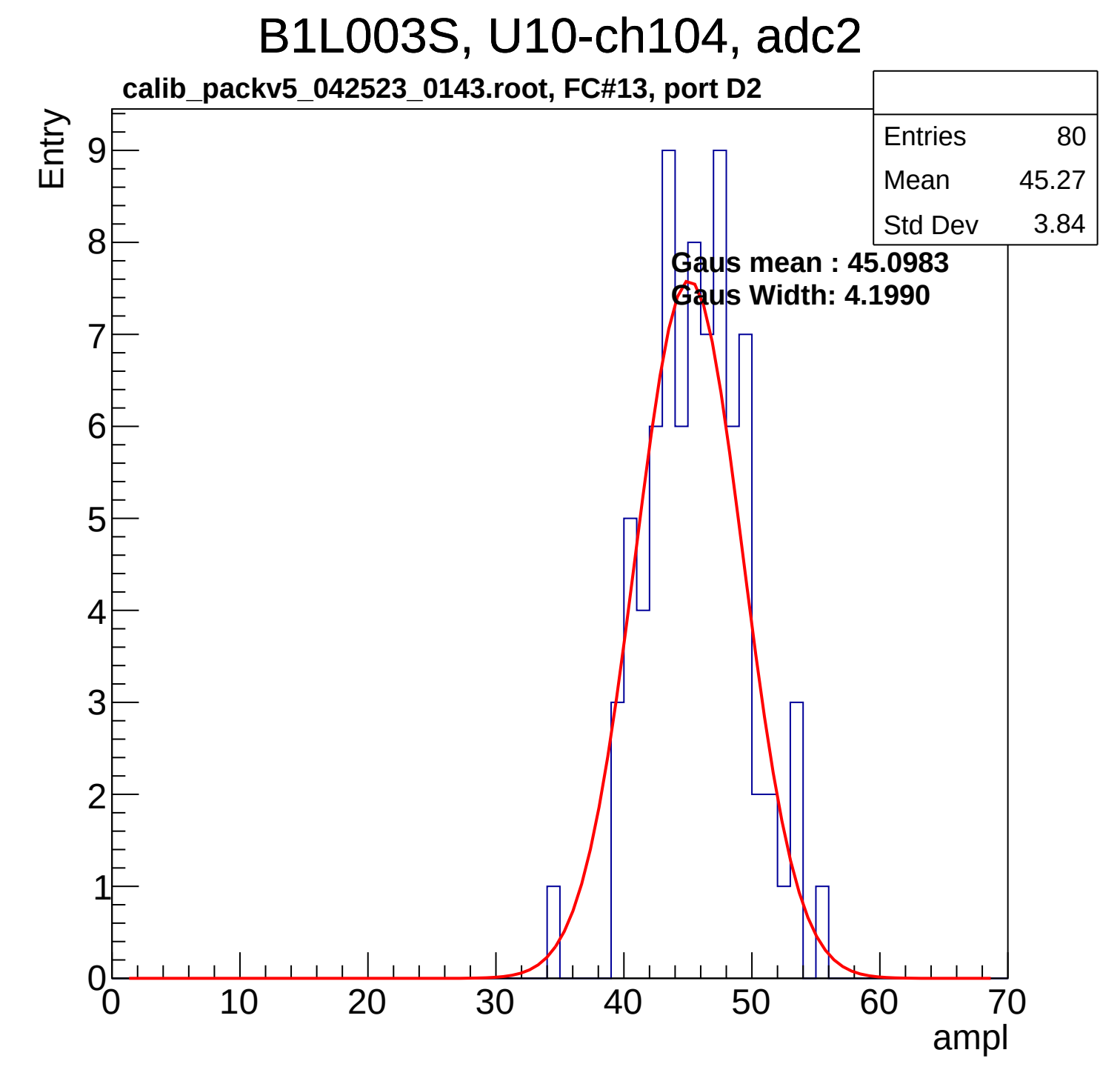
B1L003S, U10-ch104, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 45.27
Std Dev 3.84
Gaus mean : 45.0983
Gaus Width: 4.1990
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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60
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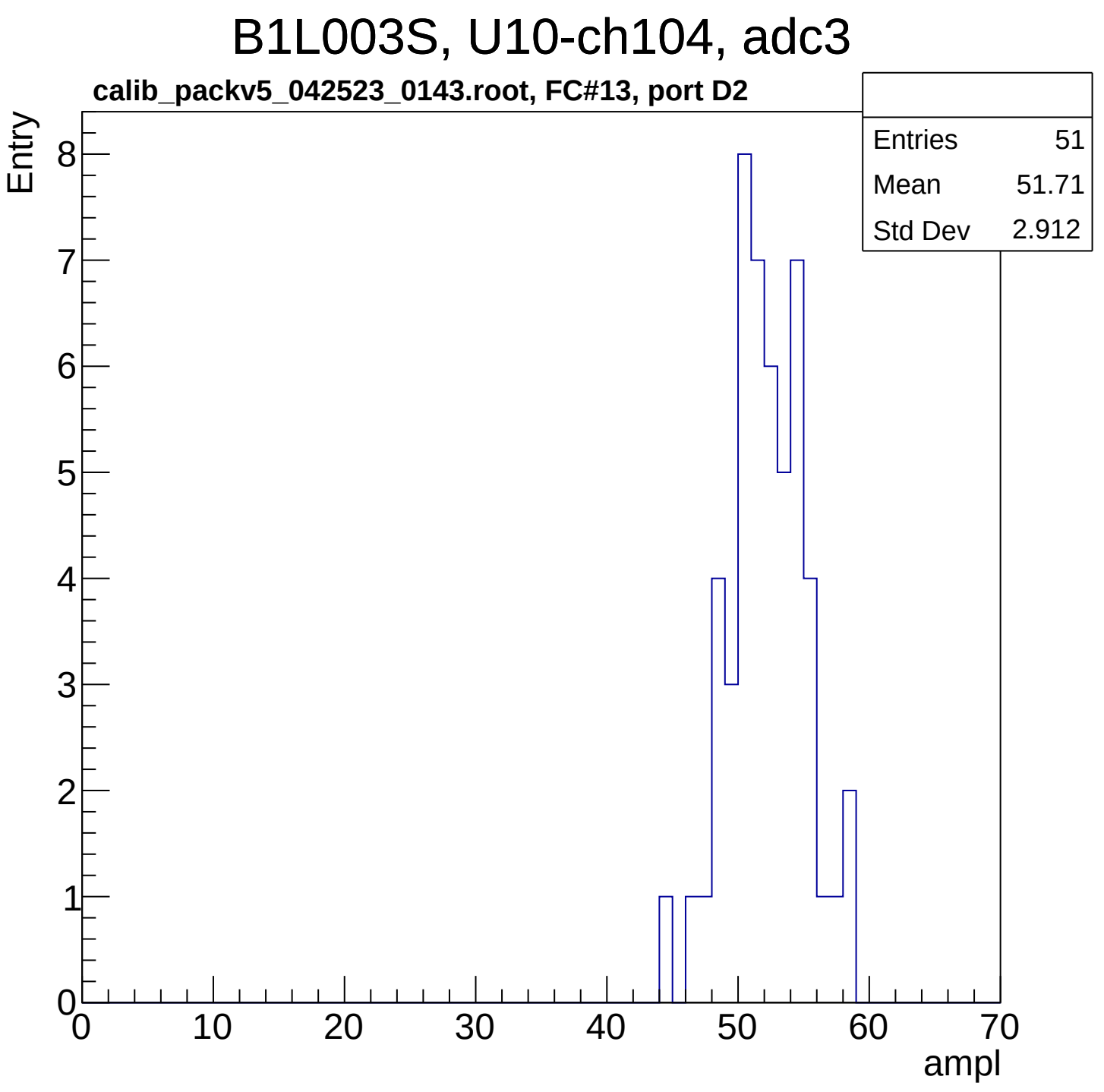
B1L003S, U10-ch104, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 51.71
Std Dev 2.912
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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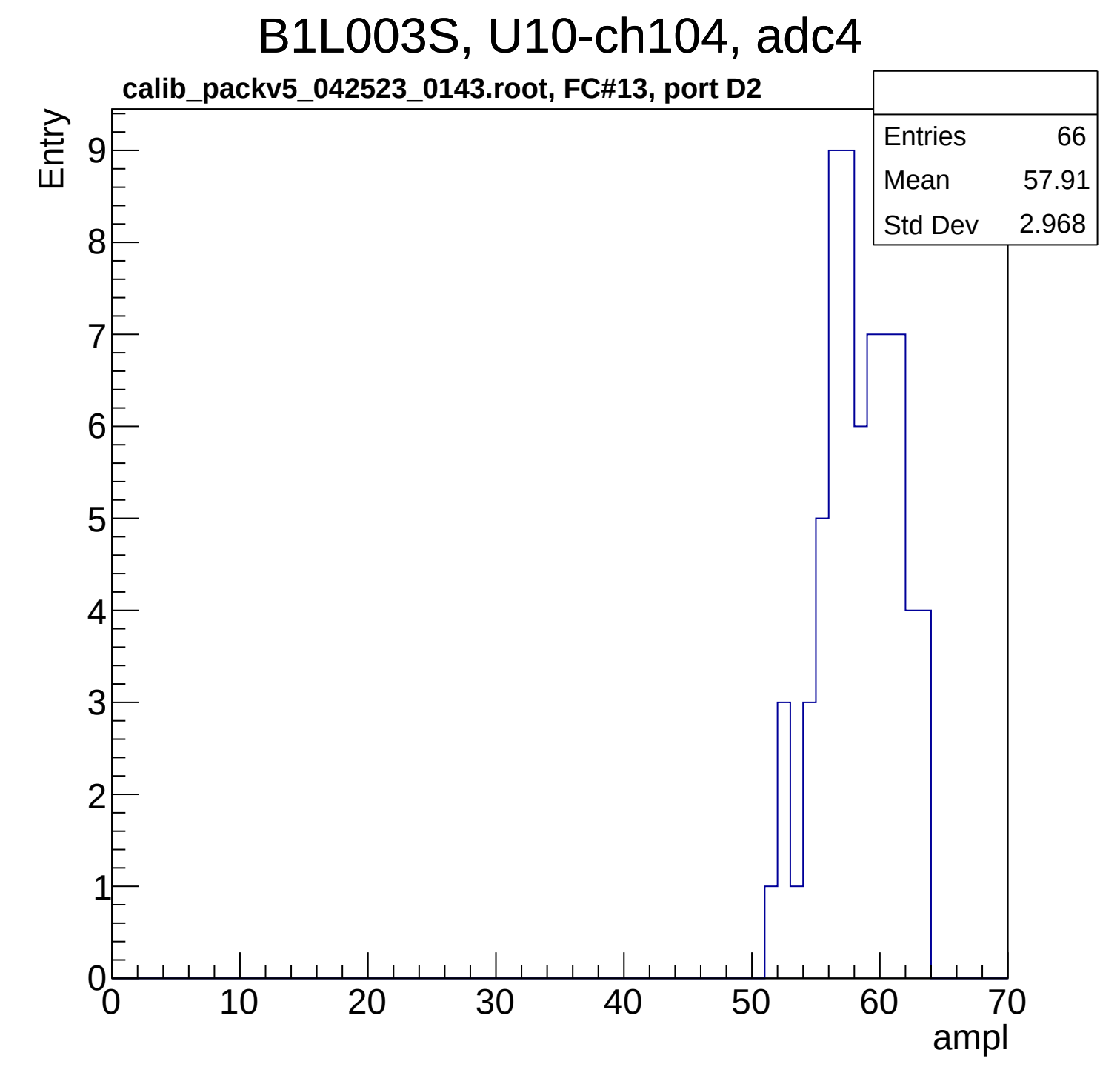
B1L003S, U10-ch104, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 57.91
Std Dev 2.968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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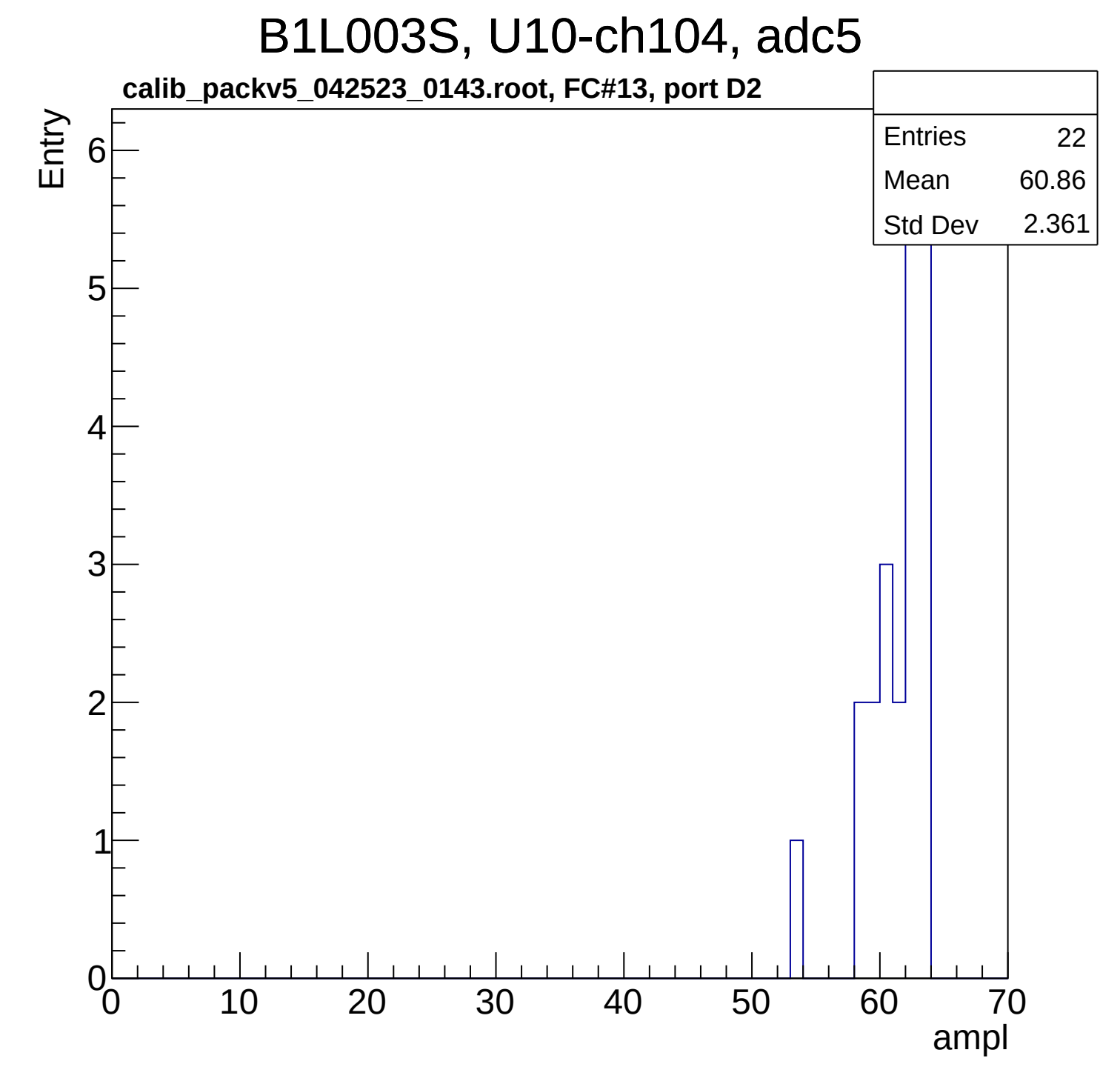
B1L003S, U10-ch104, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 60.86
Std Dev 2.361
Entry
ampl
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2
3
4
5
6
0
10
20
30
40
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60
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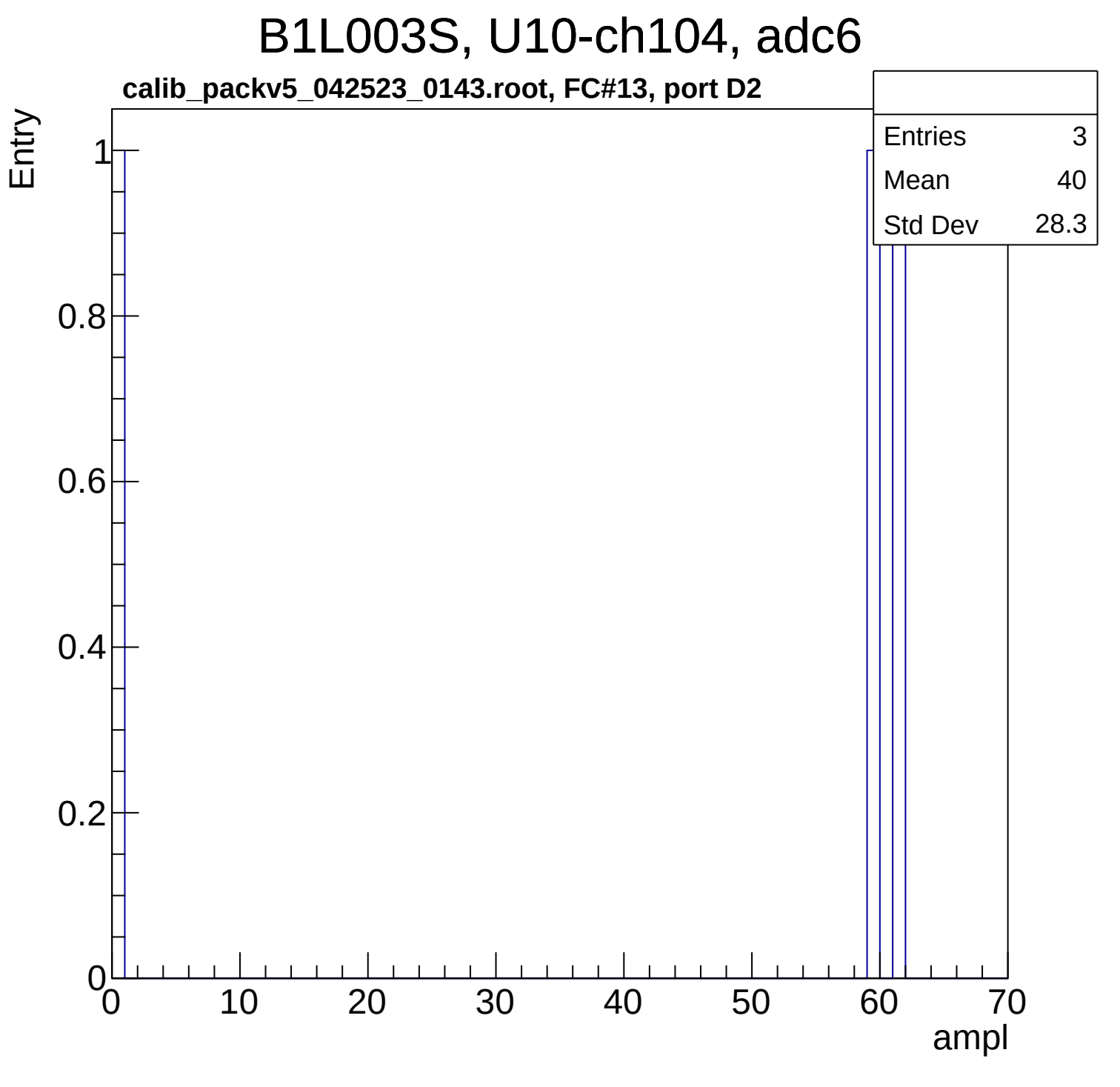
B1L003S, U10-ch104, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 40
Std Dev 28.3
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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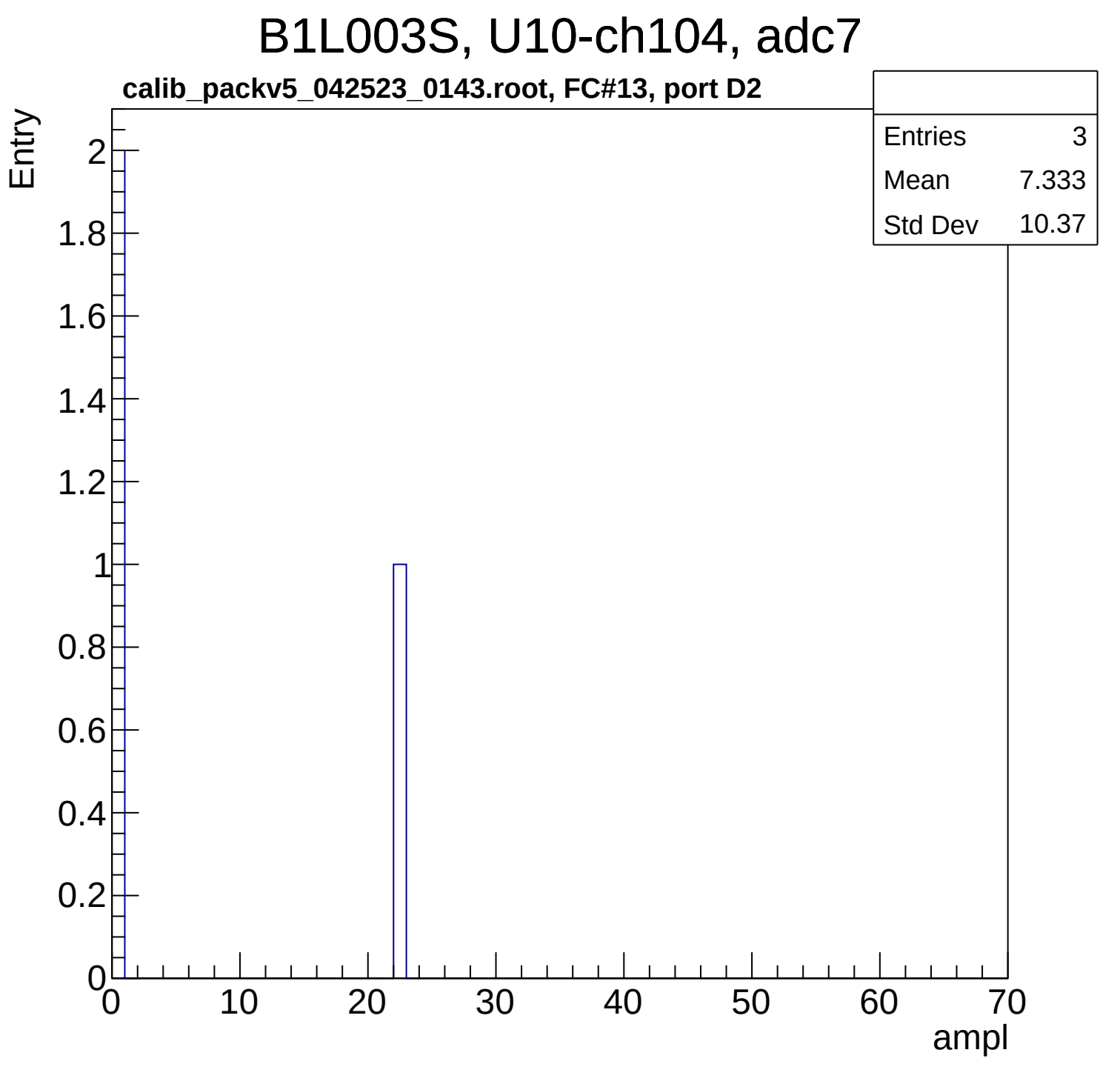
B1L003S, U10-ch104, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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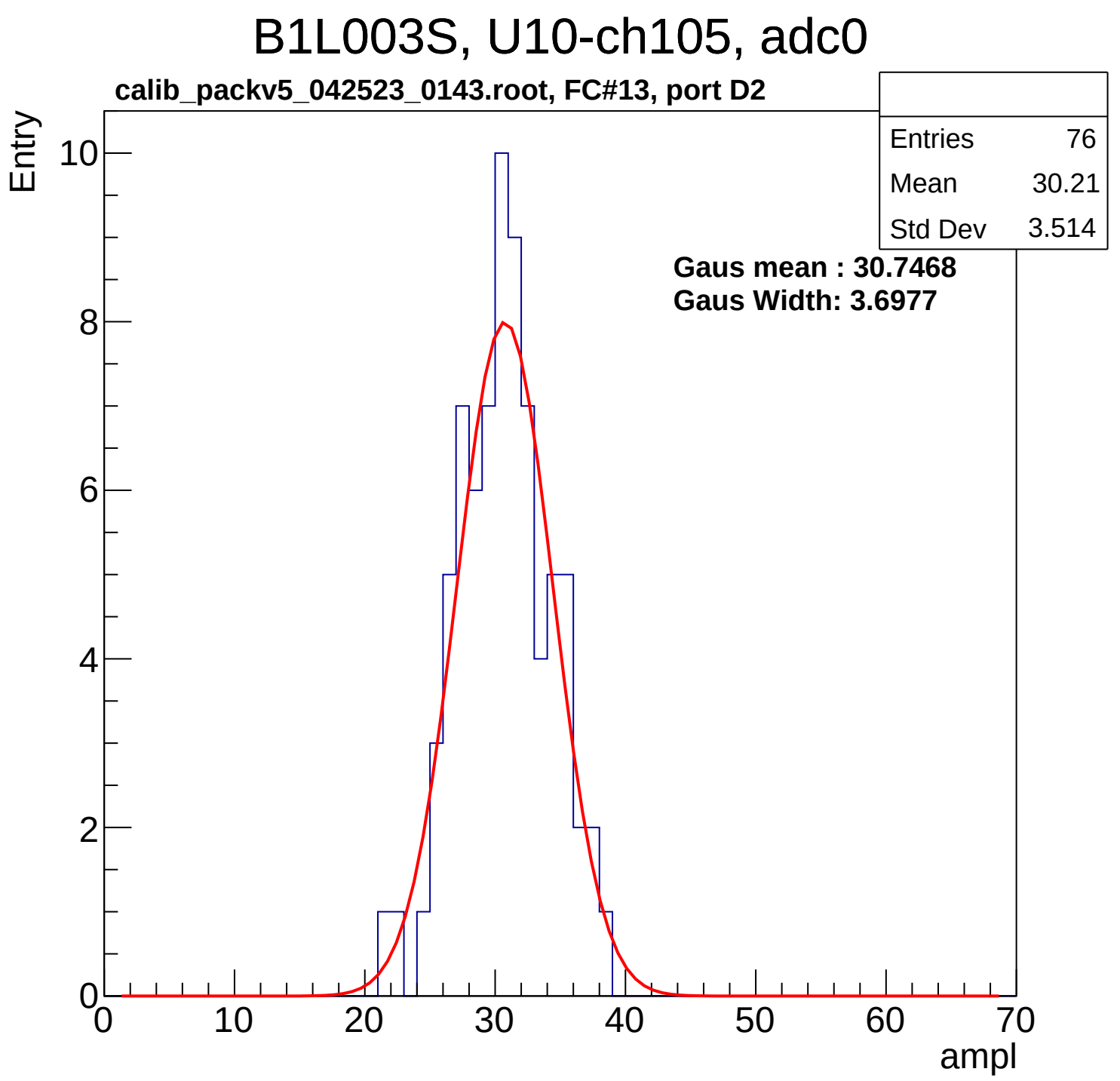
B1L003S, U10-ch105, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 30.21
Std Dev 3.514
Gaus mean : 30.7468
Gaus Width: 3.6977
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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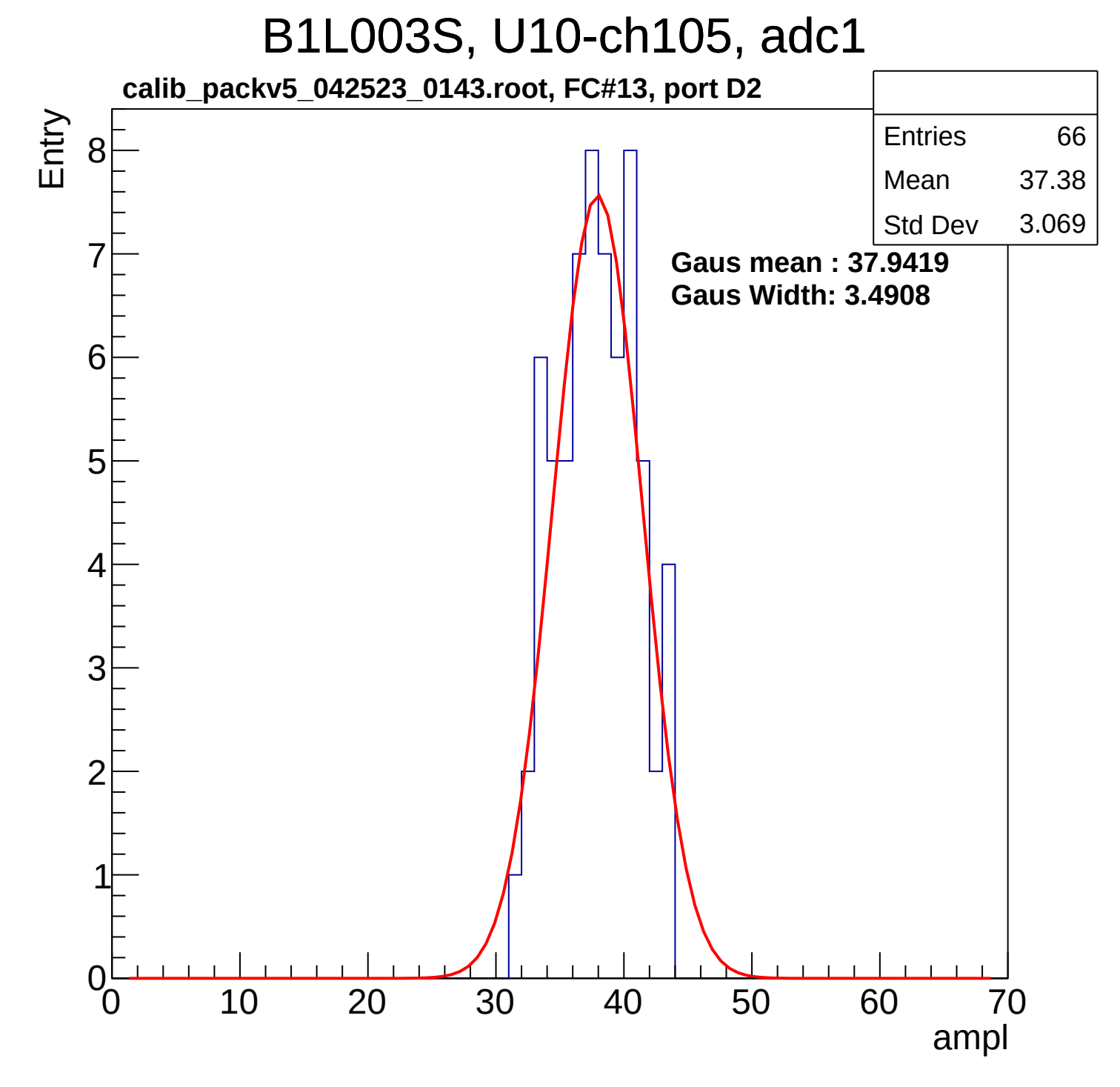
B1L003S, U10-ch105, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 37.38
Std Dev 3.069
Gaus mean : 37.9419
Gaus Width: 3.4908
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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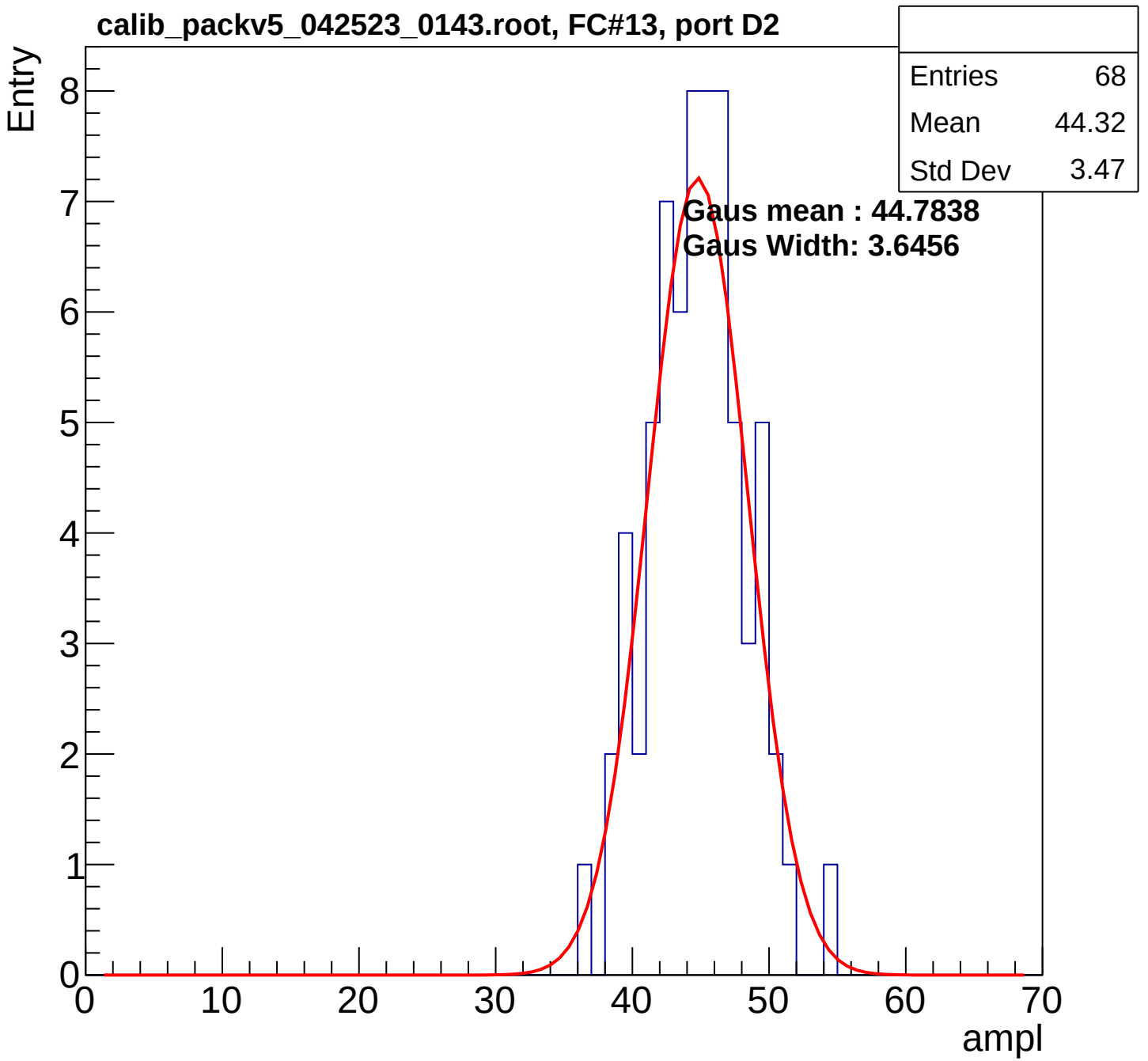
B1L003S, U10-ch105, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 44.32
Std Dev 3.47
Gaus mean : 44.7838
Gaus Width: 3.6456
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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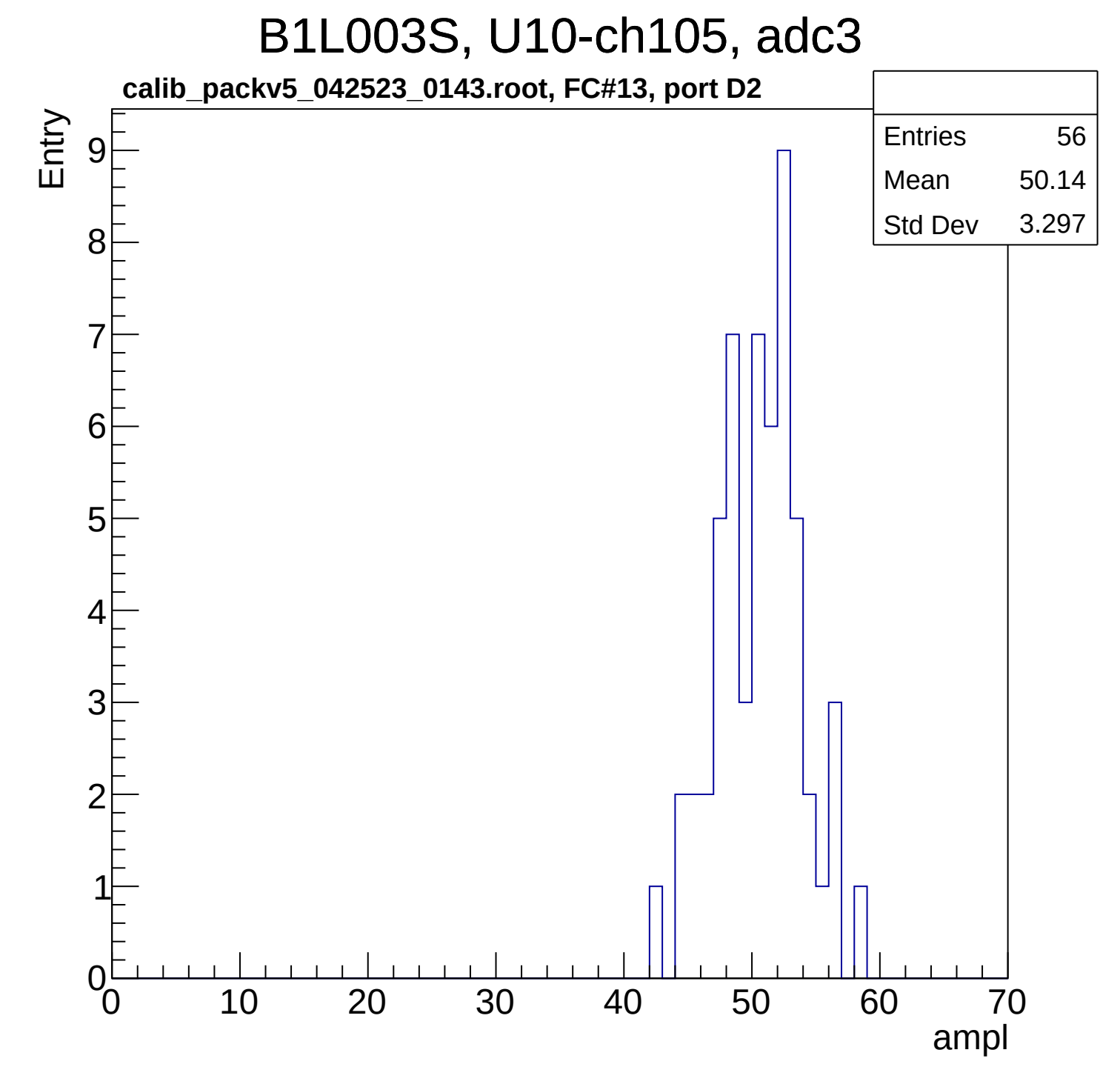
B1L003S, U10-ch105, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 50.14
Std Dev 3.297
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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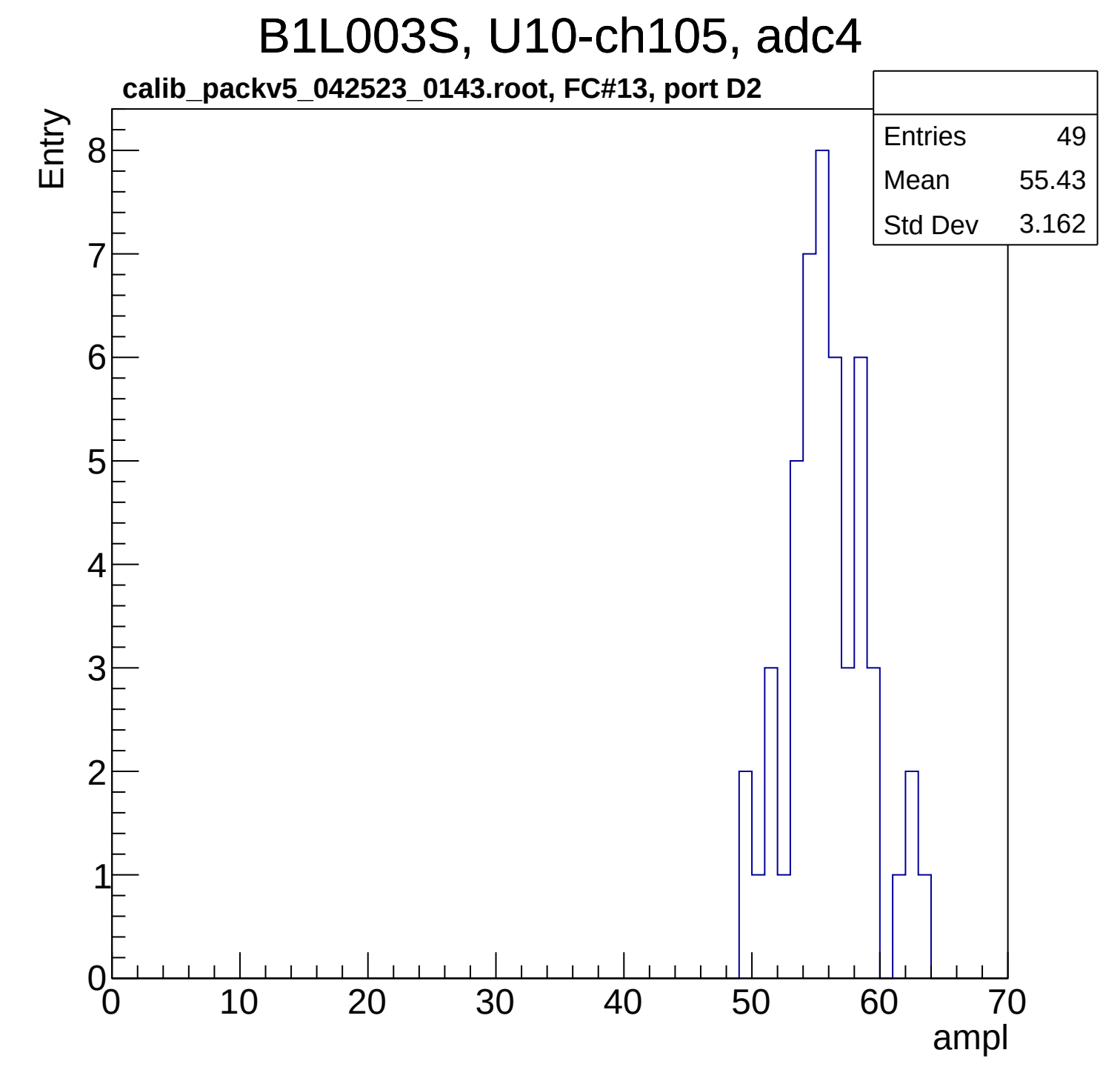
B1L003S, U10-ch105, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 55.43
Std Dev 3.162
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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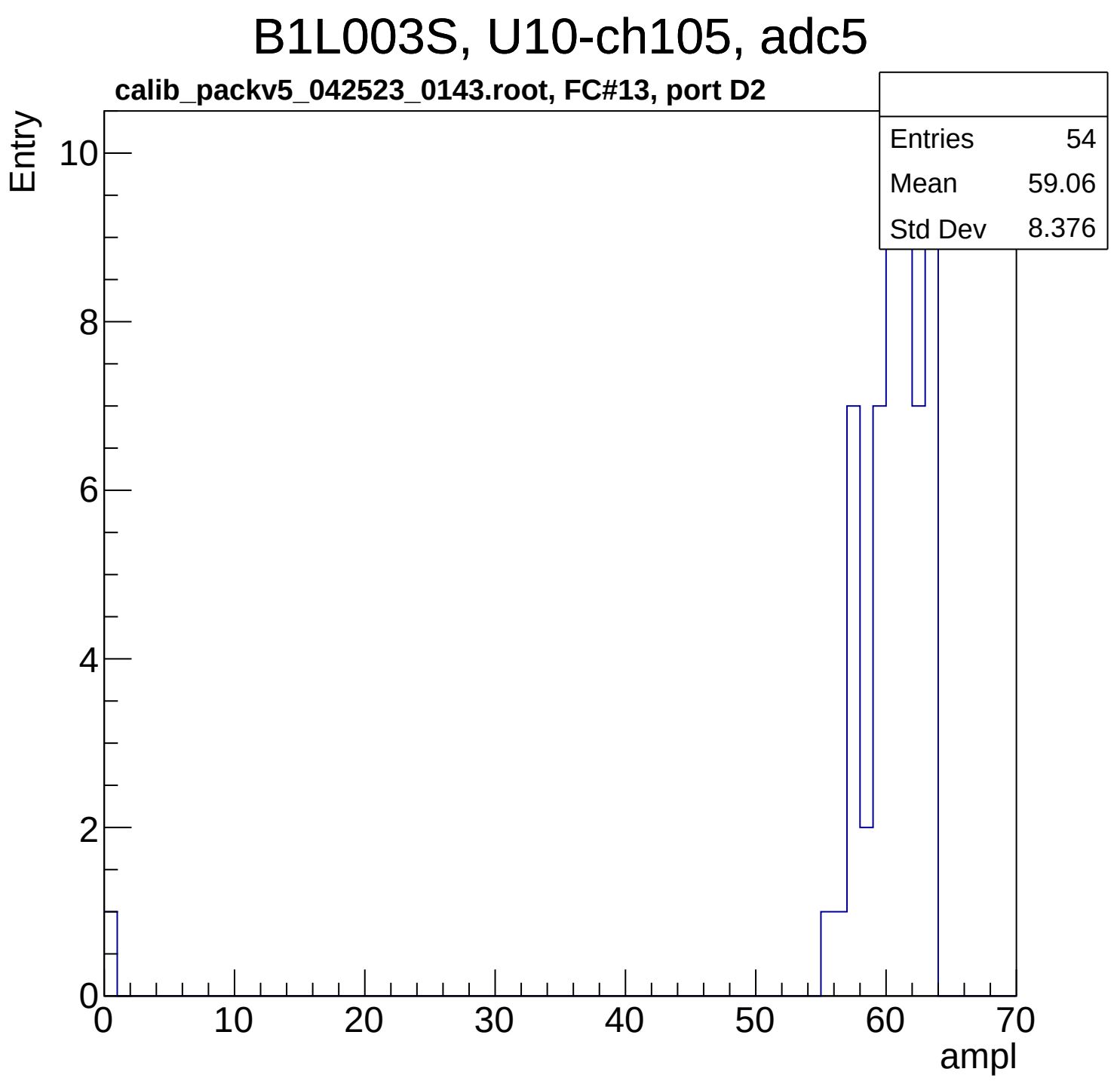
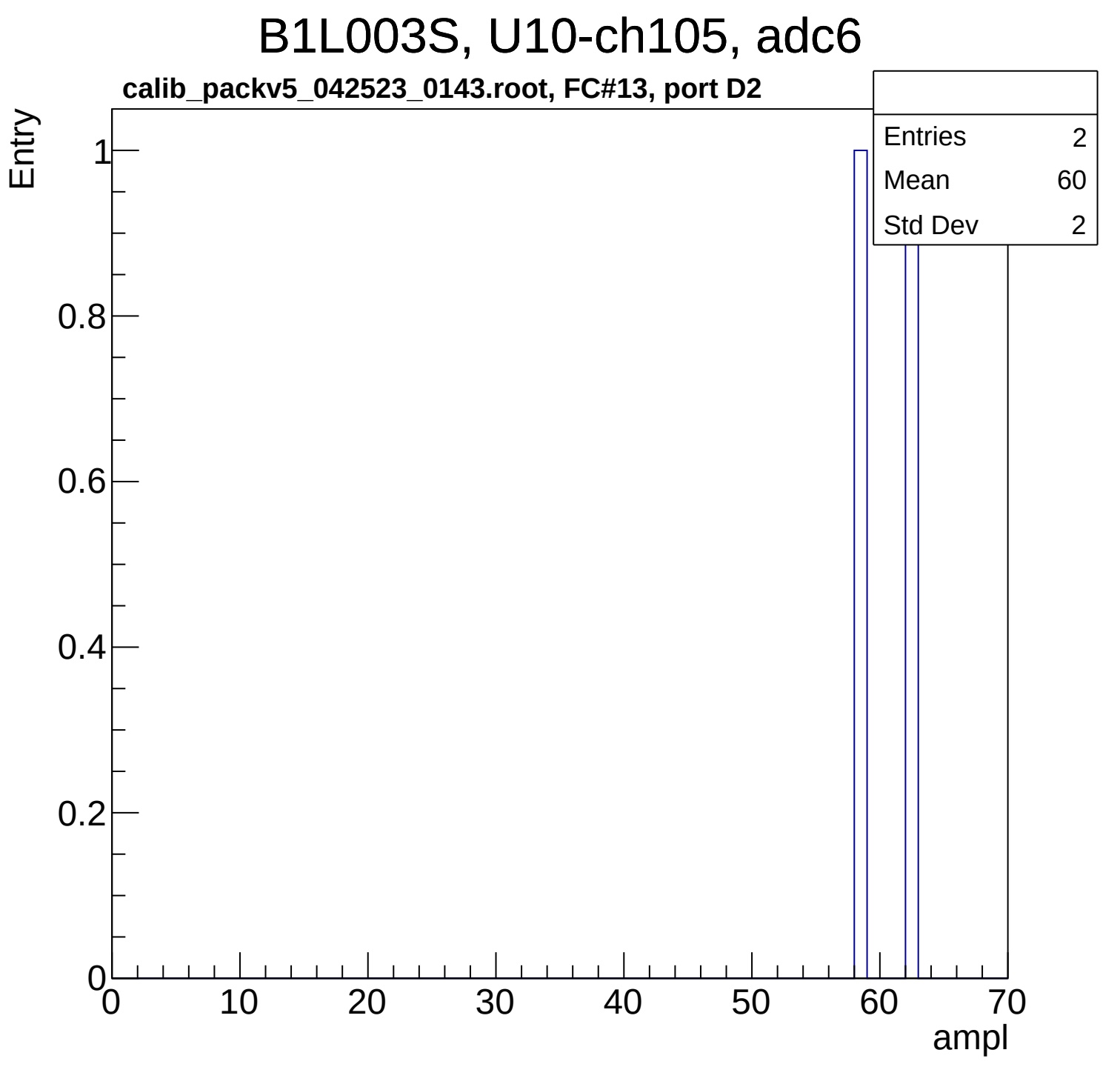
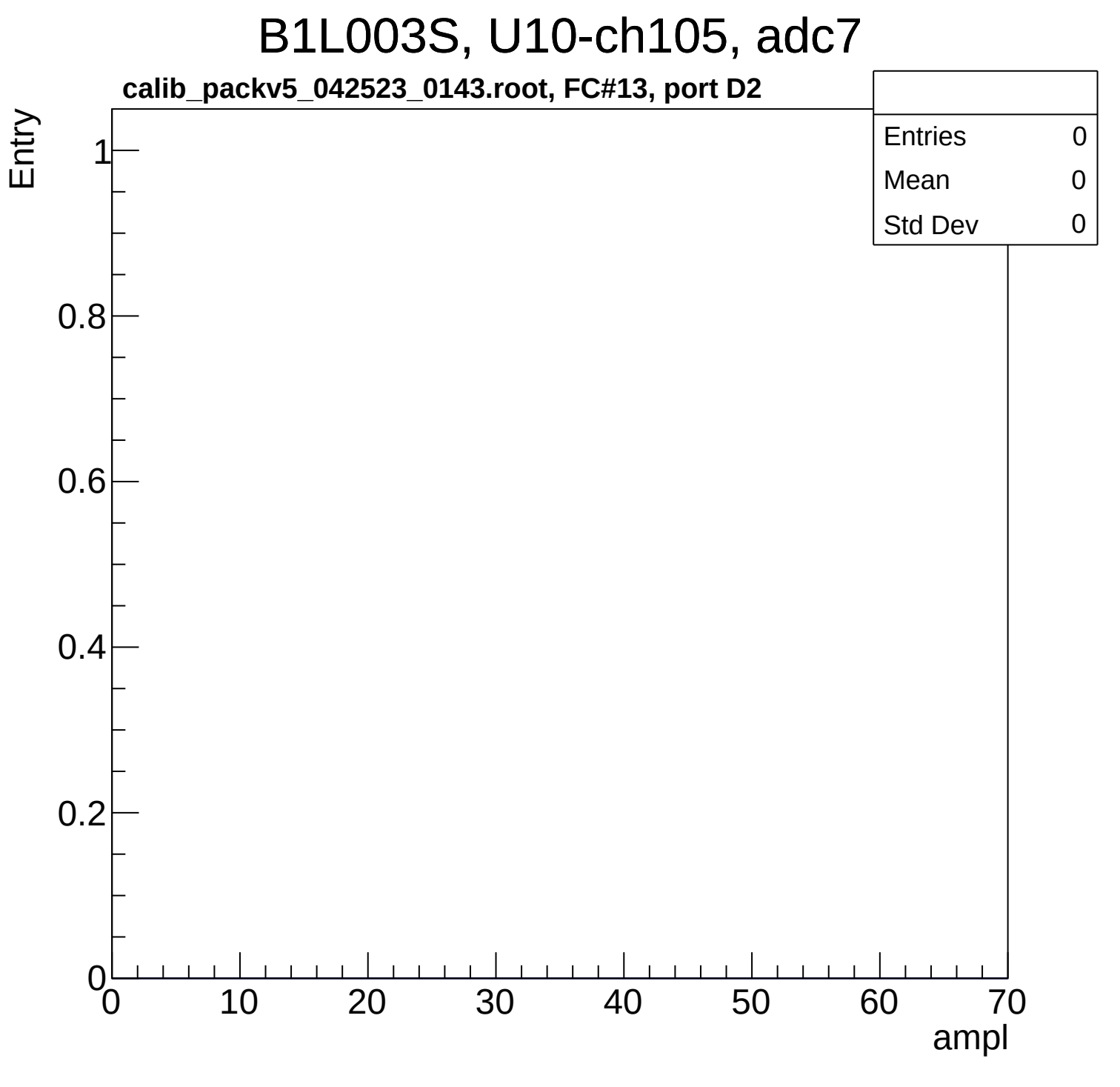
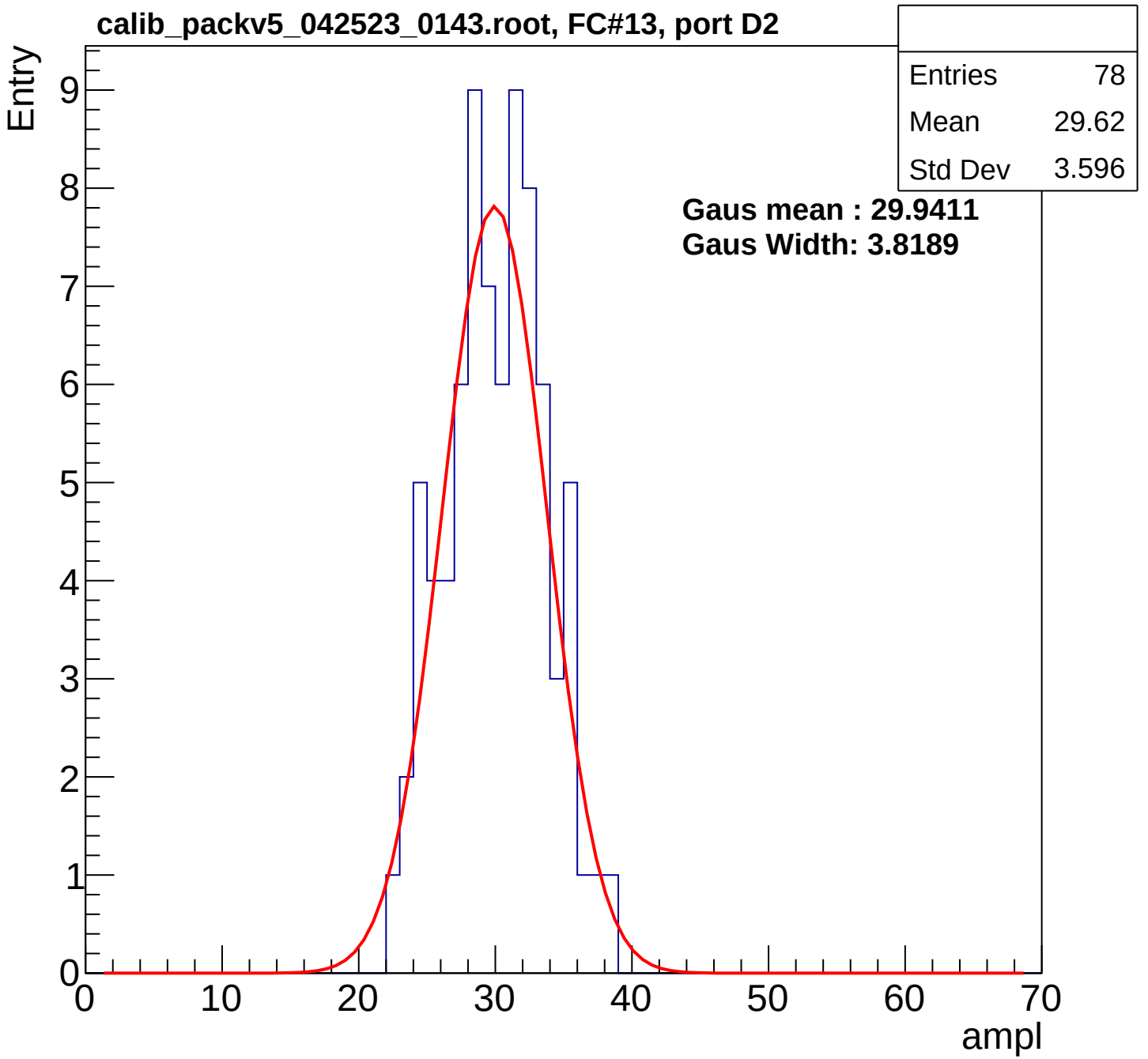

B1L003S, U10-ch105, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.06
Std Dev 8.376
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch105, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 60
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch105, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch106, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 29.62
Std Dev 3.596
Gaus mean : 29.9411
Gaus Width: 3.8189
Entry
ampl
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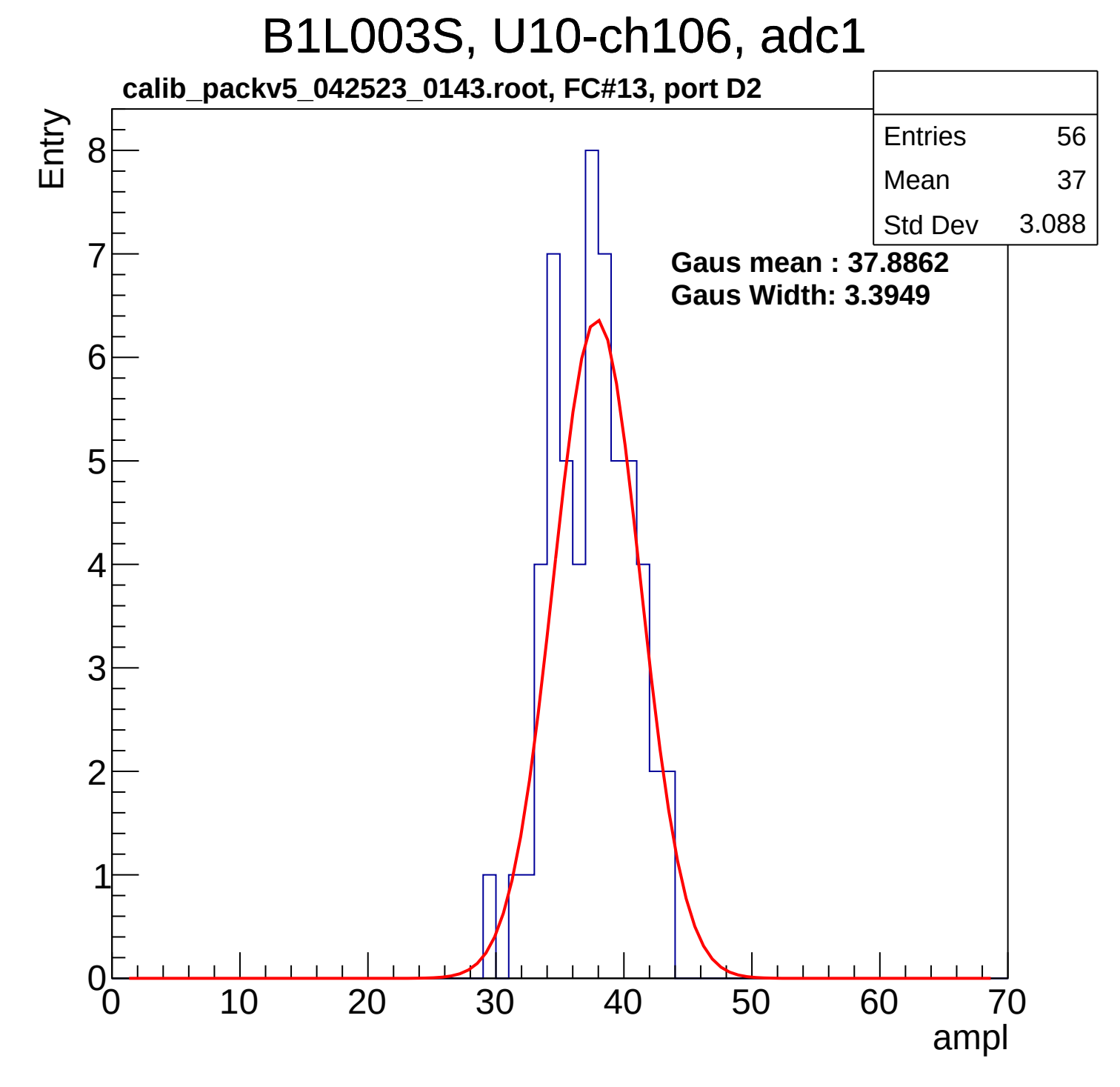
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch106, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 37
Std Dev 3.088
Gaus mean : 37.8862
Gaus Width: 3.3949
Entry
ampl
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1
2
3
4
5
6
7
8
0
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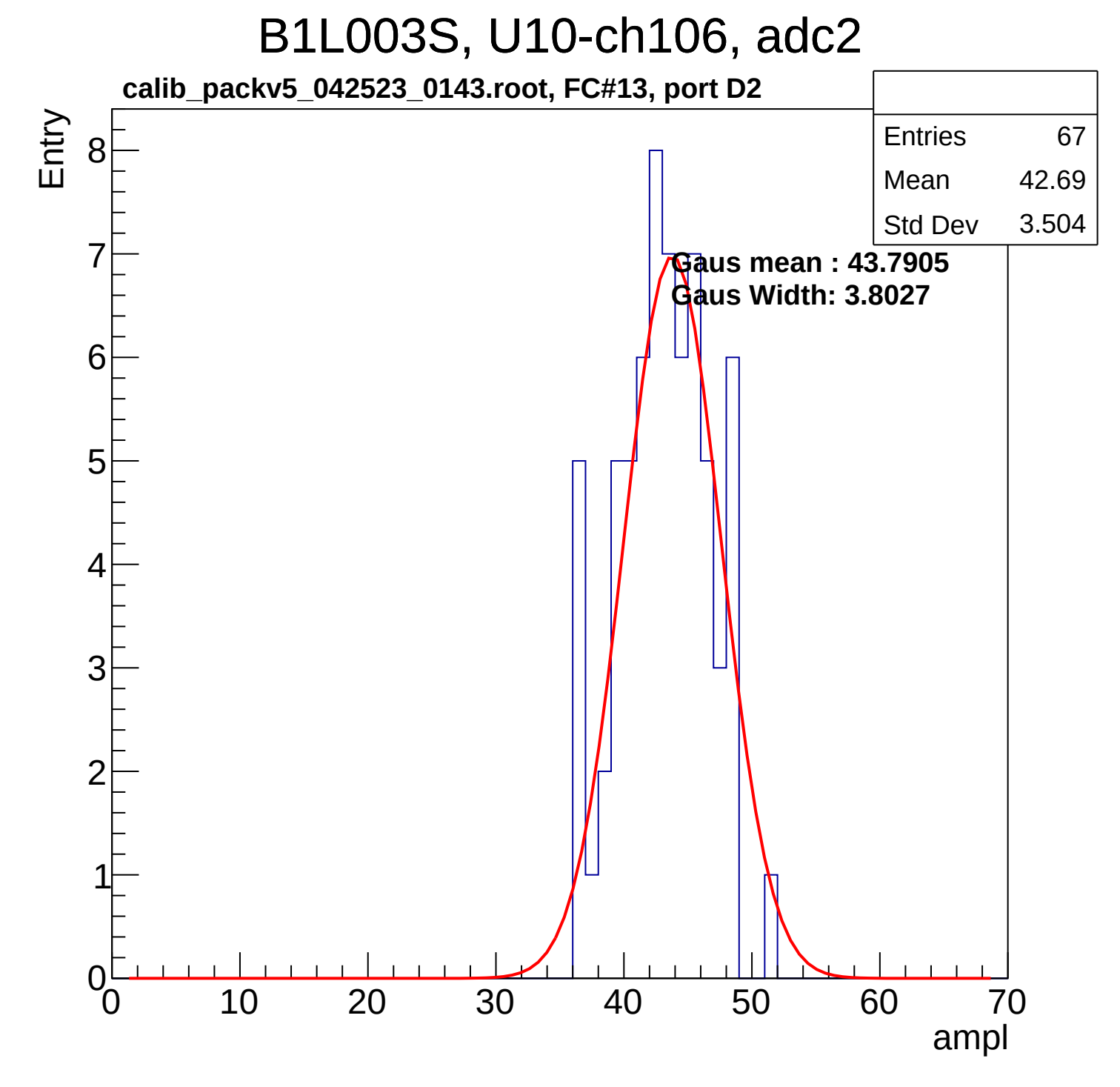
40
50
60
70


B1L003S, U10-ch106, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.69
Std Dev 3.504
Gaus mean : 43.7905
Gaus Width: 3.8027
Entry
ampl
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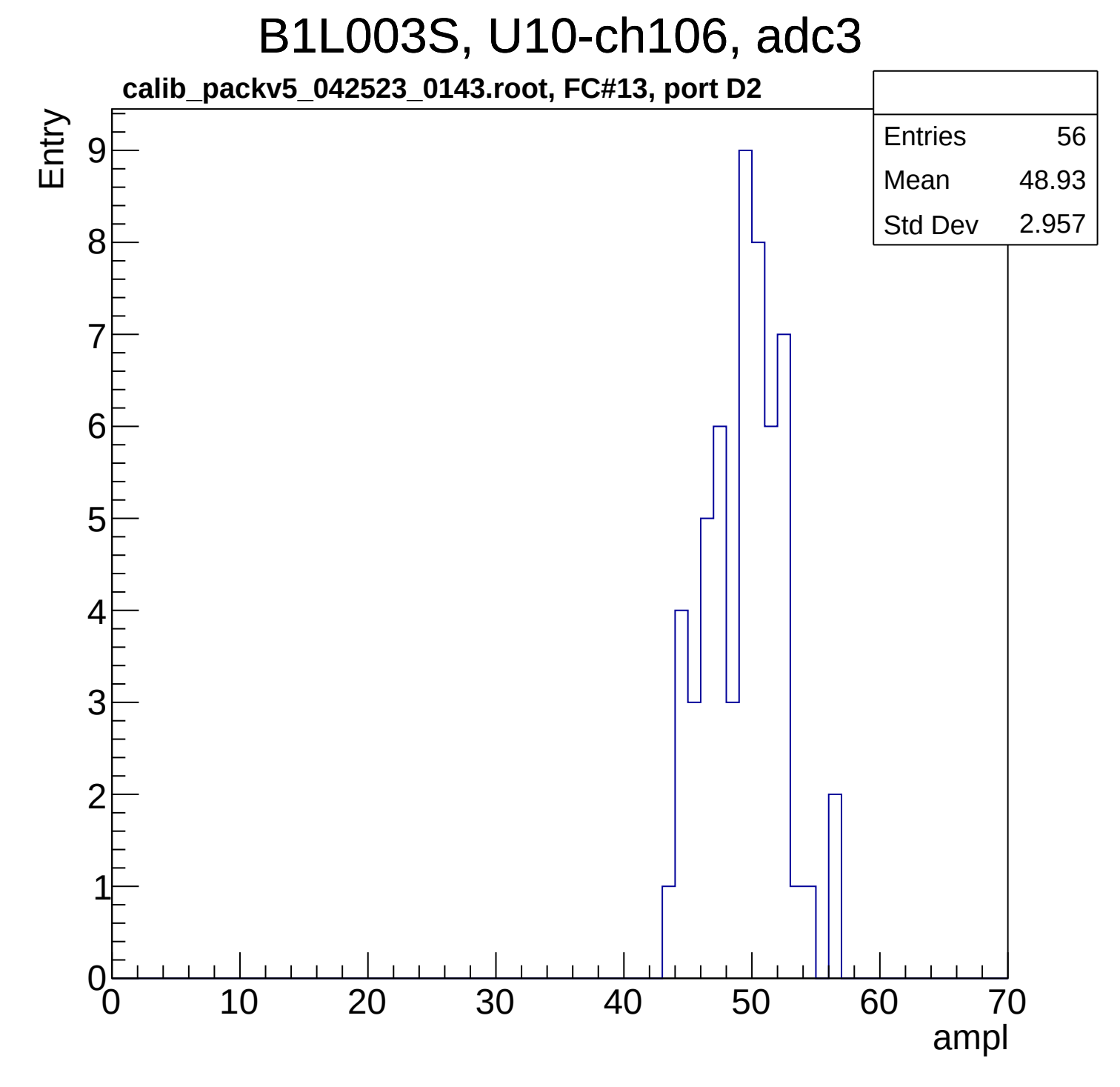
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch106, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.93
Std Dev 2.957
Entry
ampl
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0
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40
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60
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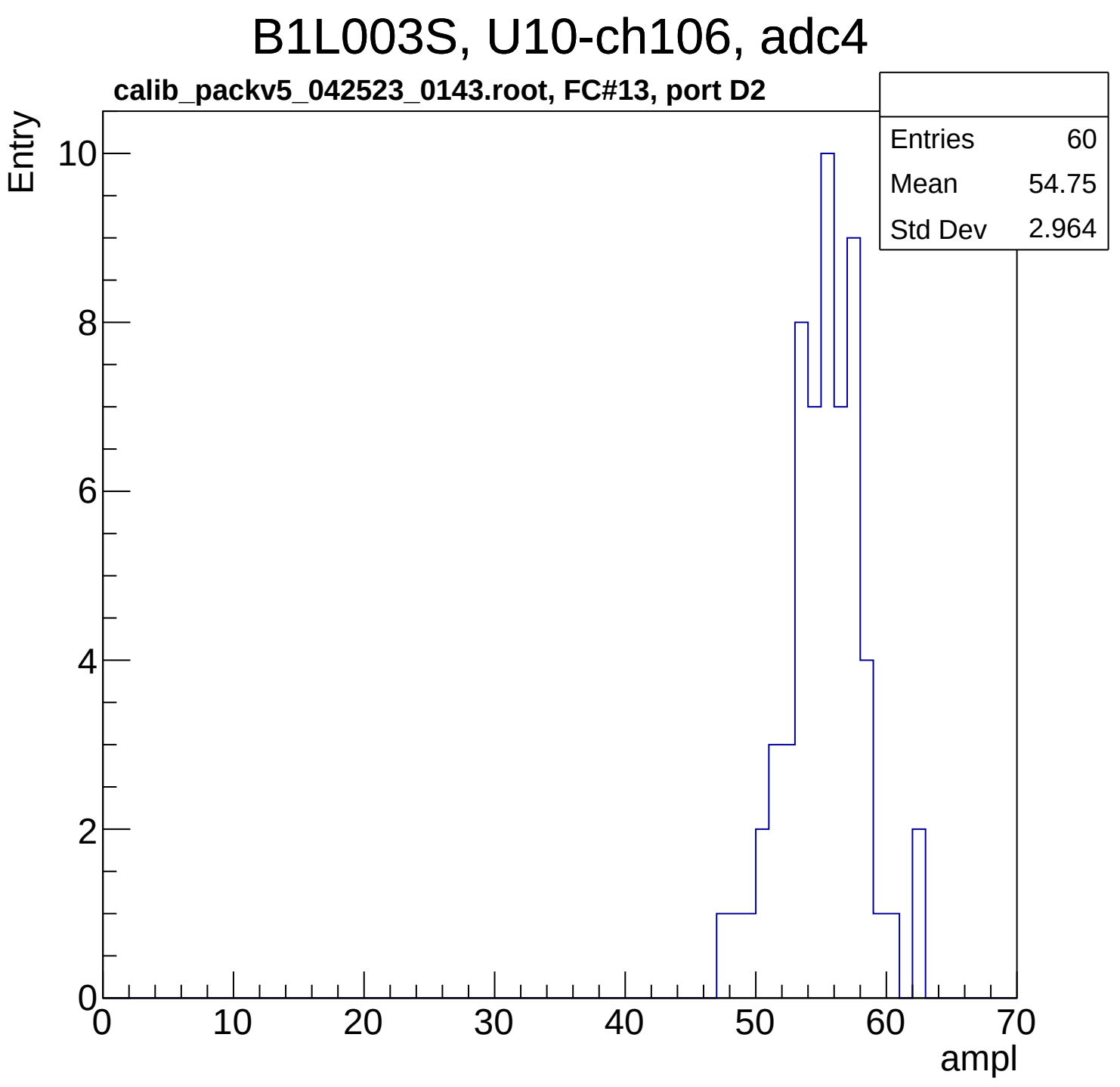
B1L003S, U10-ch106, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.75
Std Dev 2.964
Entry
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6
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10
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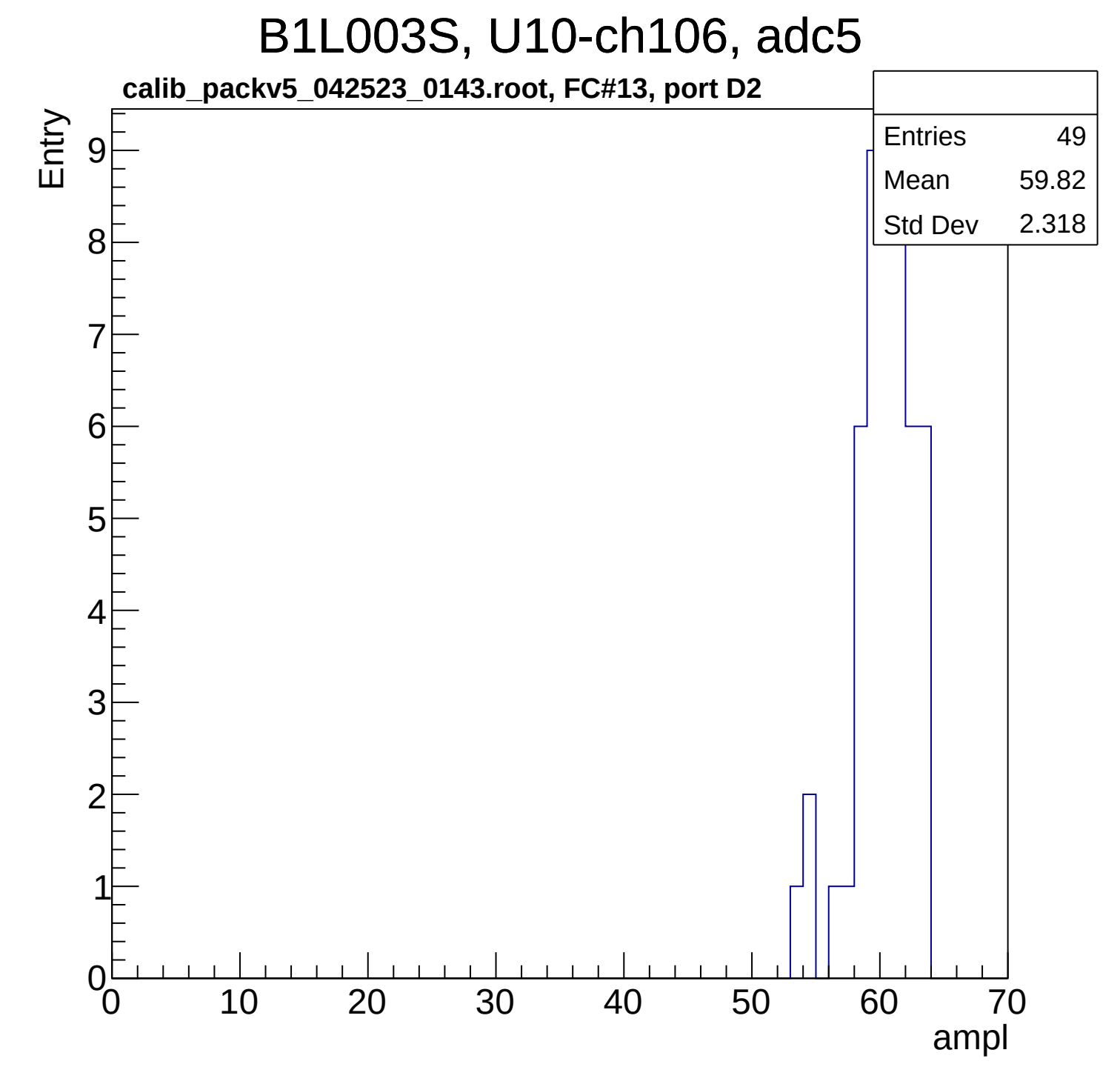
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch106, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 59.82
Std Dev 2.318
Entry
ampl
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5
6
7
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0
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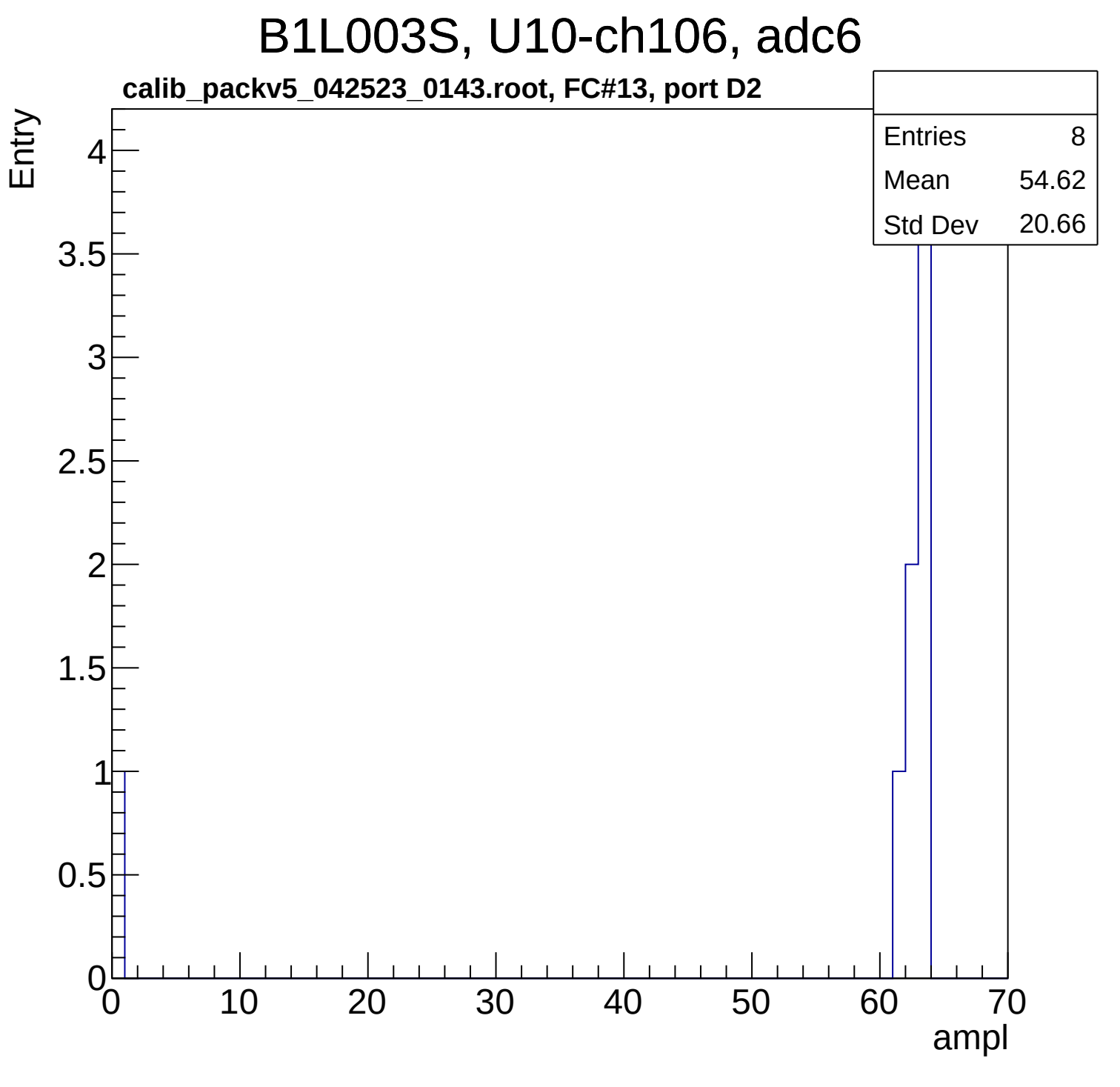
B1L003S, U10-ch106, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.62
Std Dev 20.66
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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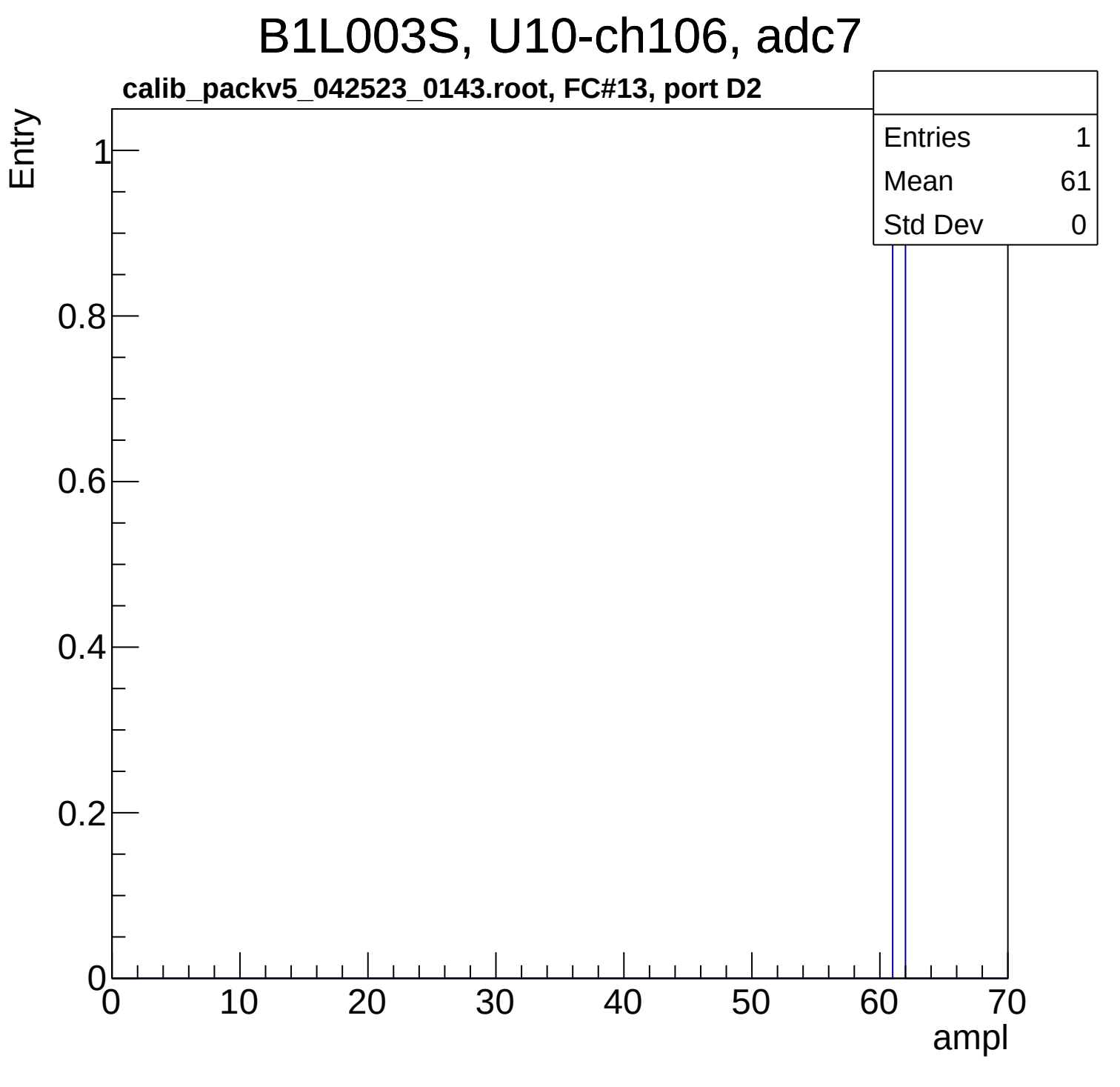
B1L003S, U10-ch106, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
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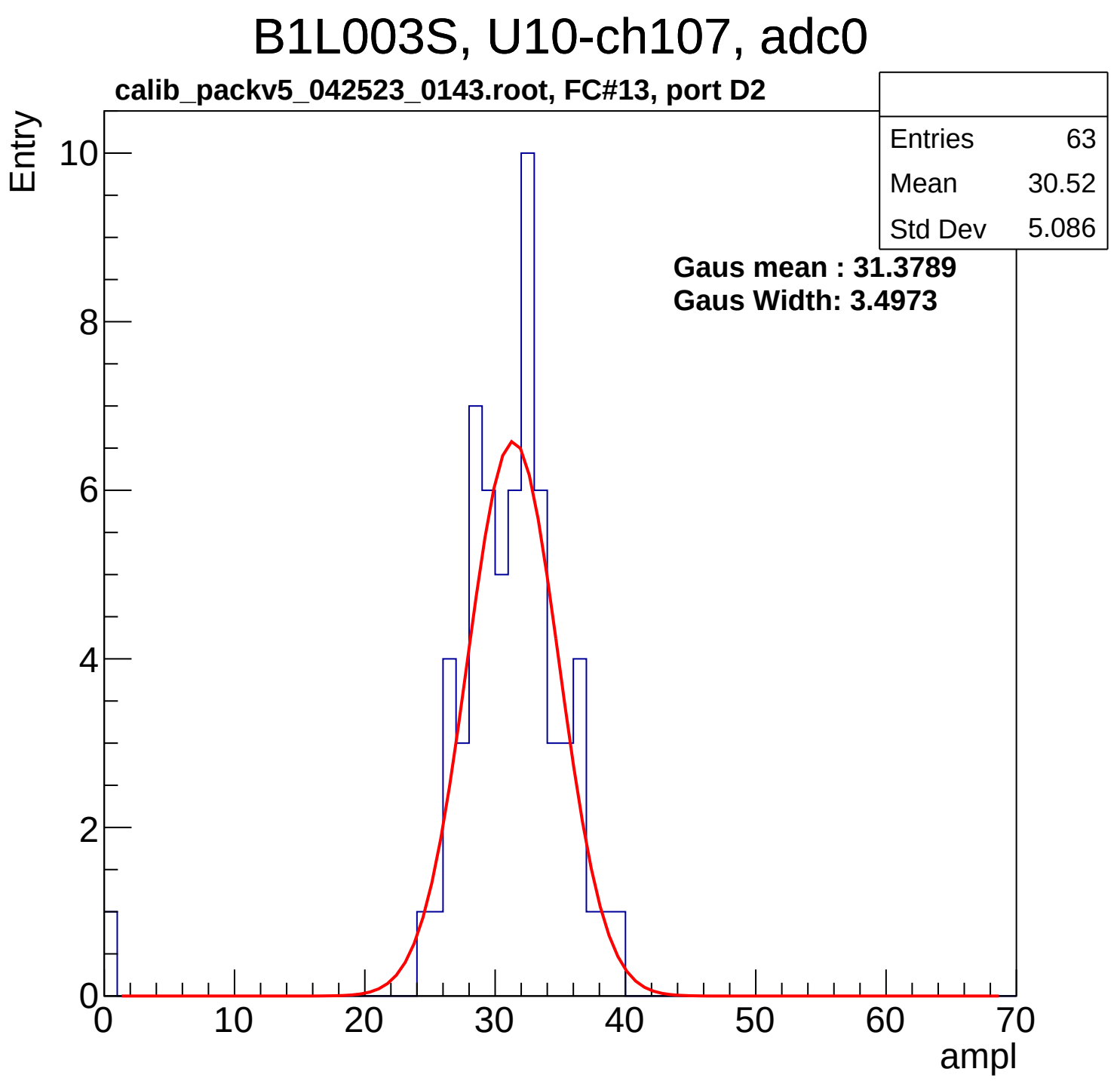
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch107, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 30.52
Std Dev 5.086
Gaus mean : 31.3789
Gaus Width: 3.4973
Entry
ampl
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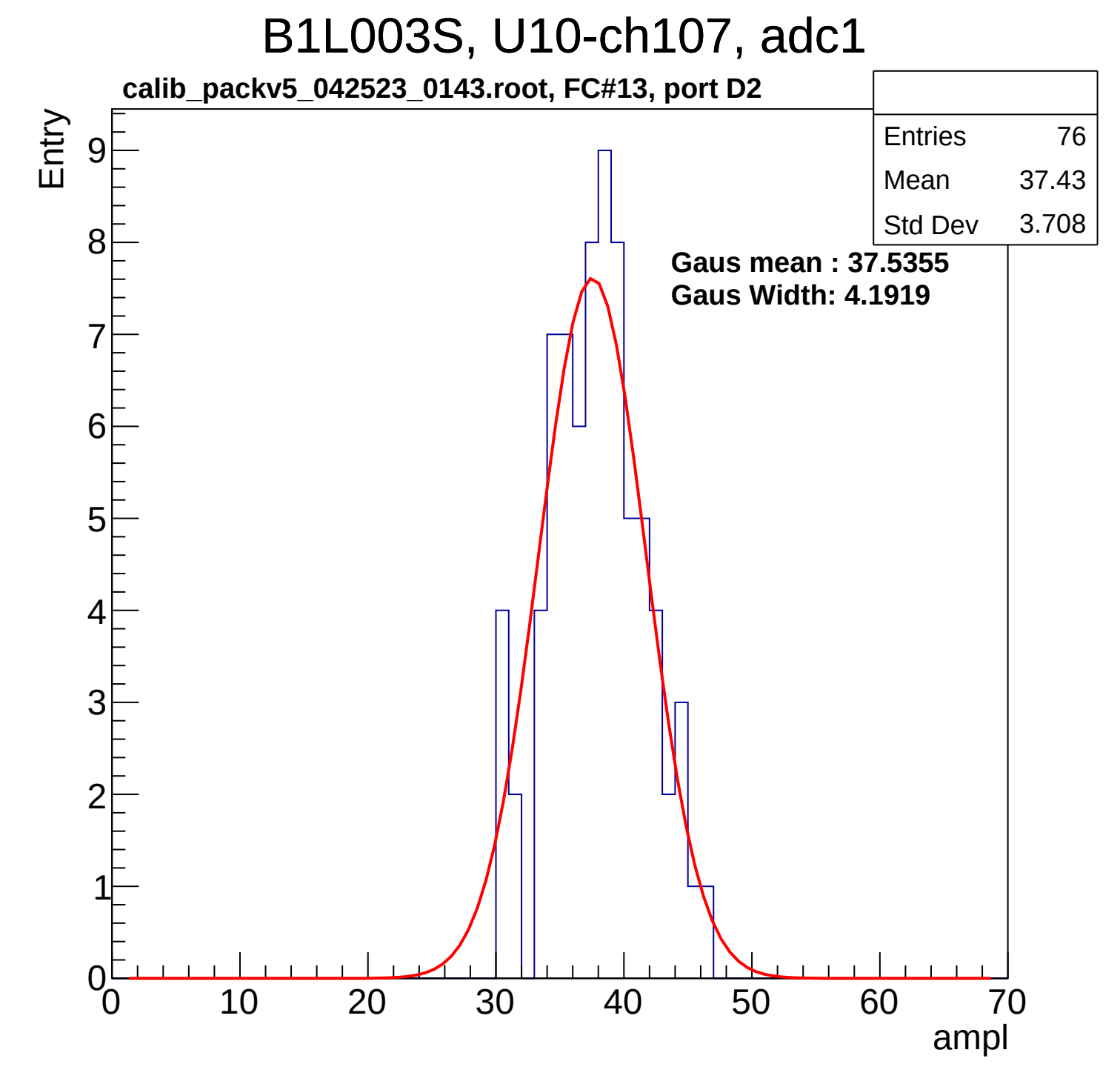
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch107, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 37.43
Std Dev 3.708
Gaus mean : 37.5355
Gaus Width: 4.1919
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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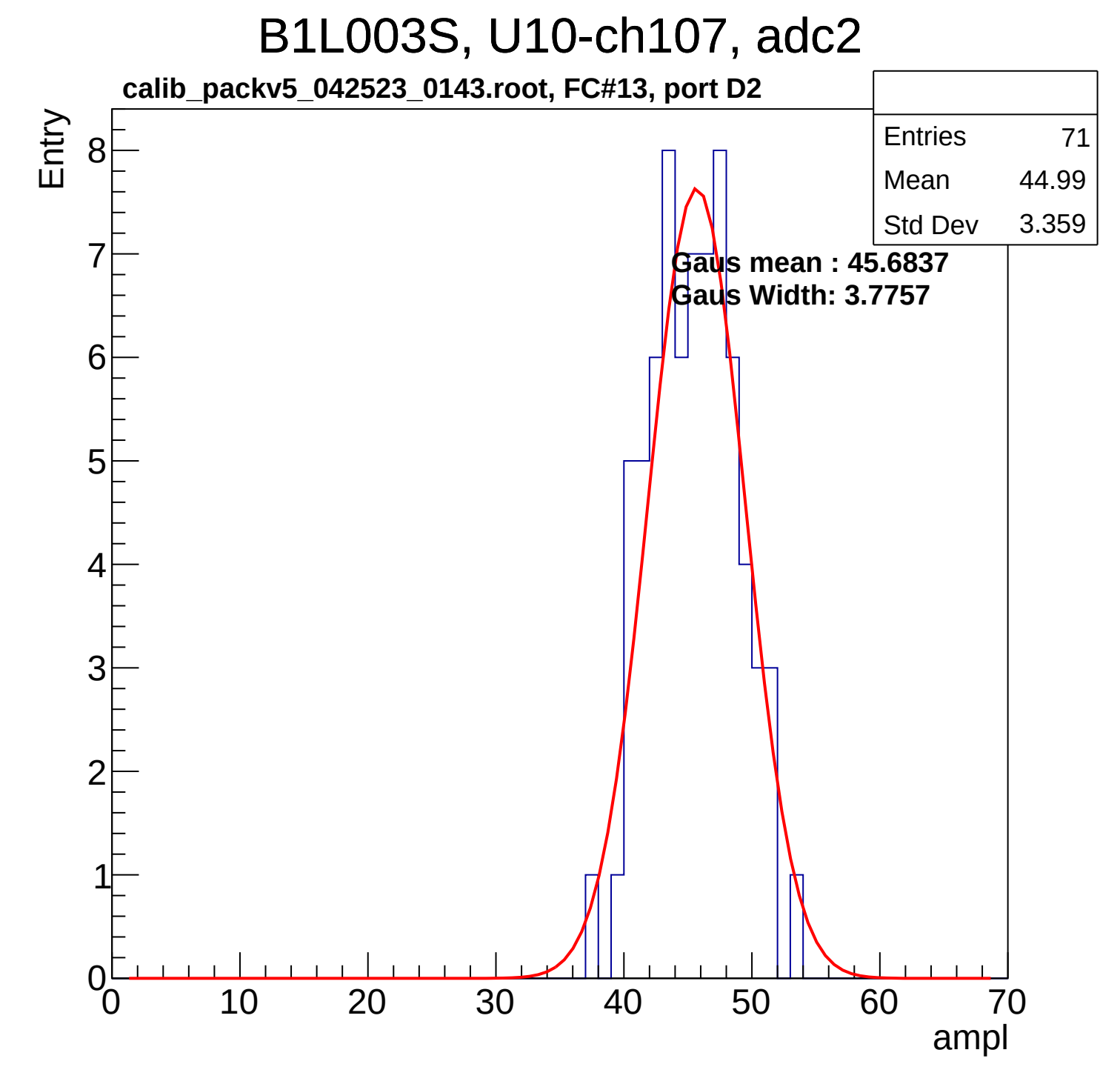
B1L003S, U10-ch107, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 44.99
Std Dev 3.359
Gaus mean : 45.6837
Gaus Width: 3.7757
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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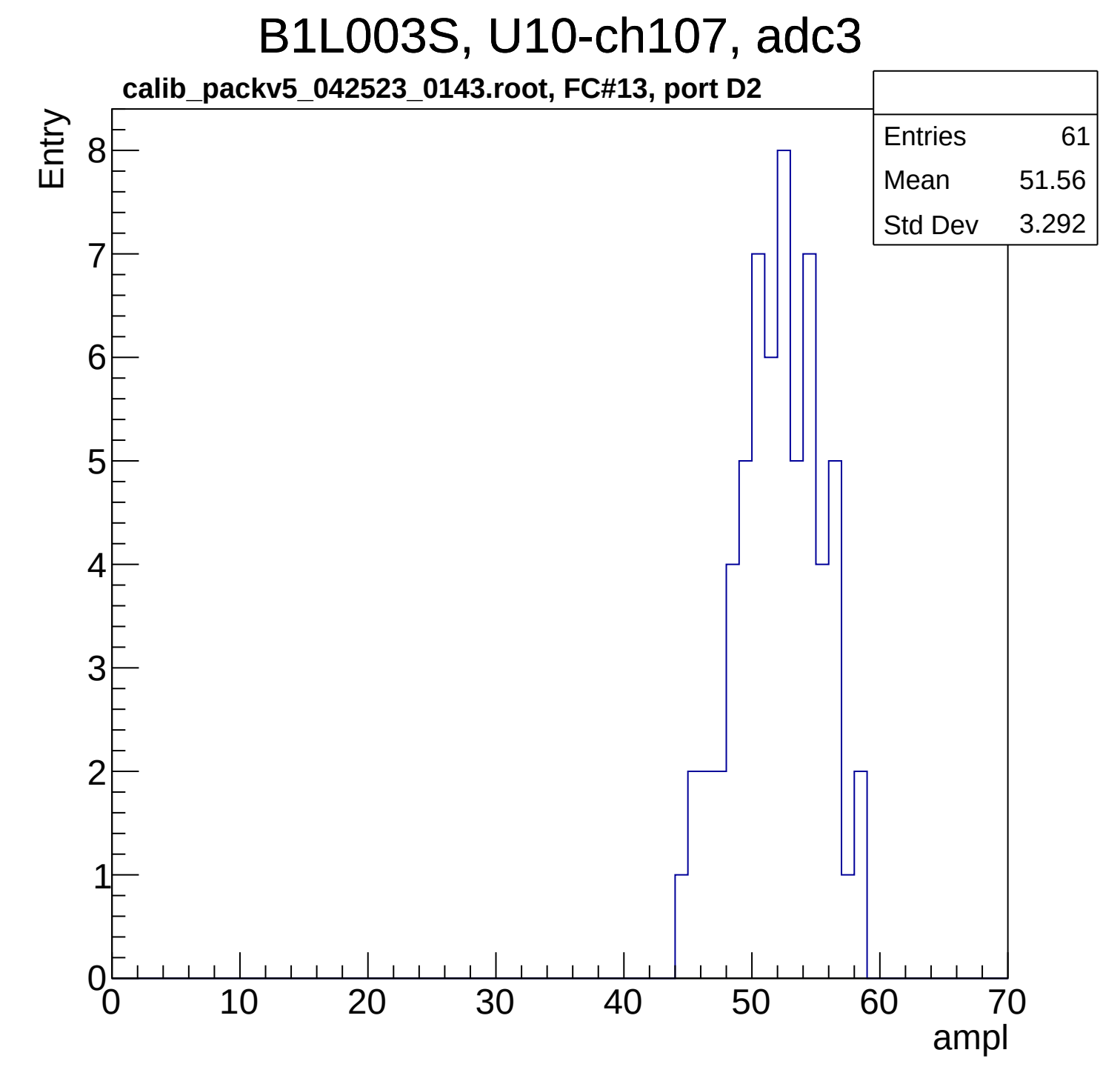
B1L003S, U10-ch107, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 51.56
Std Dev 3.292
Entry
ampl
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5
6
7
8
0
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20
30
40
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60
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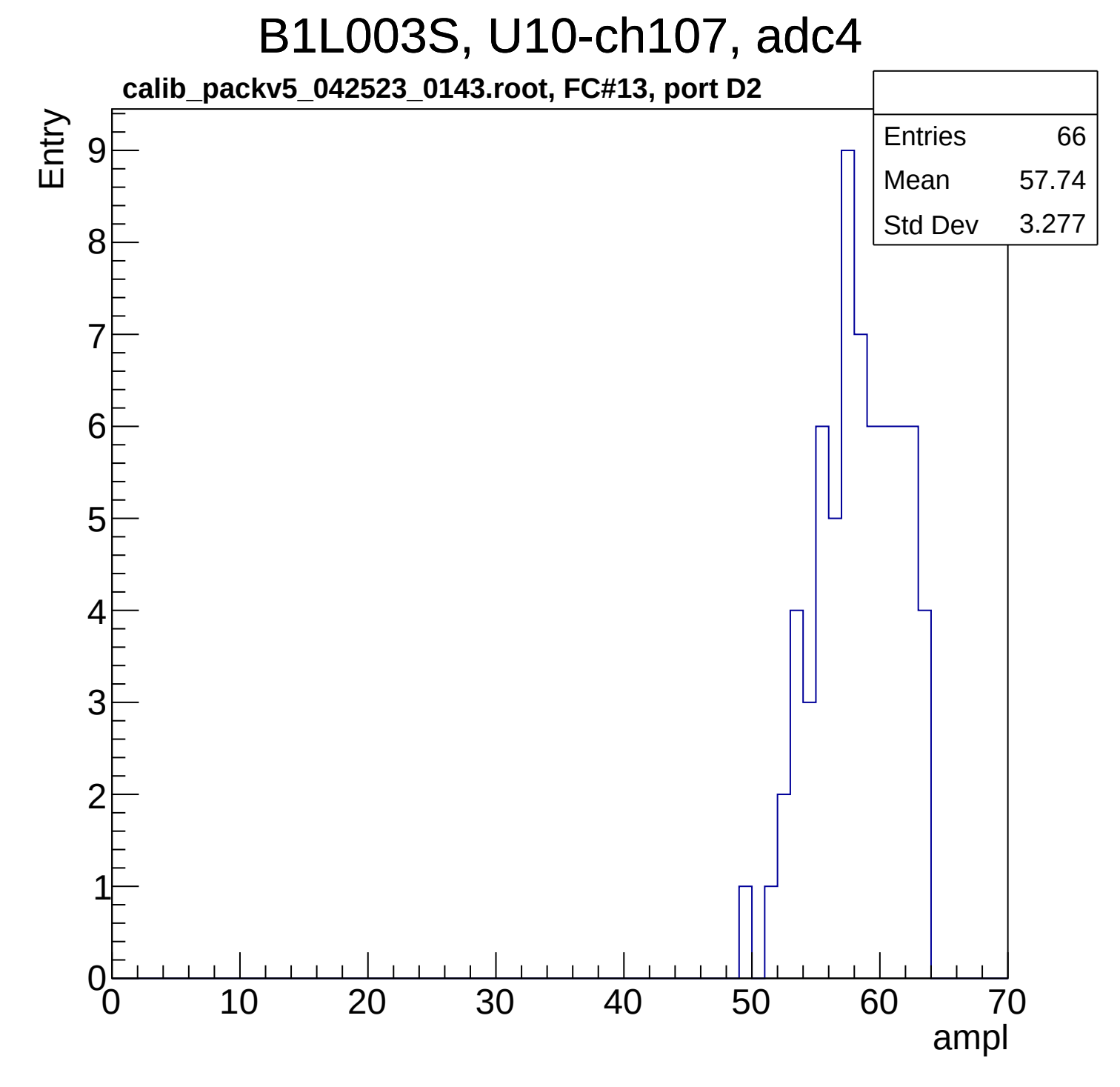
B1L003S, U10-ch107, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 57.74
Std Dev 3.277
Entry
ampl
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7
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0
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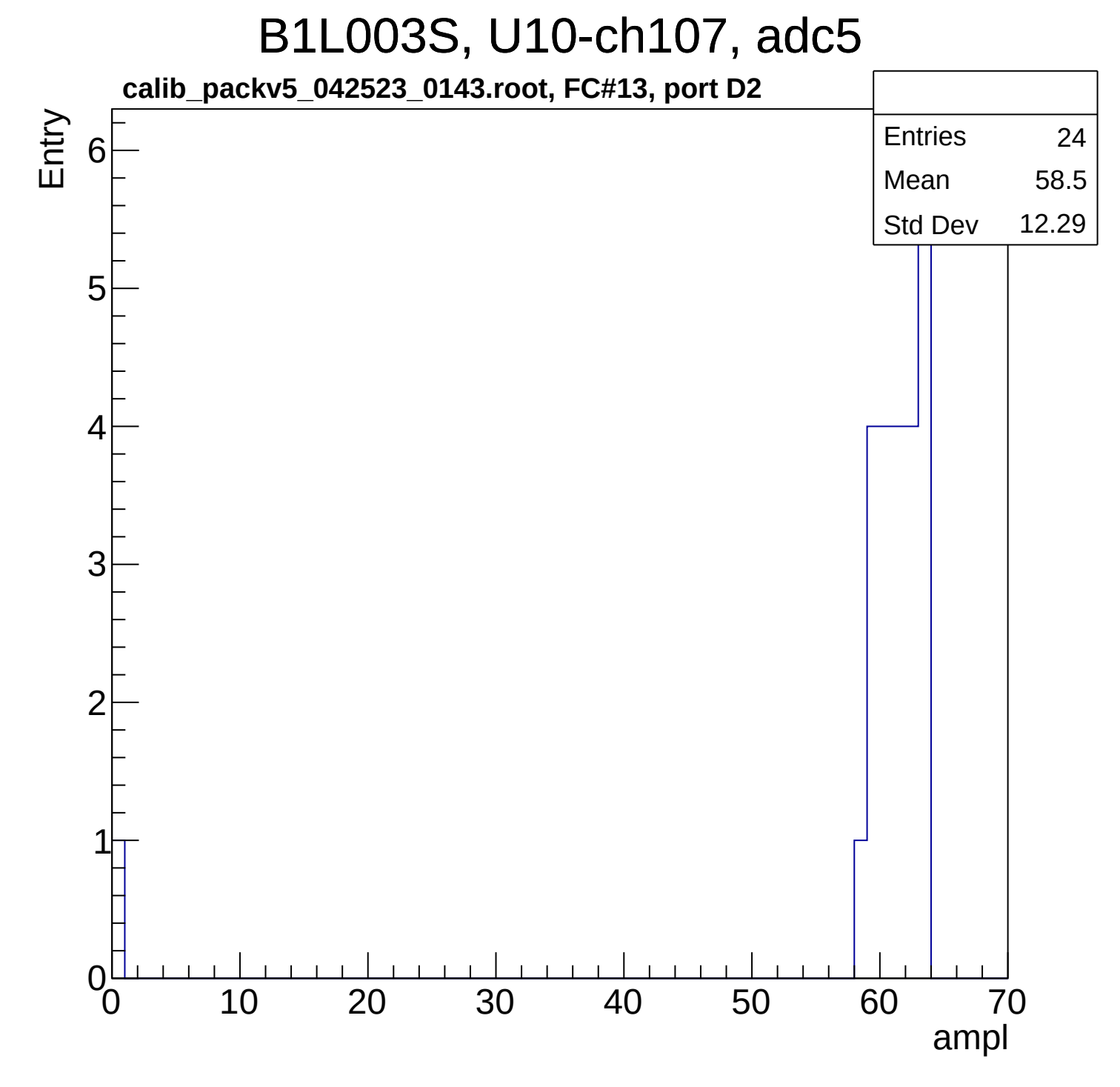
B1L003S, U10-ch107, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 58.5
Std Dev 12.29
Entry
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3
4
5
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0
10
20
30
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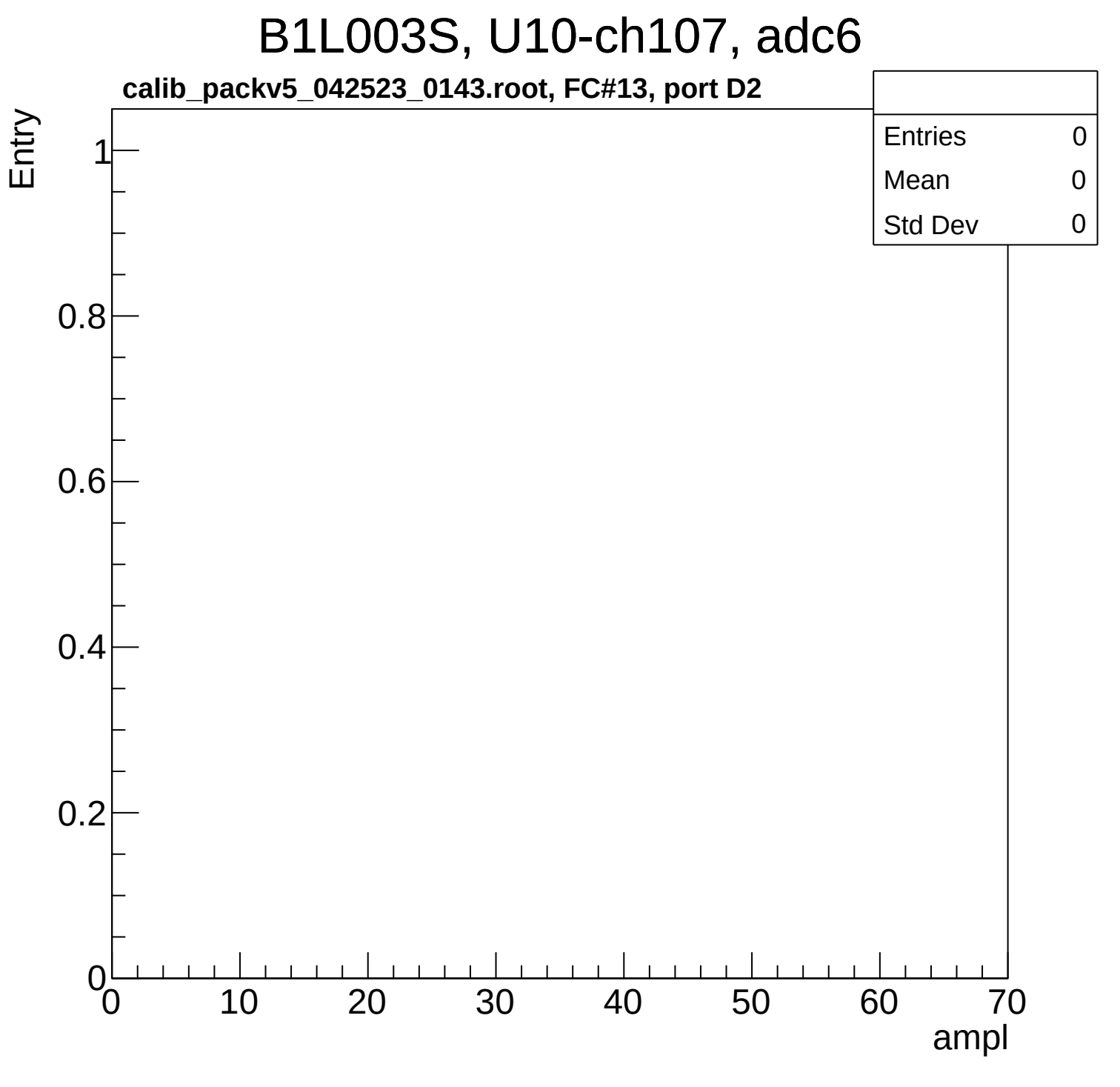
50
60
70
ampl


B1L003S, U10-ch107, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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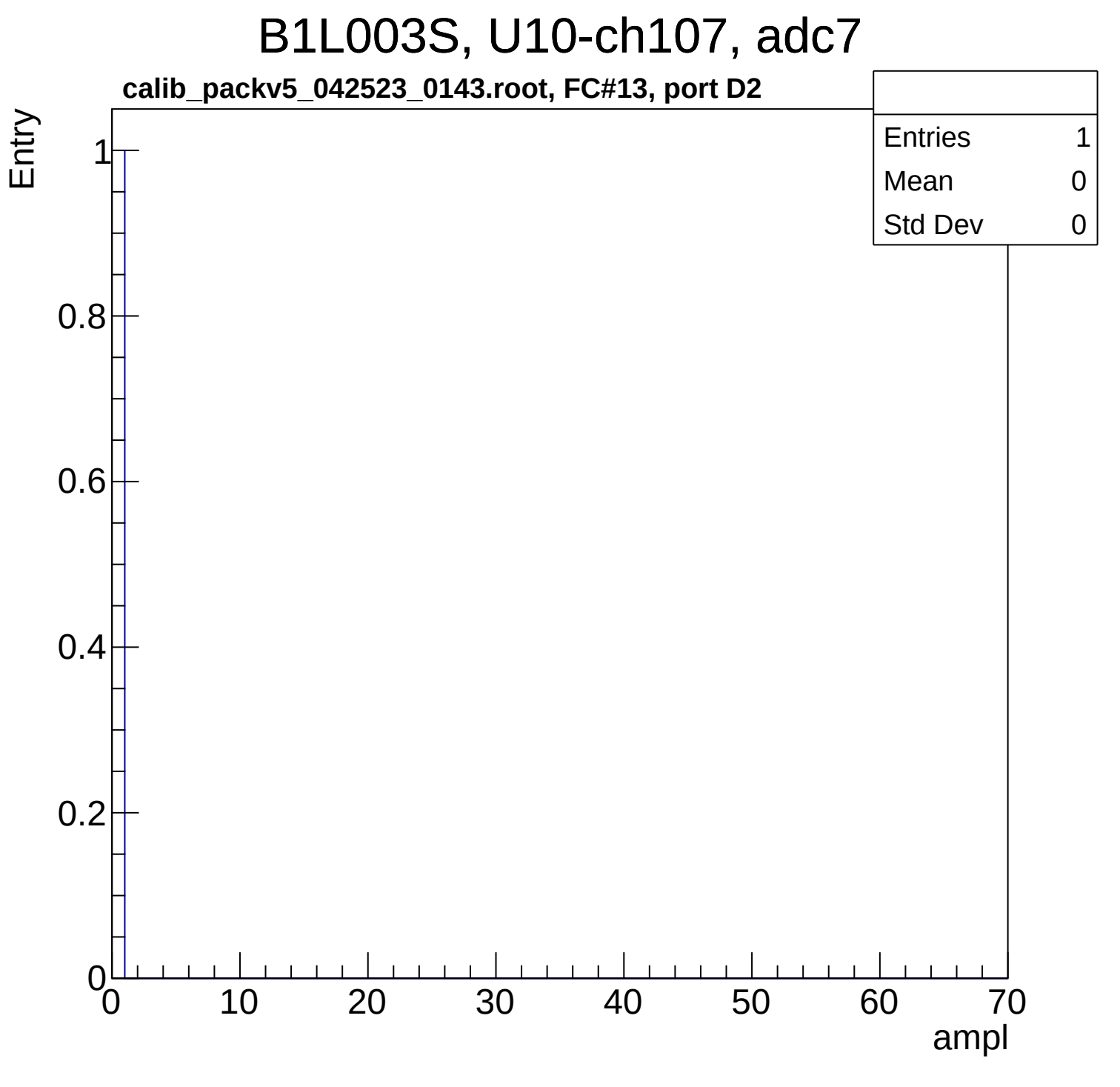
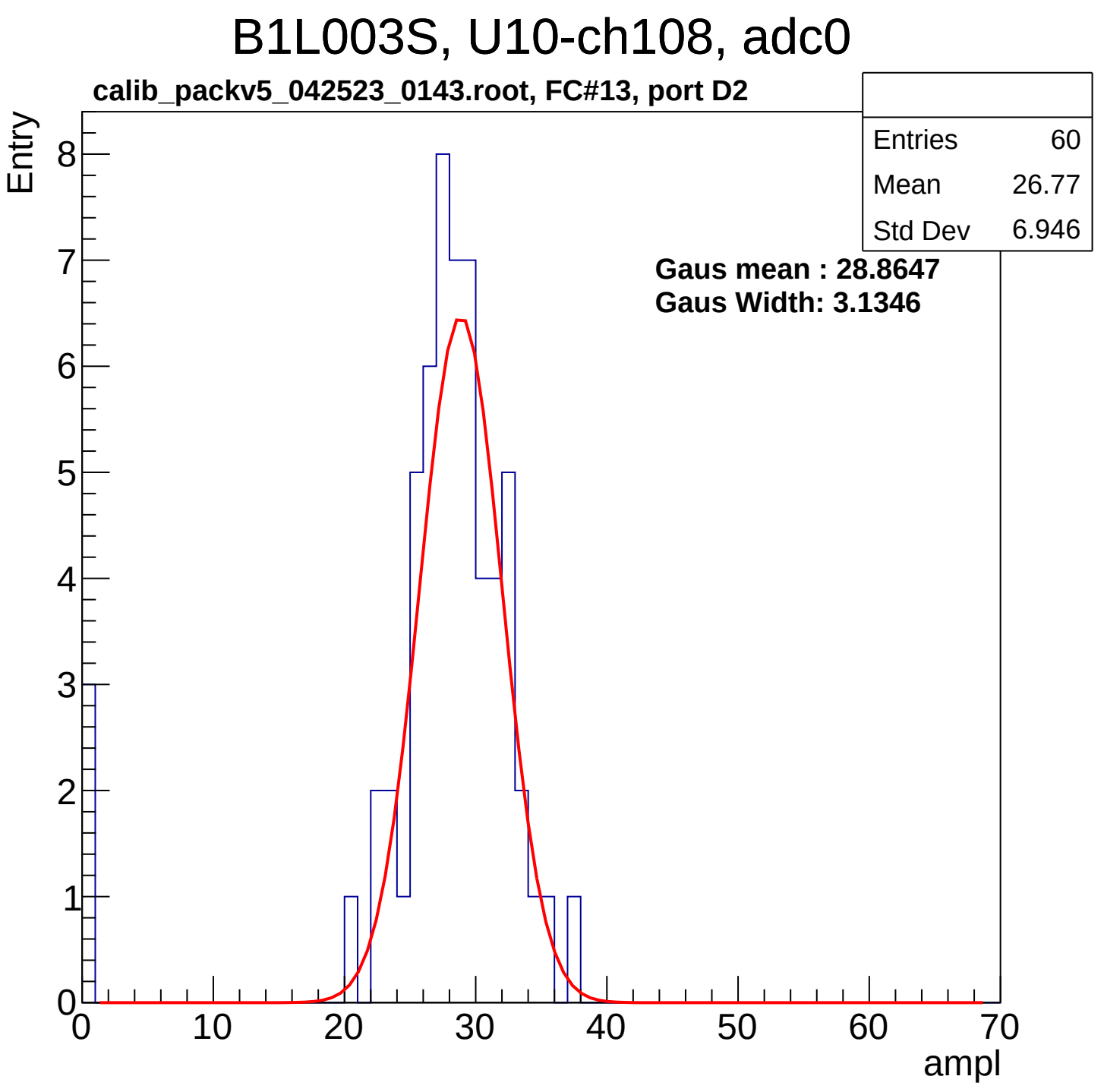
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch107, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch108, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 26.77
Std Dev 6.946
Gaus mean : 28.8647
Gaus Width: 3.1346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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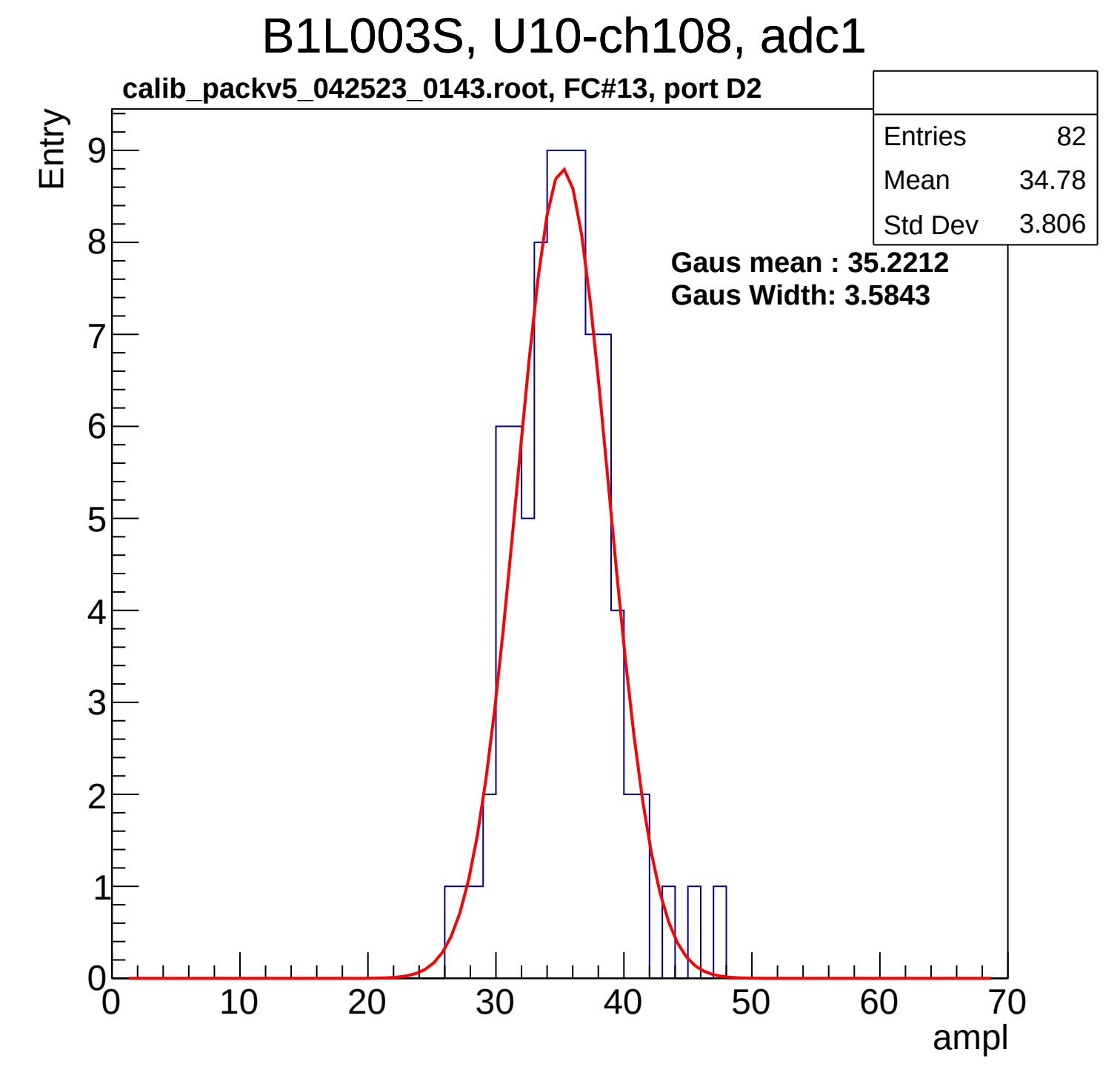
B1L003S, U10-ch108, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 34.78
Std Dev 3.806
Gaus mean : 35.2212
Gaus Width: 3.5843
Entry
ampl
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2
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4
5
6
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8
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0
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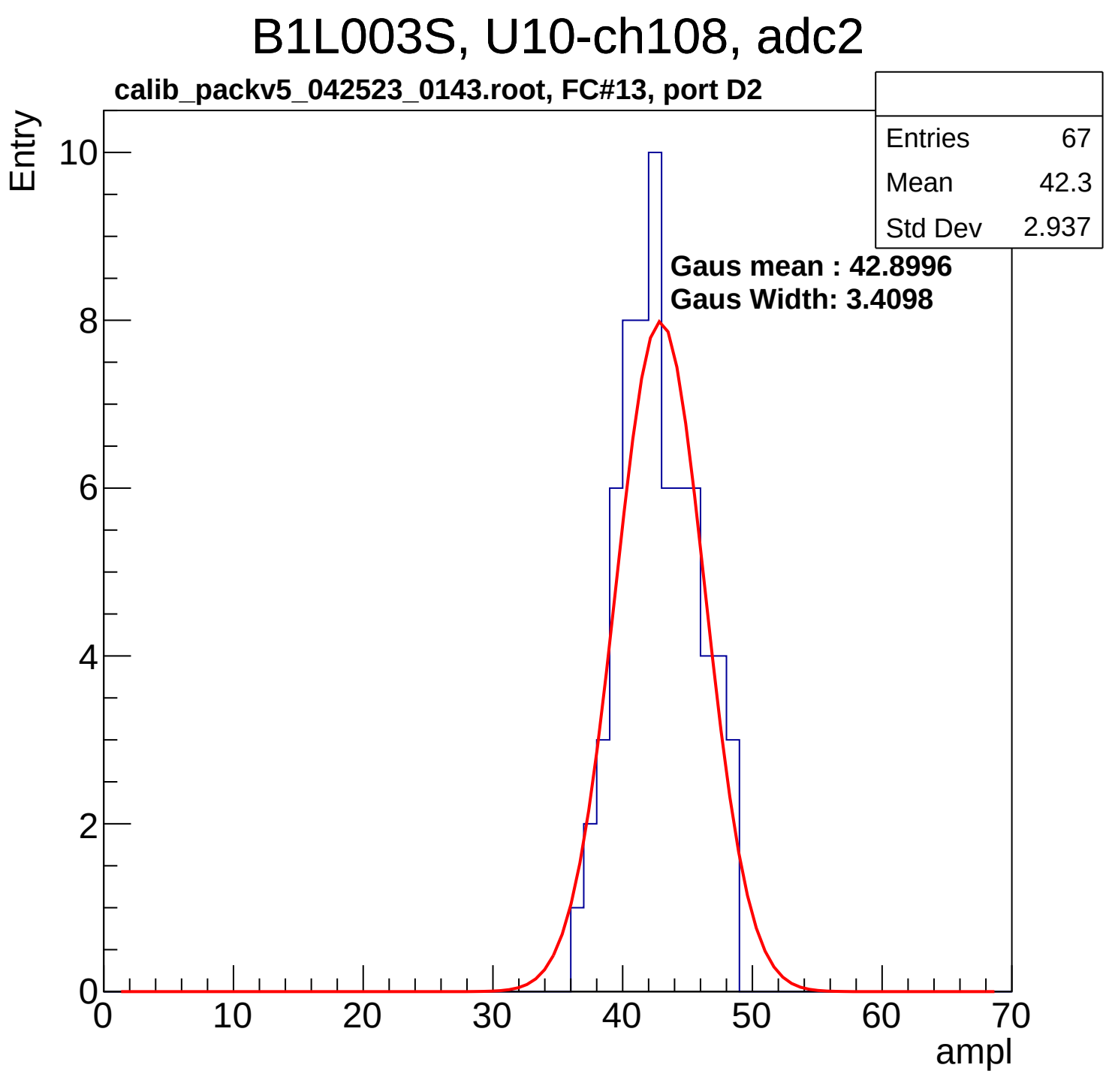
B1L003S, U10-ch108, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.3
Std Dev 2.937
Gaus mean : 42.8996
Gaus Width: 3.4098
Entry
ampl
0
2
4
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8
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0
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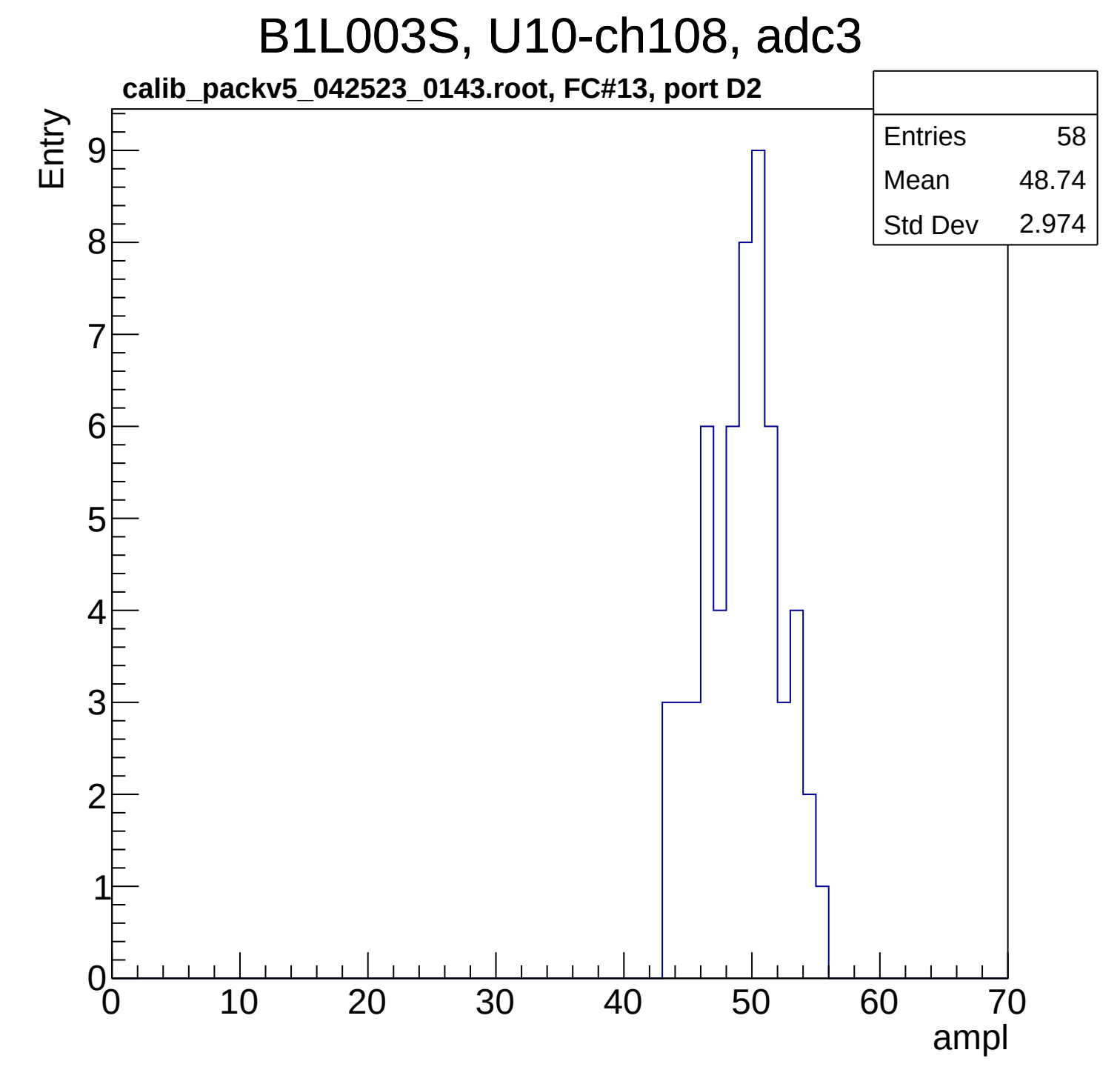
B1L003S, U10-ch108, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.74
Std Dev 2.974
Entry
ampl
0
1
2
3
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0
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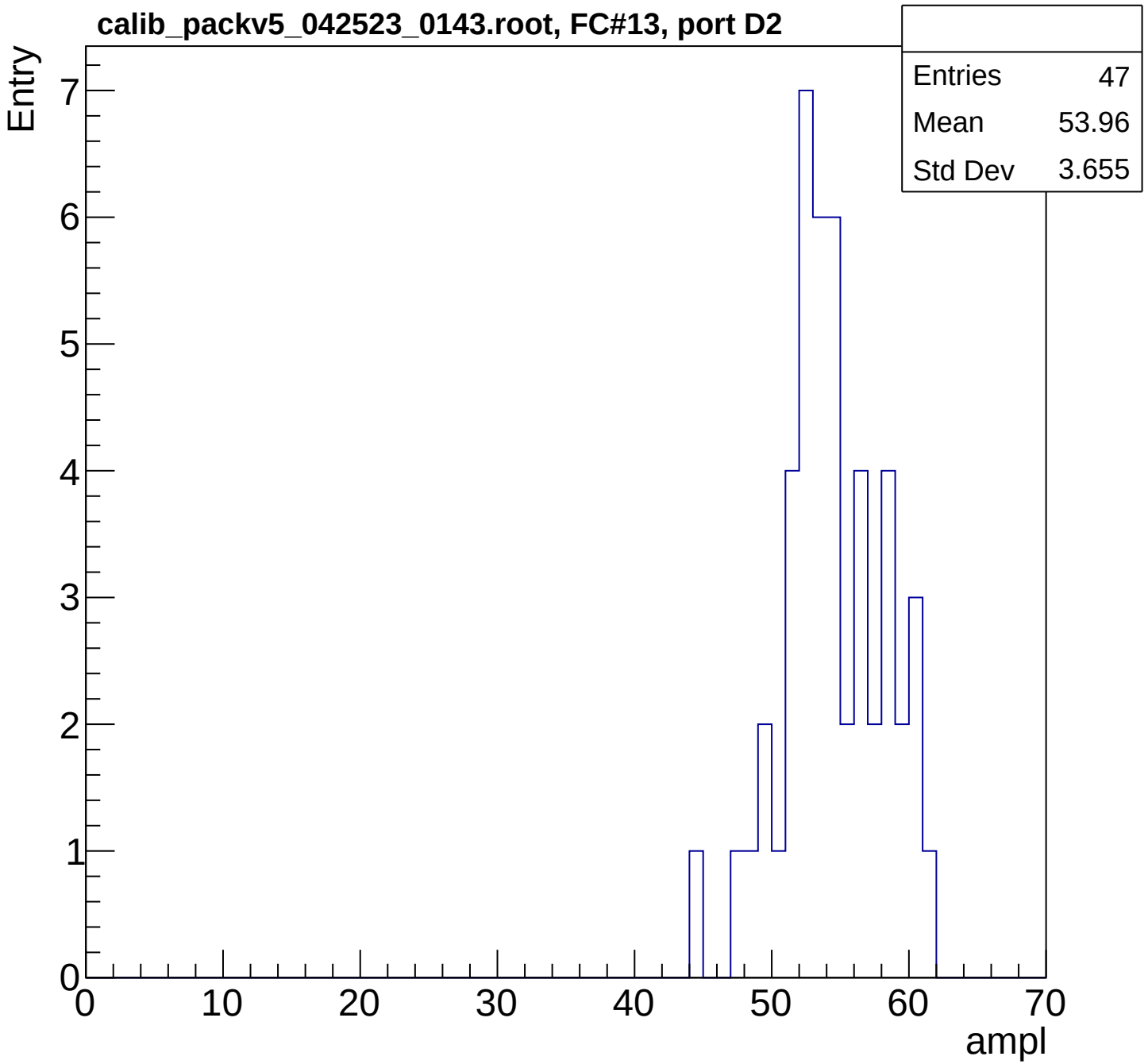
B1L003S, U10-ch108, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 53.96
Std Dev 3.655
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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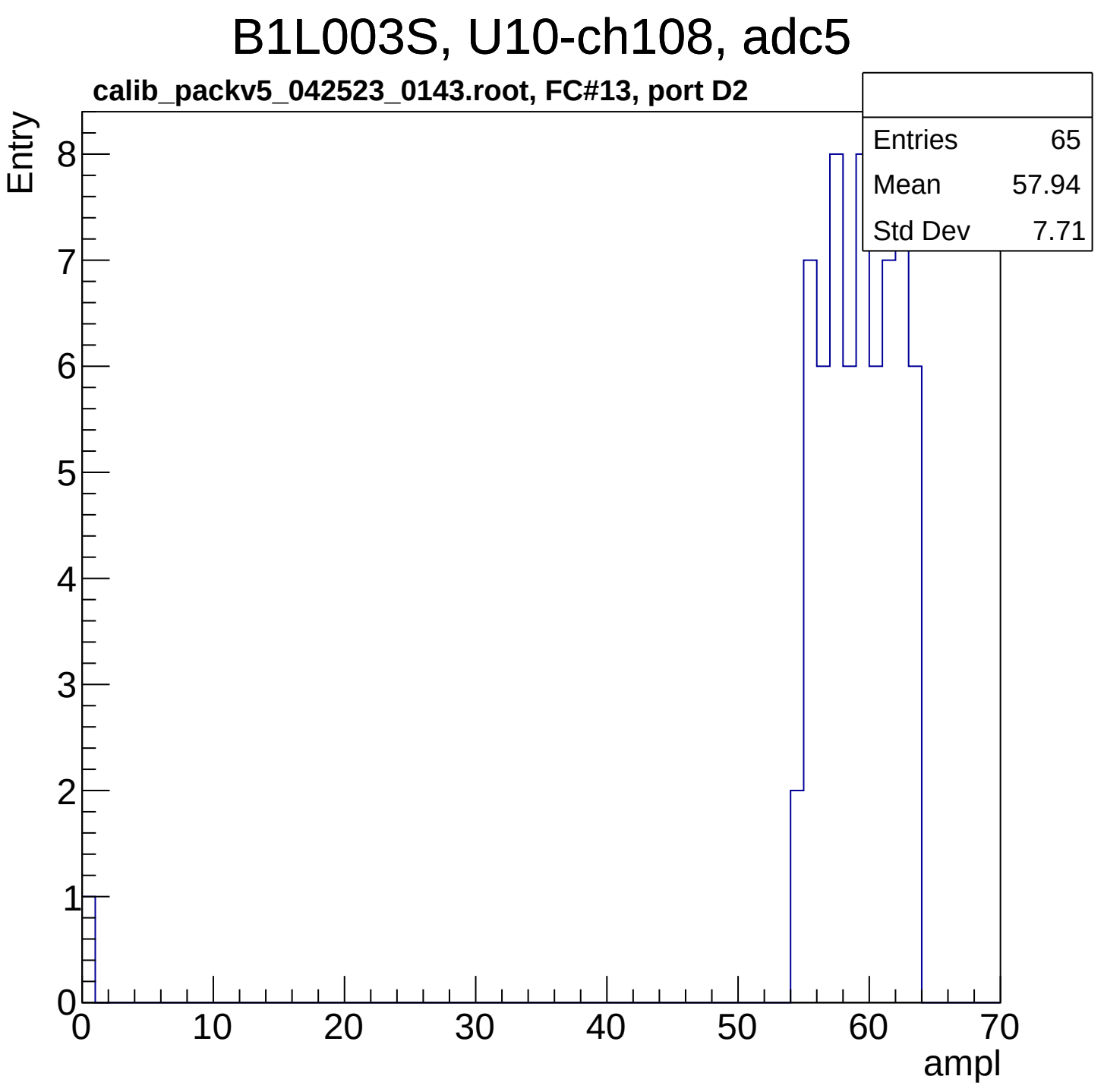
B1L003S, U10-ch108, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 57.94
Std Dev 7.71
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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40
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60
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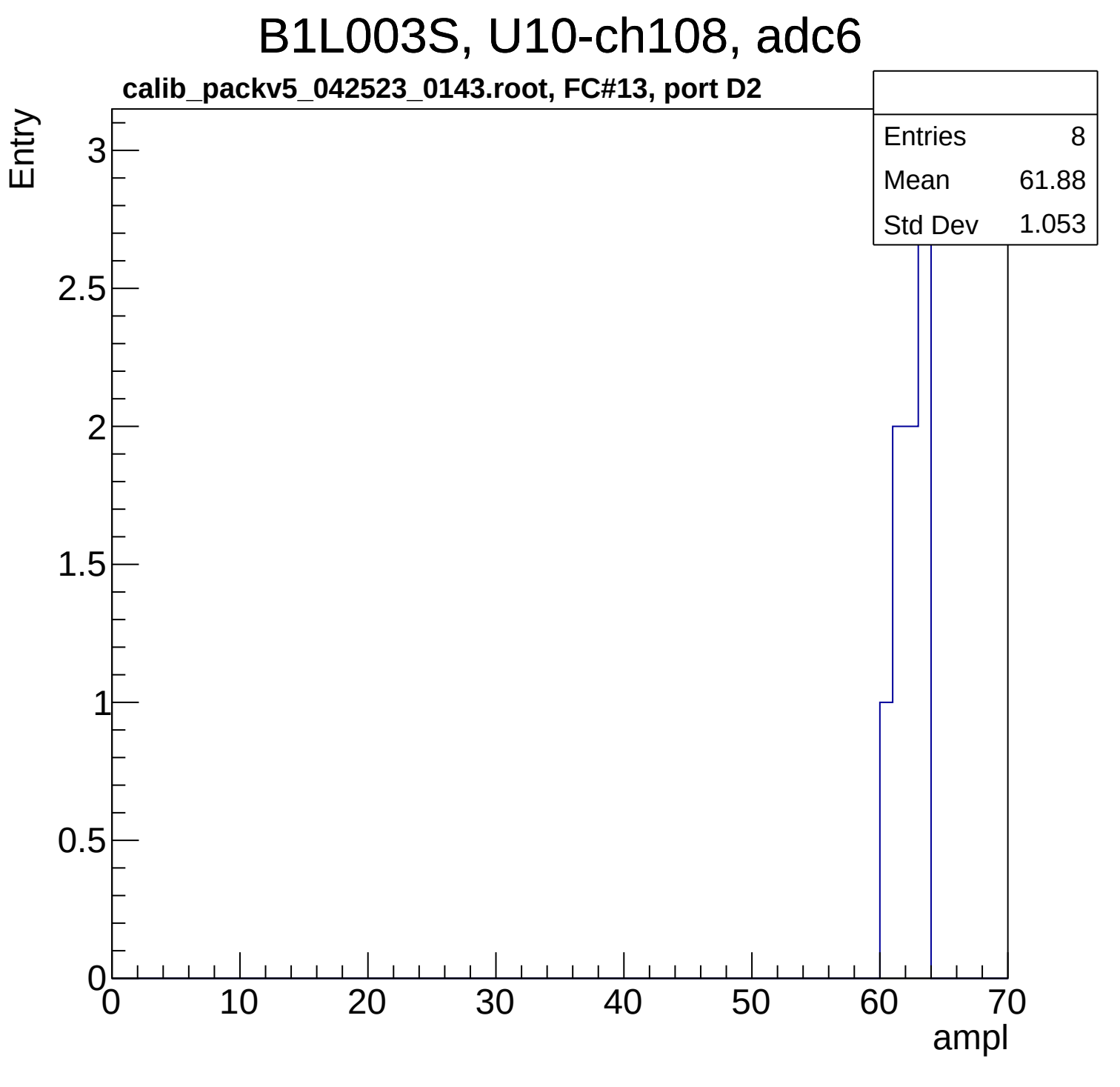
B1L003S, U10-ch108, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.88
Std Dev 1.053
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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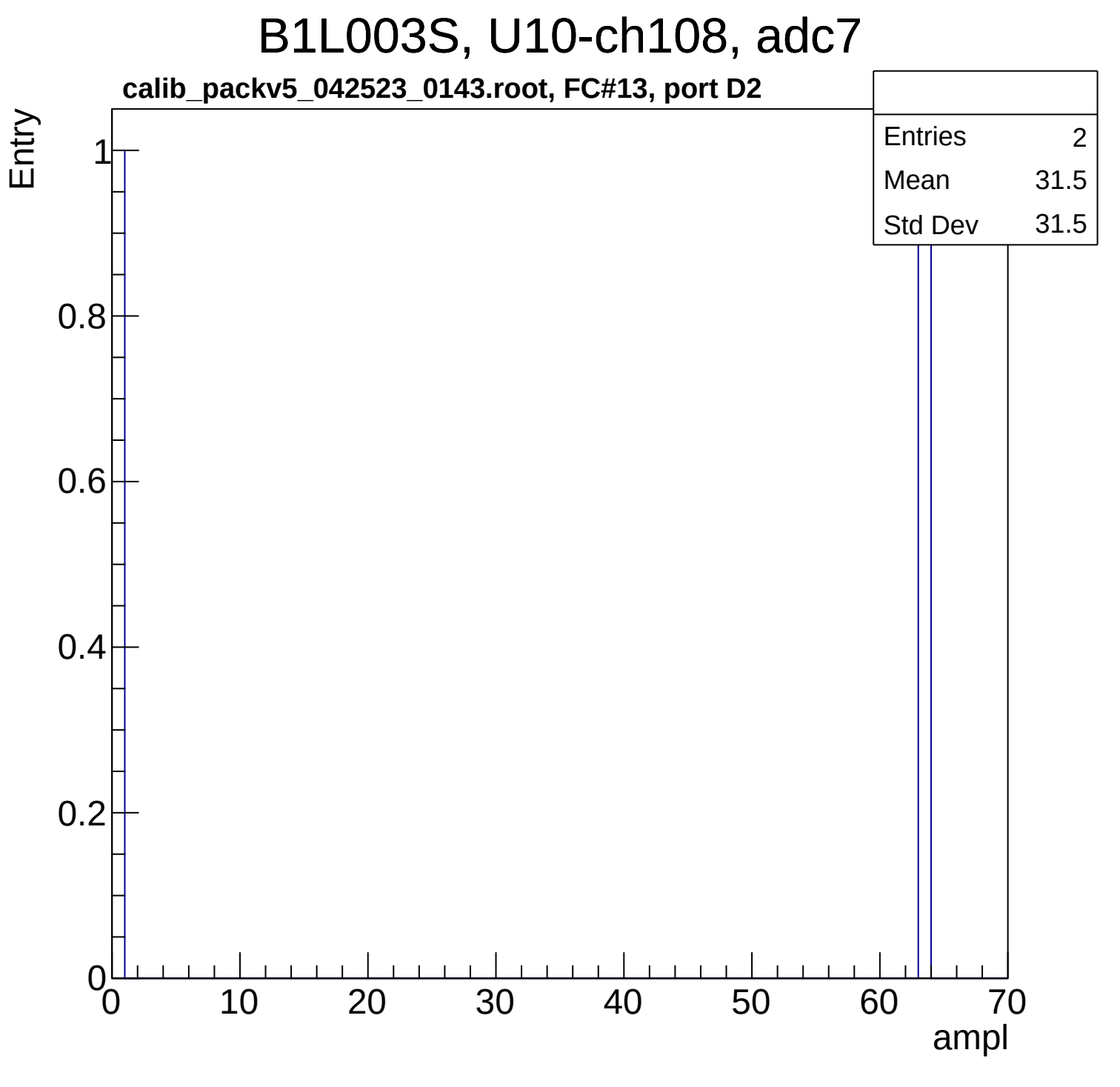
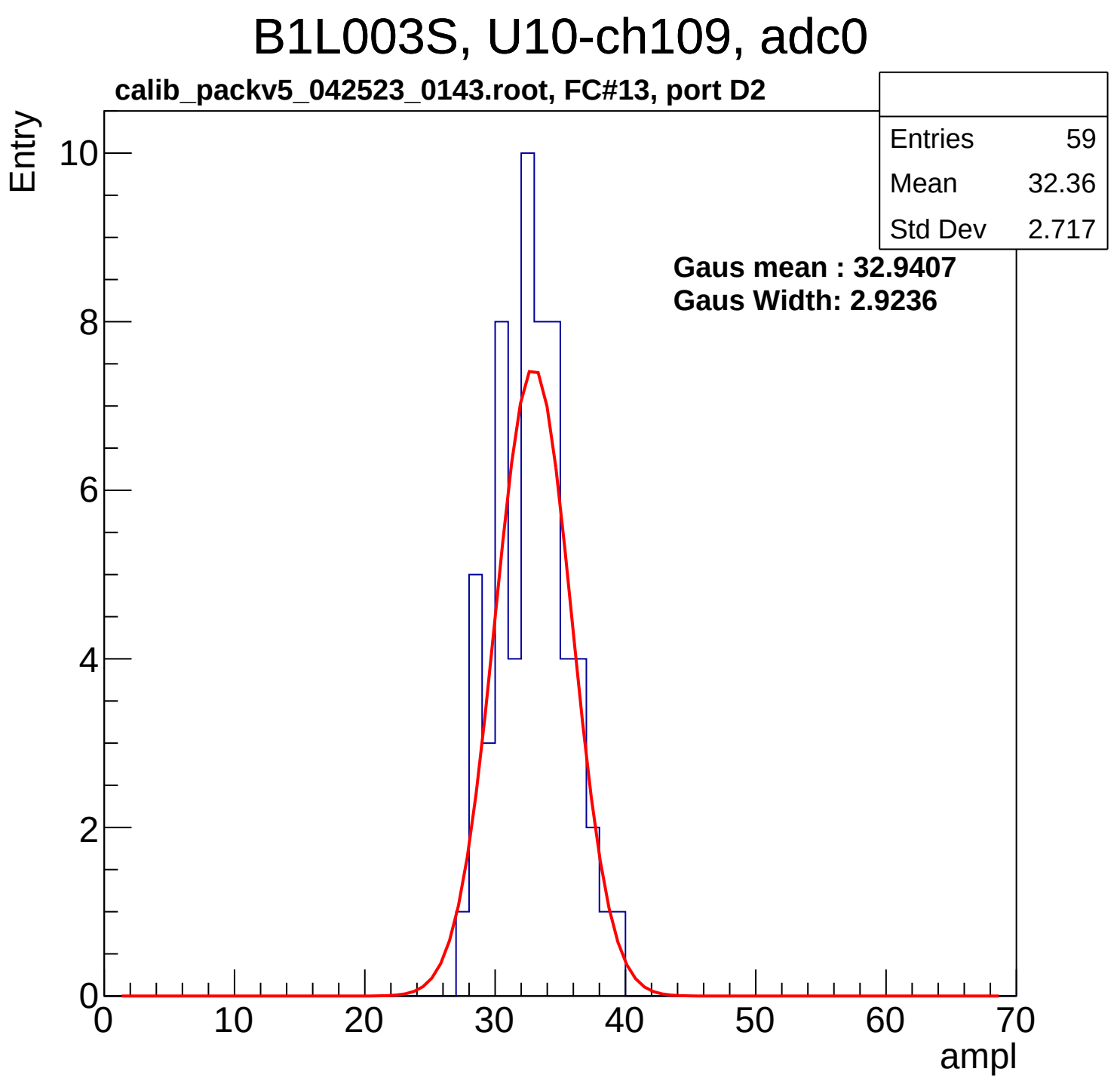
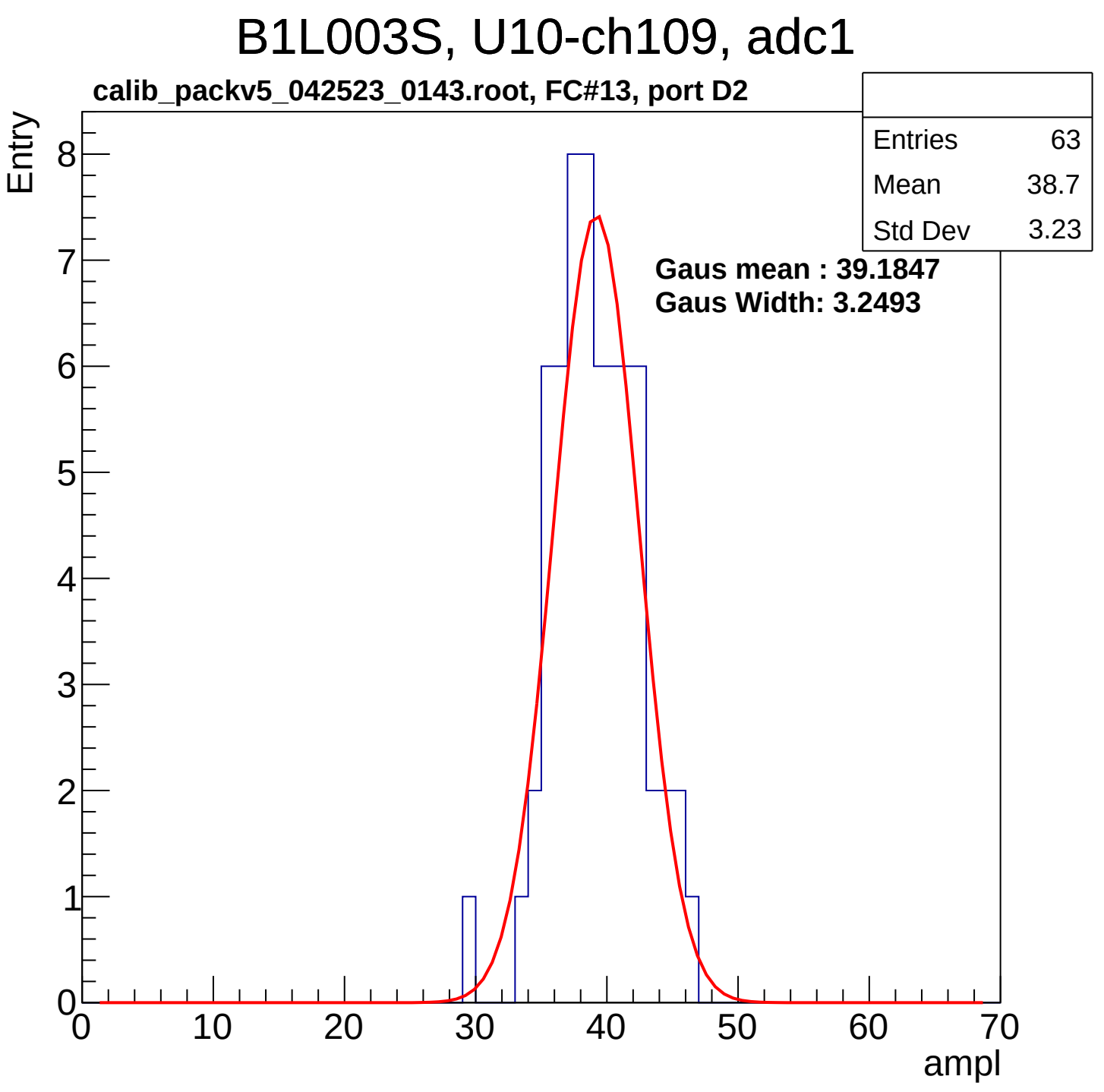

B1L003S, U10-ch108, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch109, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 32.36
Std Dev 2.717
Gaus mean : 32.9407
Gaus Width: 2.9236
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch109, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 38.7
Std Dev 3.23
Gaus mean : 39.1847
Gaus Width: 3.2493
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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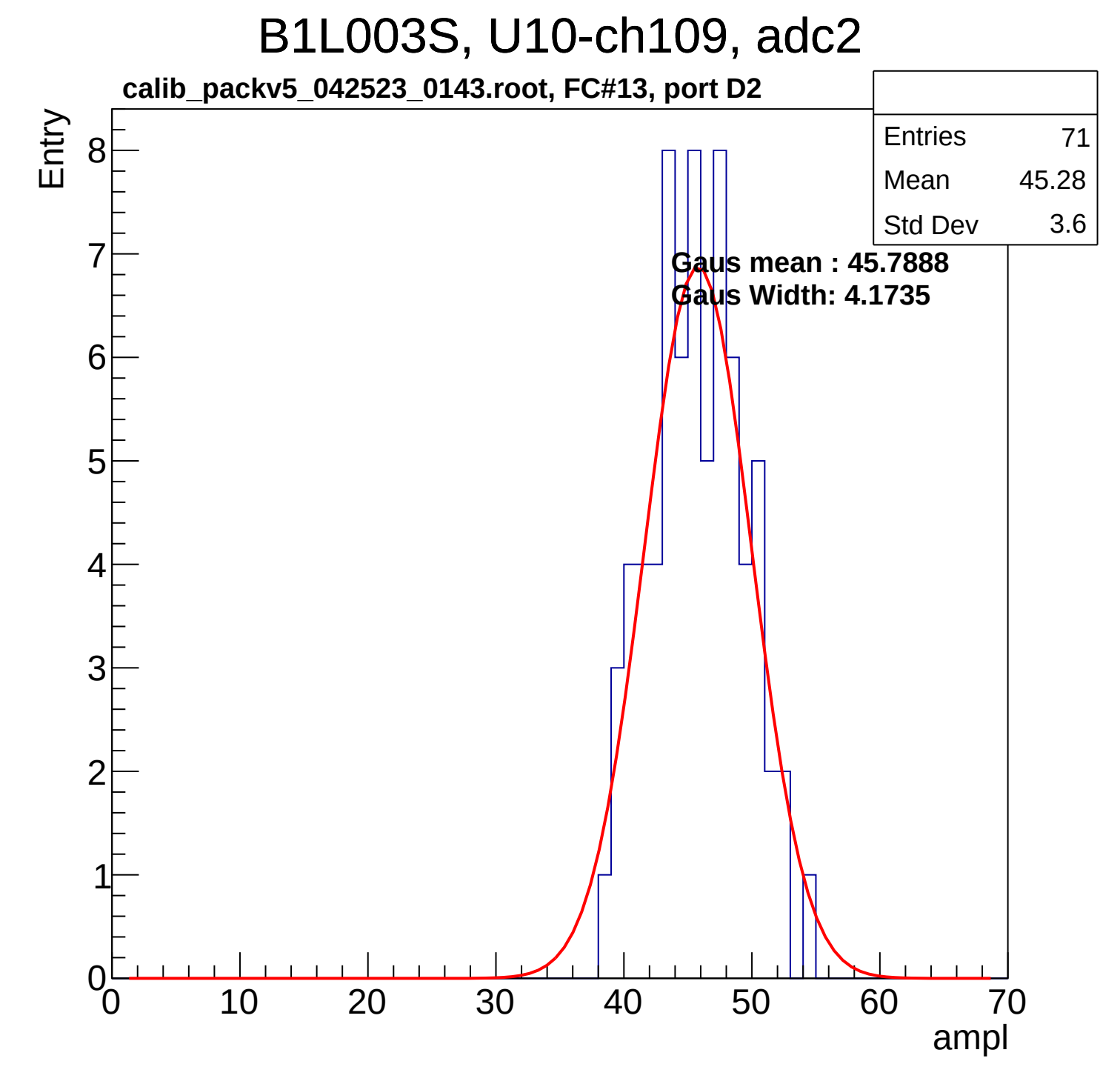
B1L003S, U10-ch109, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 45.28
Std Dev 3.6
Gaus mean : 45.7888
Gaus Width: 4.1735
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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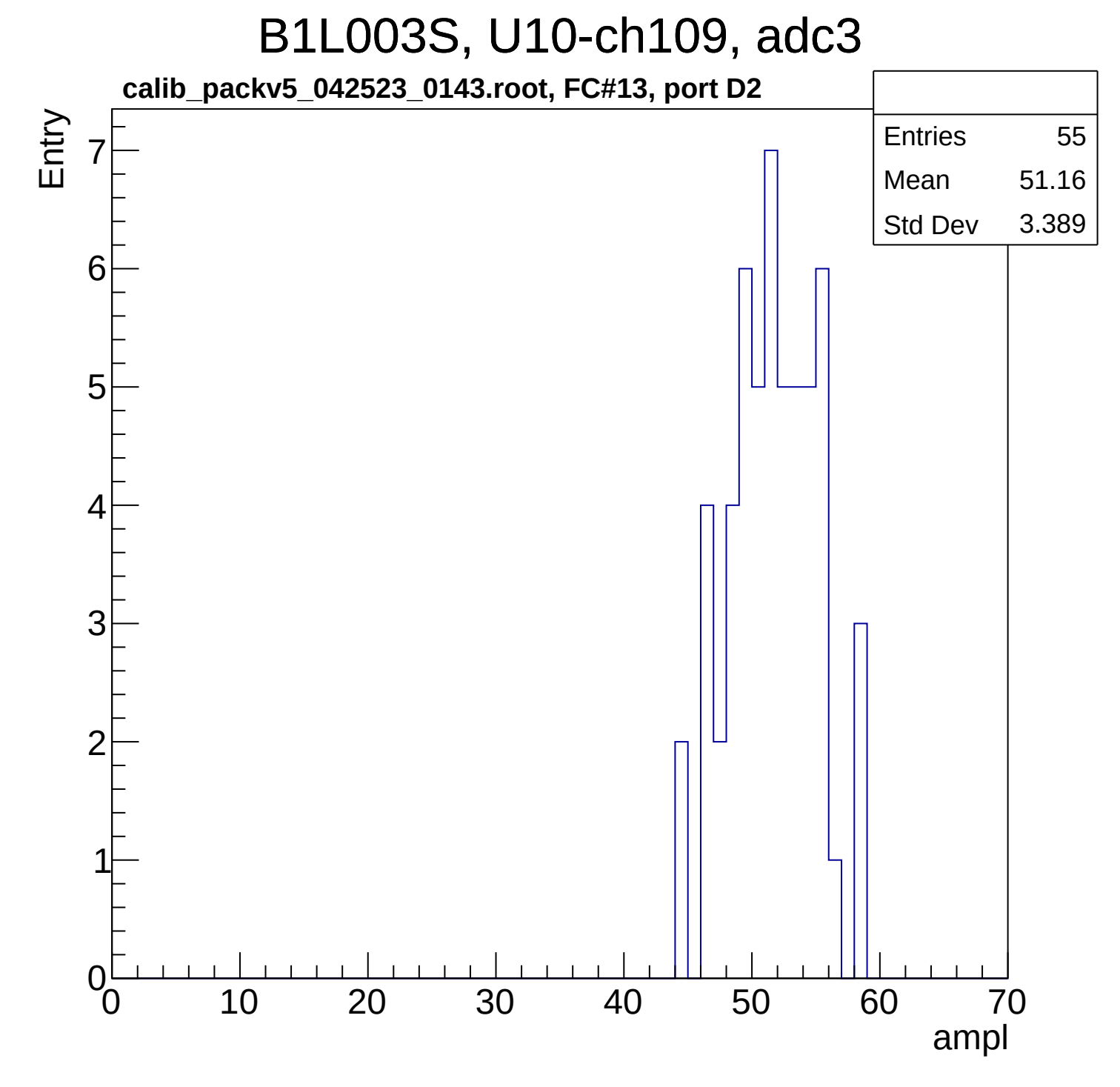
B1L003S, U10-ch109, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 51.16
Std Dev 3.389
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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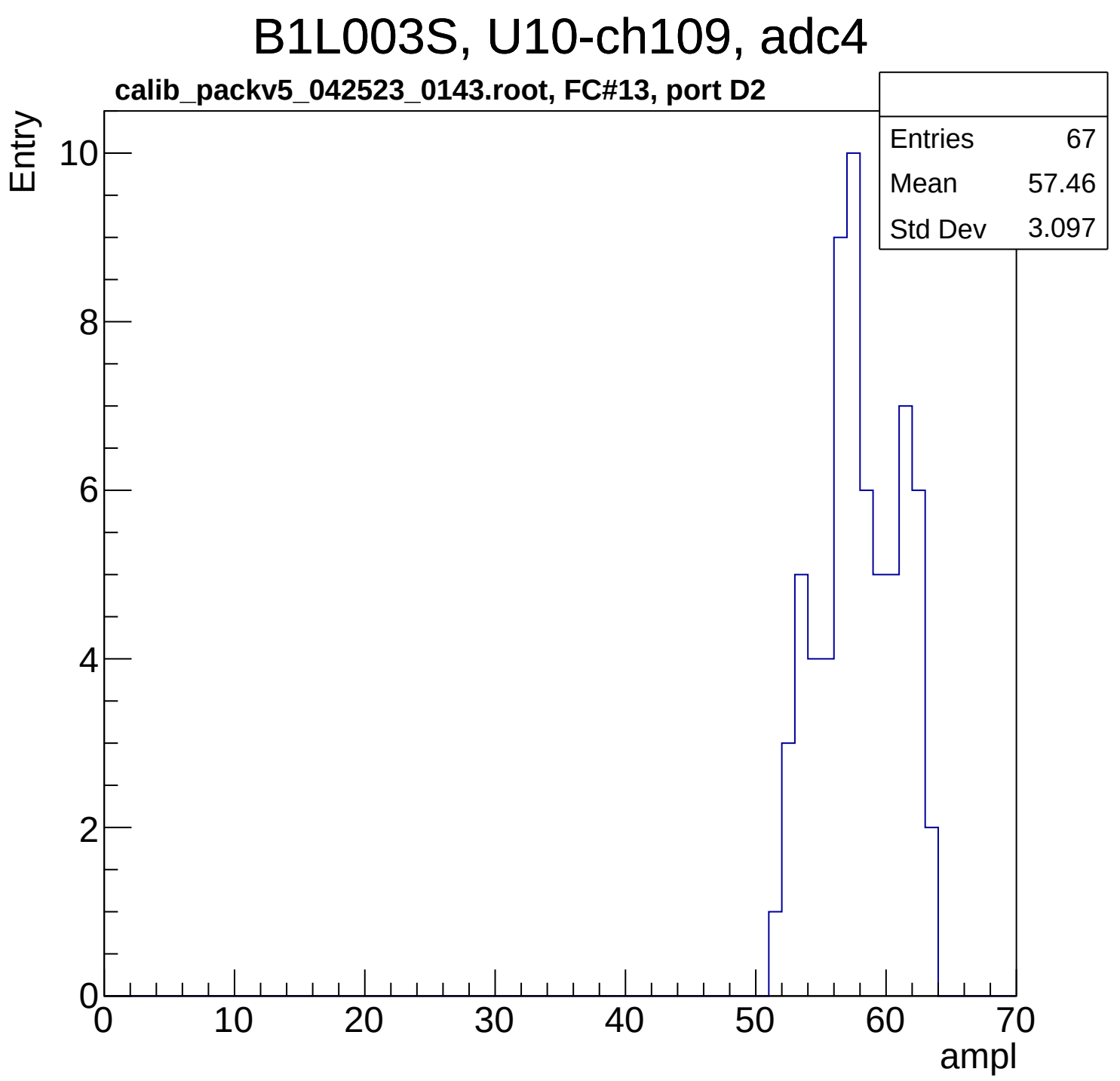
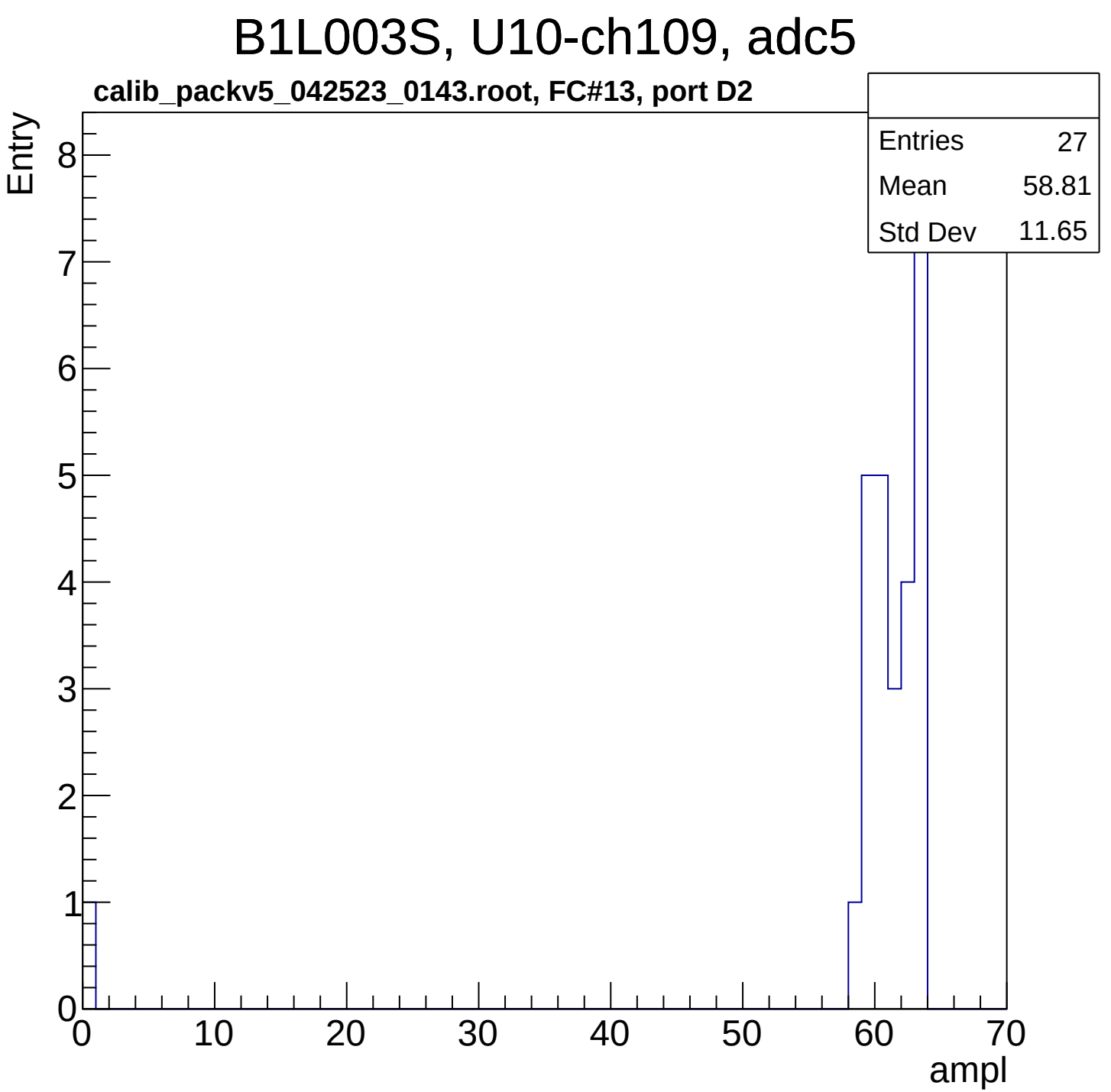
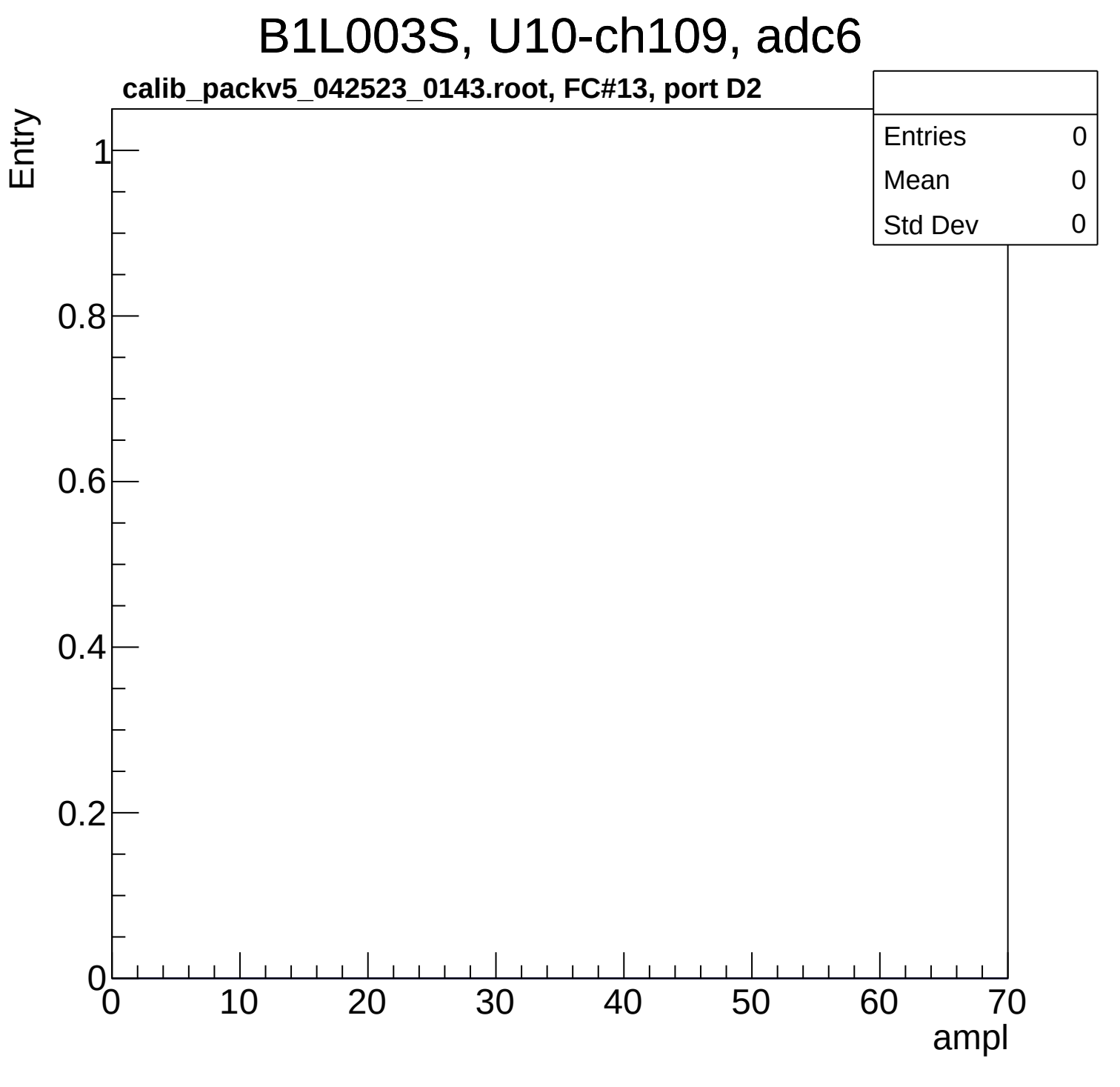
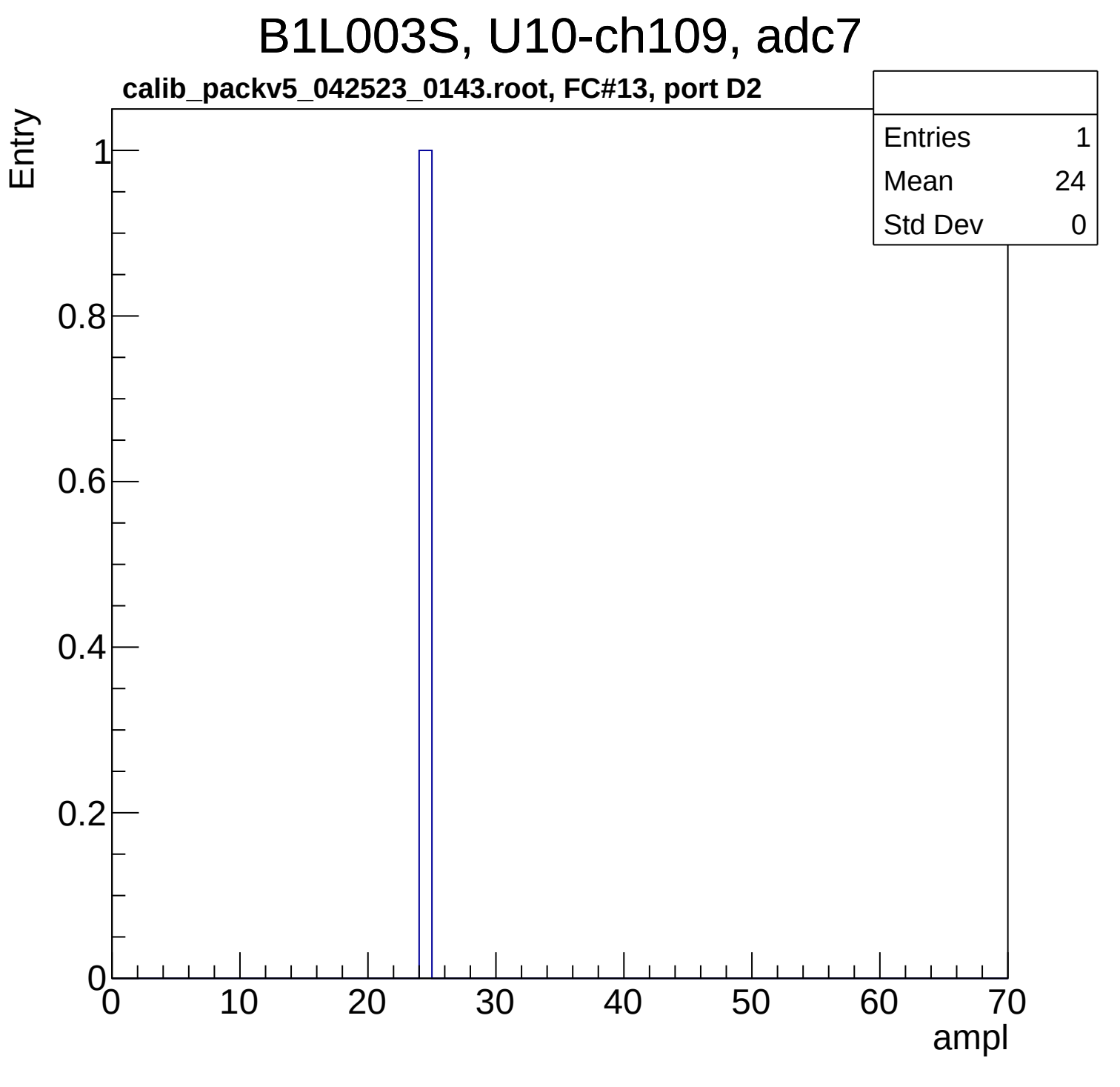
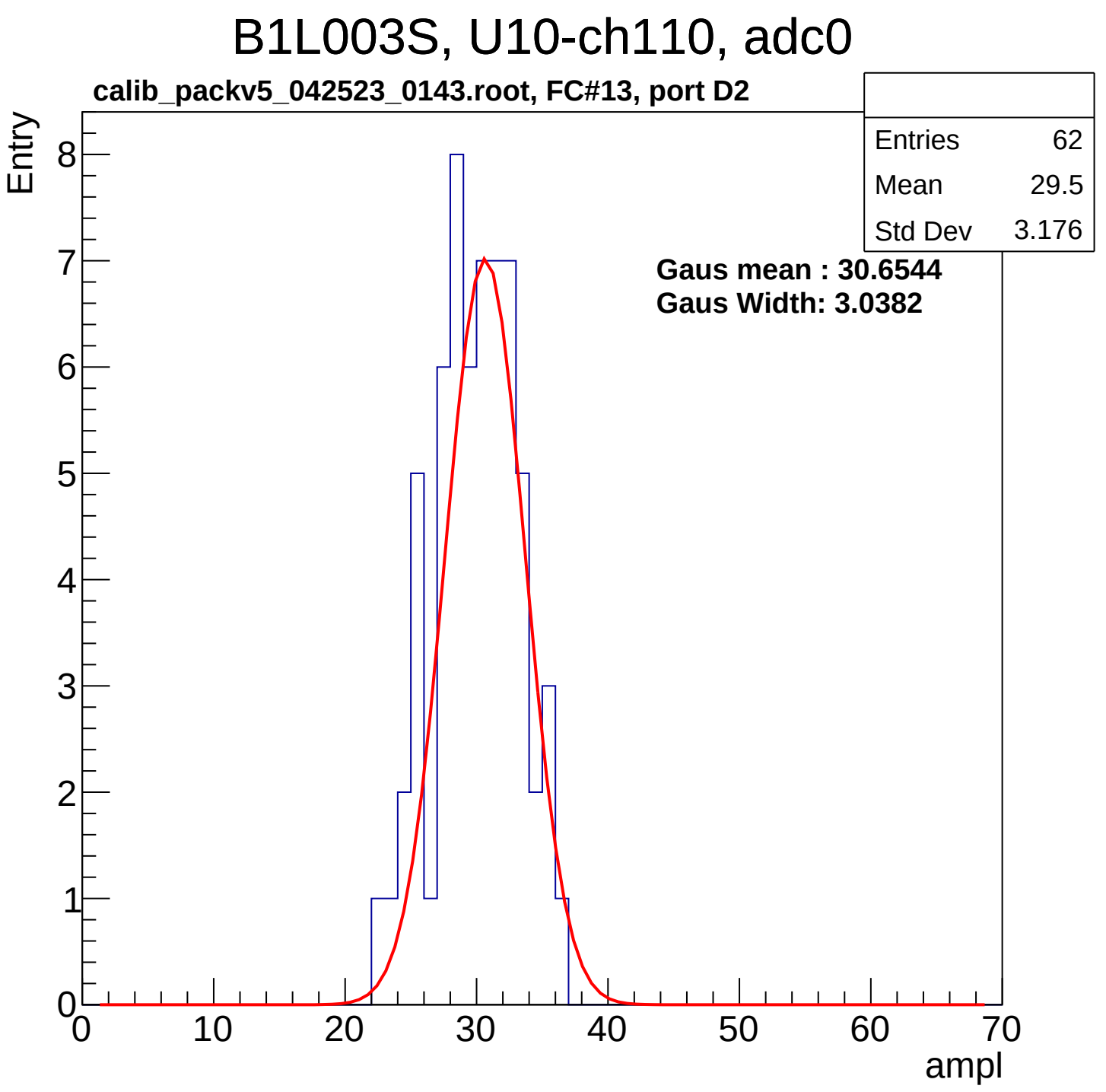

B1L003S, U10-ch109, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 57.46
Std Dev 3.097
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch109, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 27
Mean 58.81
Std Dev 11.65
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch109, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch109, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch110, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 29.5
Std Dev 3.176
Gaus mean : 30.6544
Gaus Width: 3.0382
Entry
ampl
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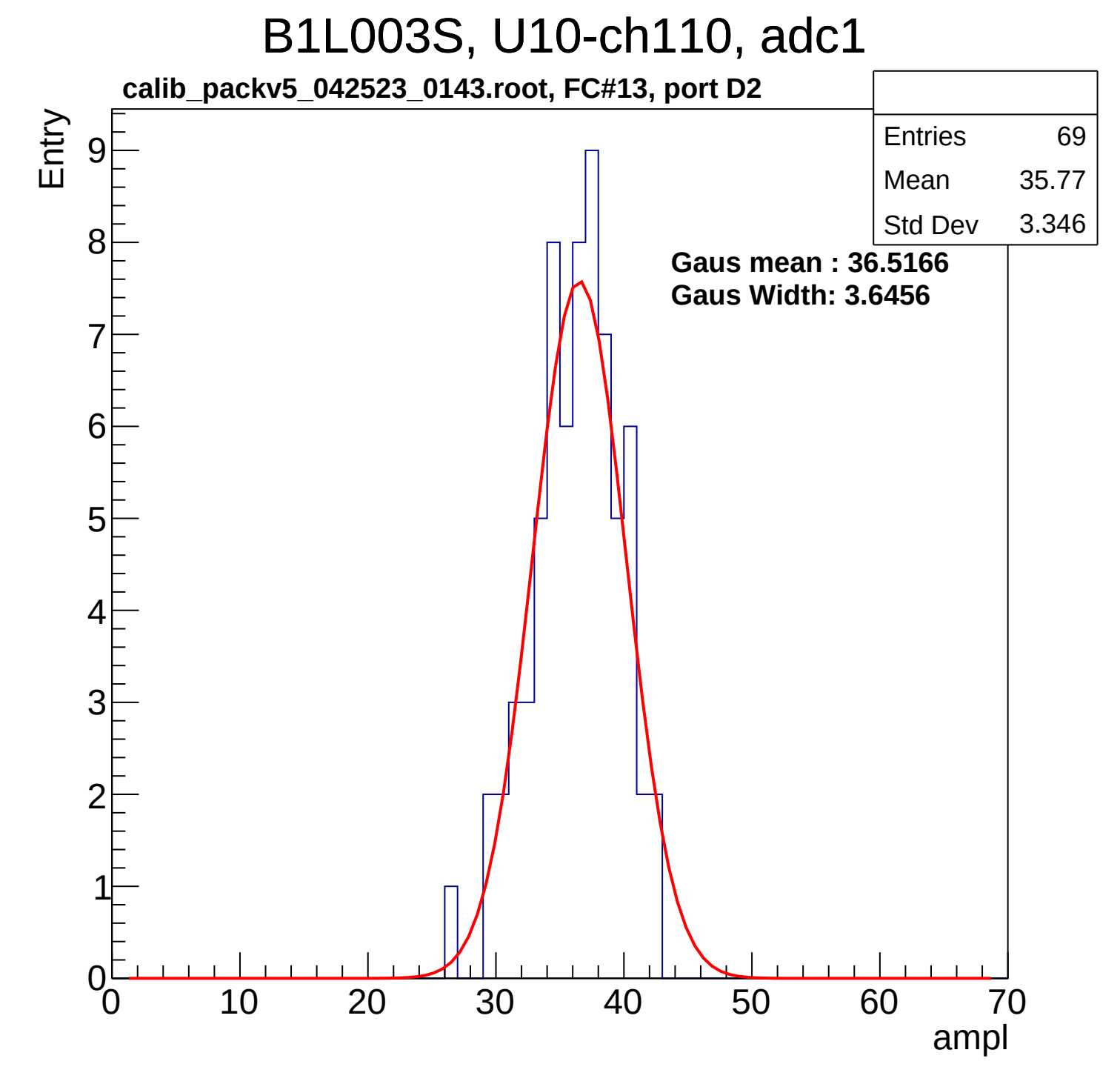
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch110, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.77
Std Dev 3.346
Gaus mean : 36.5166
Gaus Width: 3.6456
Entry
ampl
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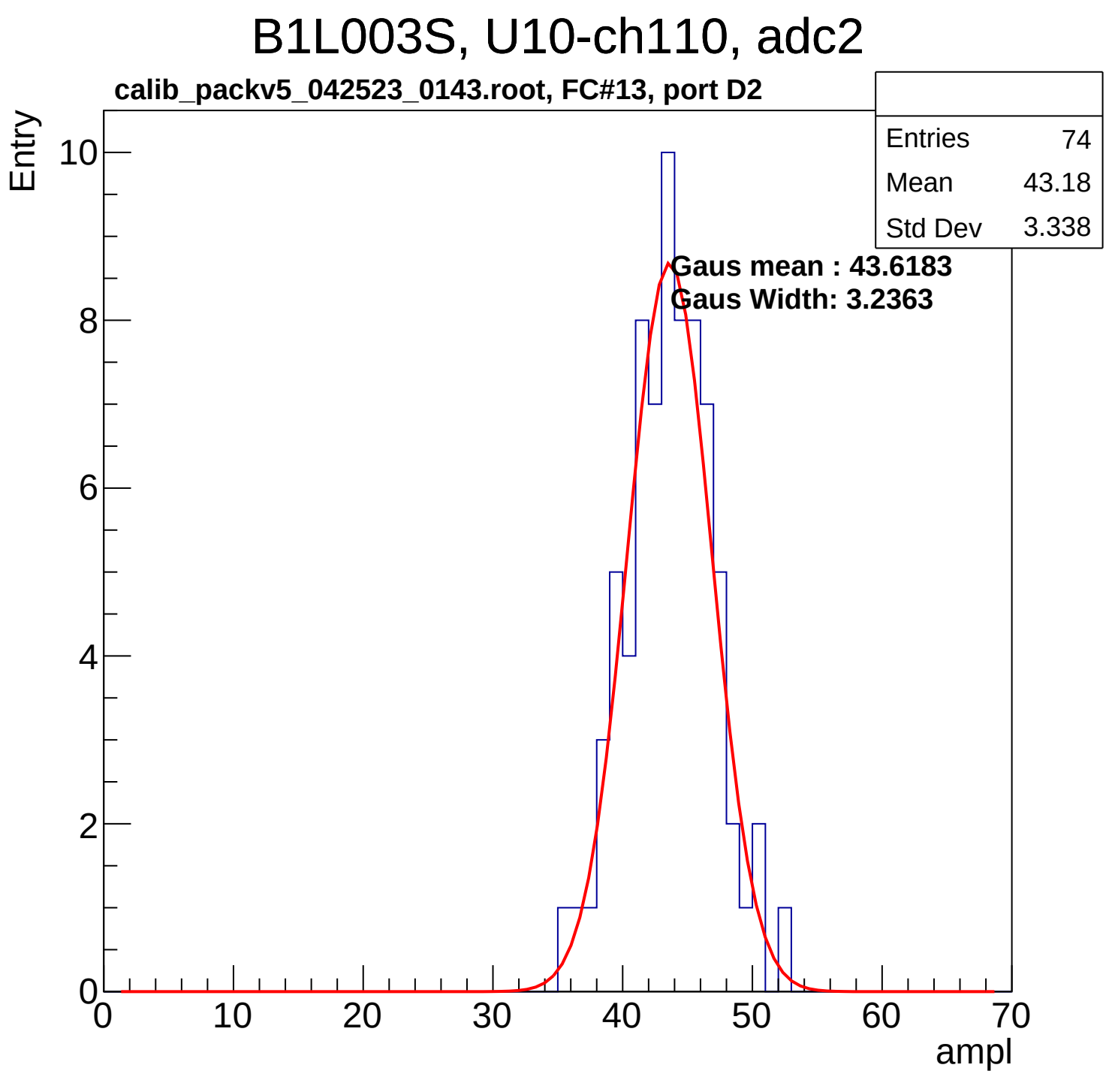
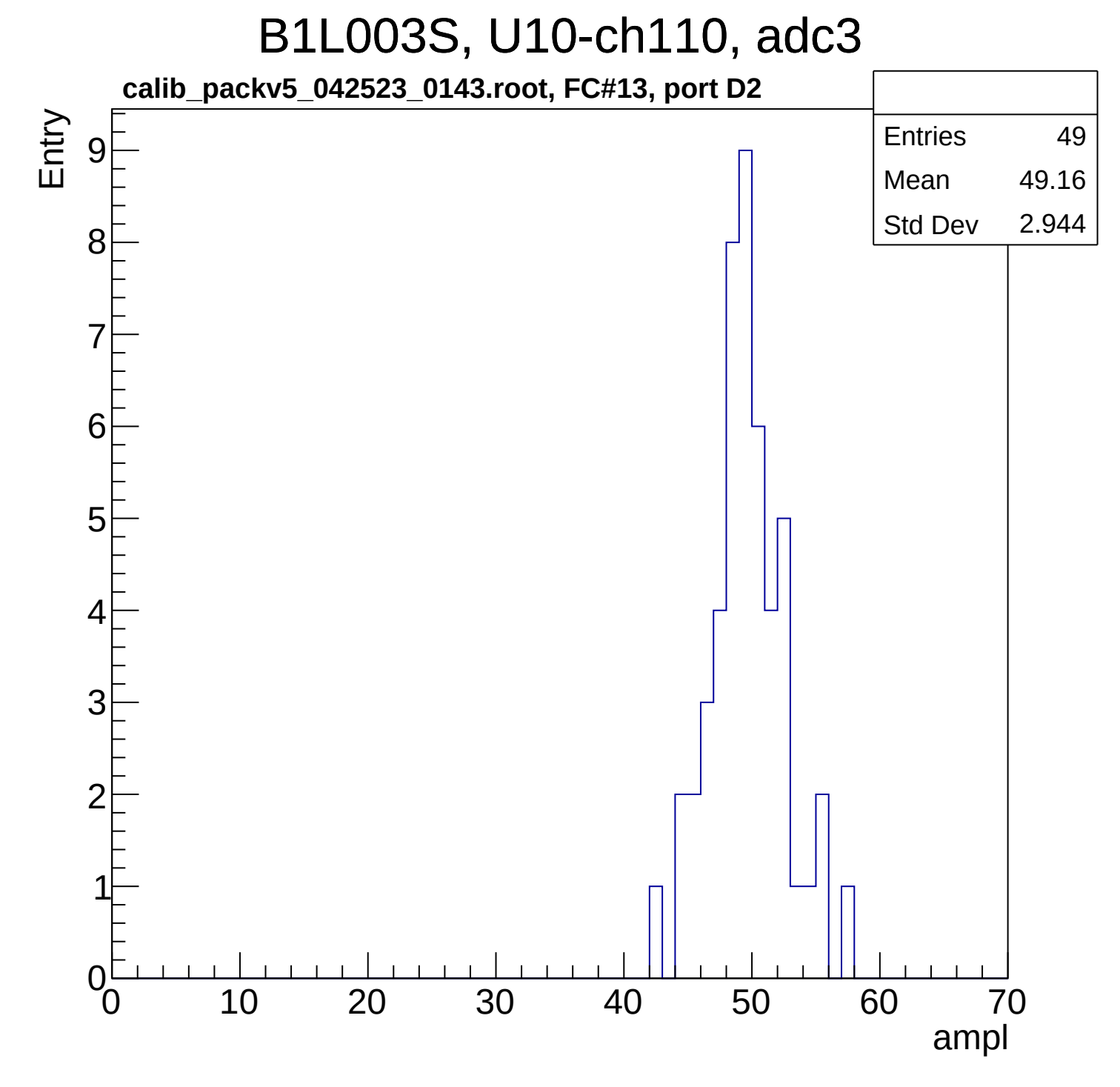
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch110, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 43.18
Std Dev 3.338
Gaus mean : 43.6183
Gaus Width: 3.2363
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch110, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 49.16
Std Dev 2.944
Entry
ampl
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1
2
3
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5
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9
0
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20
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40
50
60
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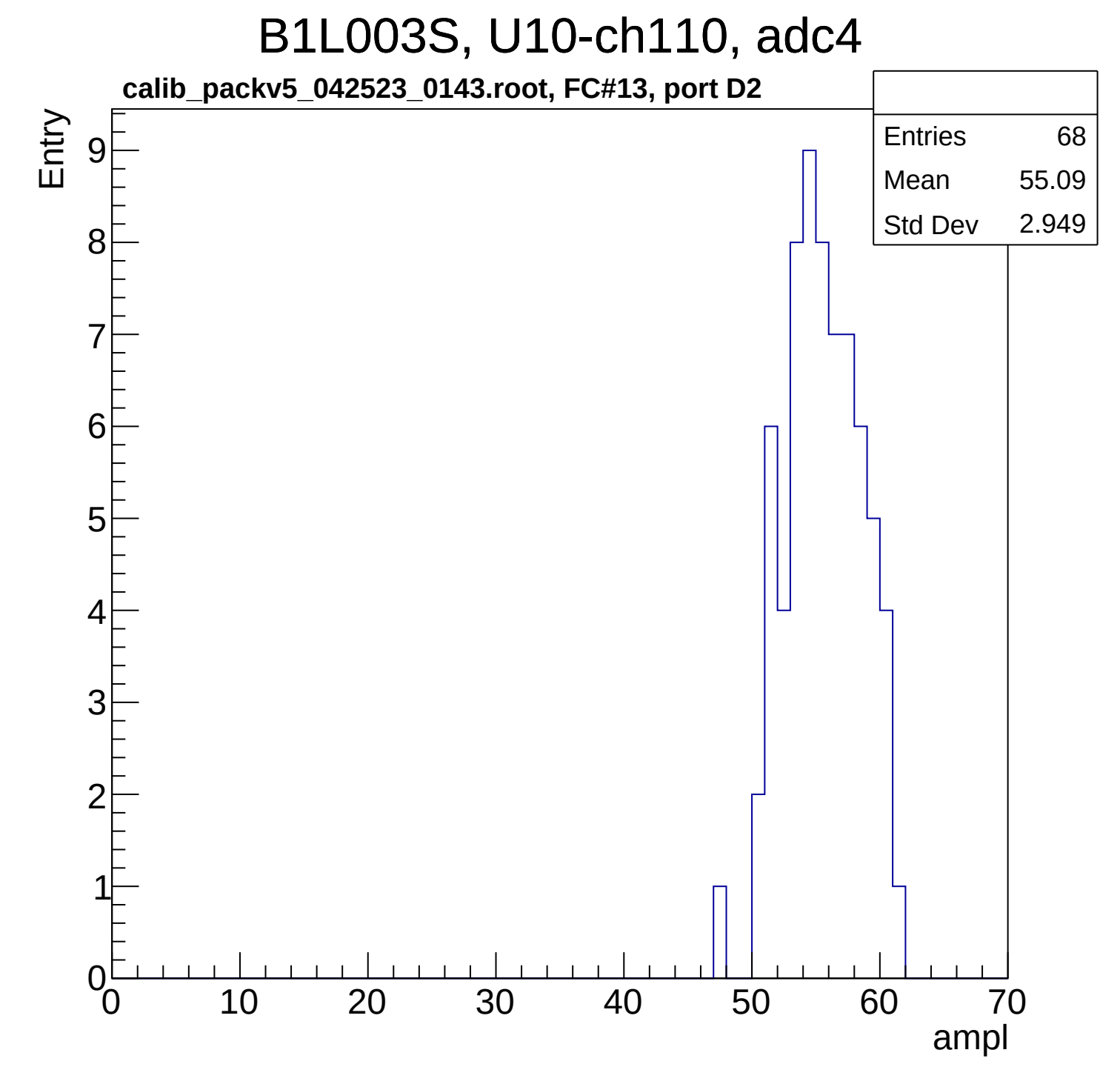
B1L003S, U10-ch110, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 55.09
Std Dev 2.949
Entry
ampl
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3
4
5
6
7
8
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0
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60
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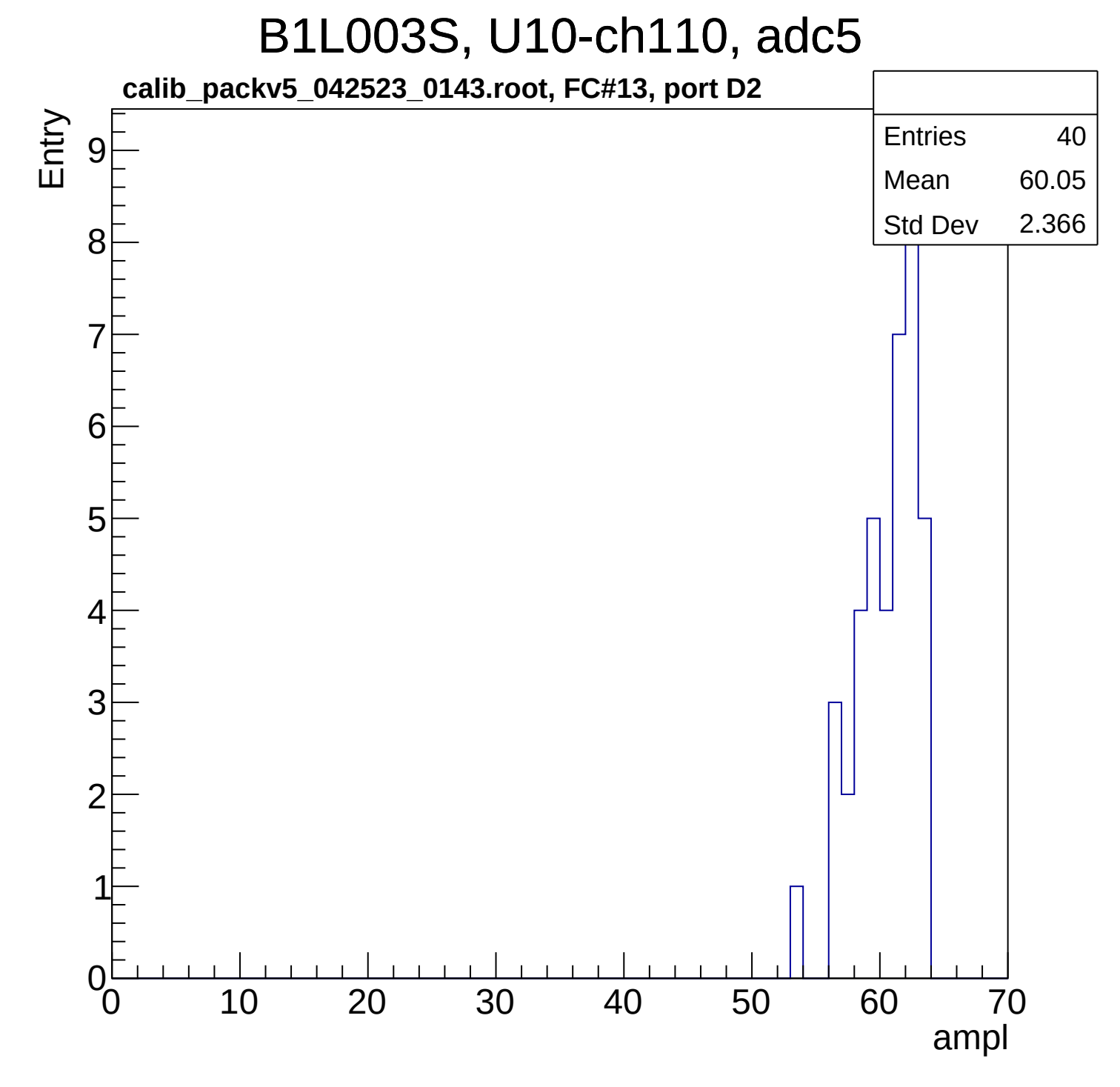
B1L003S, U10-ch110, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 60.05
Std Dev 2.366
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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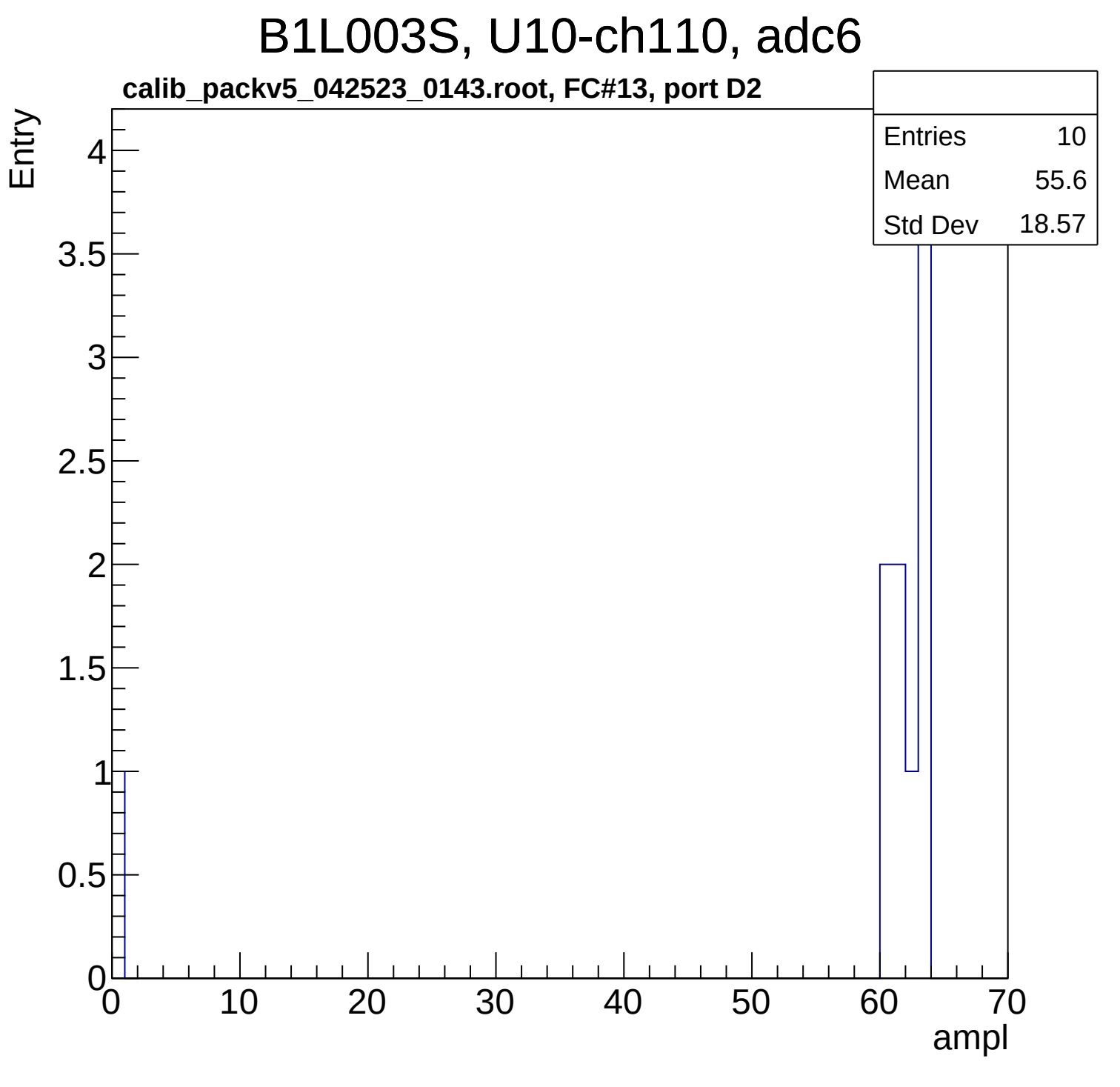
B1L003S, U10-ch110, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.6
Std Dev 18.57
Entry
ampl
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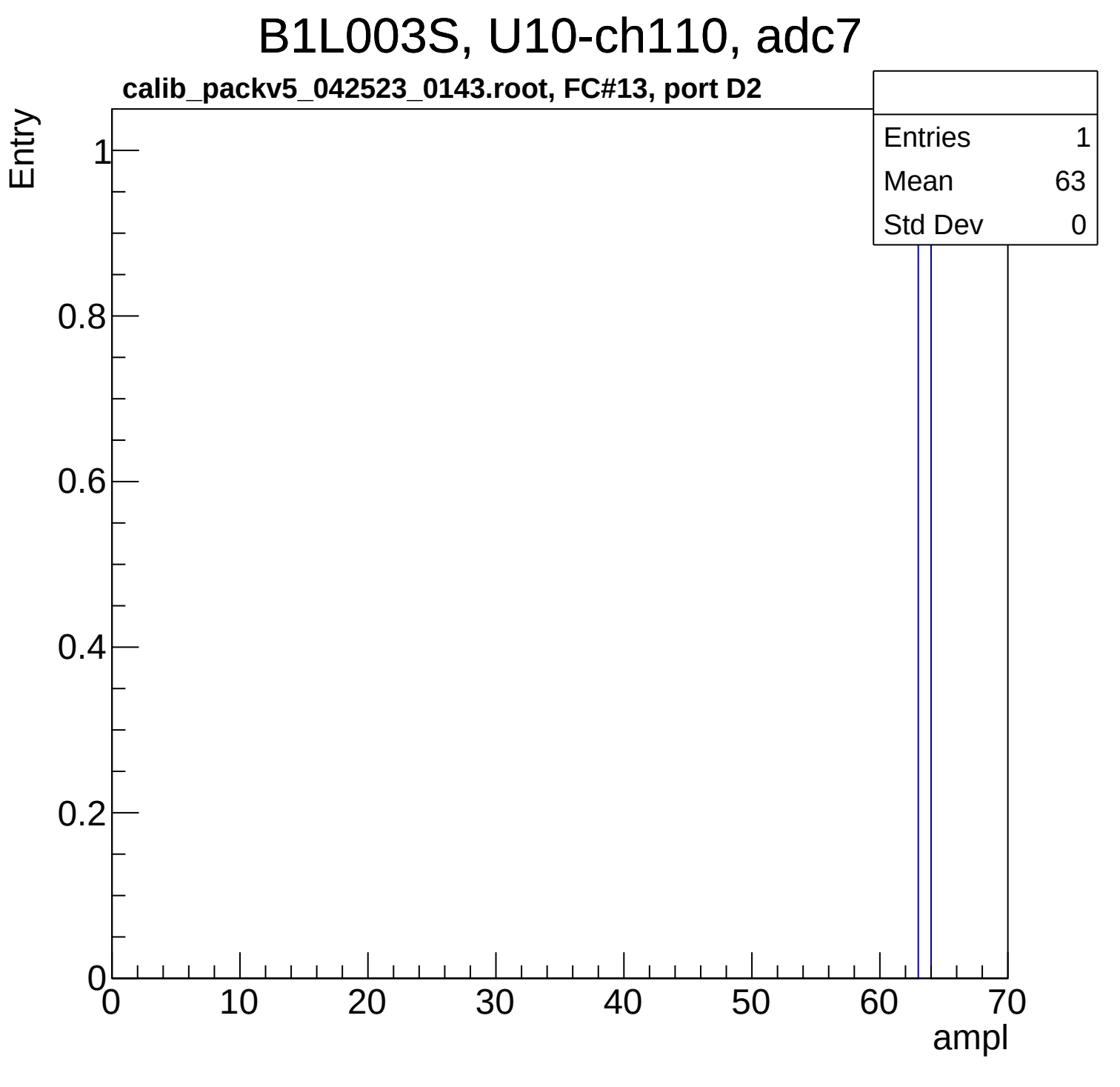
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch110, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
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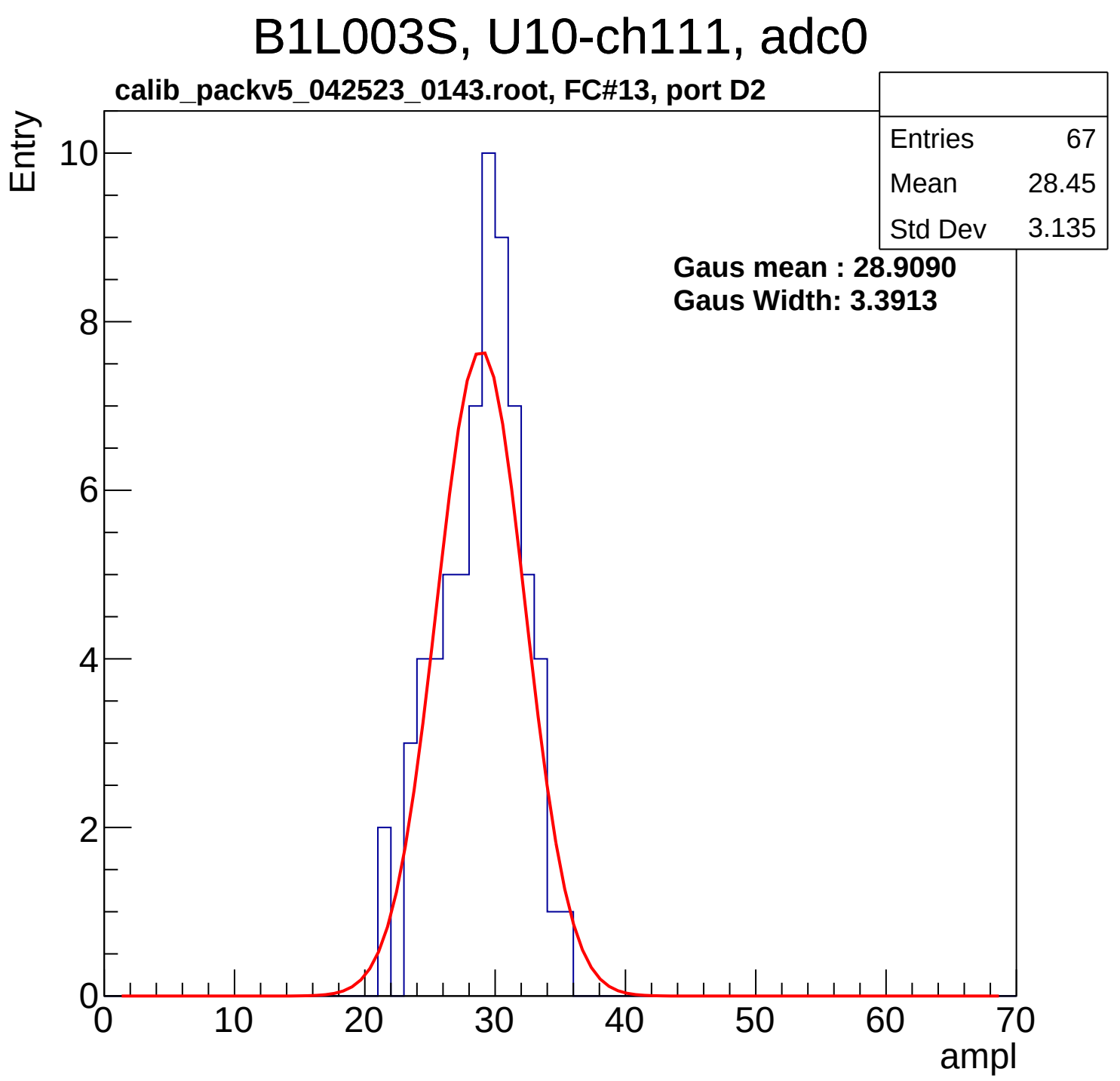
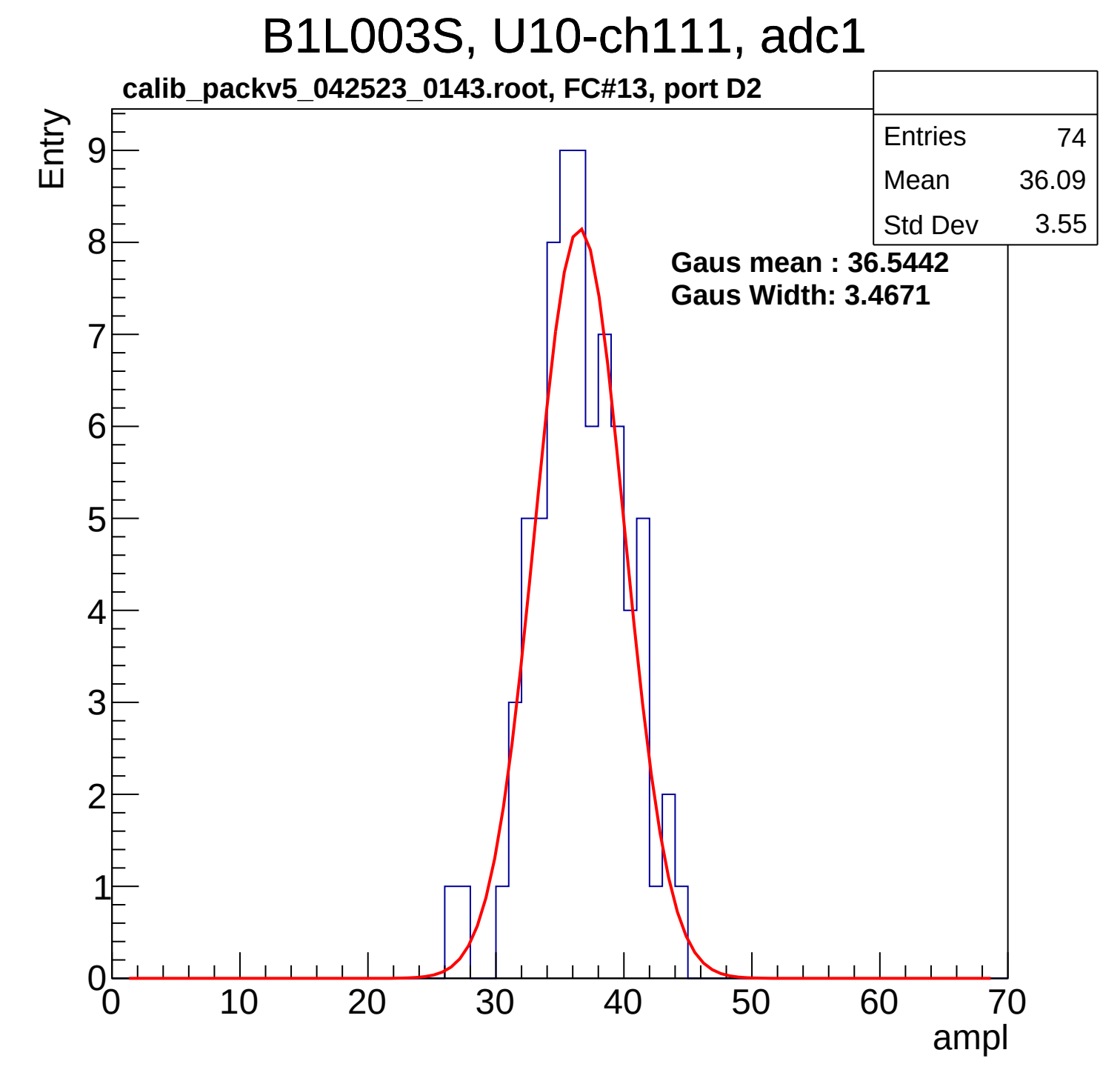
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch111, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 28.45
Std Dev 3.135
Gaus mean : 28.9090
Gaus Width: 3.3913
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch111, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.09
Std Dev 3.55
Gaus mean : 36.5442
Gaus Width: 3.4671
Entry
ampl
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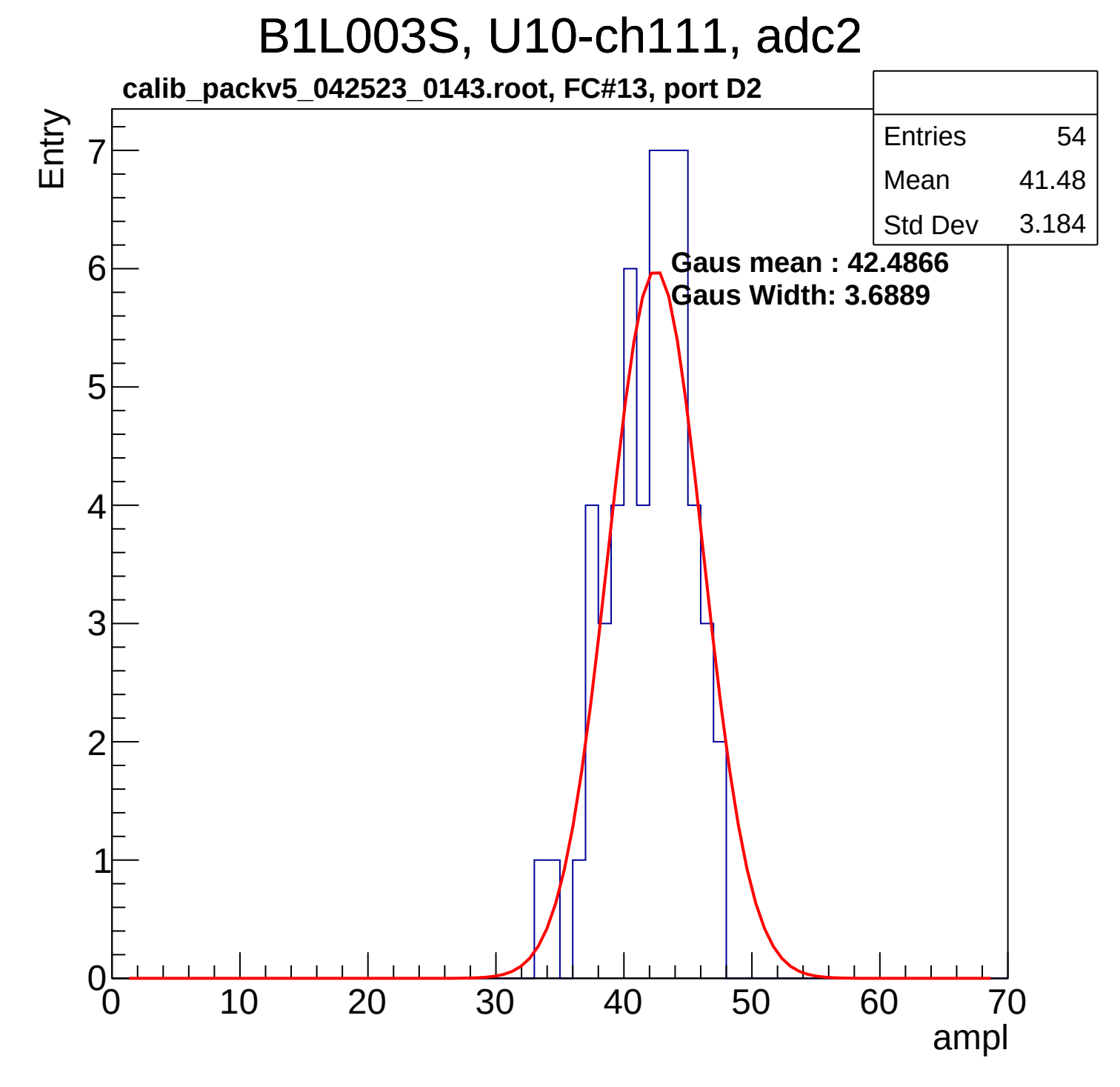
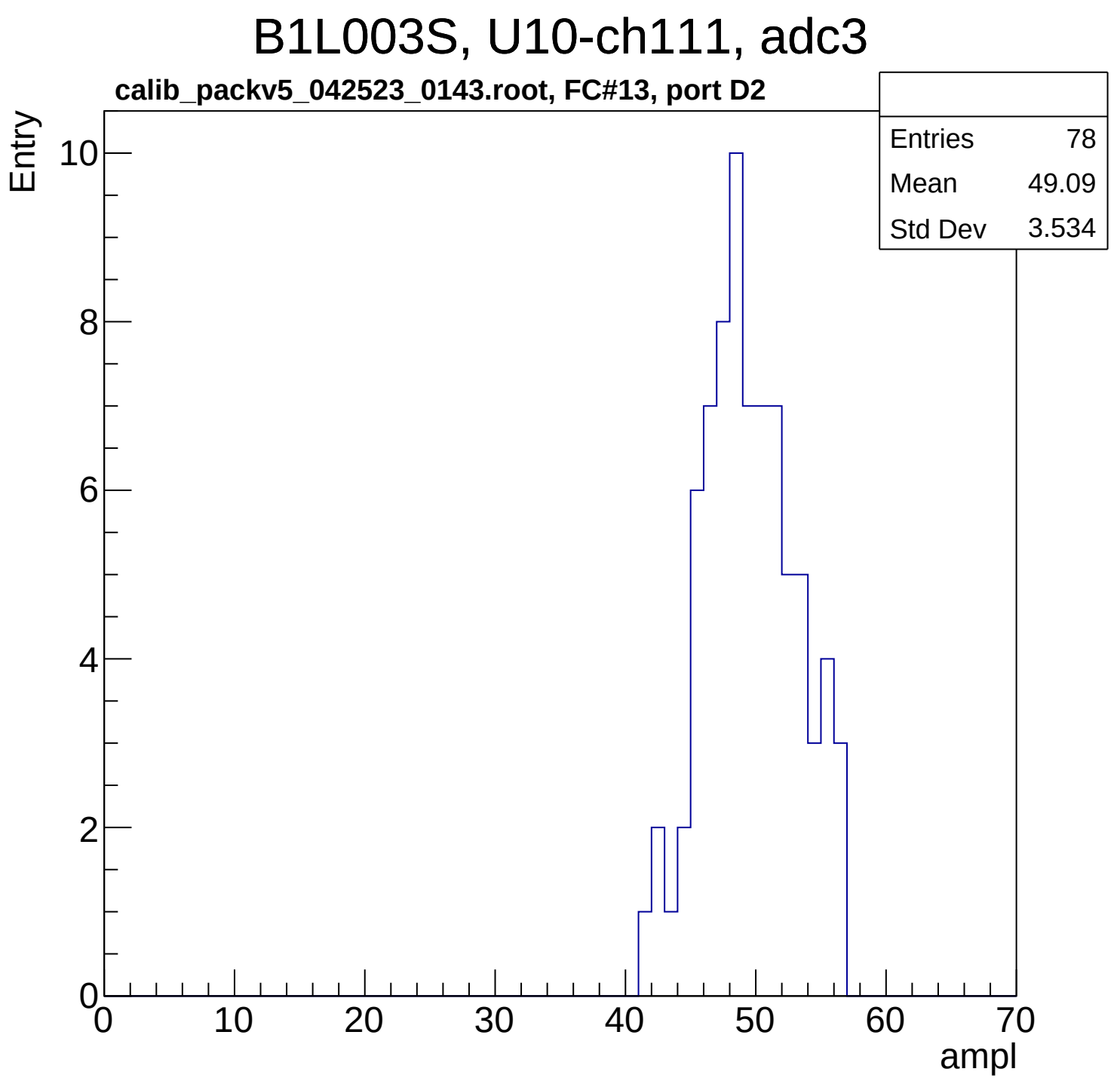
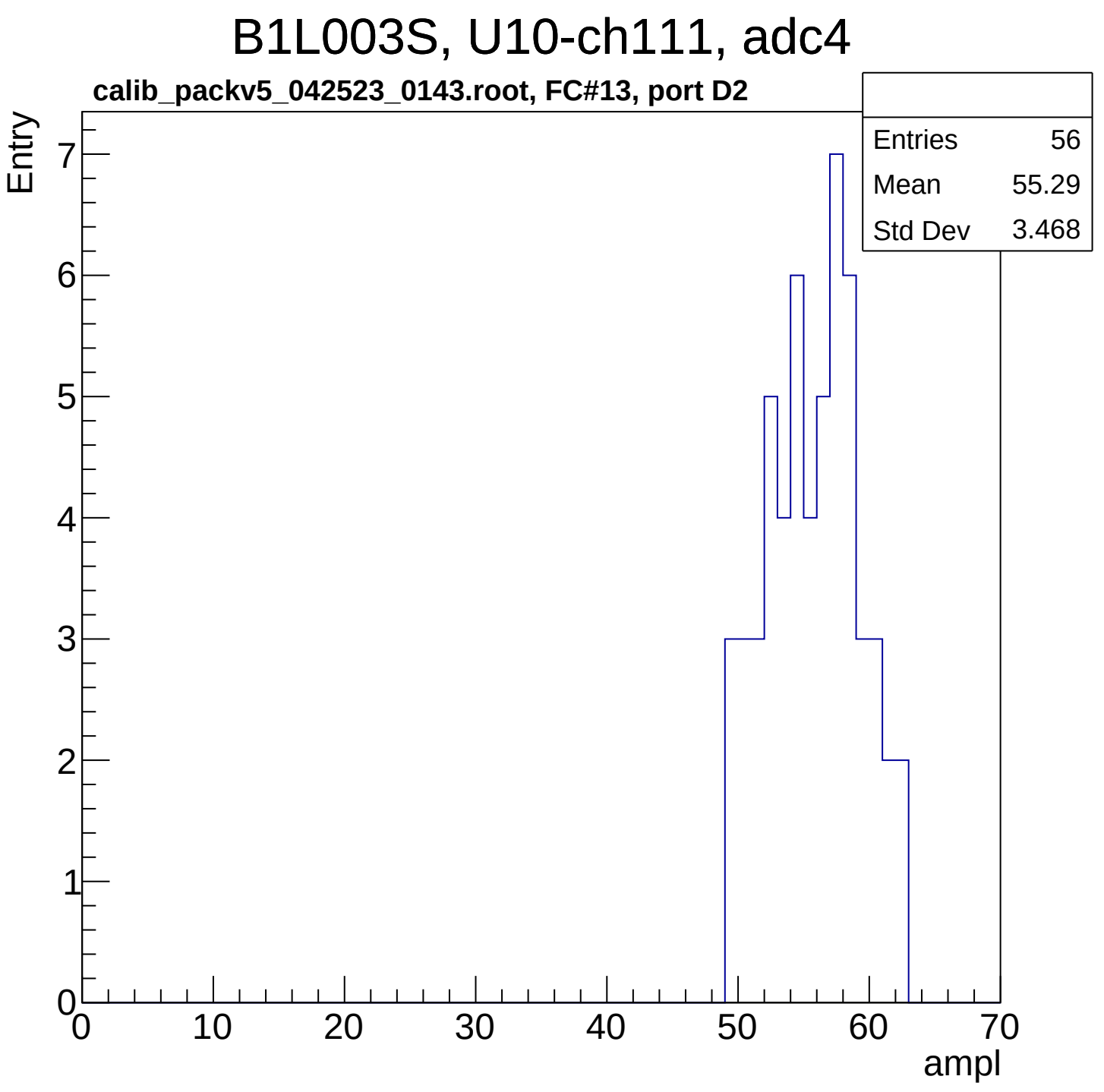
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch111, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41.48
Std Dev 3.184
Gaus mean : 42.4866
Gaus Width: 3.6889
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch111, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 49.09
Std Dev 3.534
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch111, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.29
Std Dev 3.468
Entry
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2
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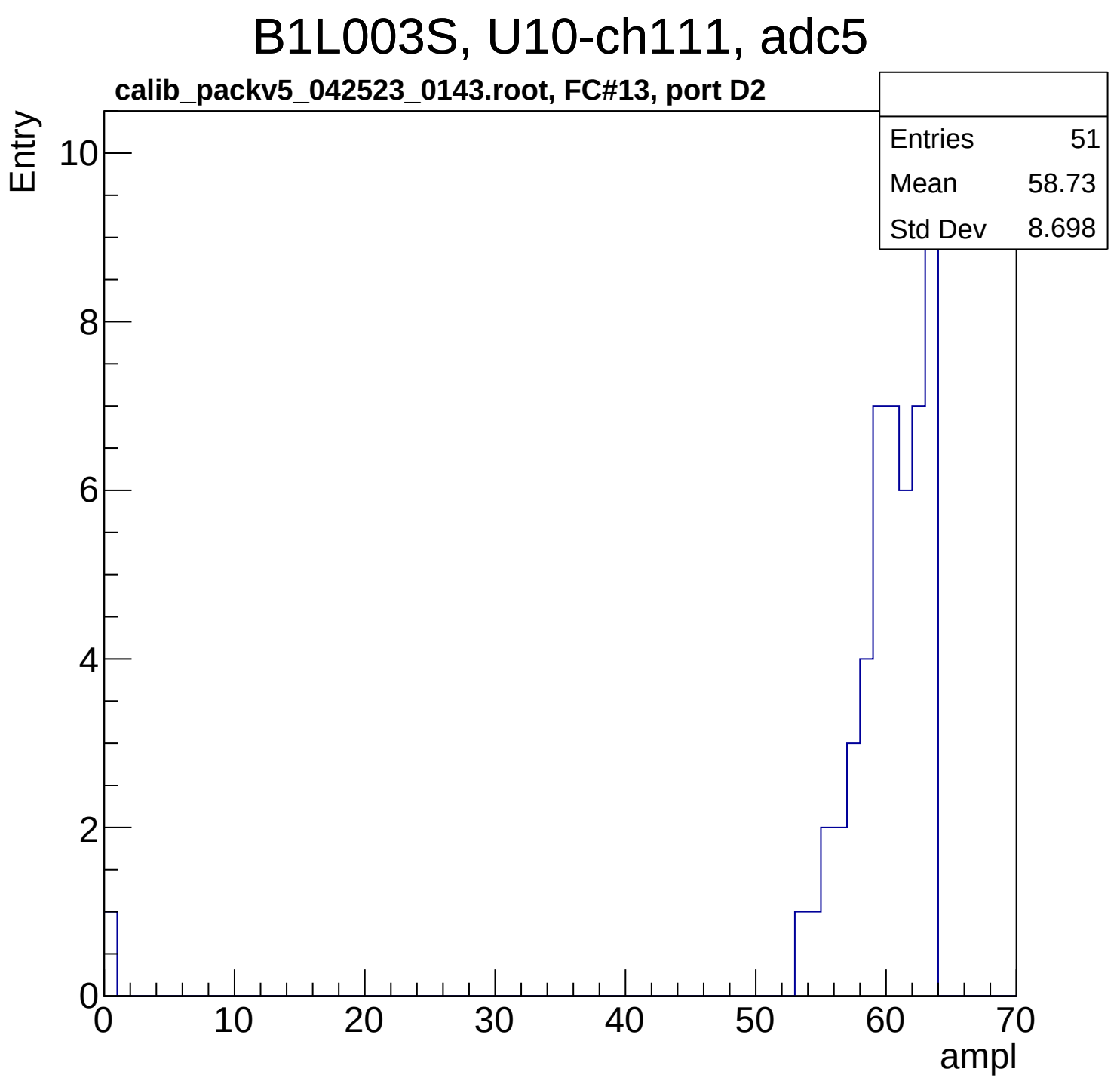
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch111, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.73
Std Dev 8.698
Entry
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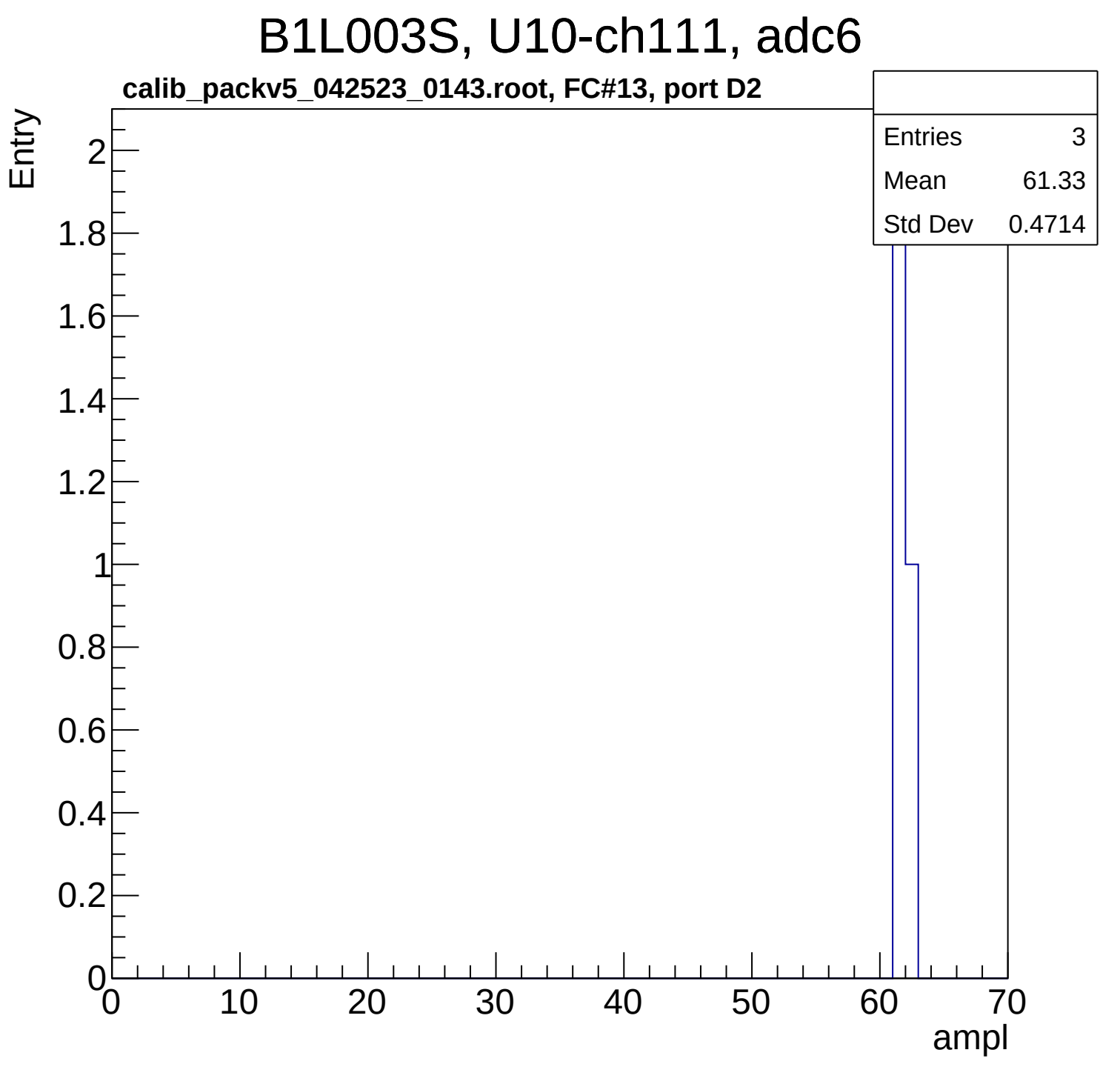
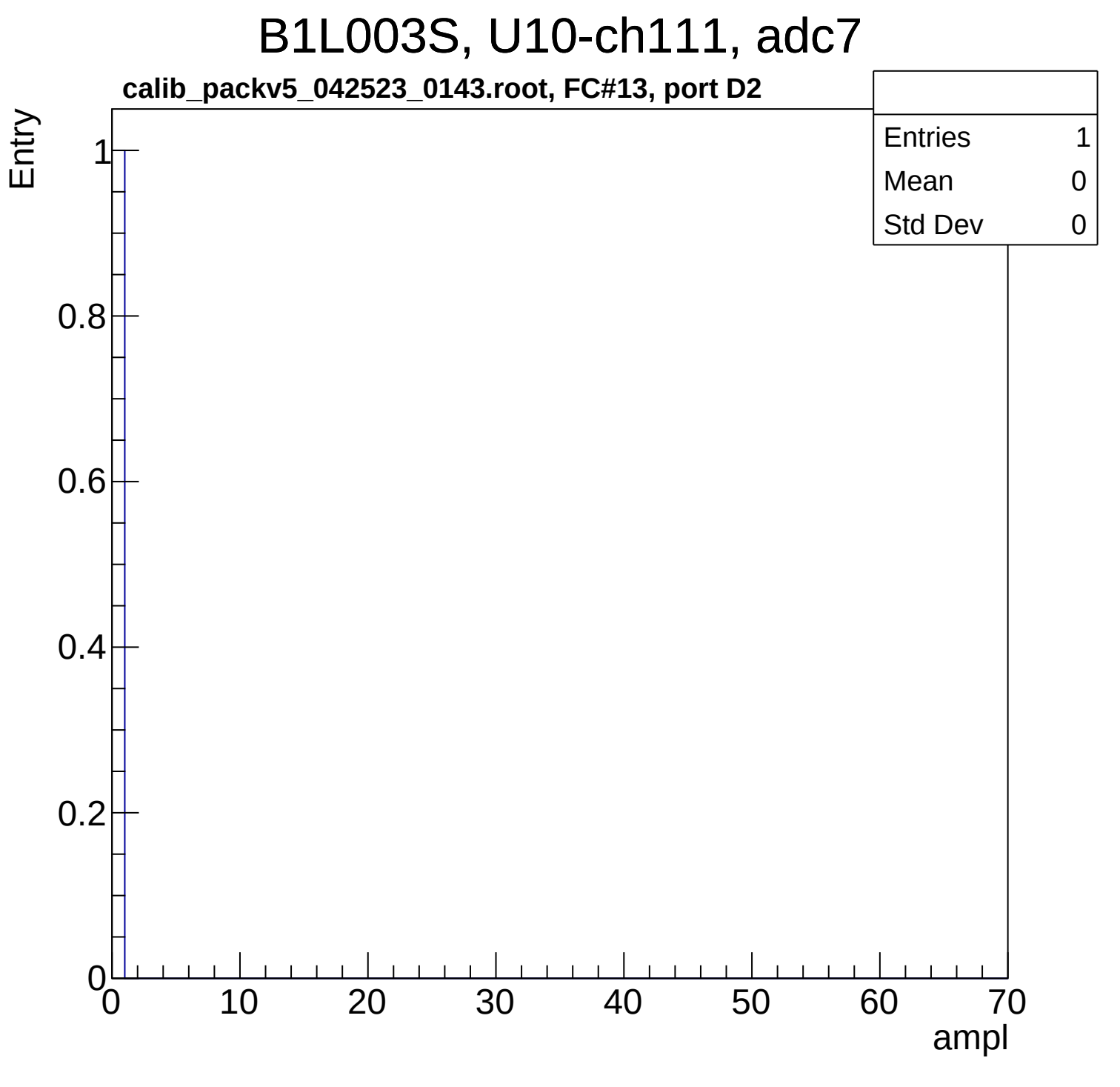
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch111, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch111, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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60
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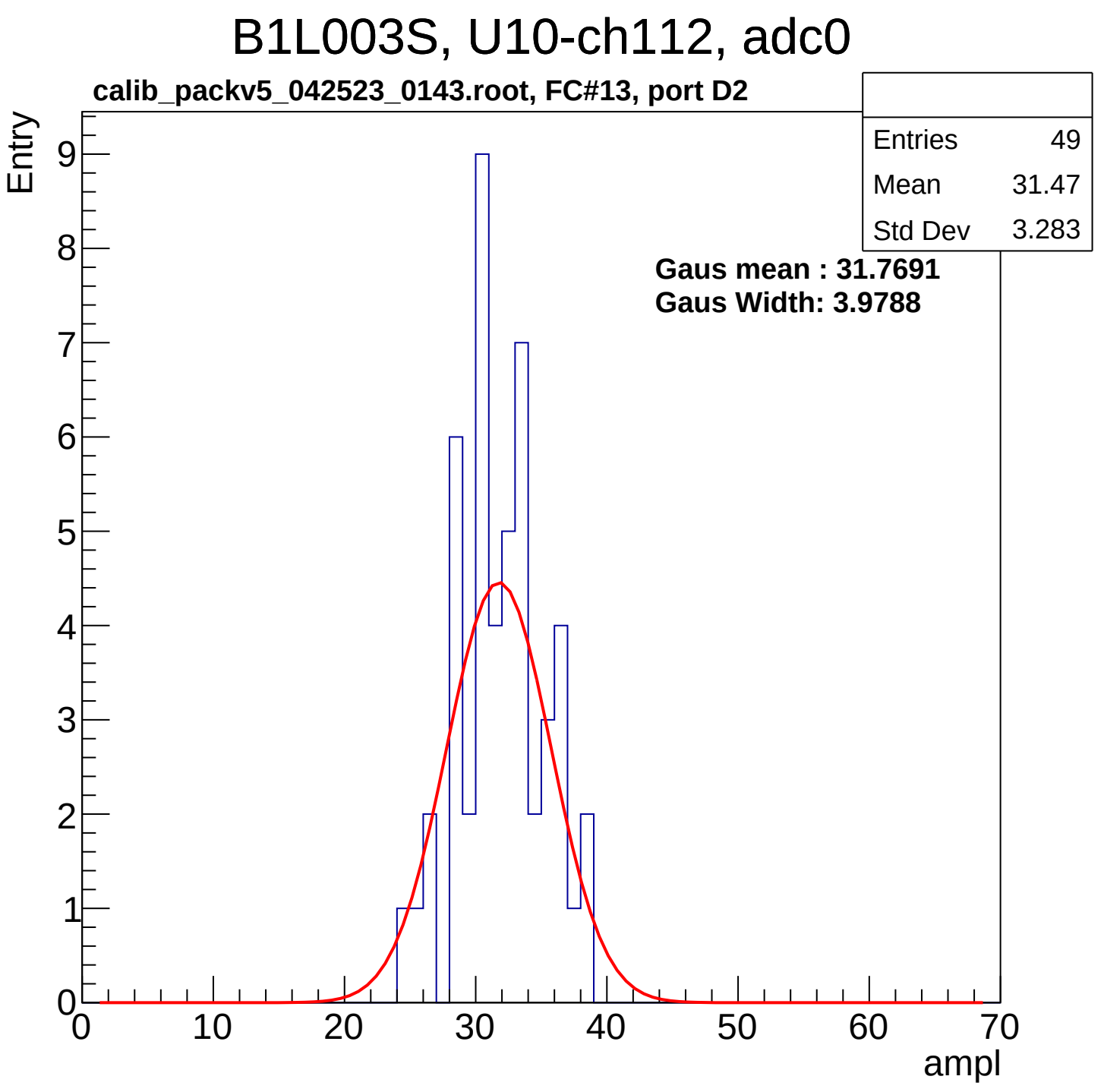
ampl


B1L003S, U10-ch112, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 31.47
Std Dev 3.283
Gaus mean : 31.7691
Gaus Width: 3.9788
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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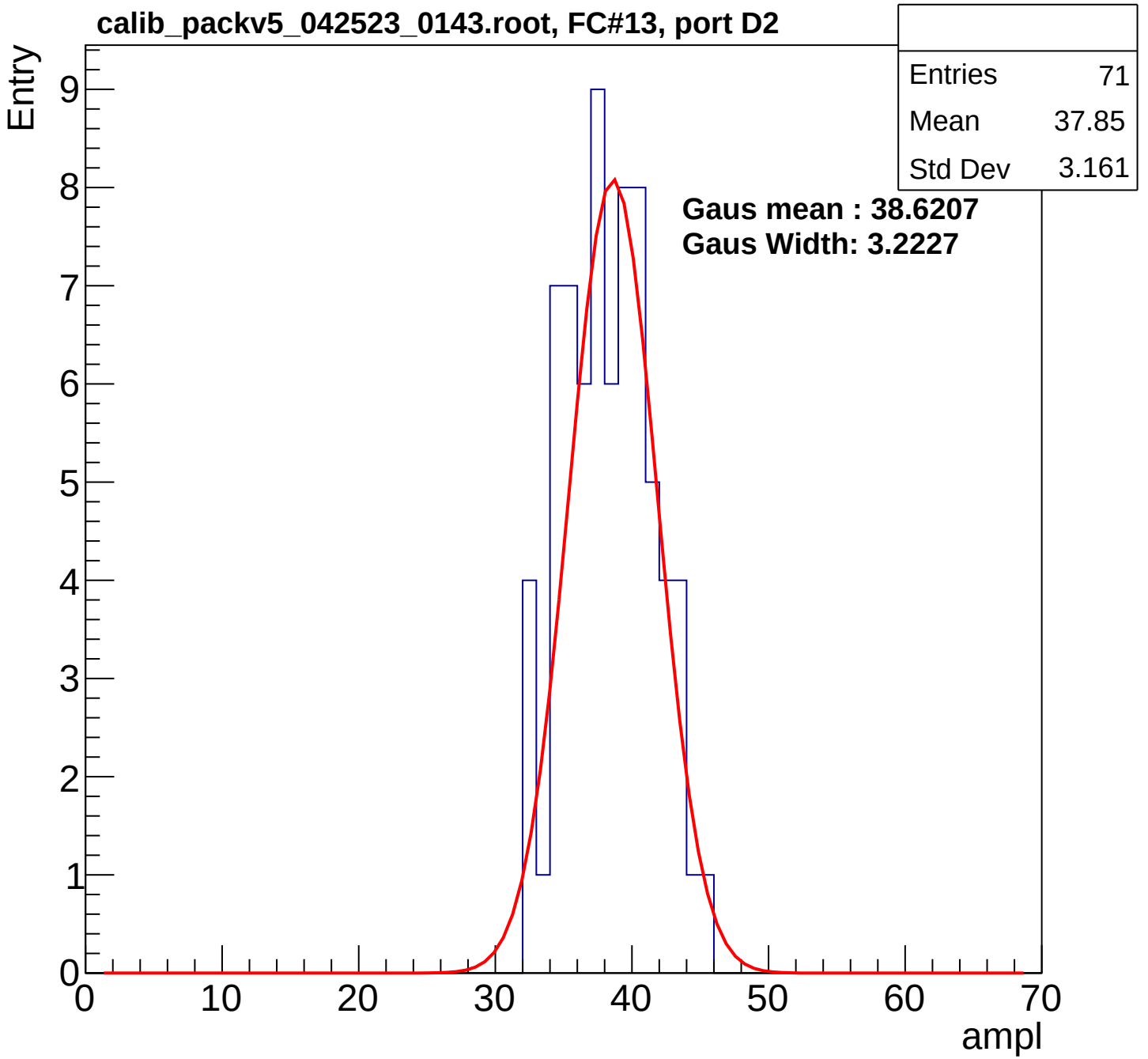
B1L003S, U10-ch112, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 37.85
Std Dev 3.161
Gaus mean : 38.6207
Gaus Width: 3.2227
Entry
ampl
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1
2
3
4
5
6
7
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0
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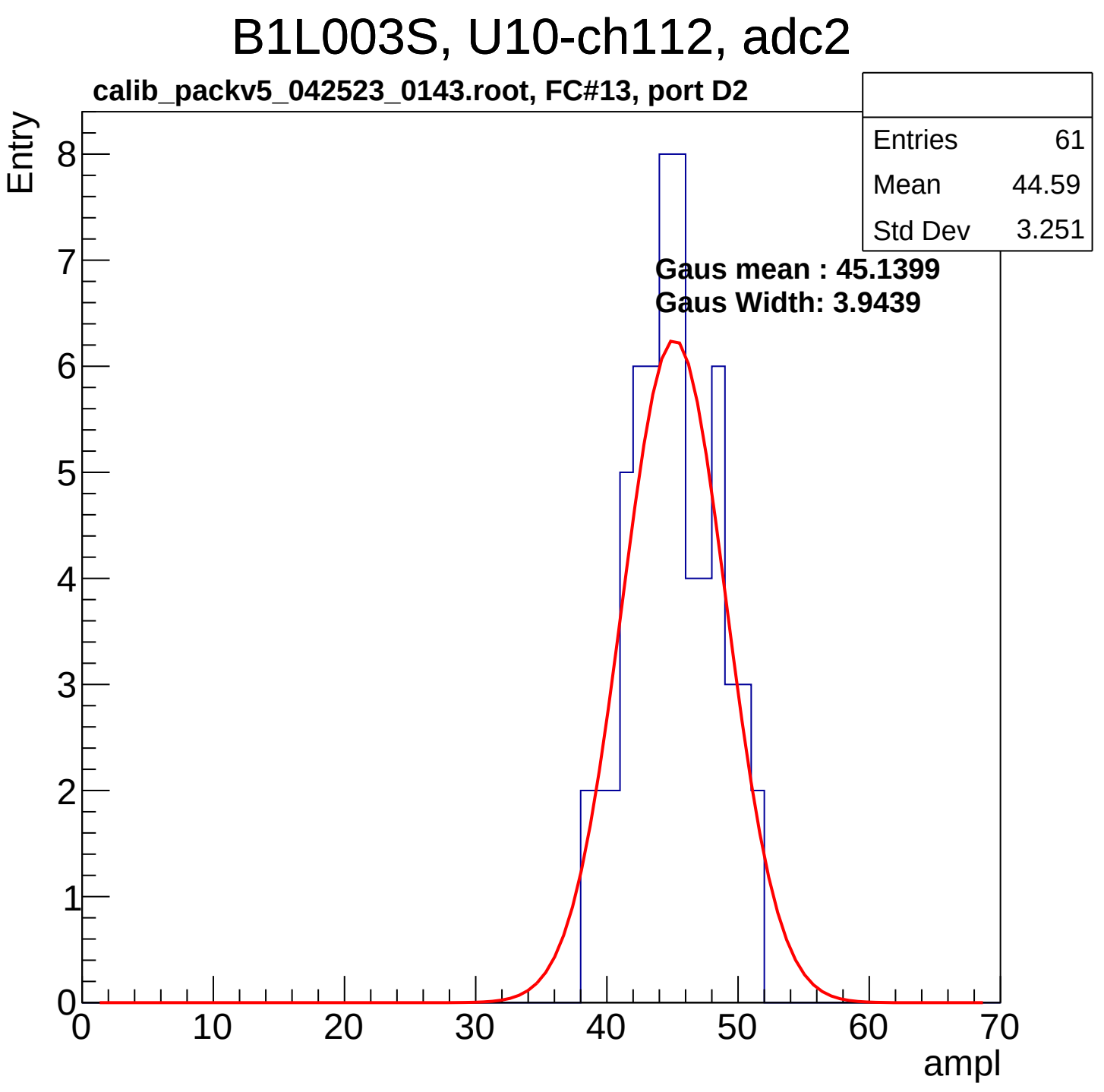
B1L003S, U10-ch112, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 44.59
Std Dev 3.251
Gaus mean : 45.1399
Gaus Width: 3.9439
Entry
ampl
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1
2
3
4
5
6
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0
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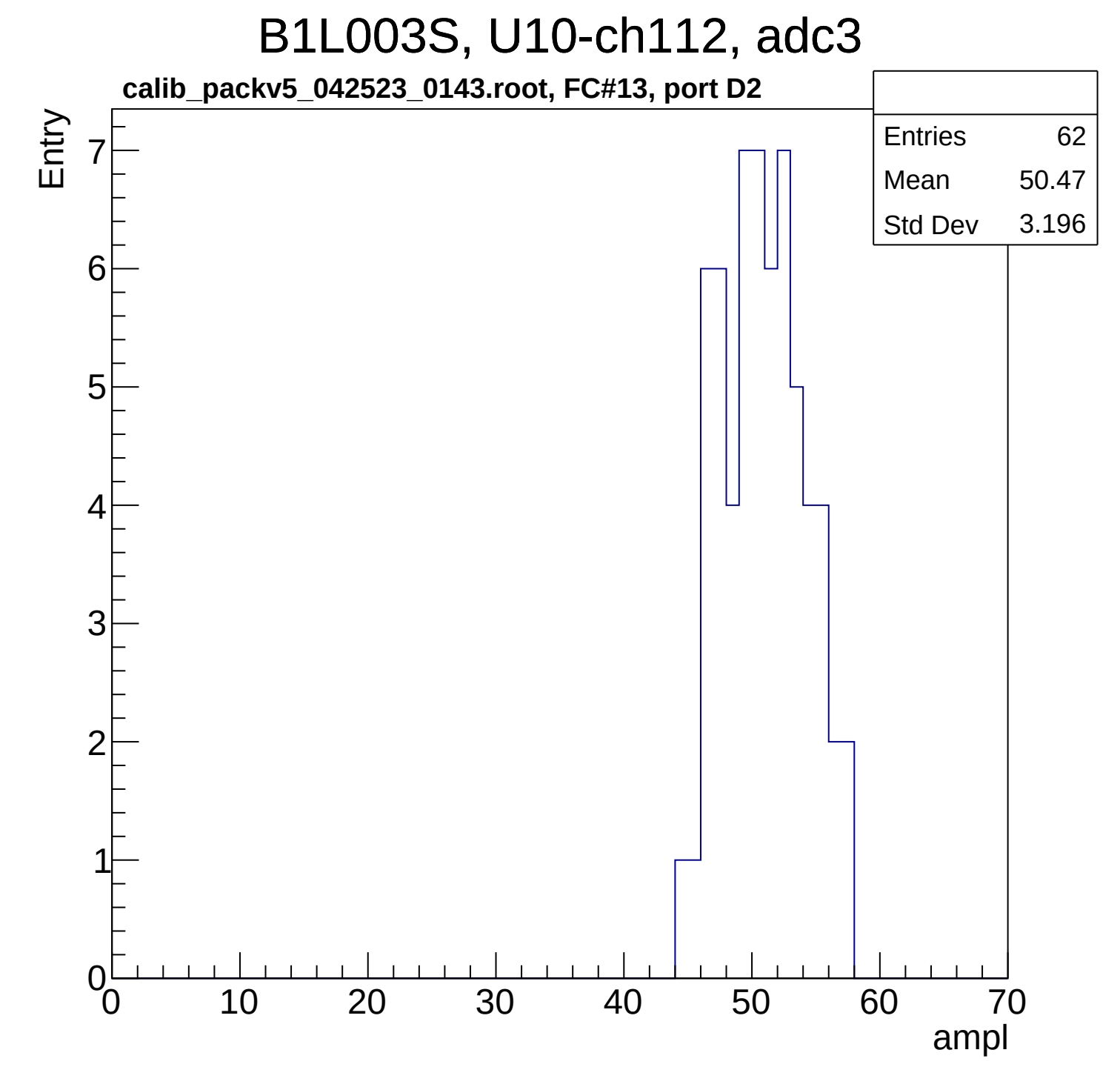
B1L003S, U10-ch112, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 50.47
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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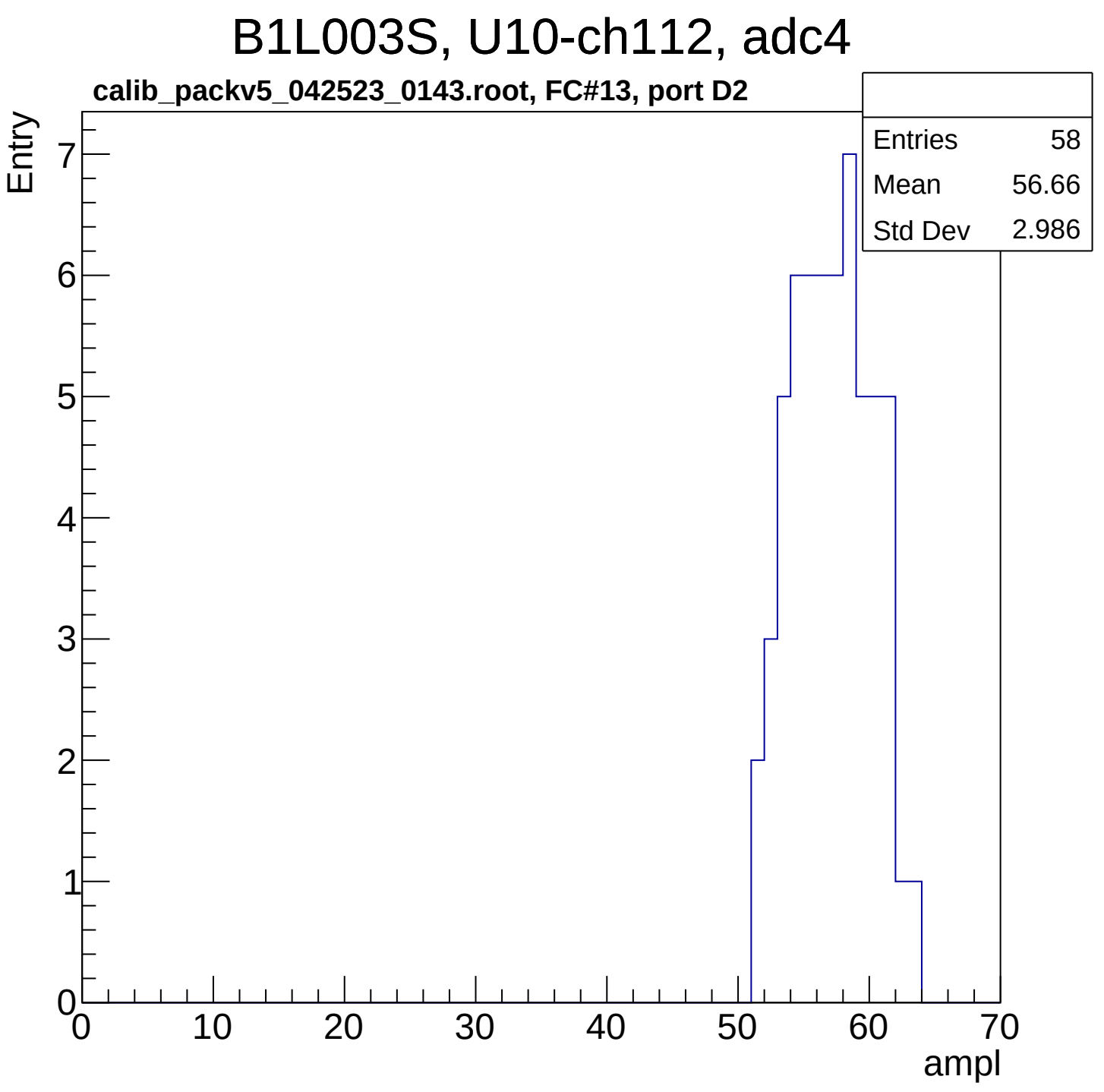
B1L003S, U10-ch112, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 56.66
Std Dev 2.986
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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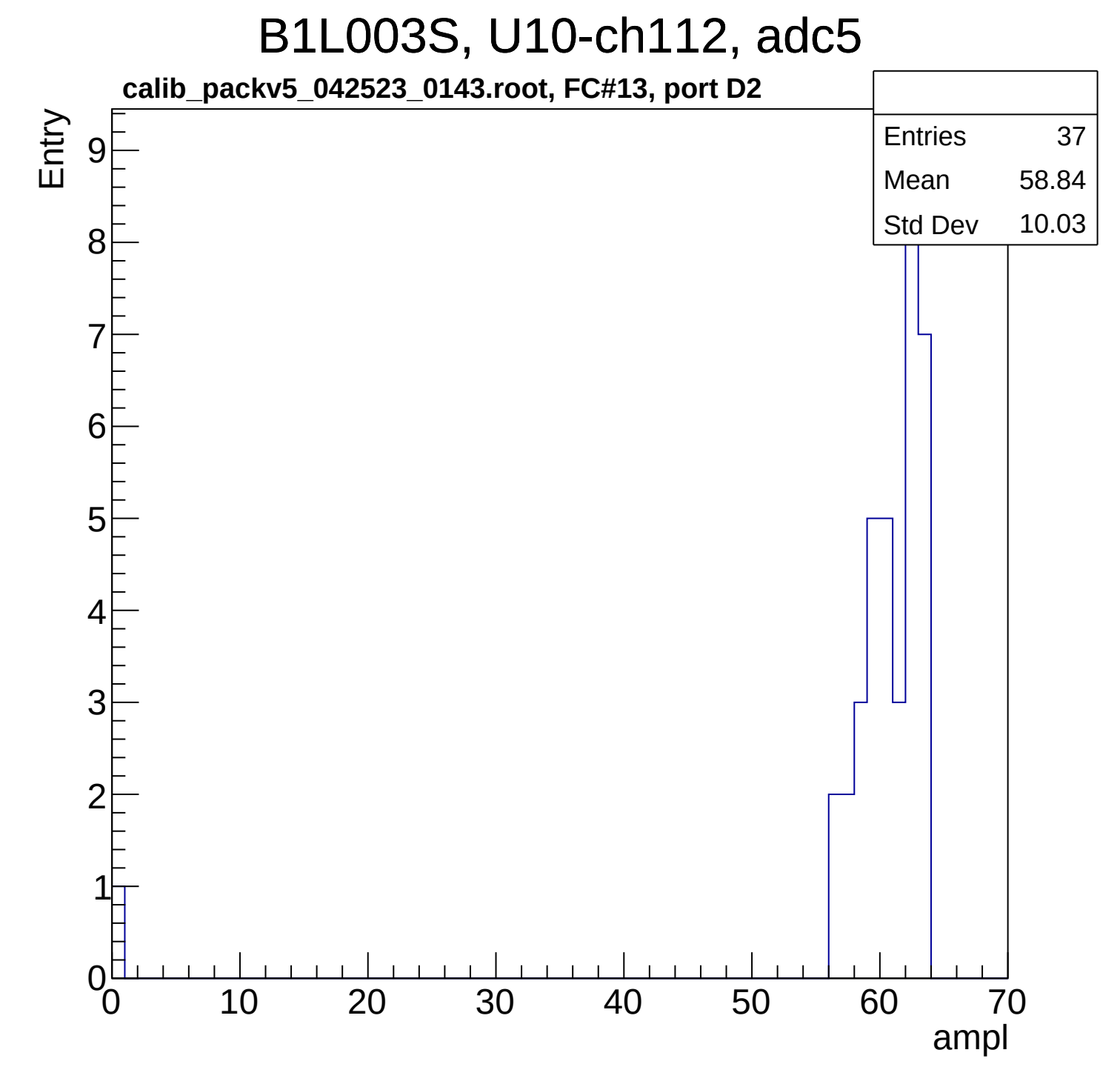
B1L003S, U10-ch112, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 58.84
Std Dev 10.03
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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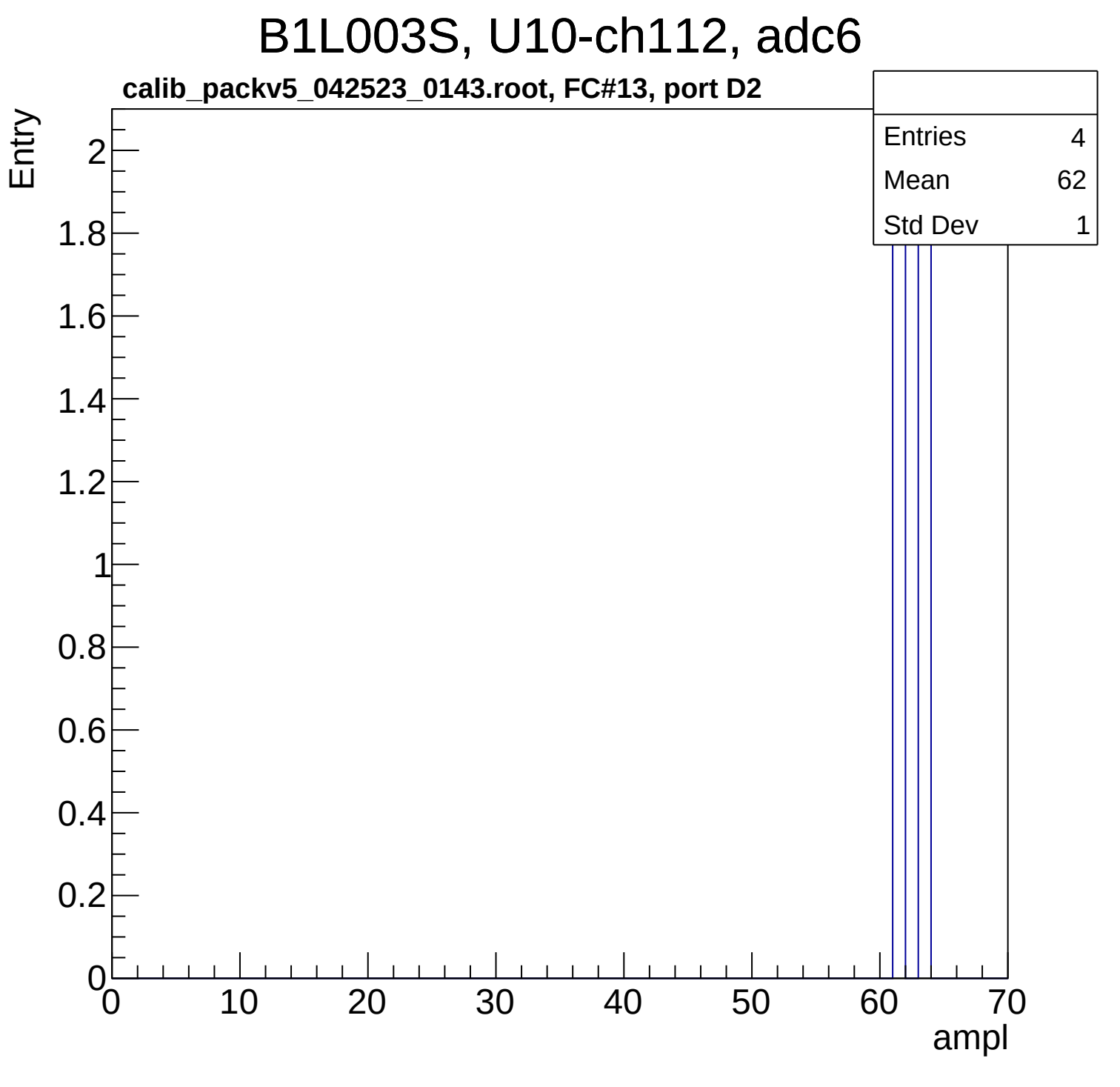
B1L003S, U10-ch112, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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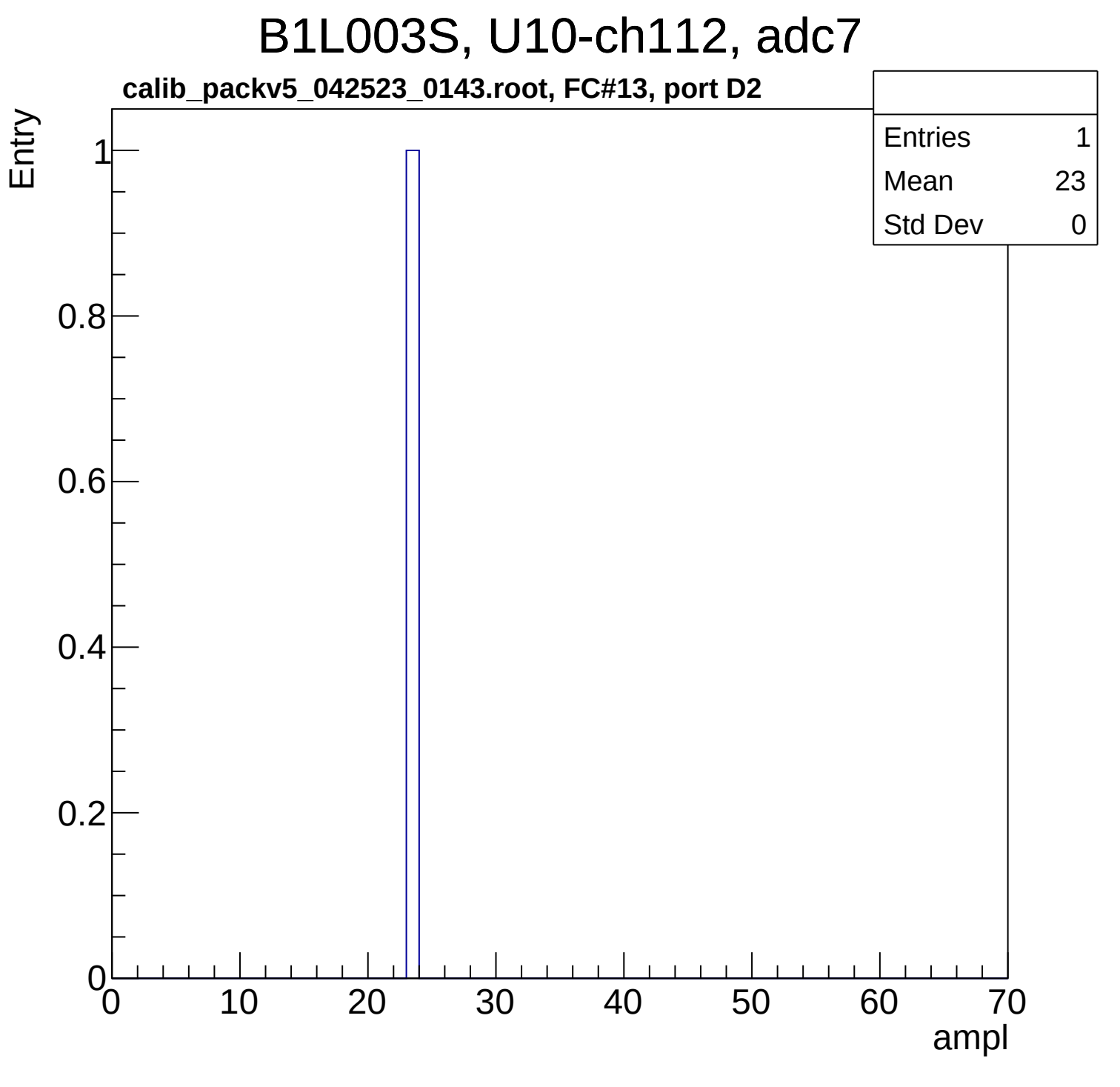
B1L003S, U10-ch112, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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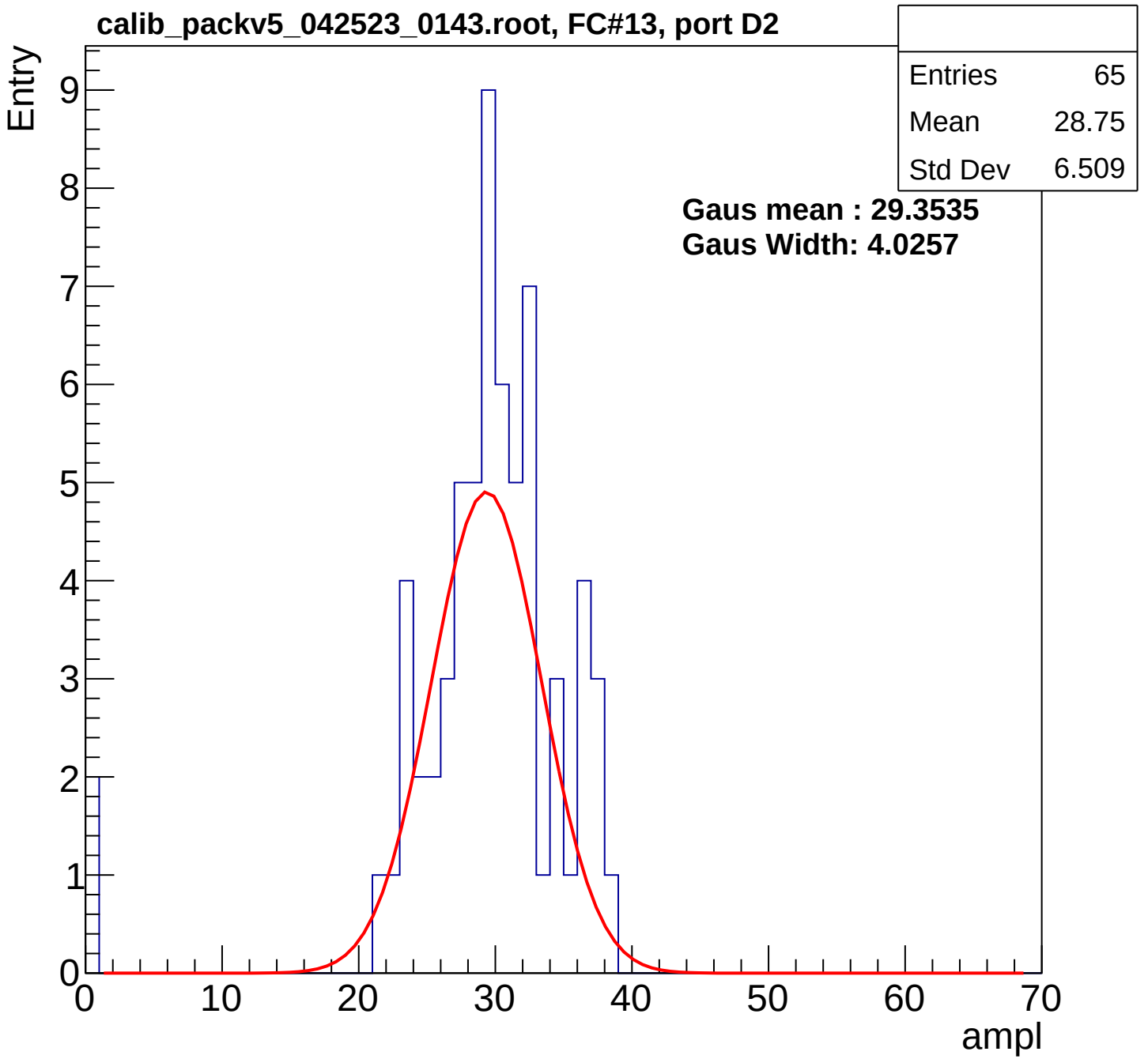
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch113, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.75
Std Dev 6.509
Gaus mean : 29.3535
Gaus Width: 4.0257
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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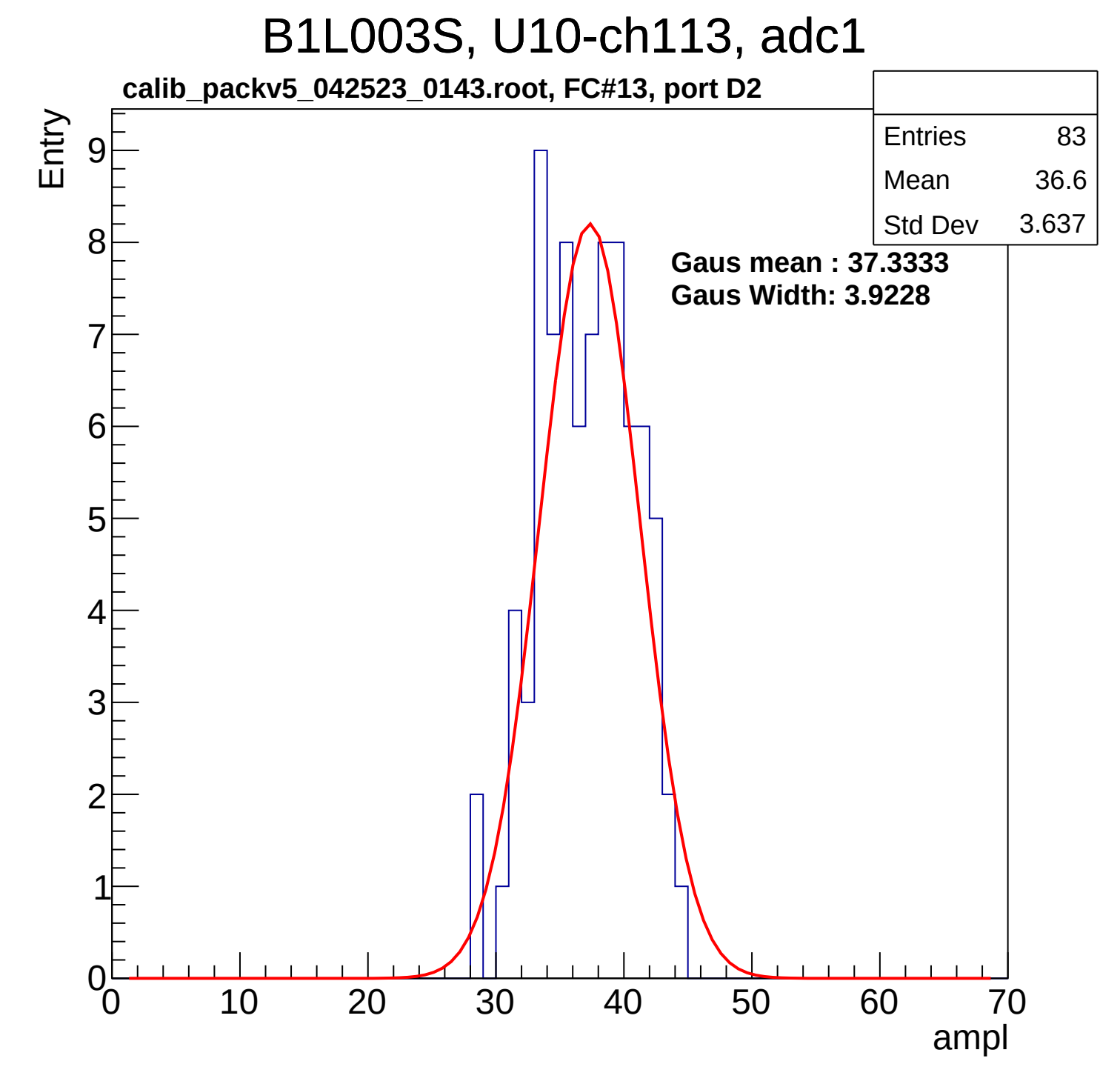
B1L003S, U10-ch113, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 36.6
Std Dev 3.637
Gaus mean : 37.3333
Gaus Width: 3.9228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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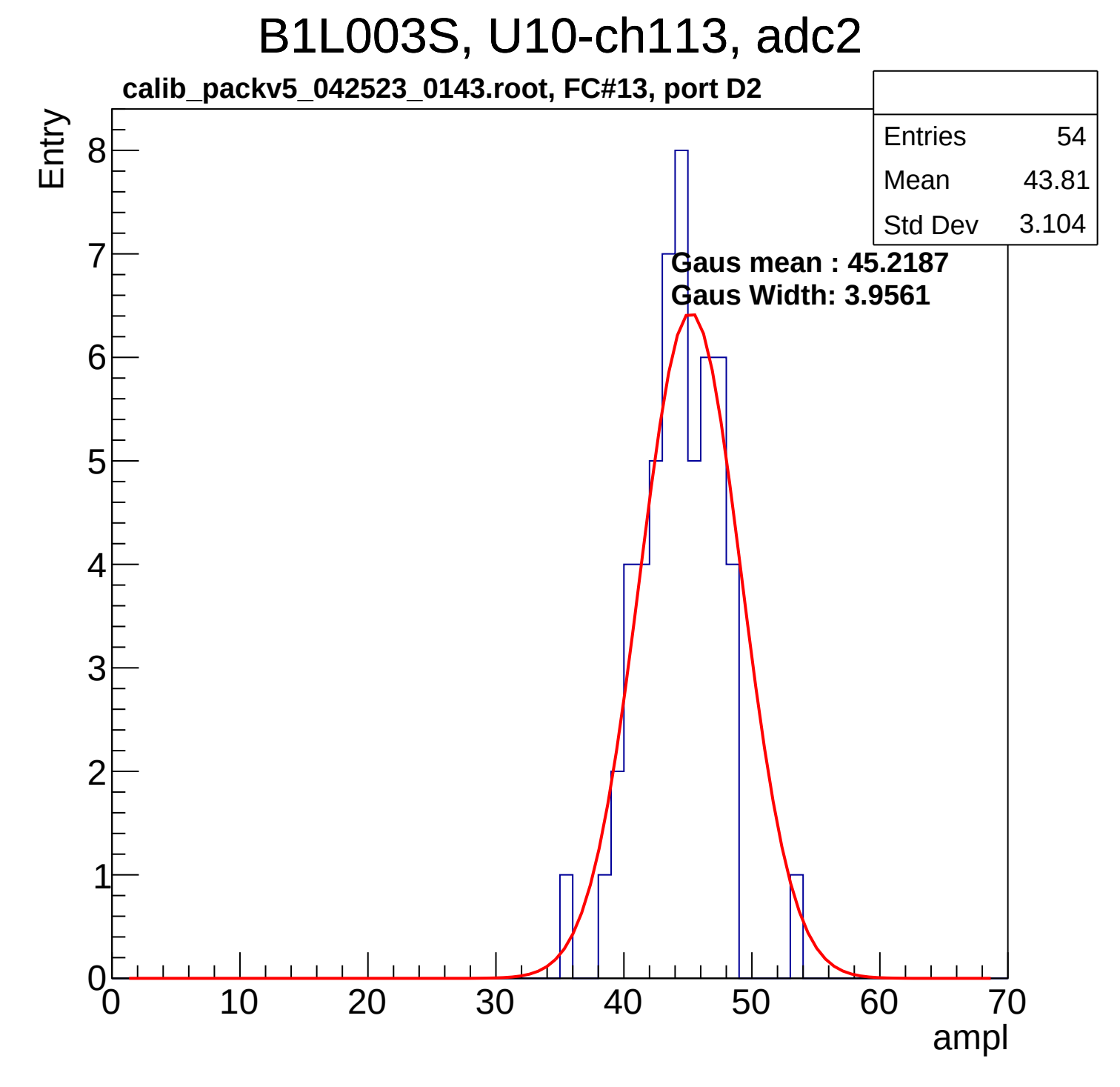
B1L003S, U10-ch113, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 43.81
Std Dev 3.104
Gaus mean : 45.2187
Gaus Width: 3.9561
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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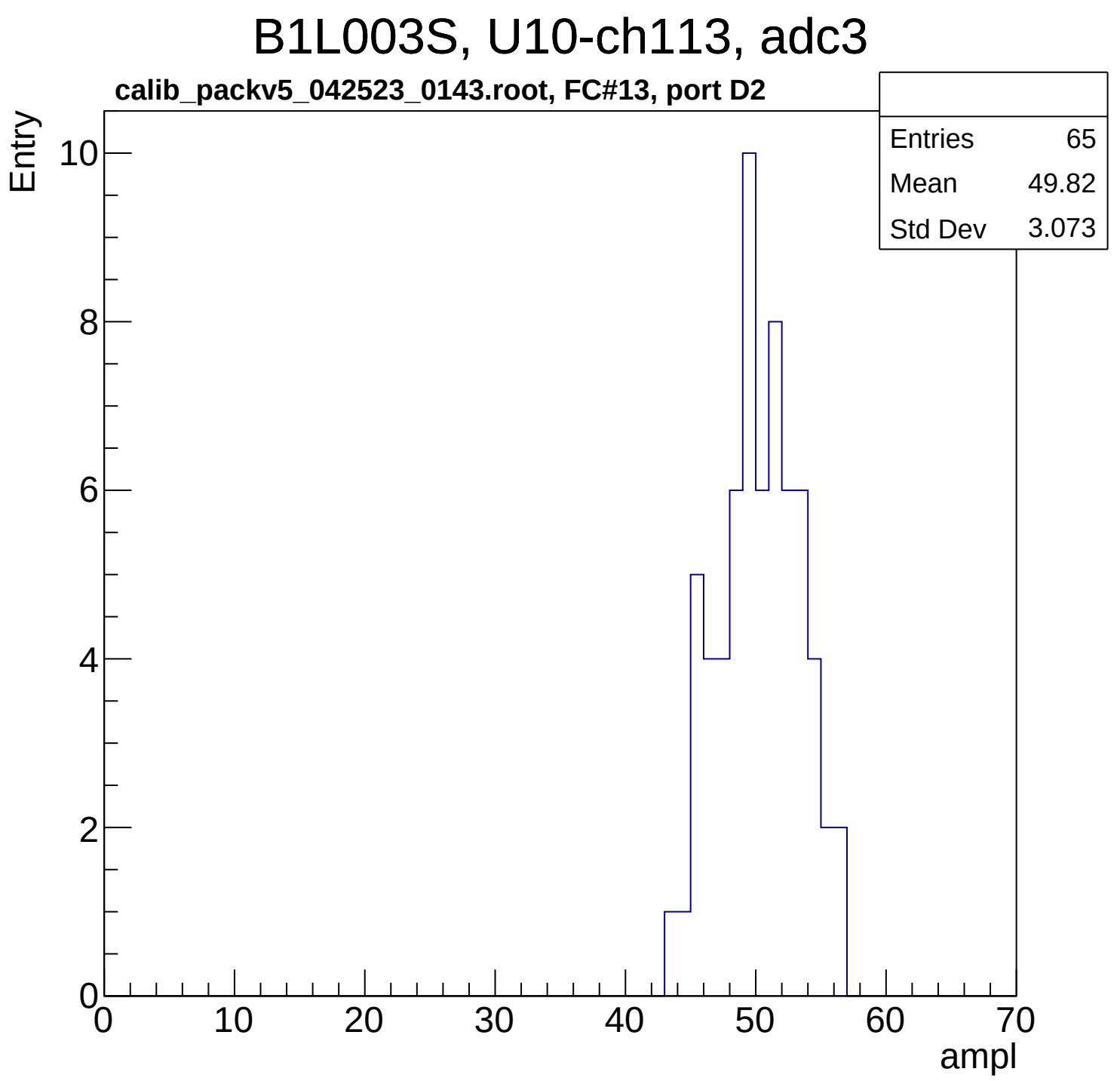
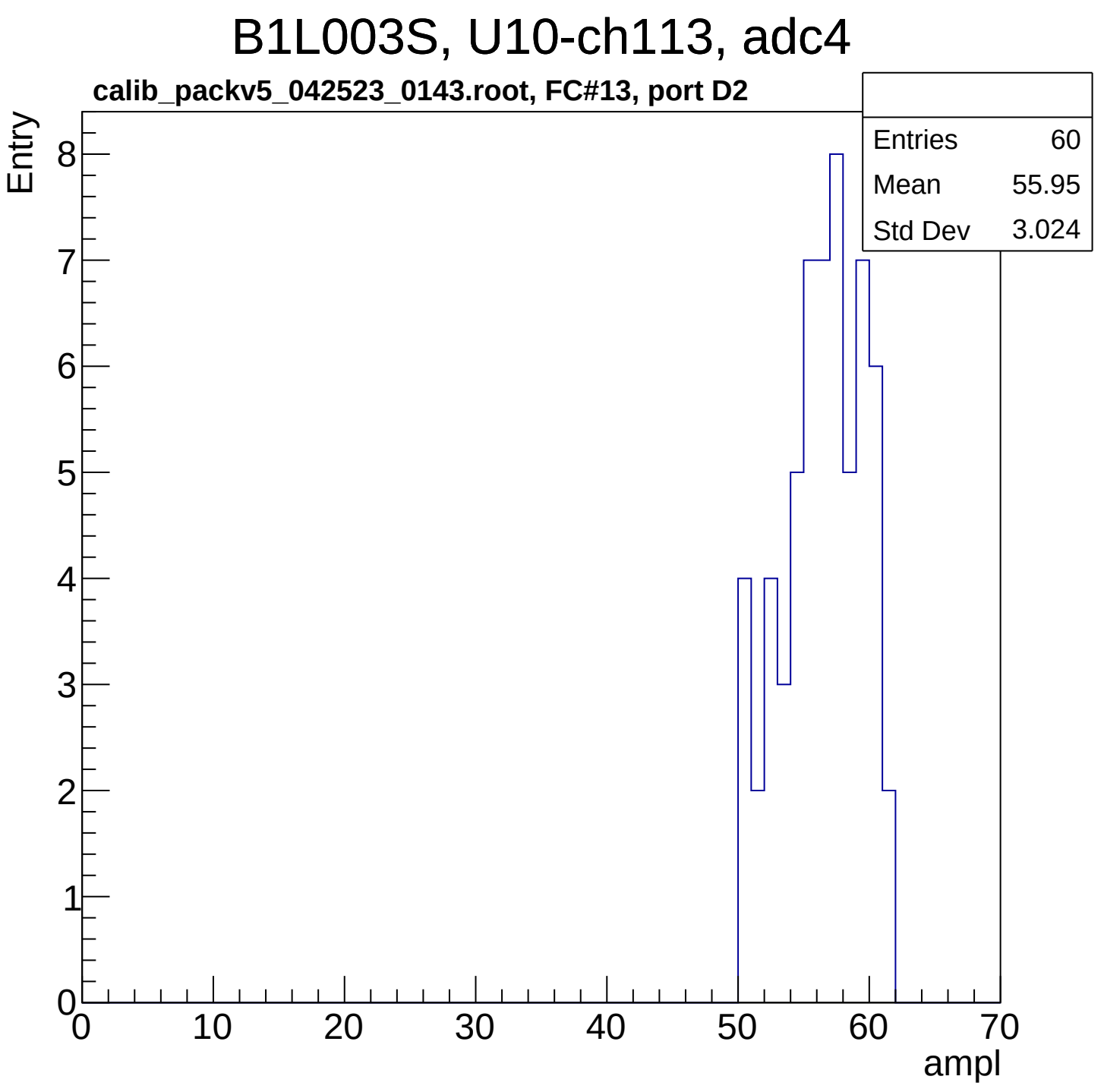

B1L003S, U10-ch113, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 49.82
Std Dev 3.073
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch113, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.95
Std Dev 3.024
Entry
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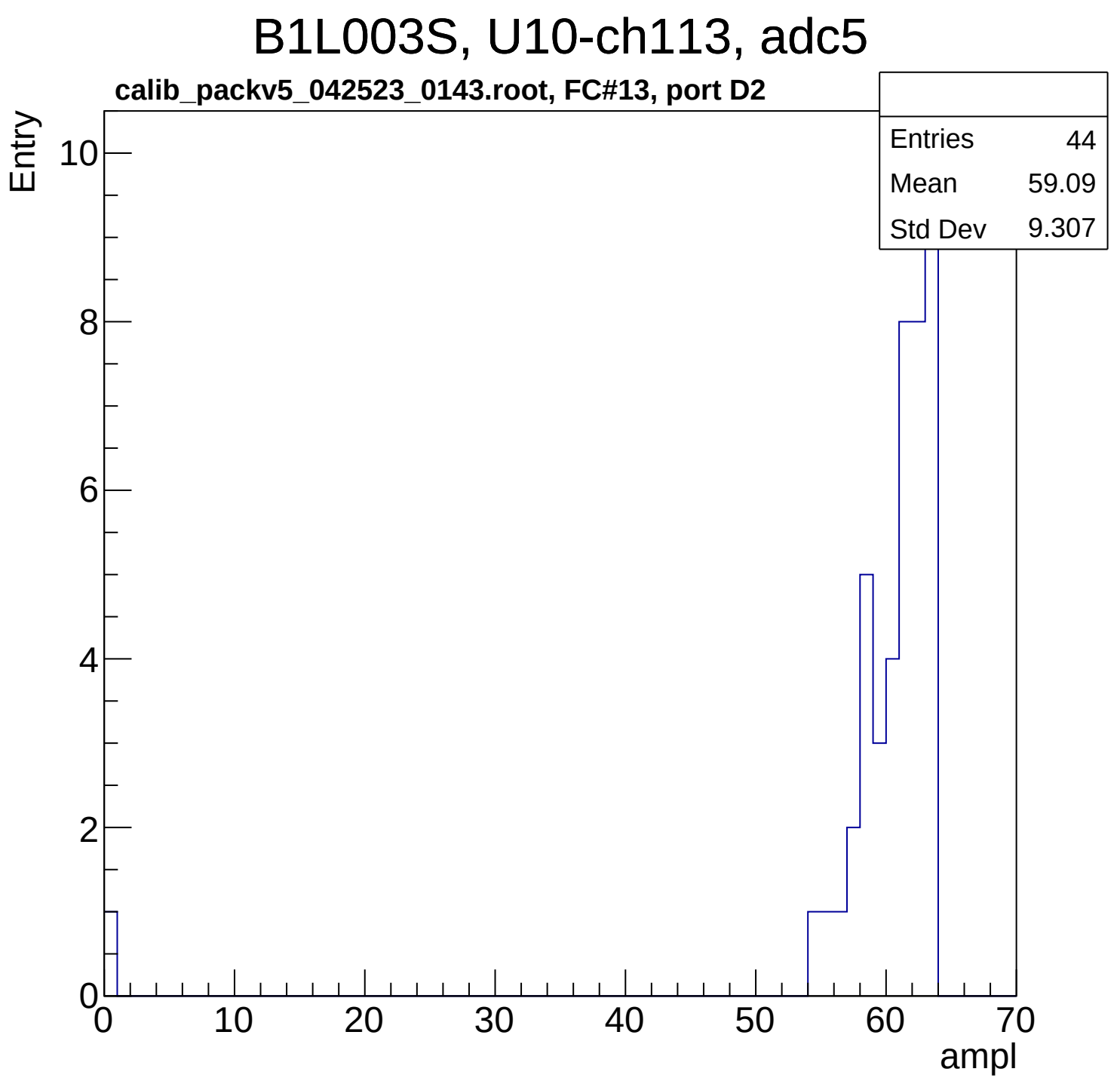
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch113, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.09
Std Dev 9.307
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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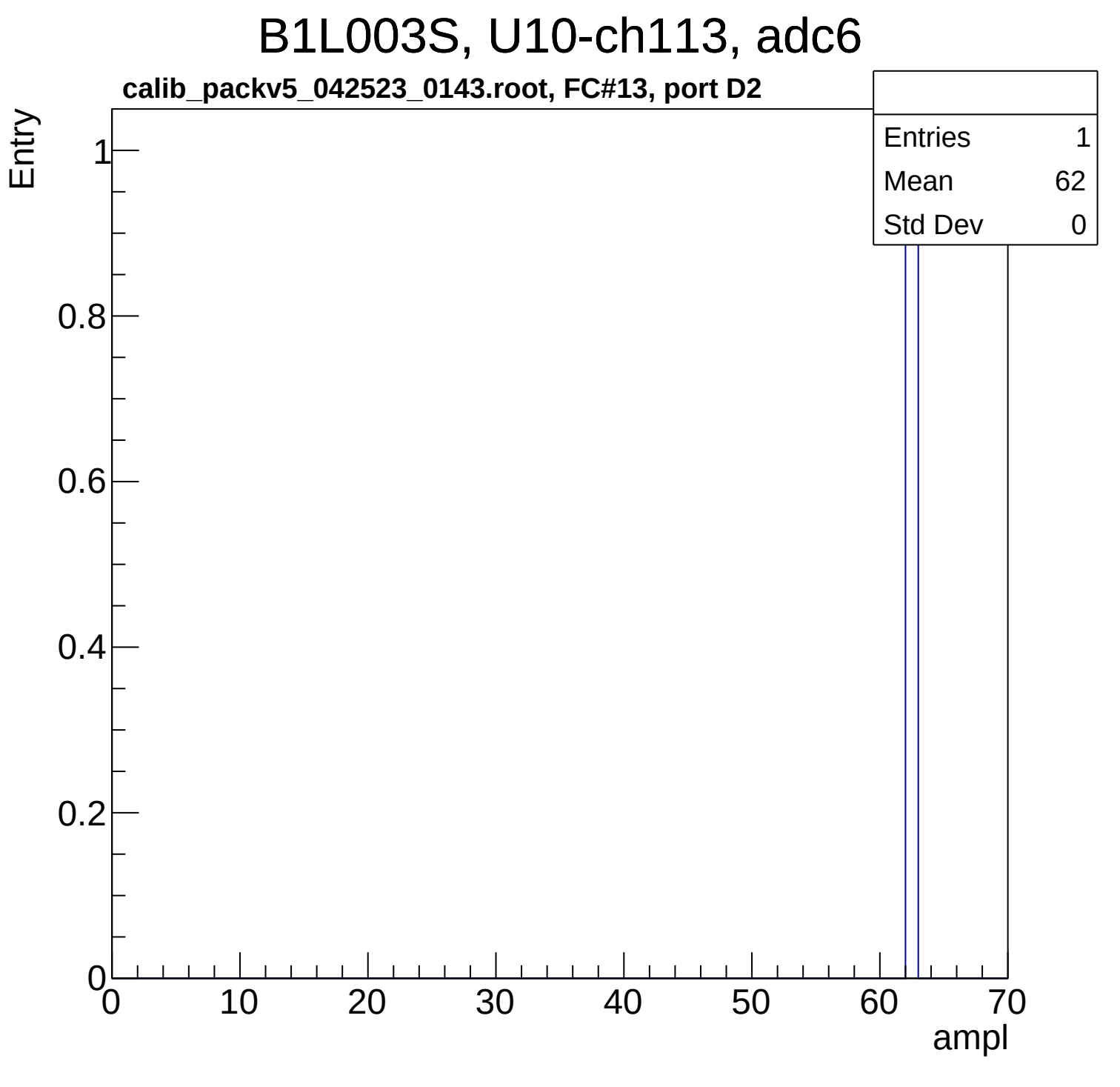
B1L003S, U10-ch113, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
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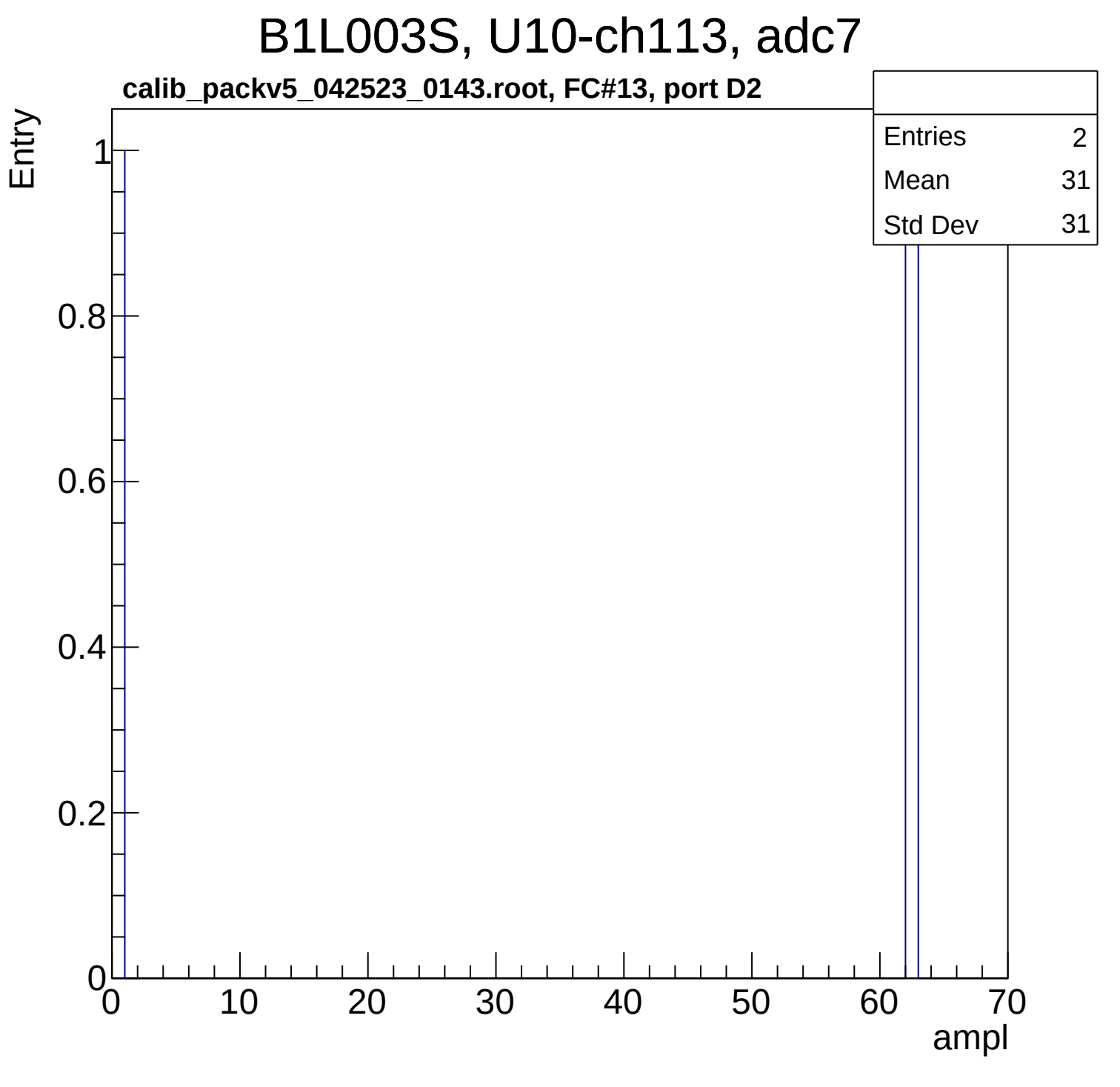
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch113, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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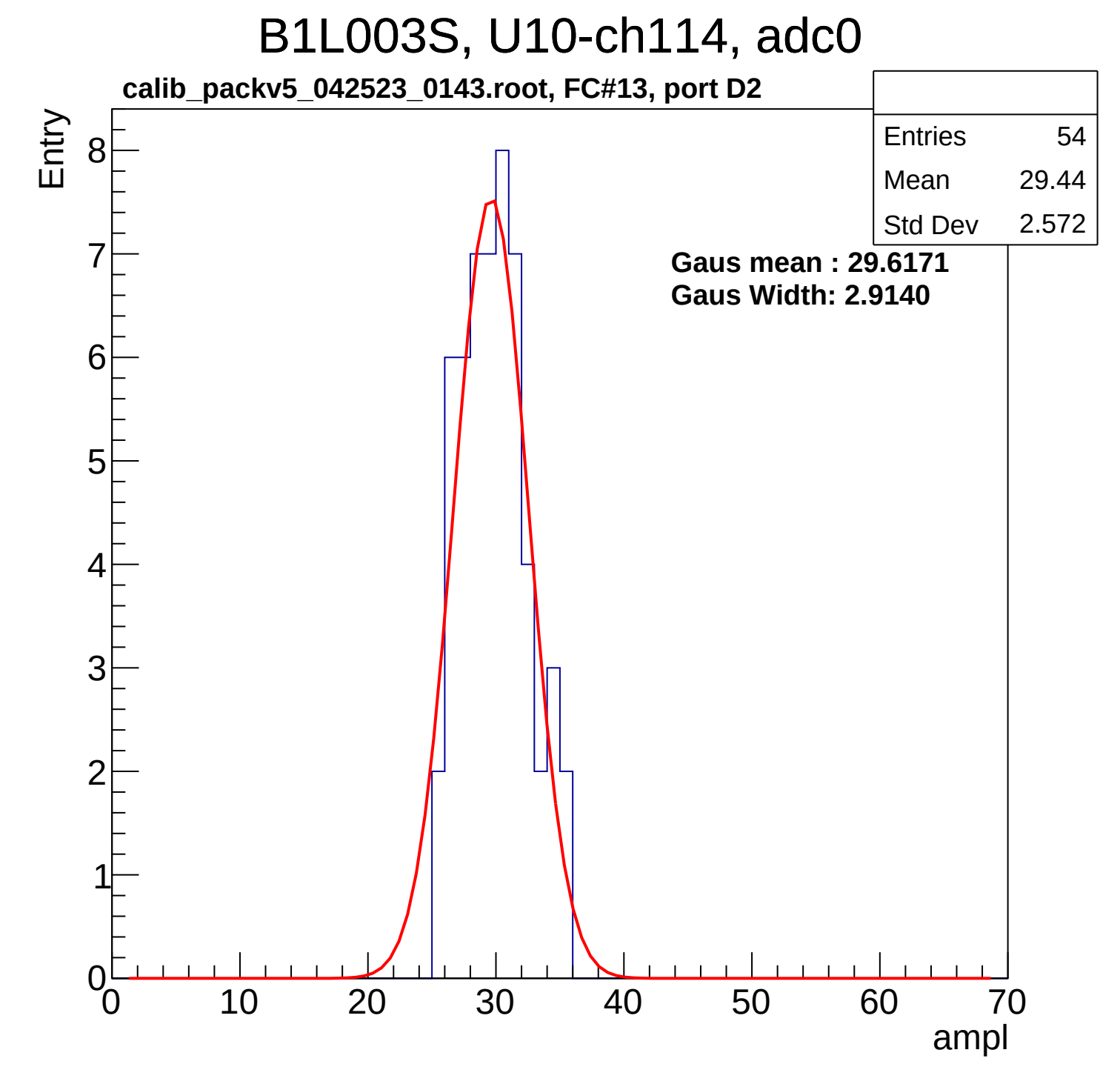
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch114, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.44
Std Dev 2.572
Gaus mean : 29.6171
Gaus Width: 2.9140
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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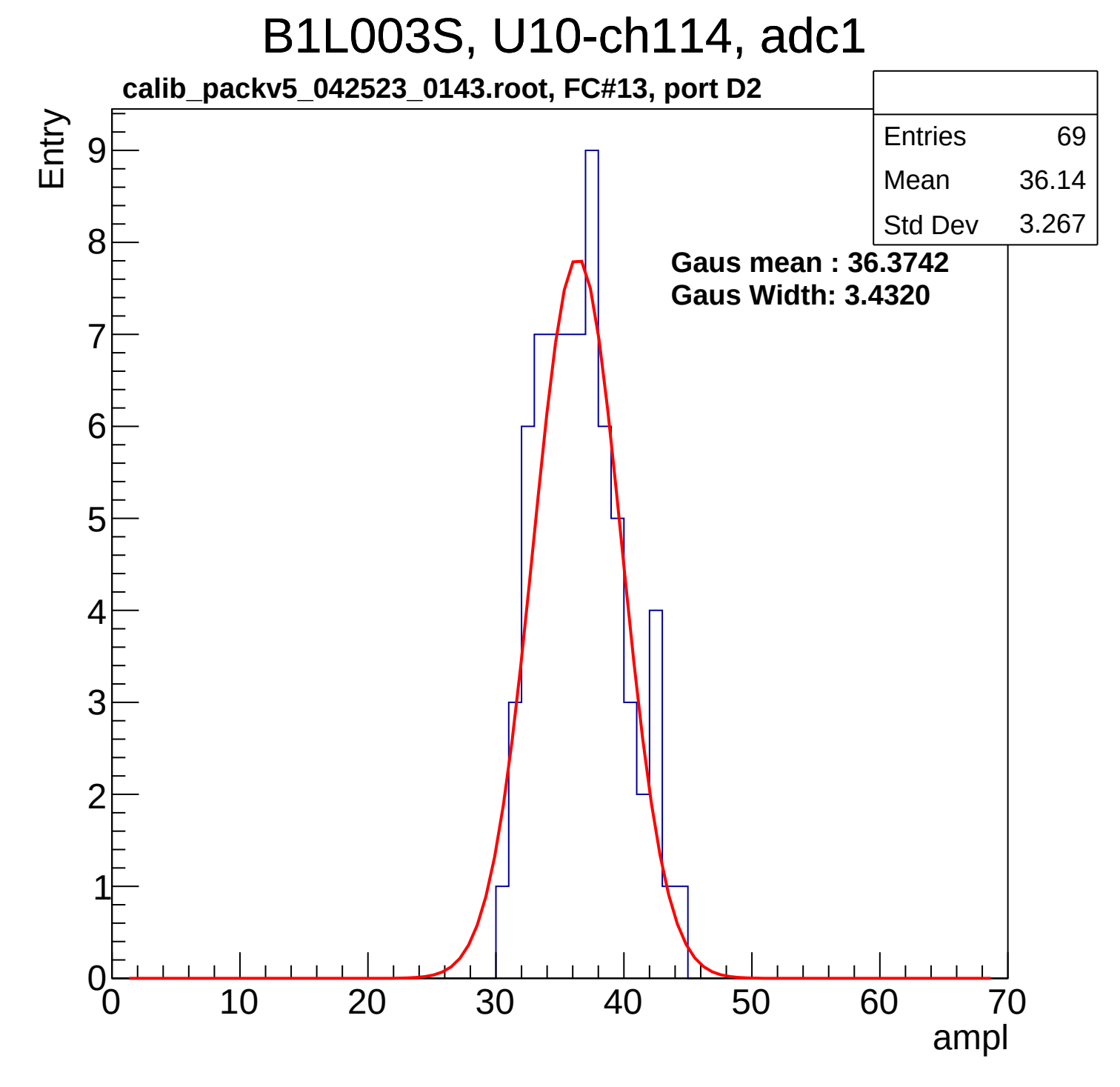
B1L003S, U10-ch114, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36.14
Std Dev 3.267
Gaus mean : 36.3742
Gaus Width: 3.4320
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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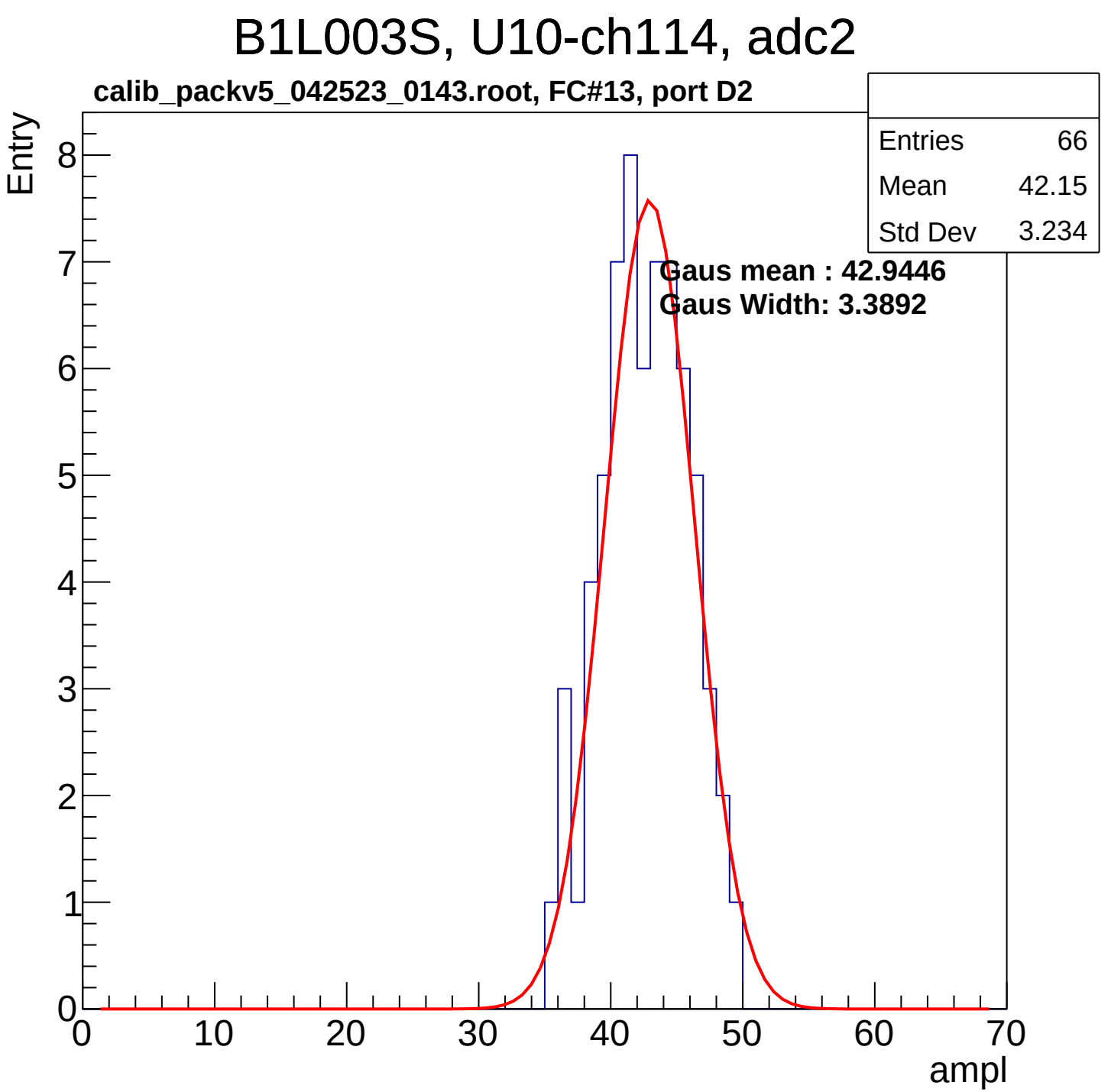
B1L003S, U10-ch114, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.15
Std Dev 3.234
Gaus mean : 42.9446
Gaus Width: 3.3892
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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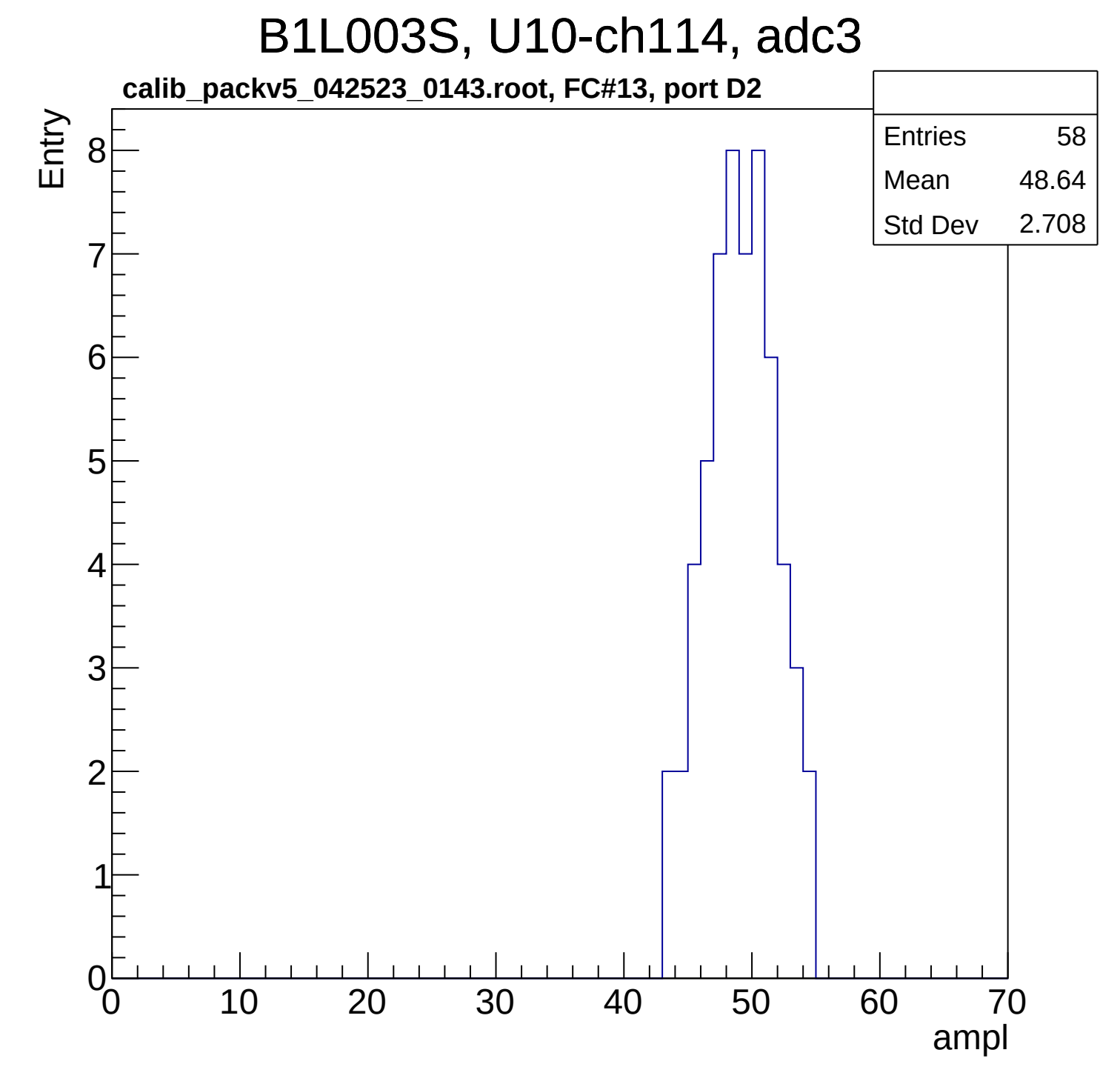
B1L003S, U10-ch114, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.64
Std Dev 2.708
Entry
ampl
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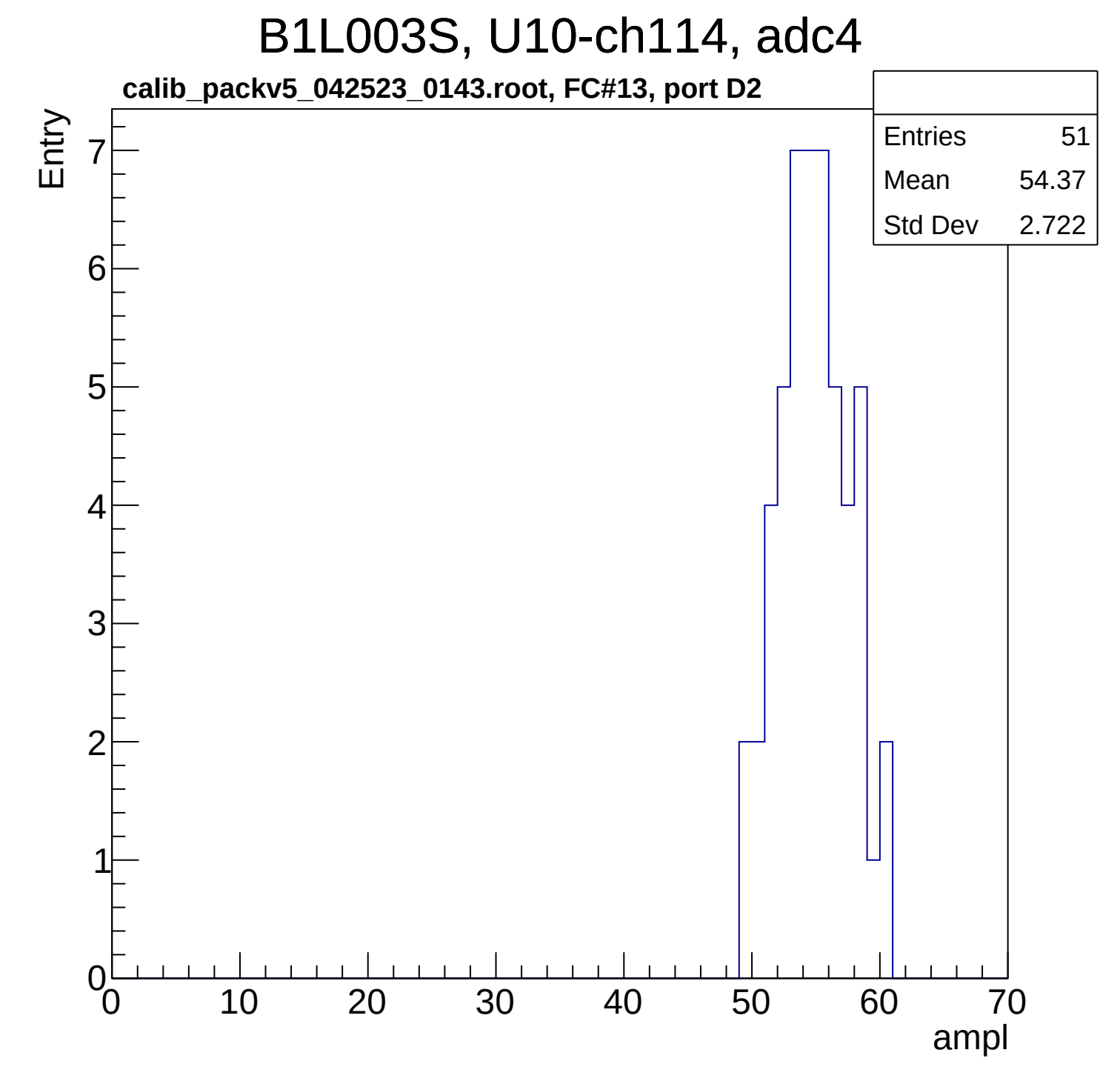
B1L003S, U10-ch114, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 54.37
Std Dev 2.722
Entry
ampl
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4
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0
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60
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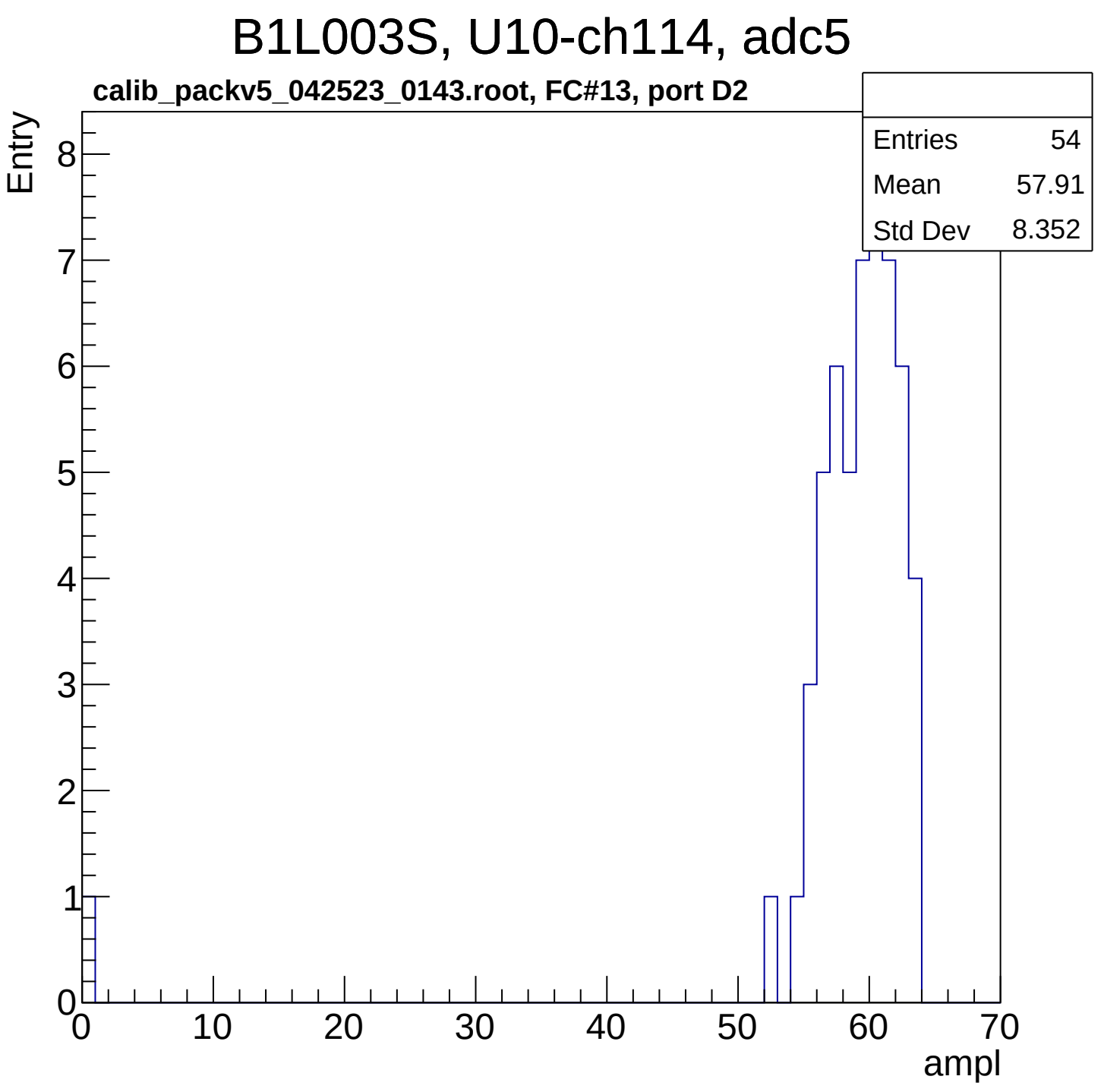
B1L003S, U10-ch114, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 57.91
Std Dev 8.352
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
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60
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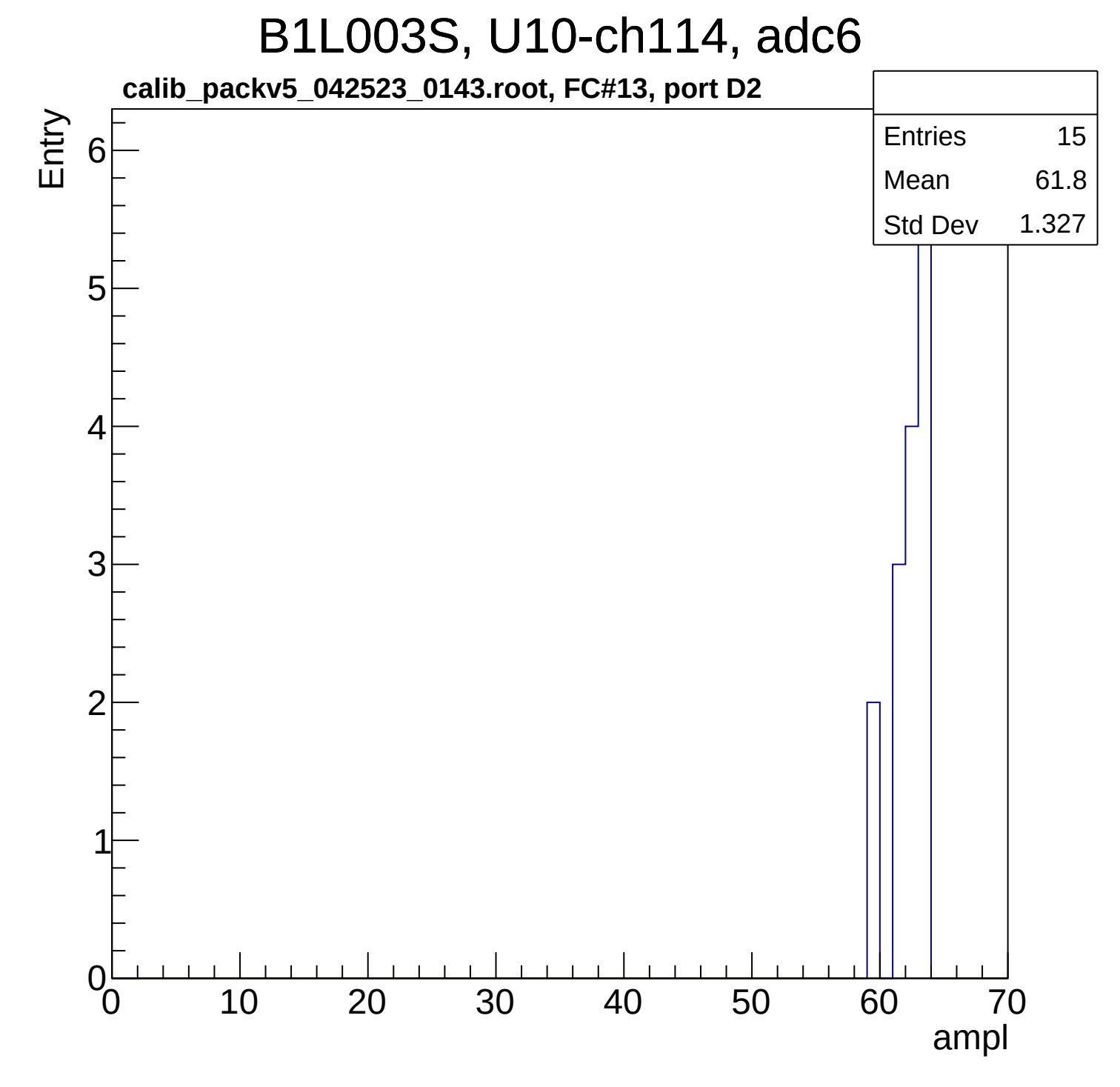
B1L003S, U10-ch114, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.8
Std Dev 1.327
Entry
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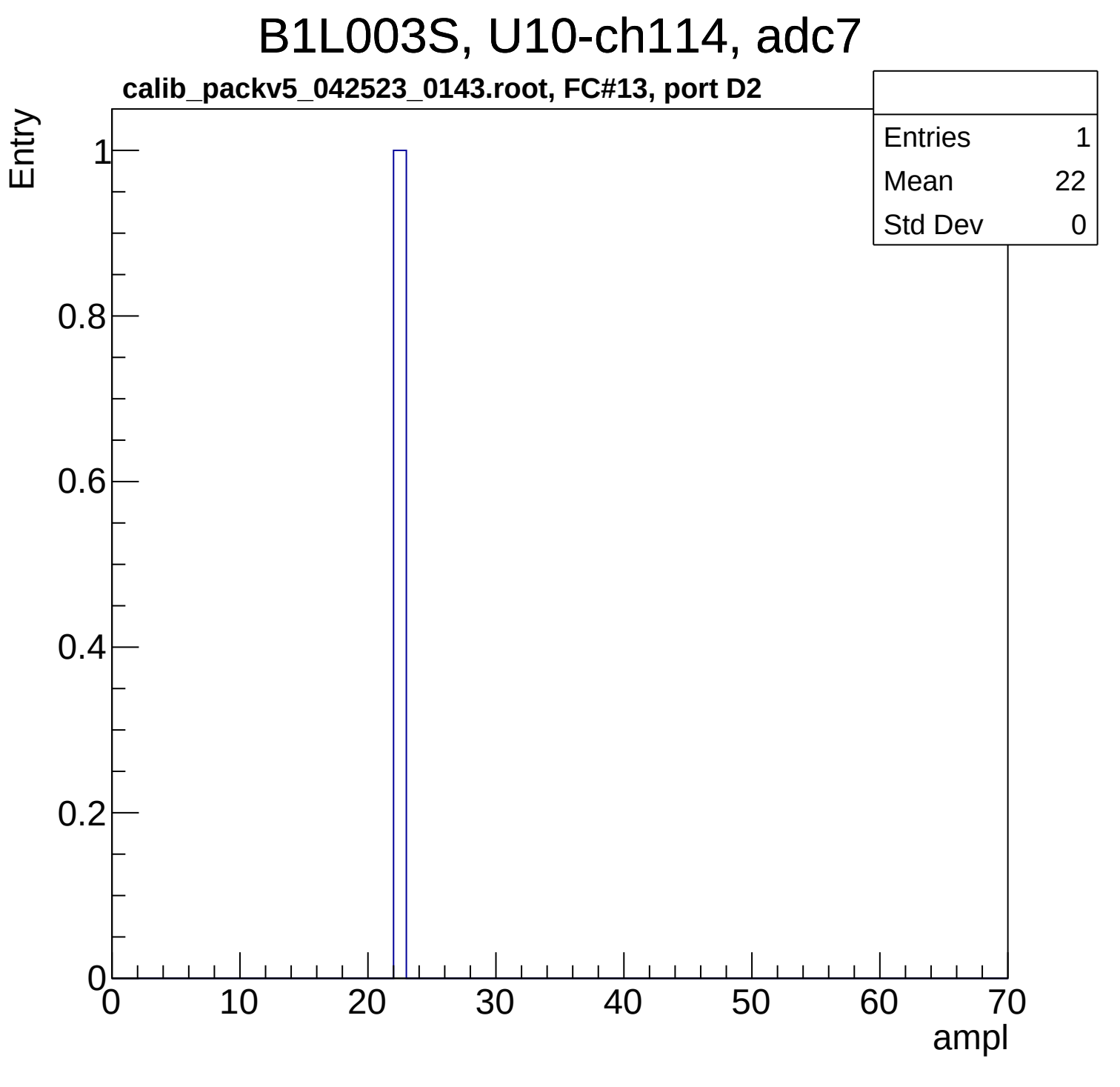
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch114, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
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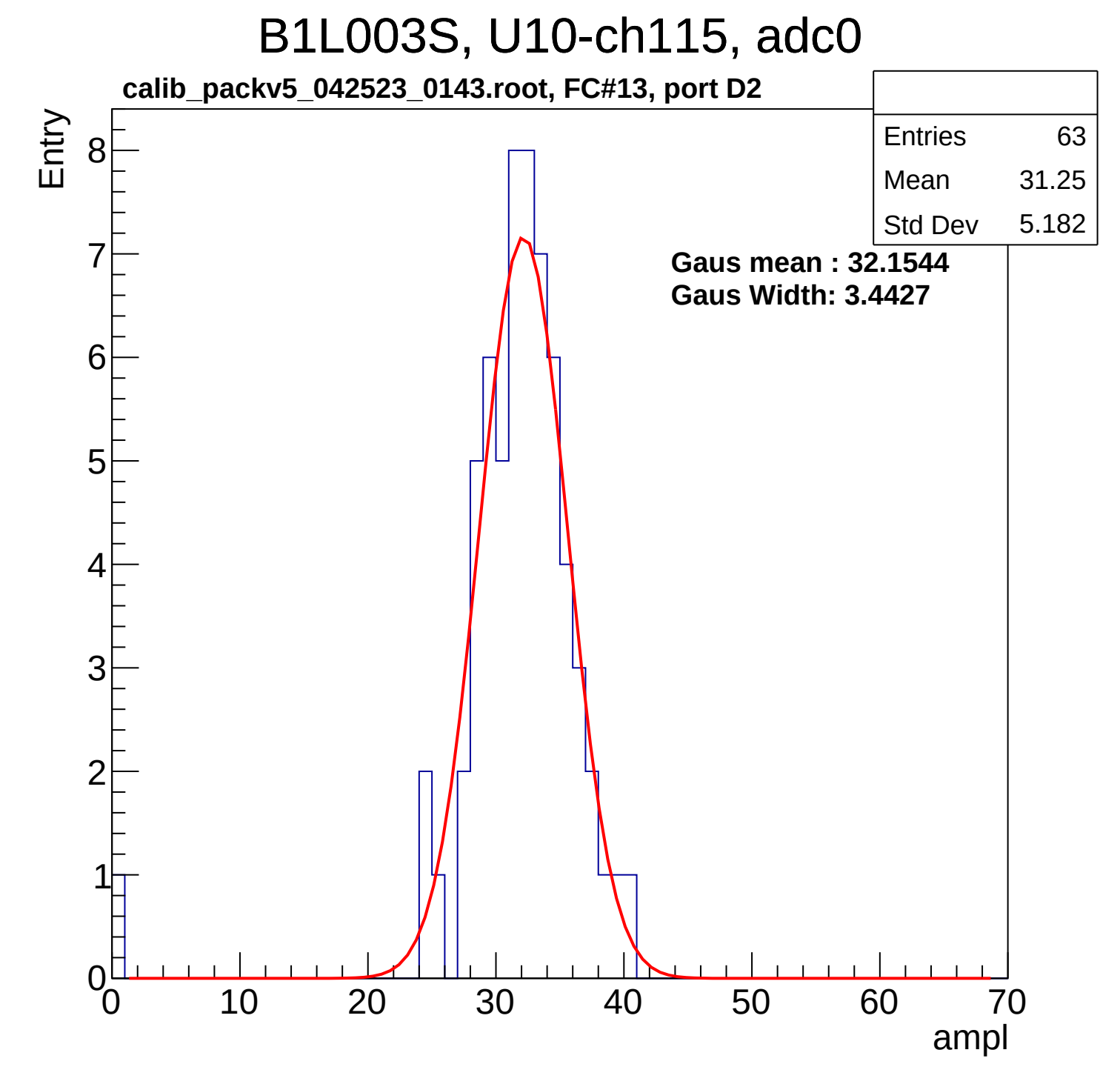
30
40
50
60
70
ampl


B1L003S, U10-ch115, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 31.25
Std Dev 5.182
Gaus mean : 32.1544
Gaus Width: 3.4427
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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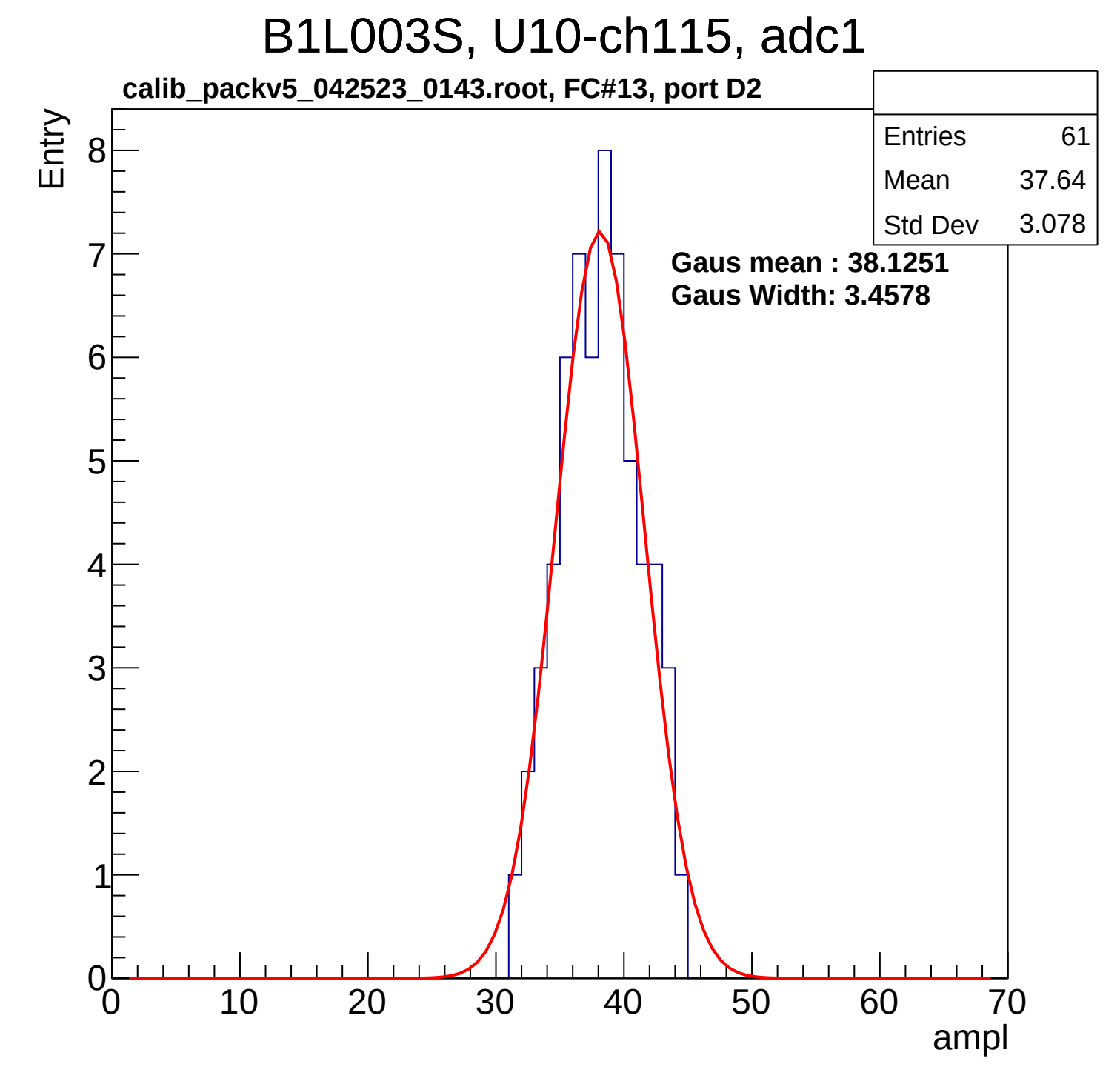
B1L003S, U10-ch115, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 37.64
Std Dev 3.078
Gaus mean : 38.1251
Gaus Width: 3.4578
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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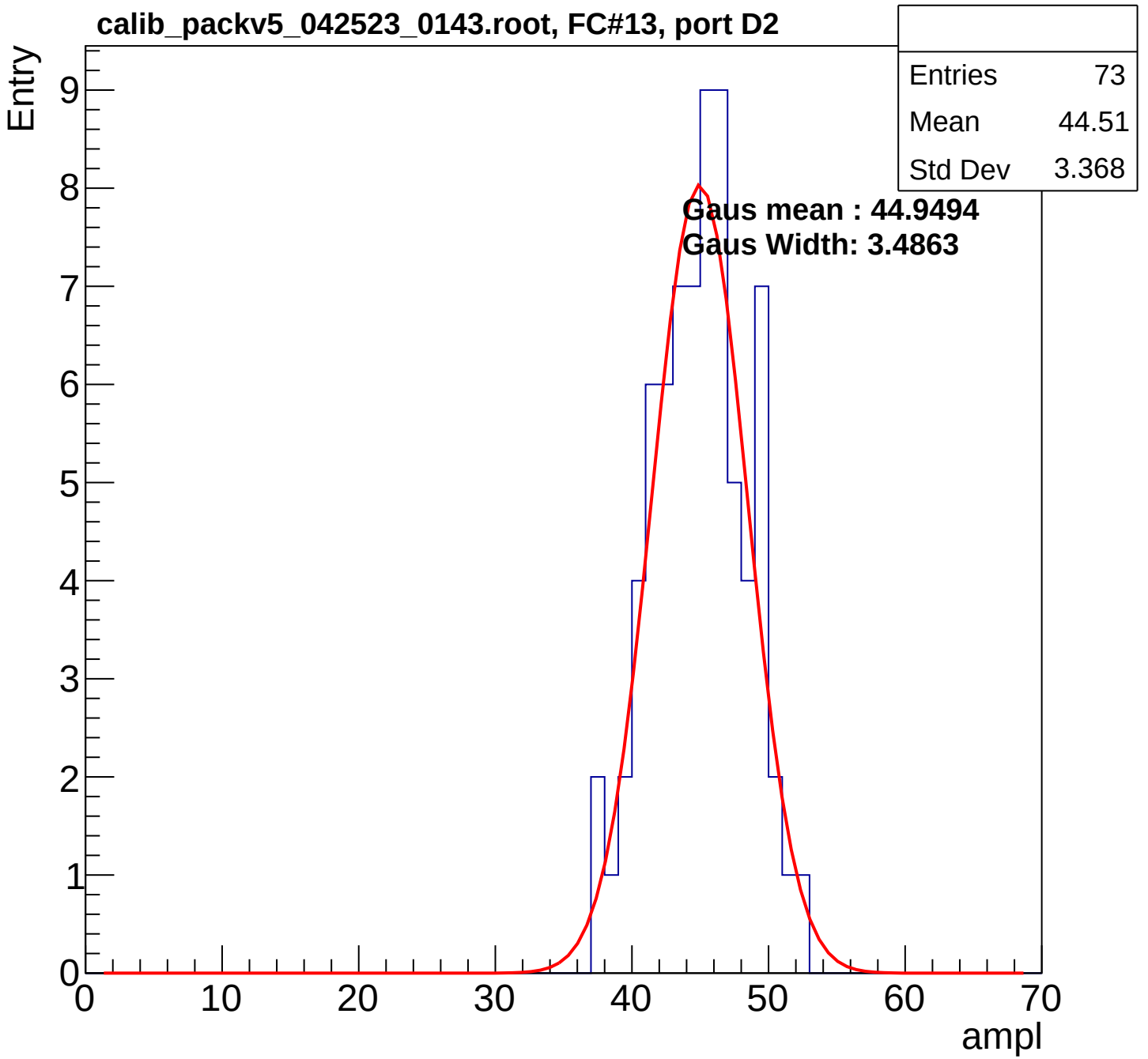
B1L003S, U10-ch115, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 44.51
Std Dev 3.368
Gaus mean : 44.9494
Gaus Width: 3.4863
Entry
ampl
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3
4
5
6
7
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9
0
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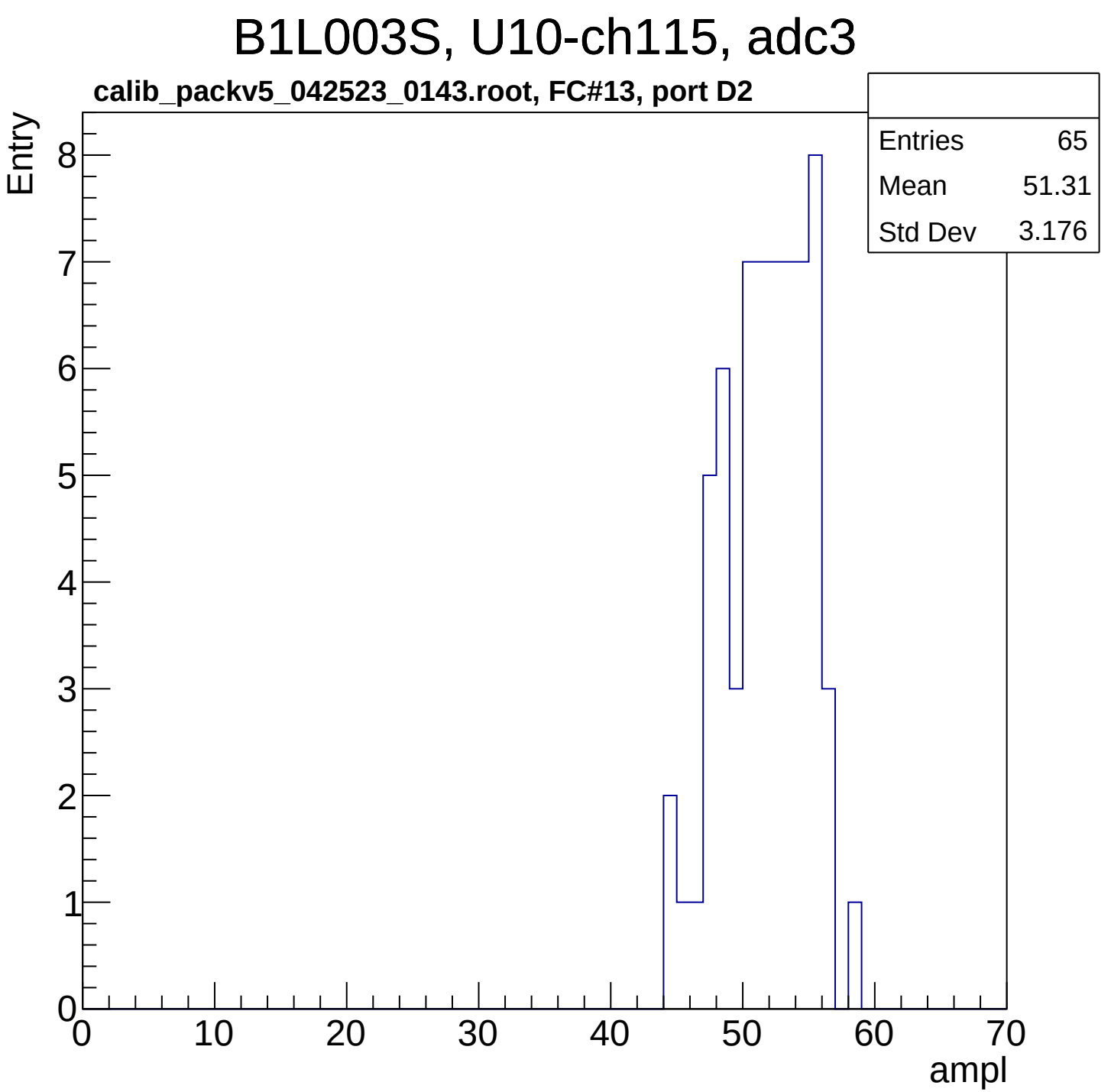
B1L003S, U10-ch115, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 51.31
Std Dev 3.176
Entry
ampl
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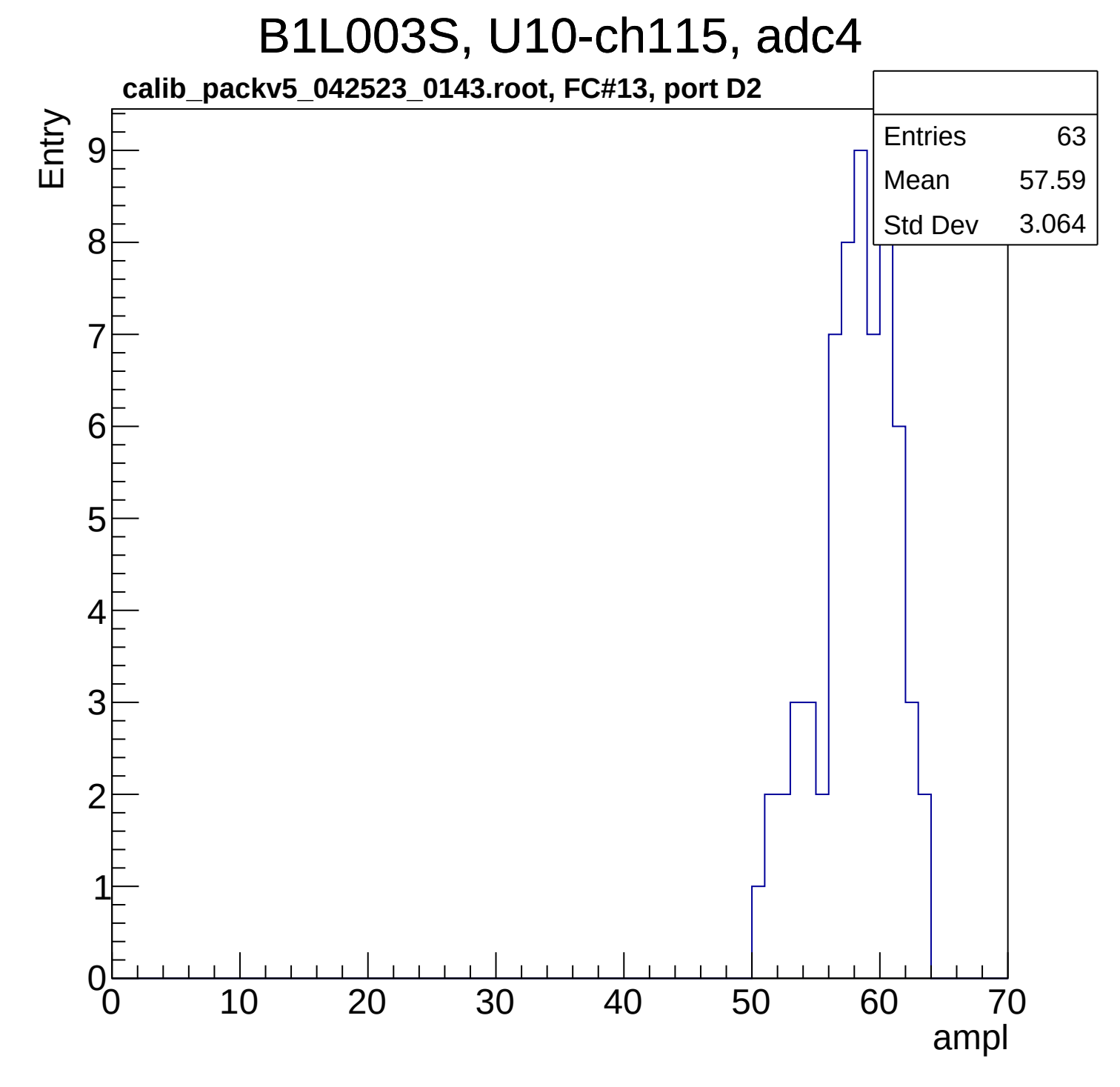
B1L003S, U10-ch115, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 57.59
Std Dev 3.064
Entry
ampl
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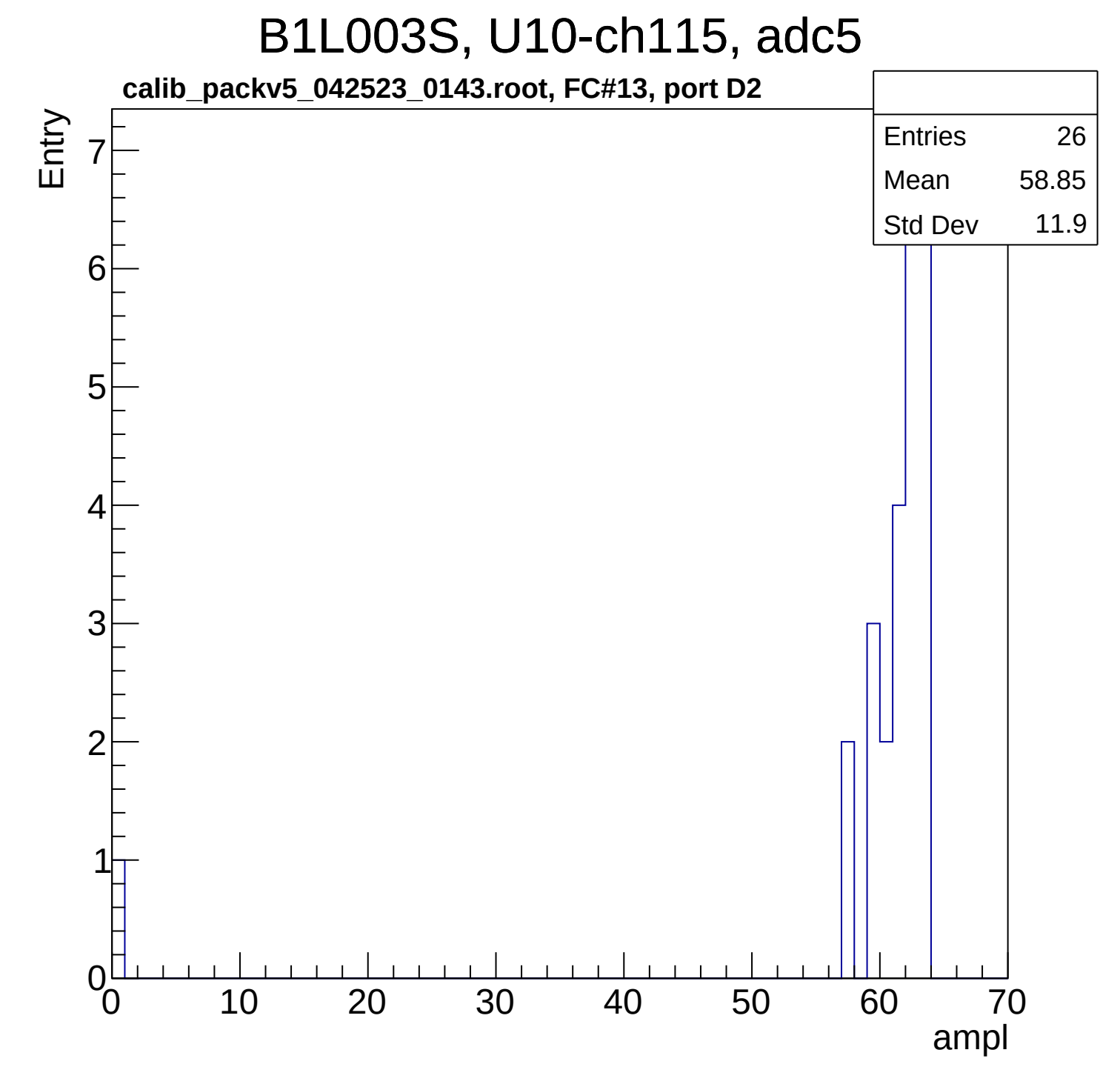
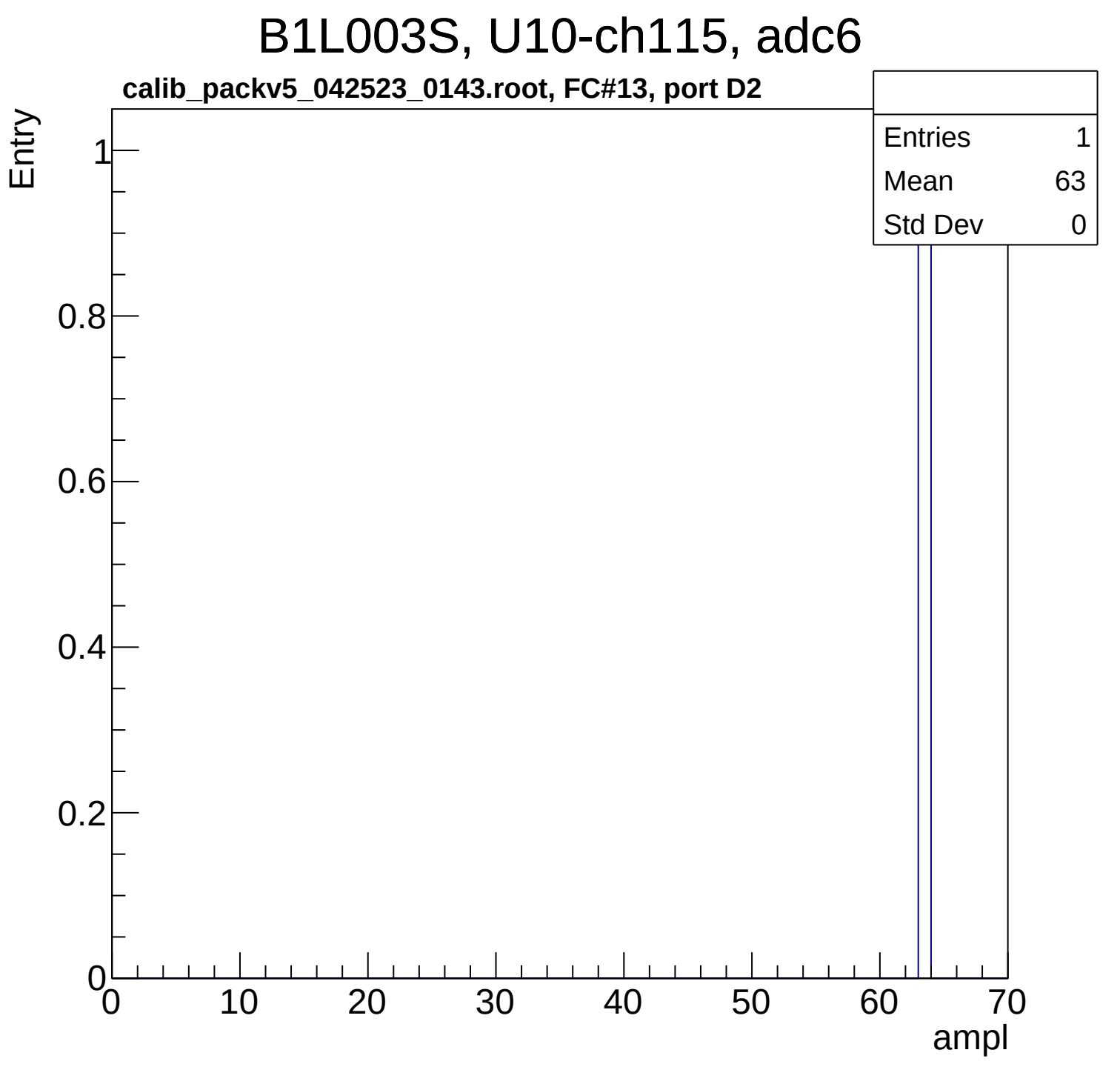

B1L003S, U10-ch115, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 26
Mean 58.85
Std Dev 11.9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch115, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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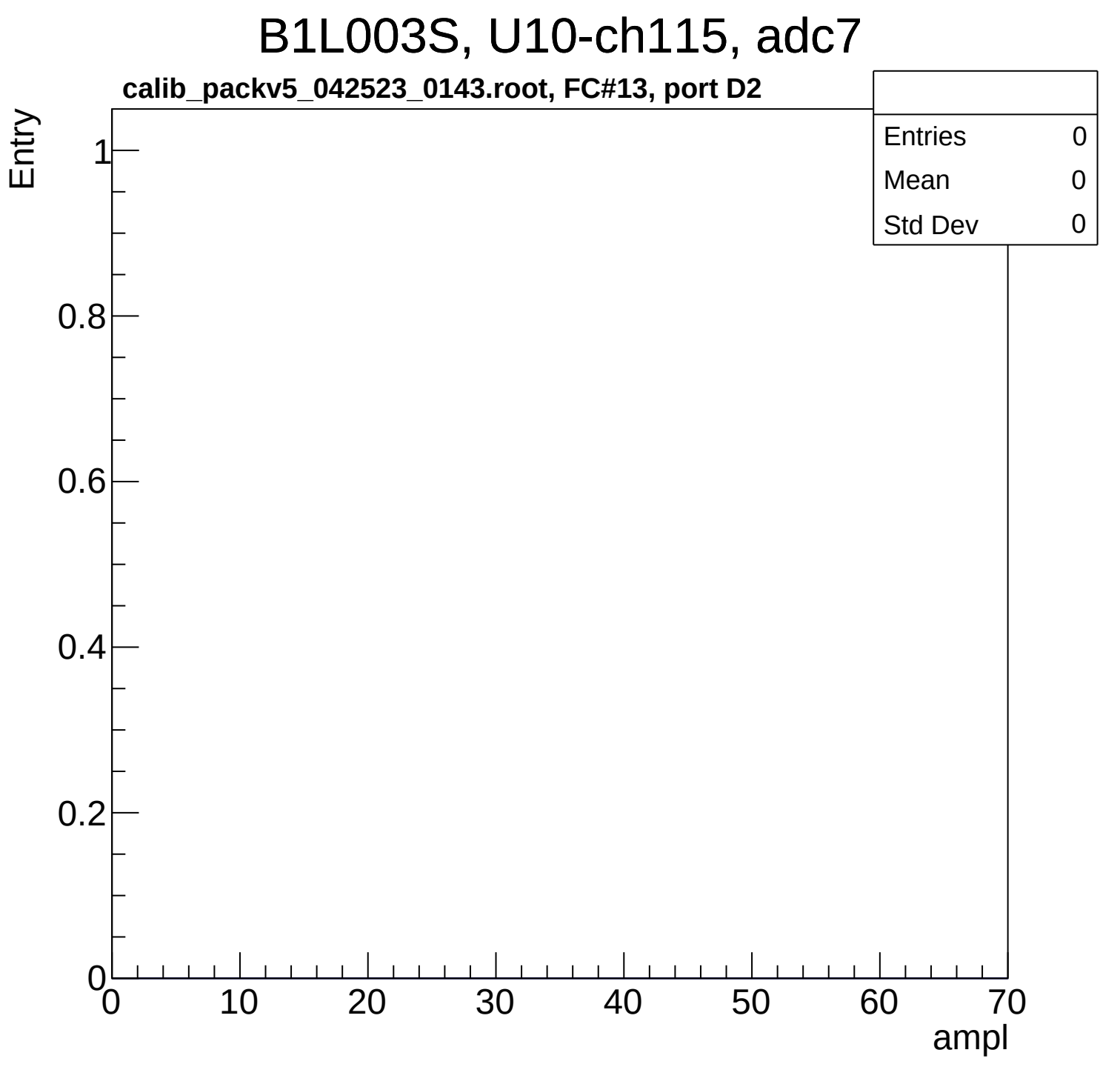
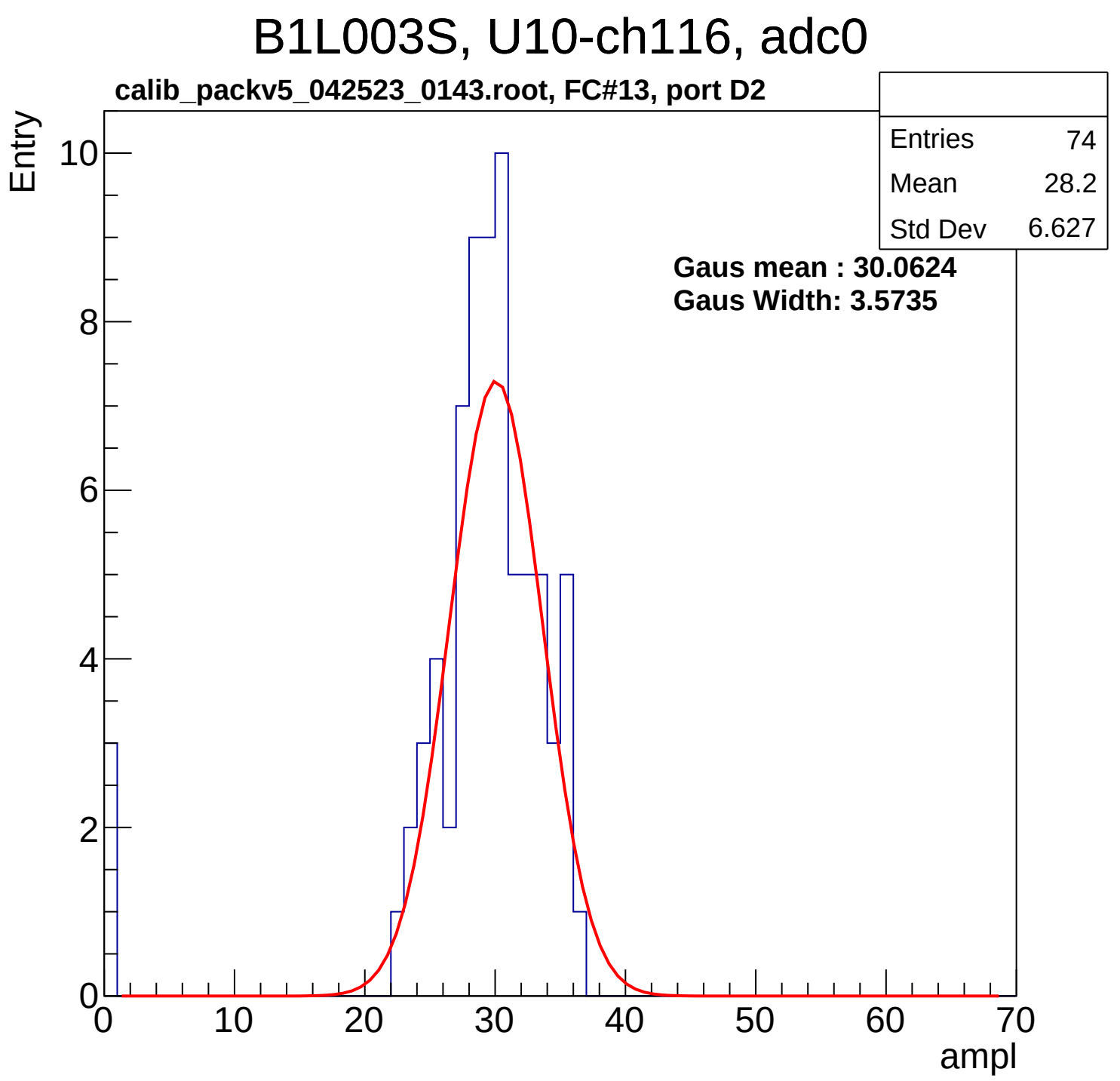
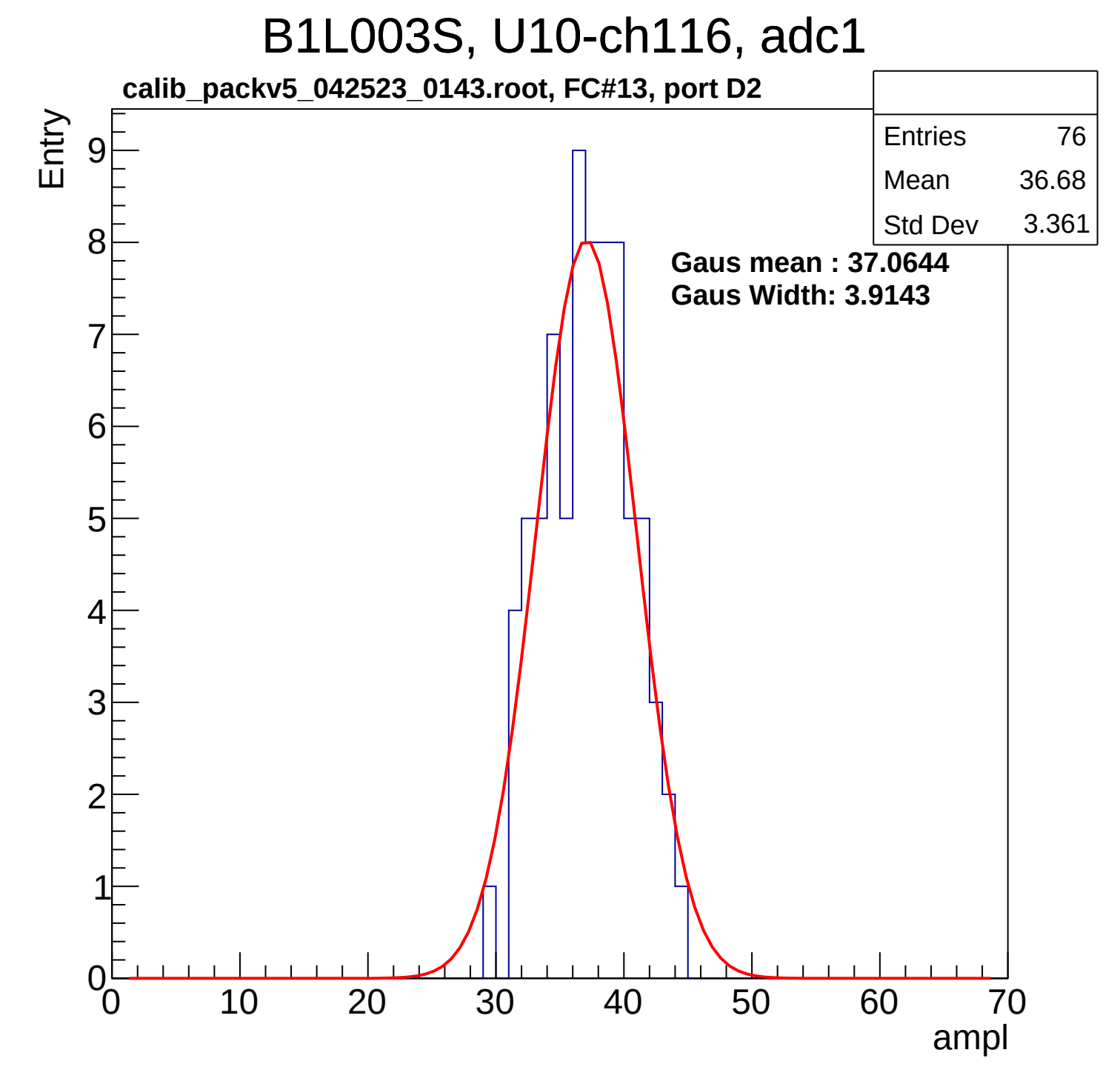
40
50
60
70
ampl


B1L003S, U10-ch115, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch116, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 28.2
Std Dev 6.627
Gaus mean : 30.0624
Gaus Width: 3.5735
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch116, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.68
Std Dev 3.361
Gaus mean : 37.0644
Gaus Width: 3.9143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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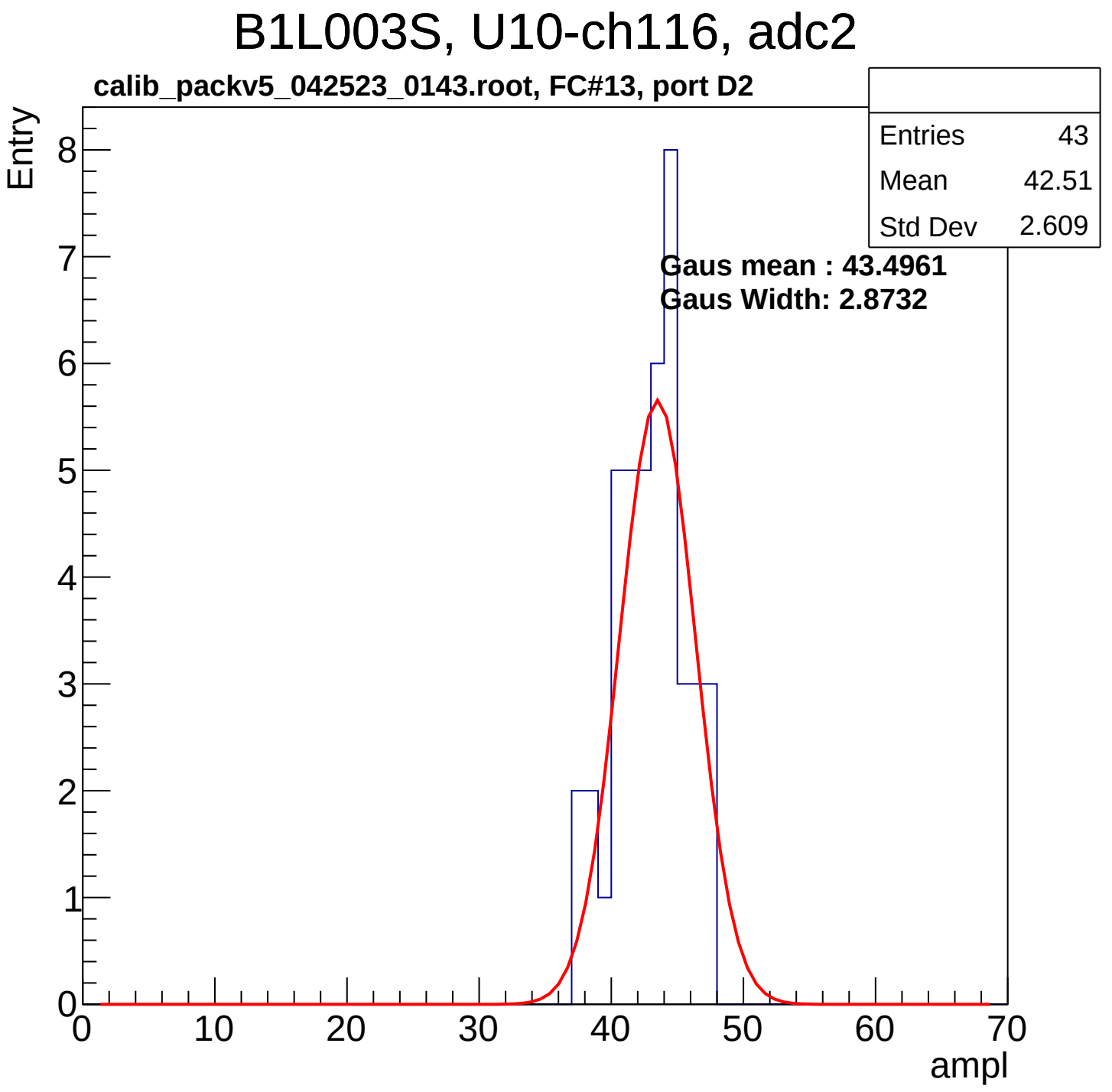
B1L003S, U10-ch116, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 42.51
Std Dev 2.609
Gaus mean : 43.4961
Gaus Width: 2.8732
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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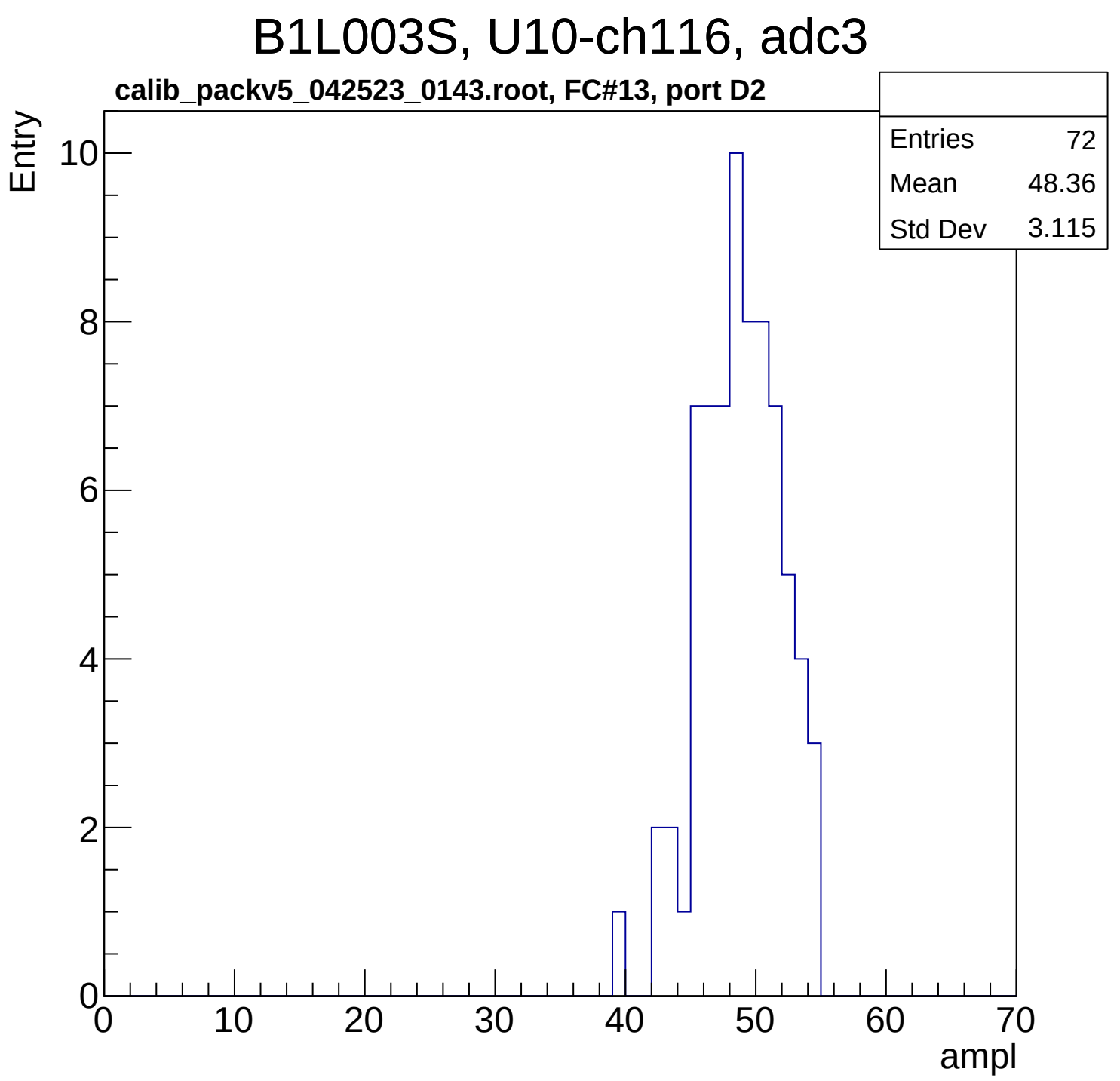
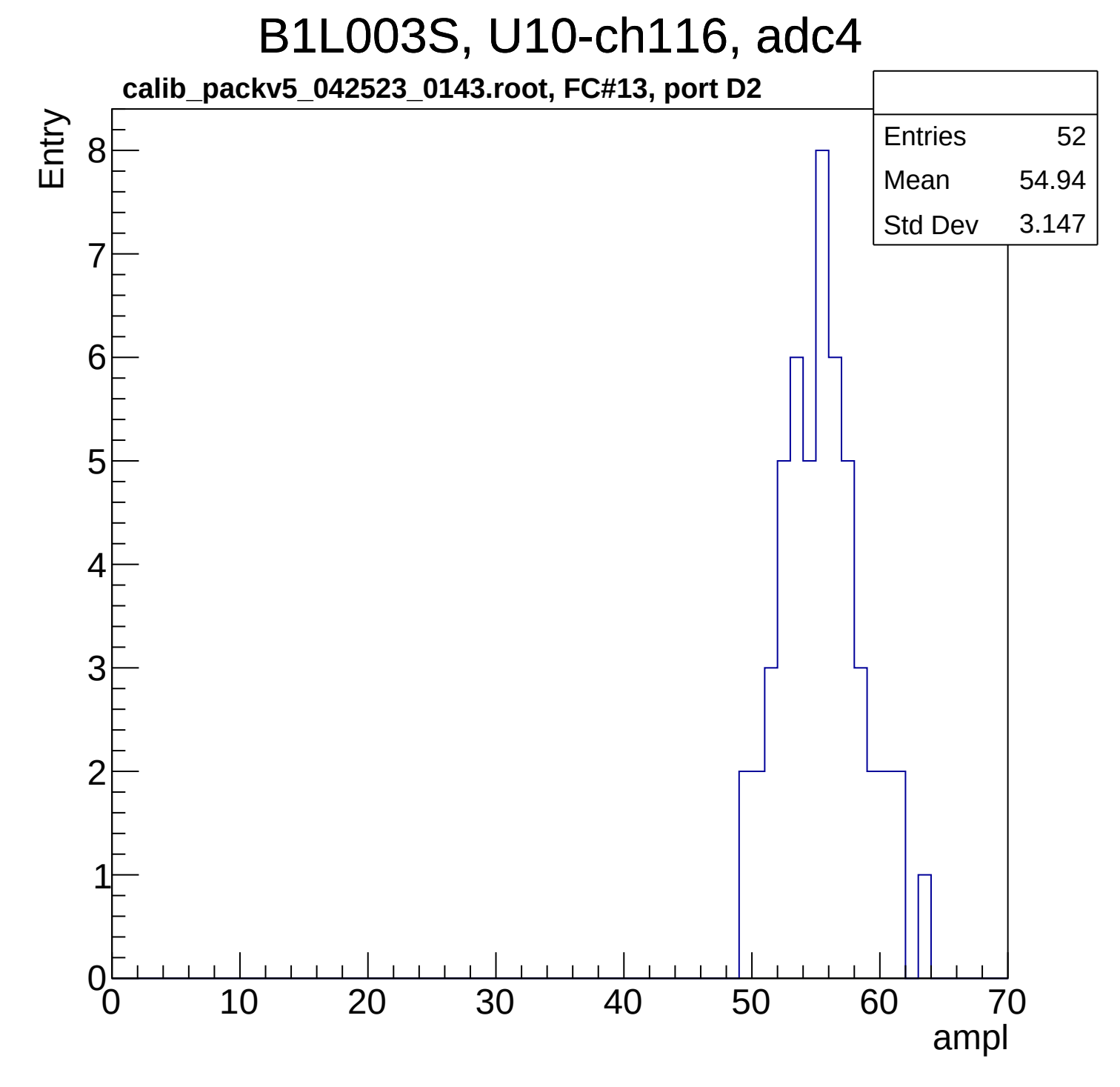
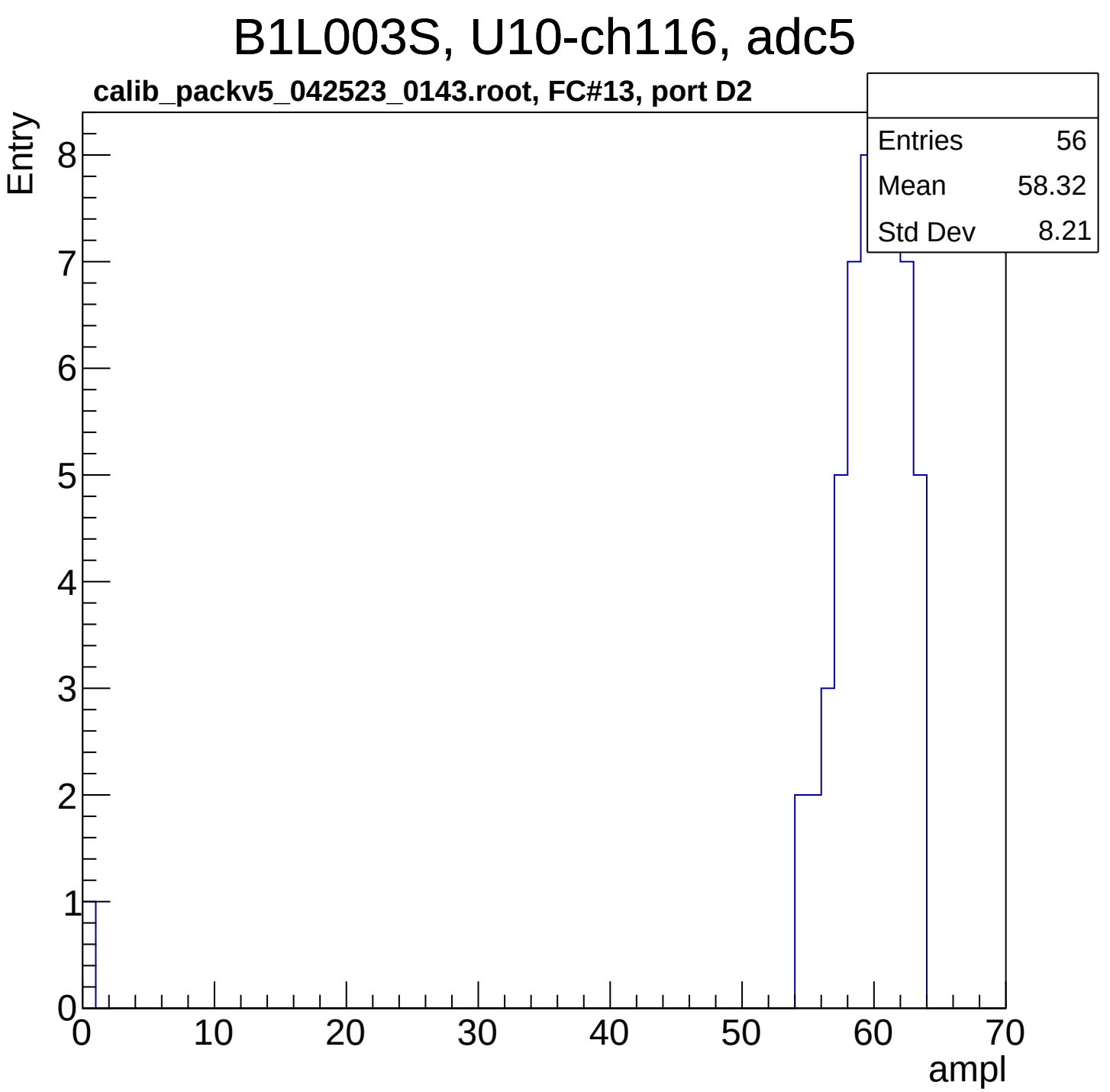
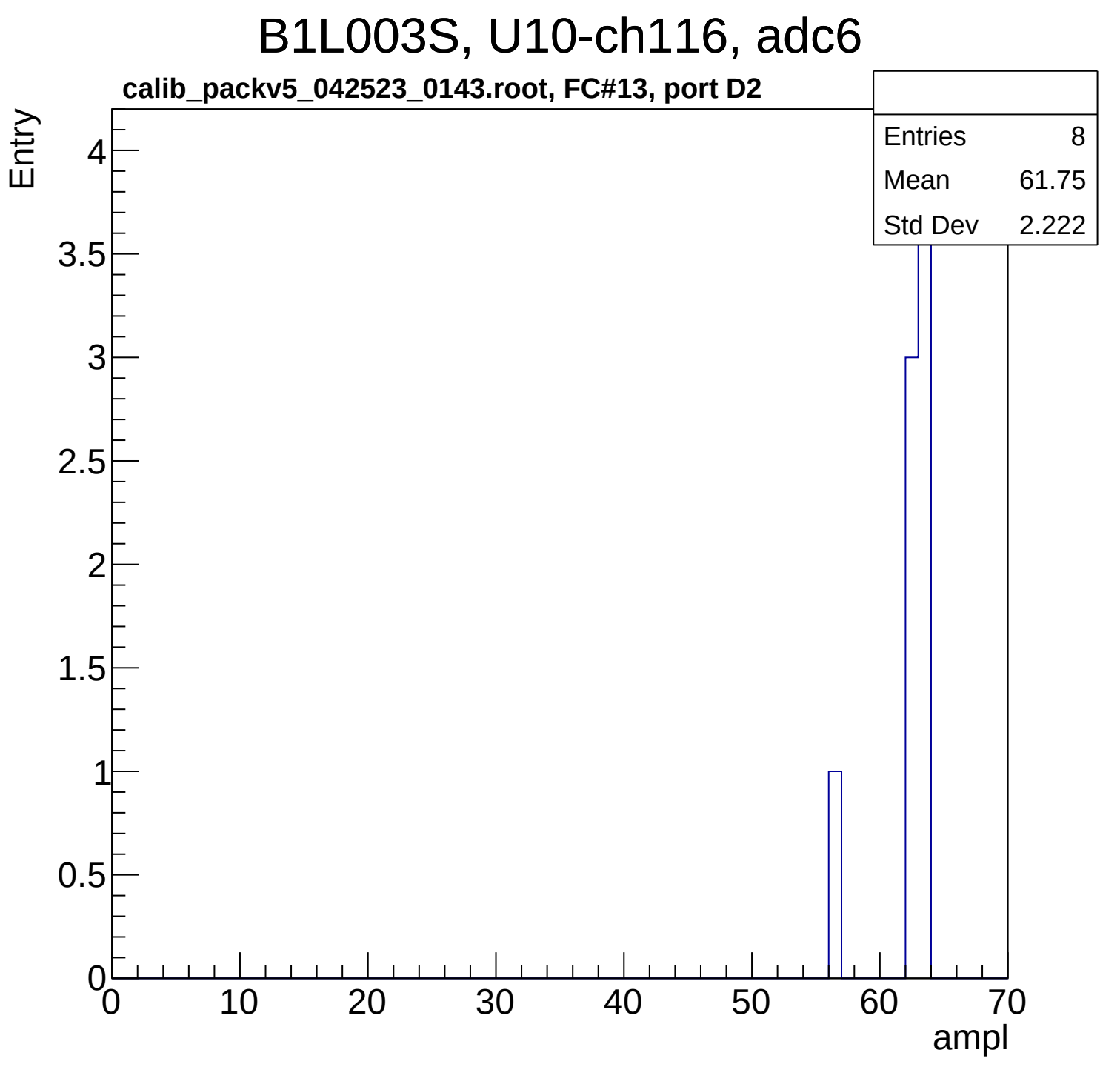
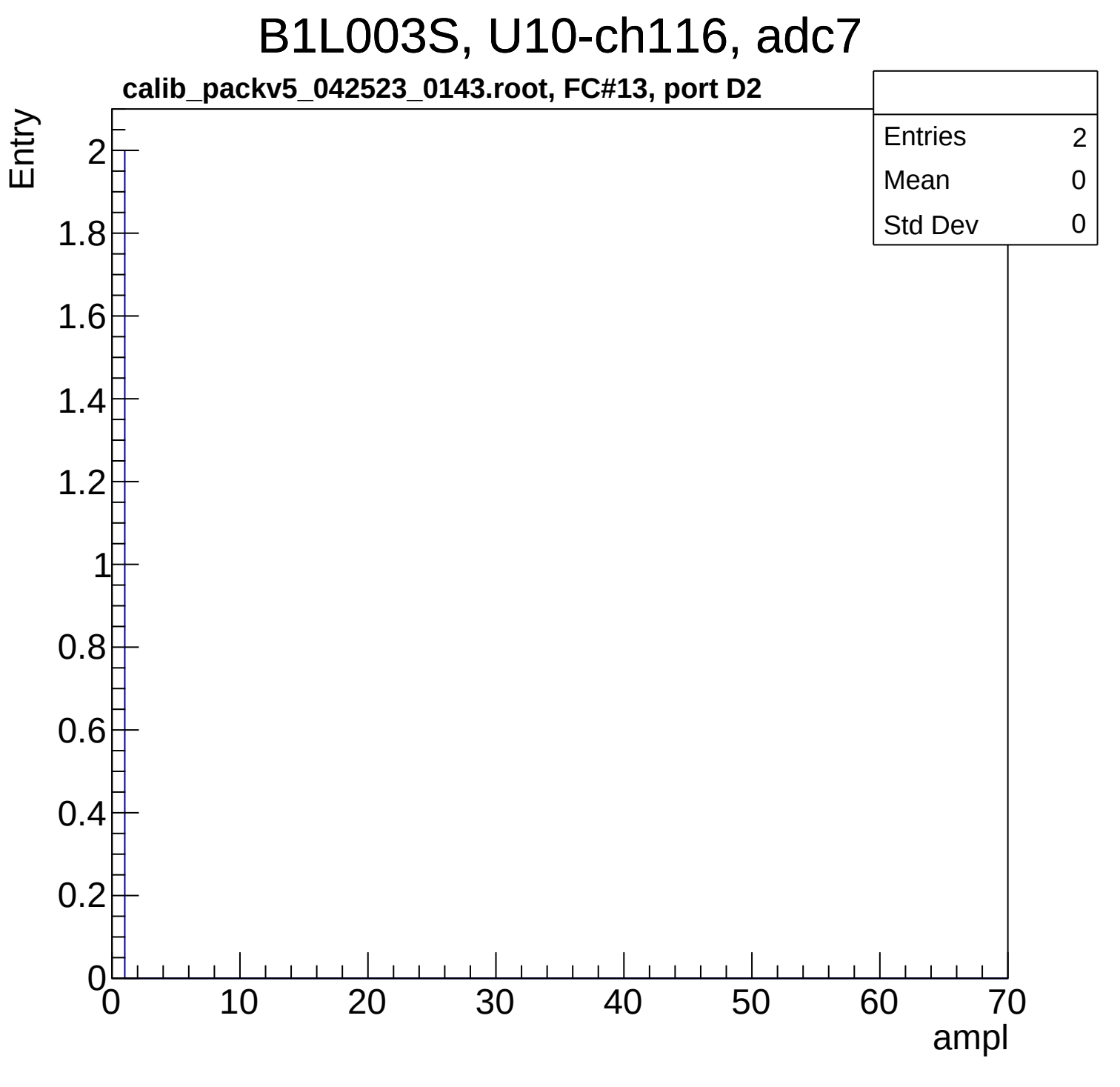
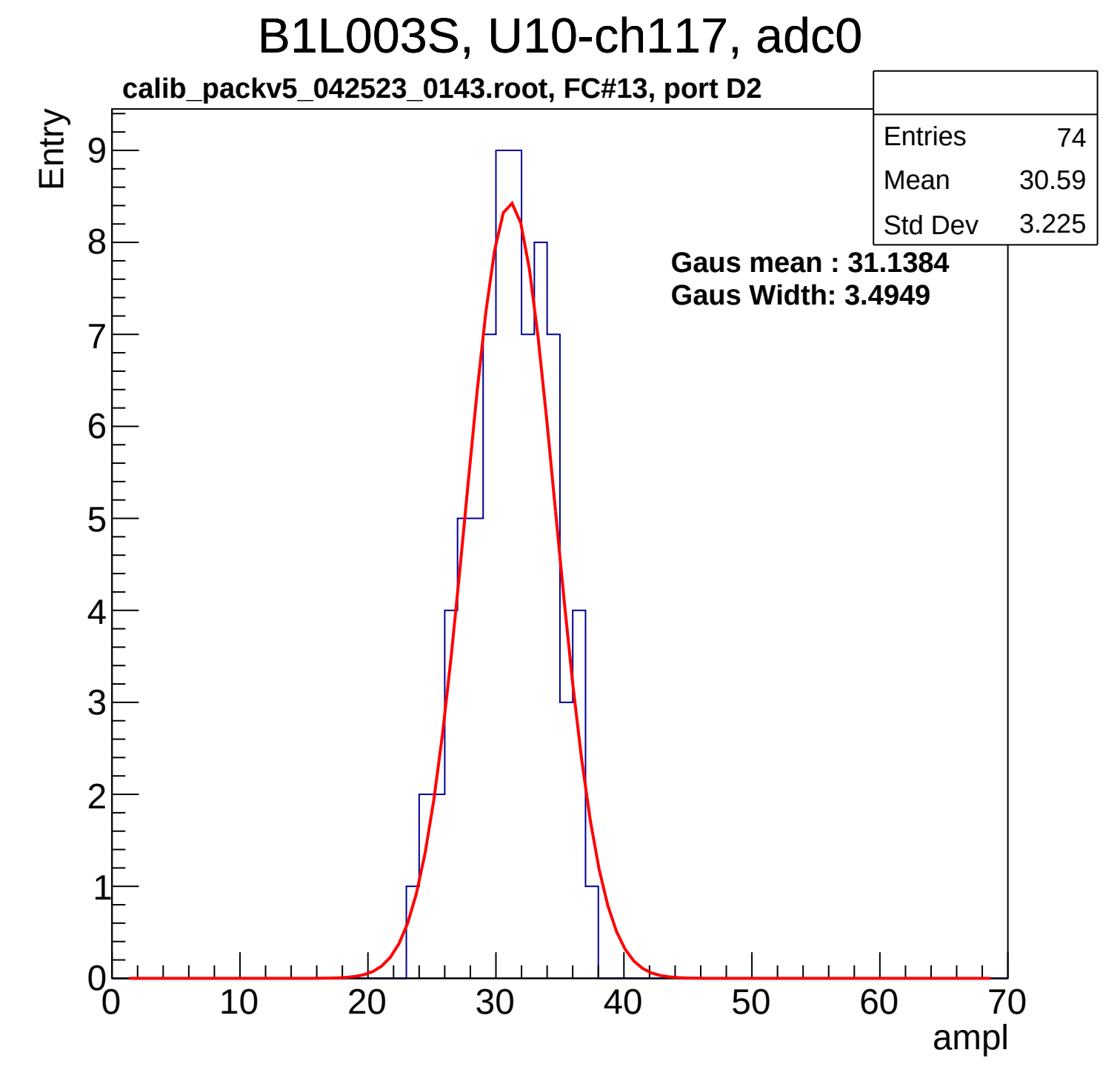

B1L003S, U10-ch116, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 48.36
Std Dev 3.115
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch116, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.94
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch116, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.32
Std Dev 8.21
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch116, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.75
Std Dev 2.222
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch116, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U10-ch117, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 30.59
Std Dev 3.225
Gaus mean : 31.1384
Gaus Width: 3.4949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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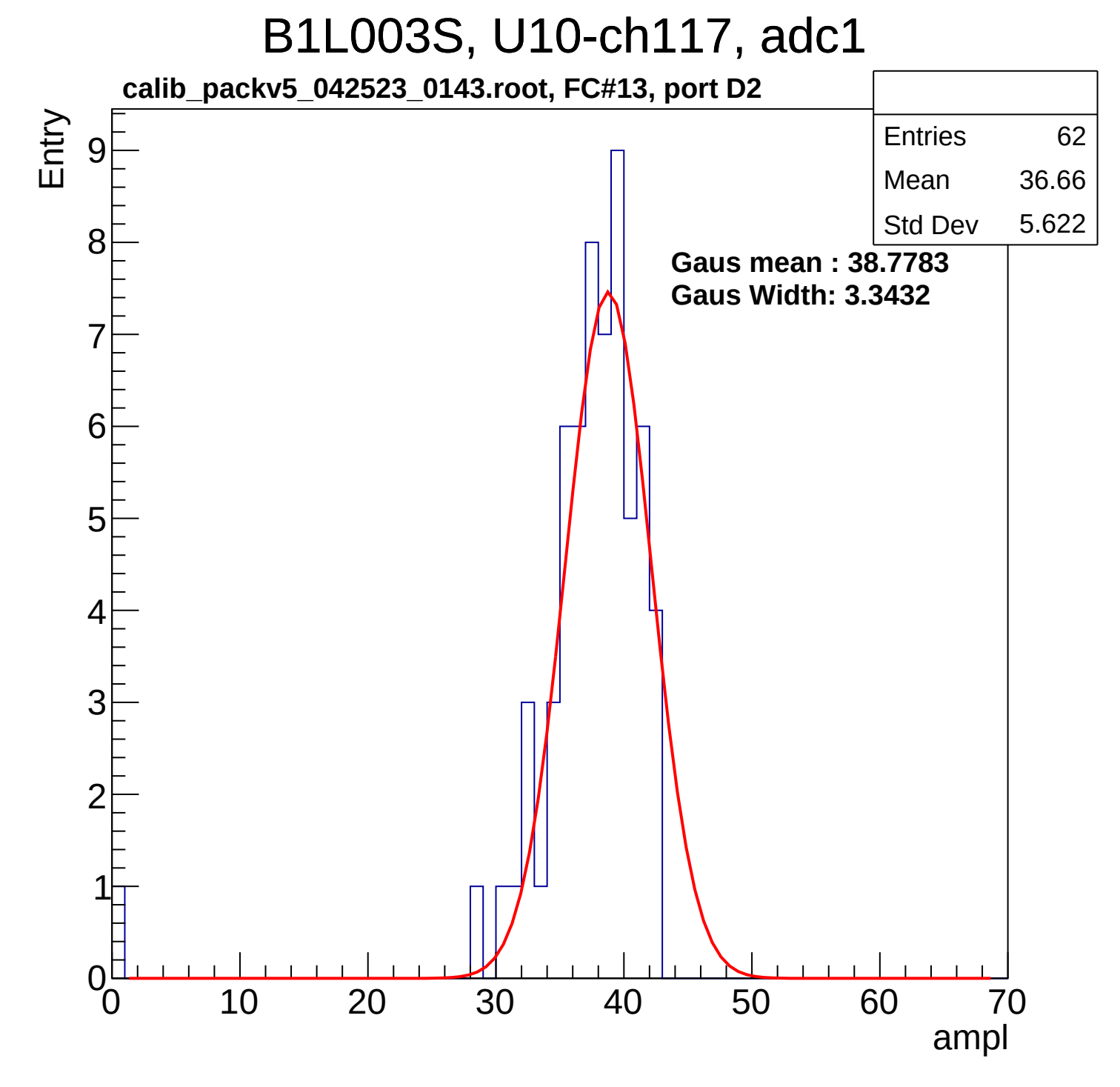
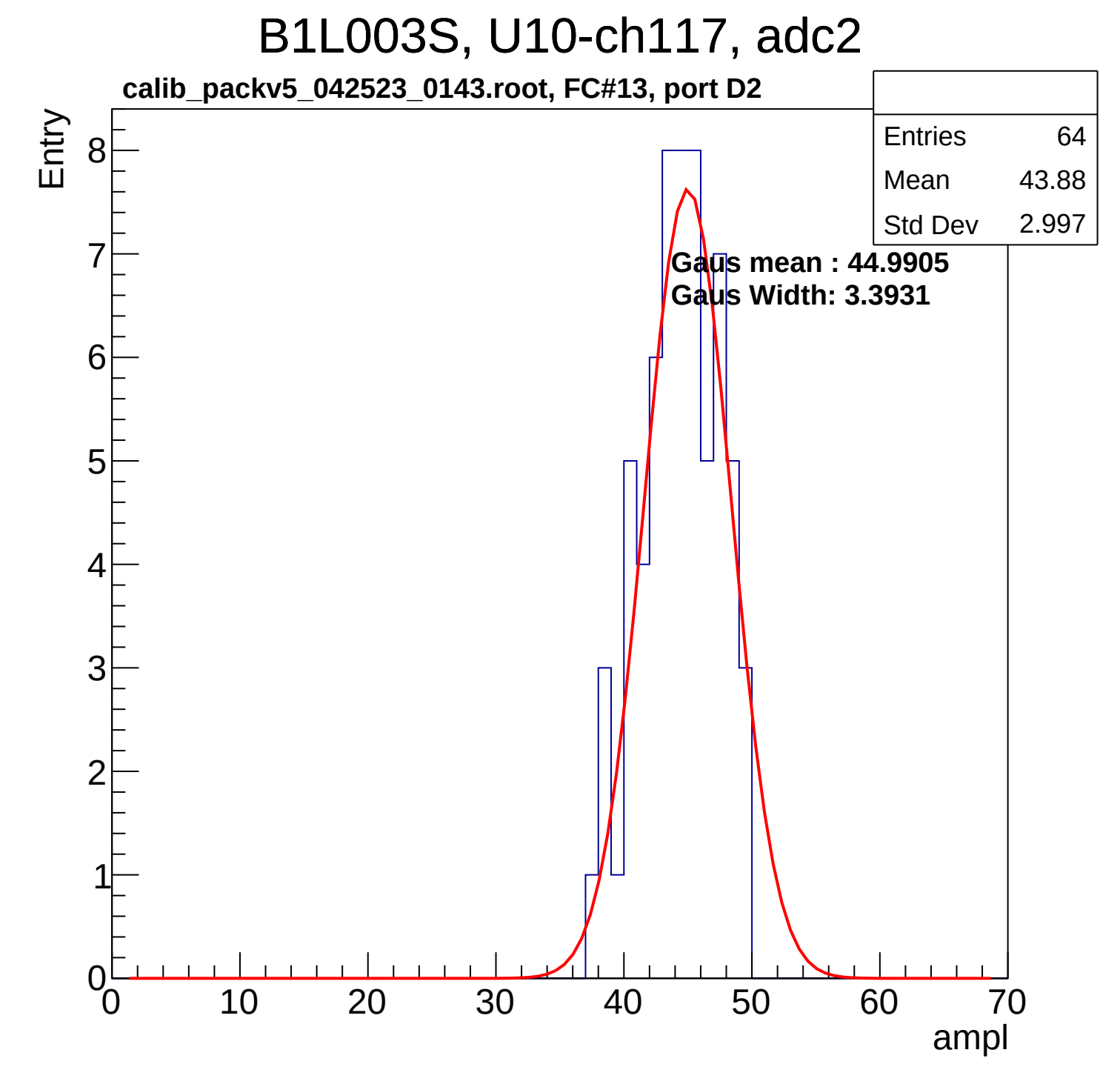
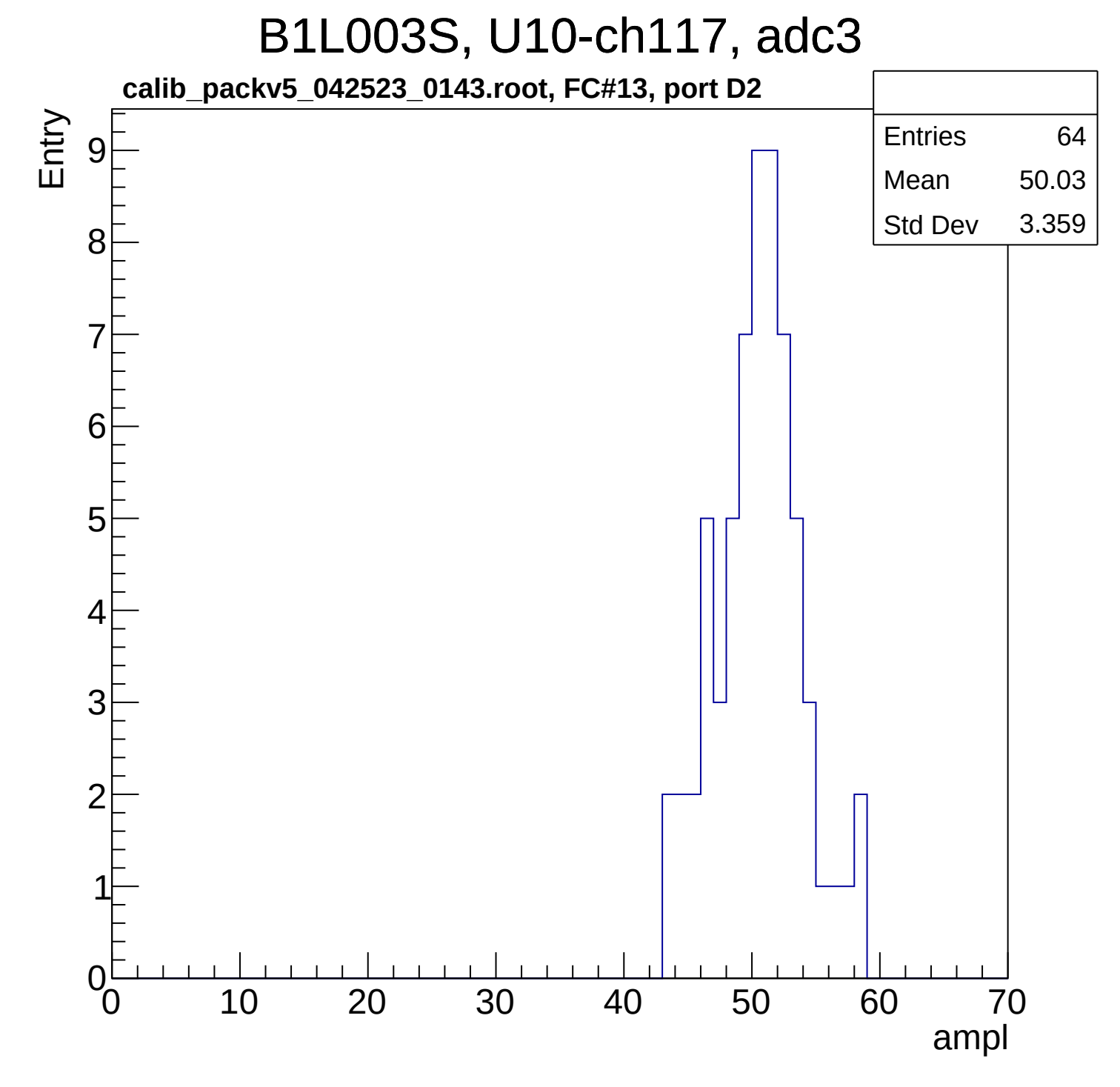

B1L003S, U10-ch117, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.66
Std Dev 5.622
Gaus mean : 38.7783
Gaus Width: 3.3432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch117, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.88
Std Dev 2.997
Gaus mean : 44.9905
Gaus Width: 3.3931
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch117, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 50.03
Std Dev 3.359
Entry
ampl
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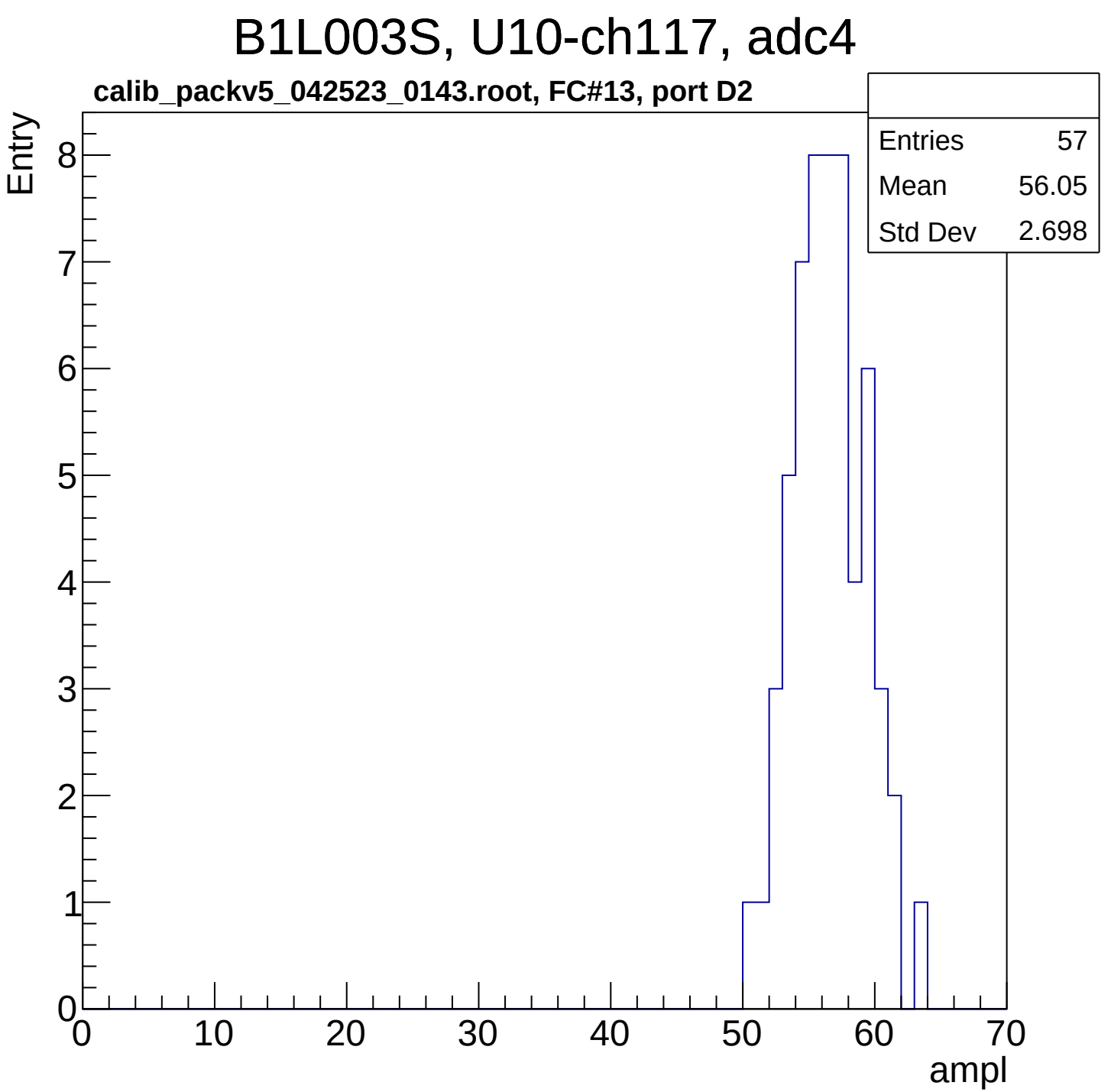
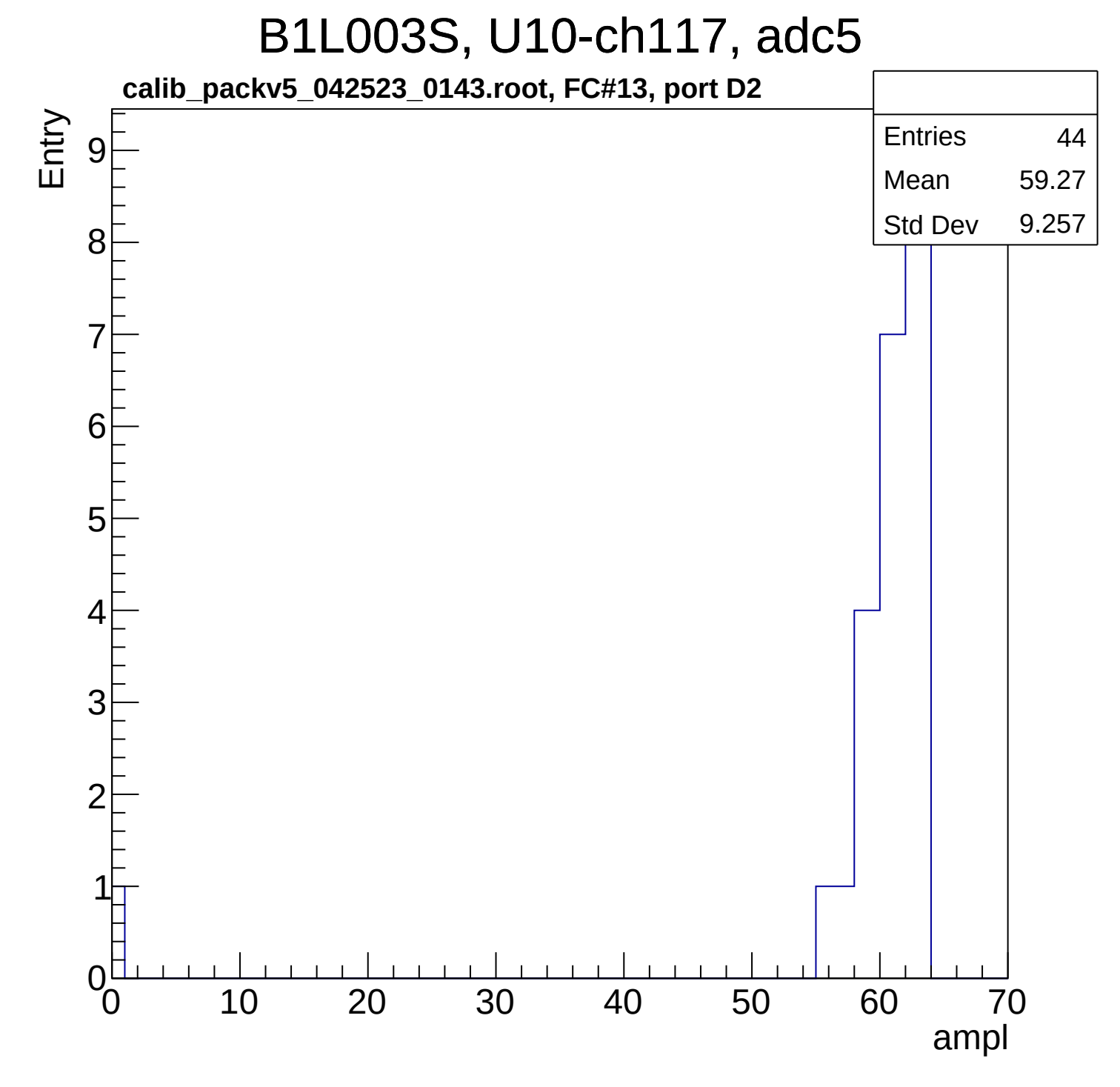
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch117, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 56.05
Std Dev 2.698
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch117, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.27
Std Dev 9.257
Entry
ampl
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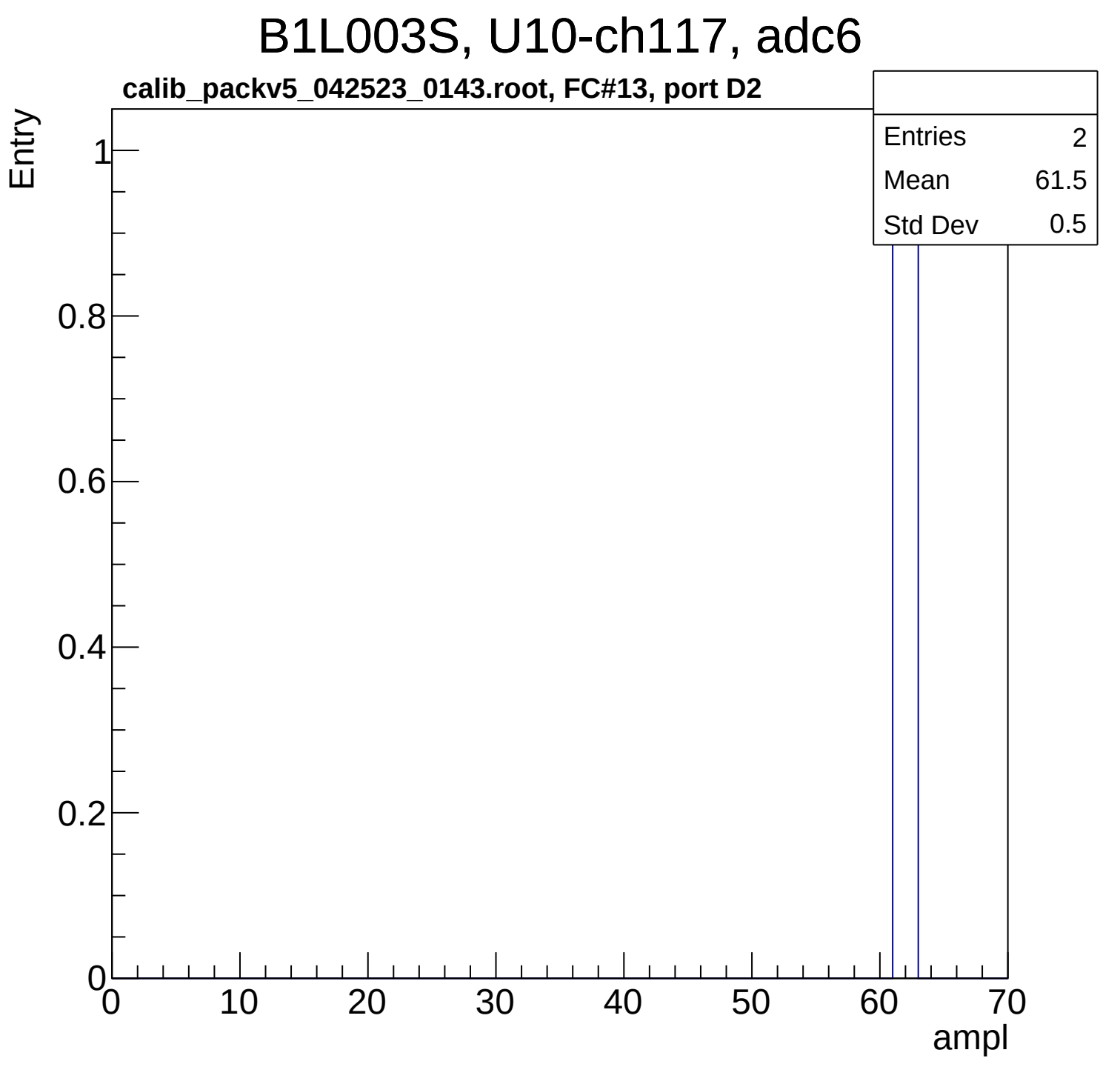
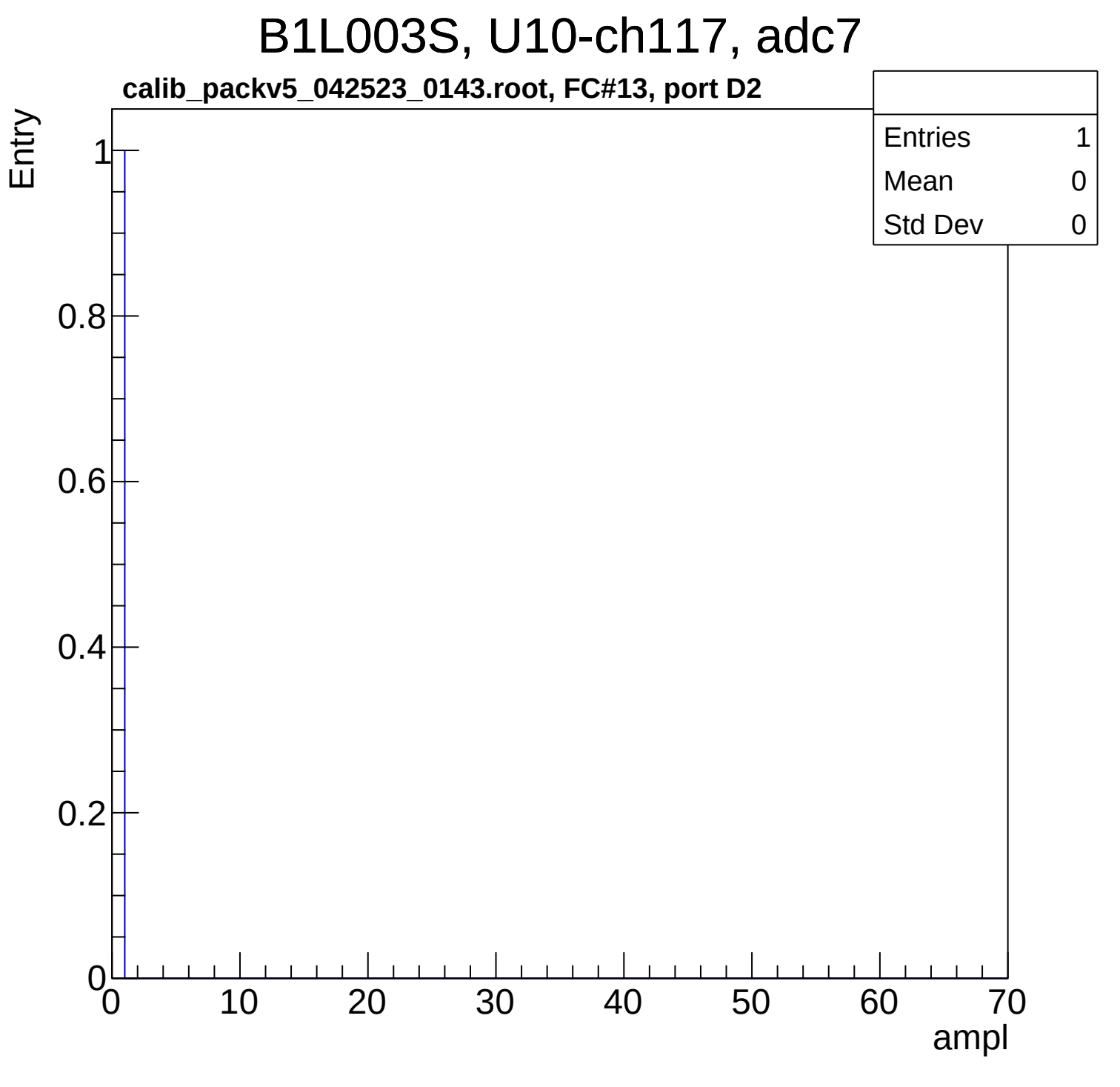
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch117, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch117, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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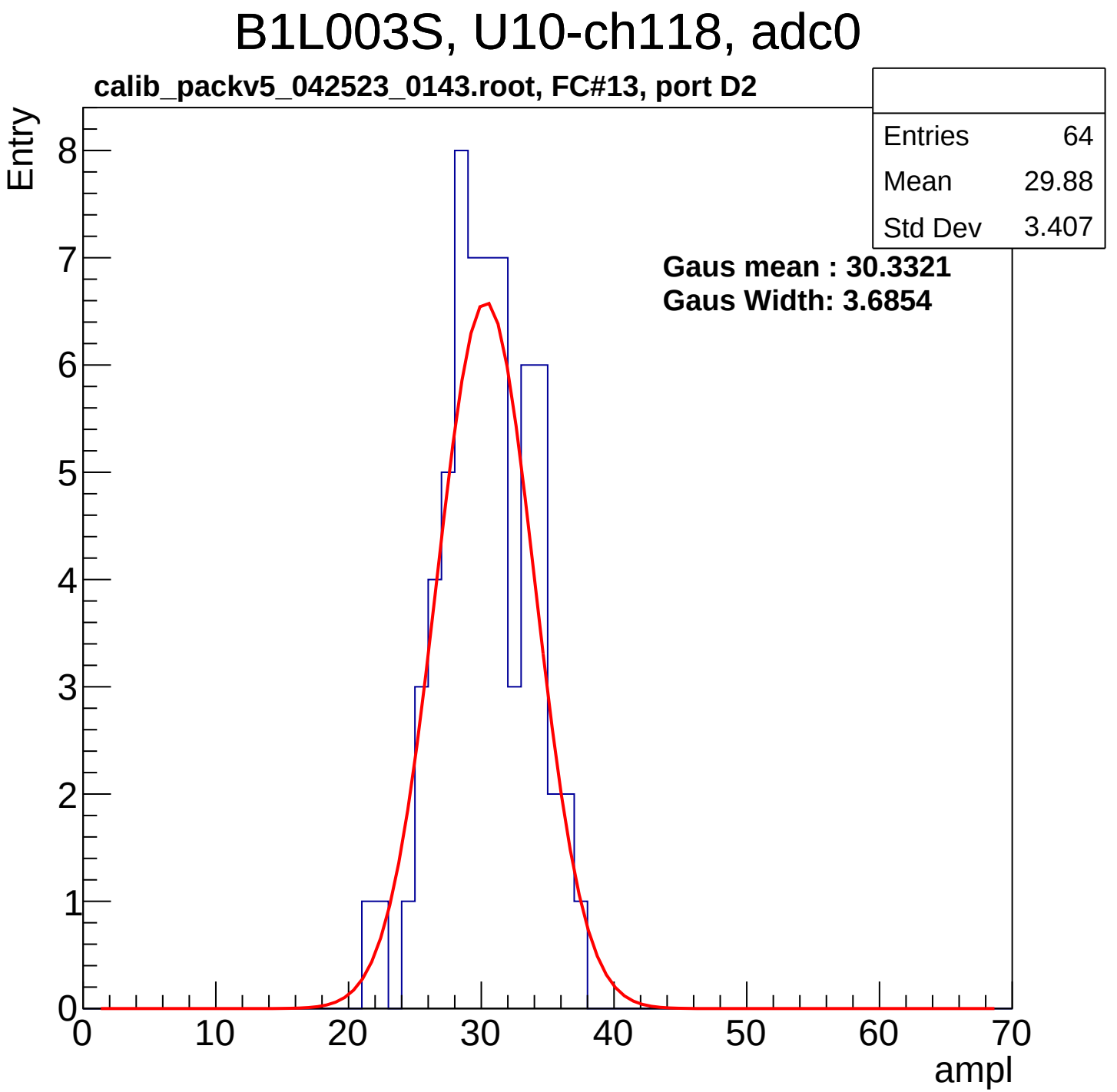
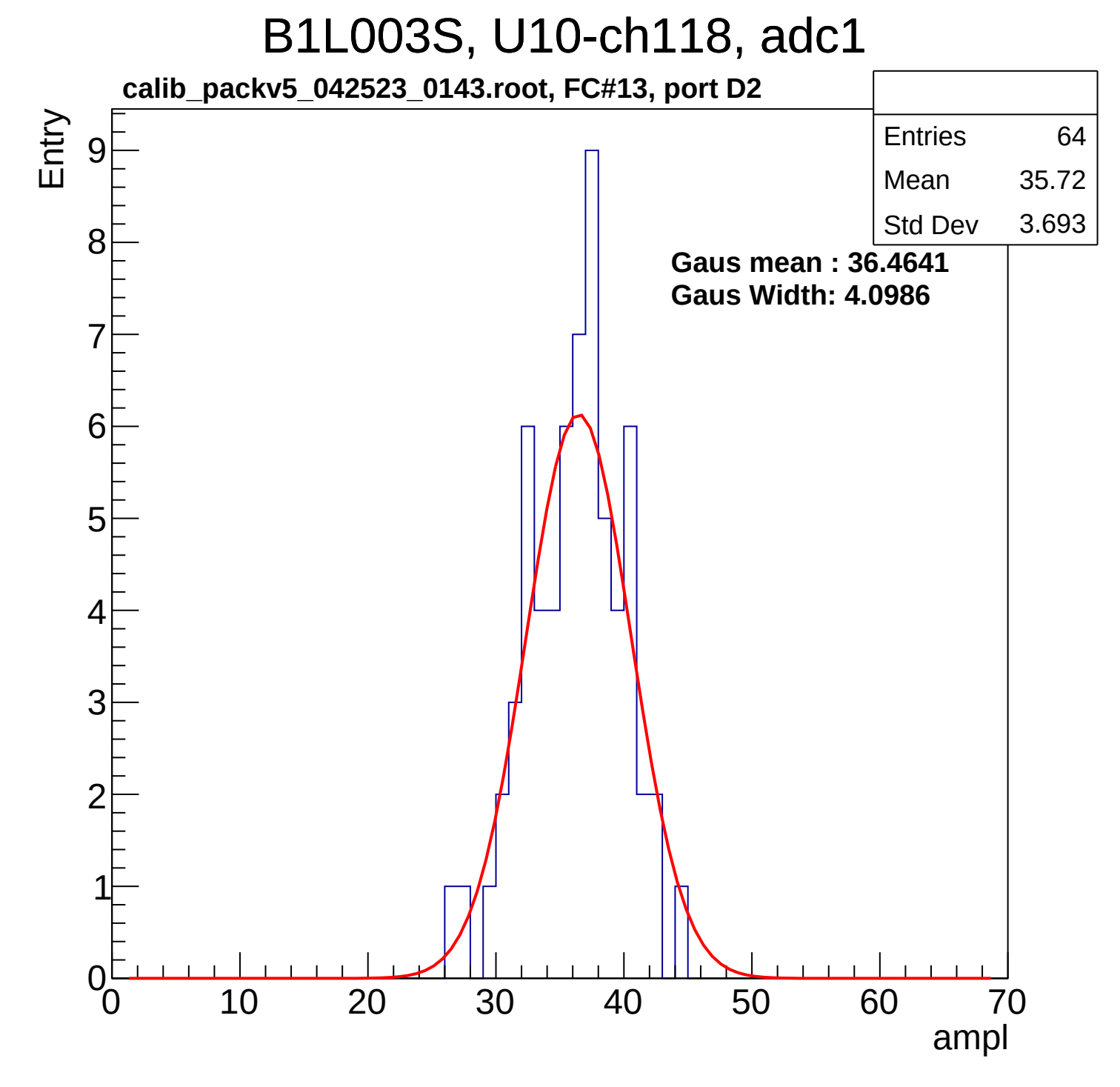
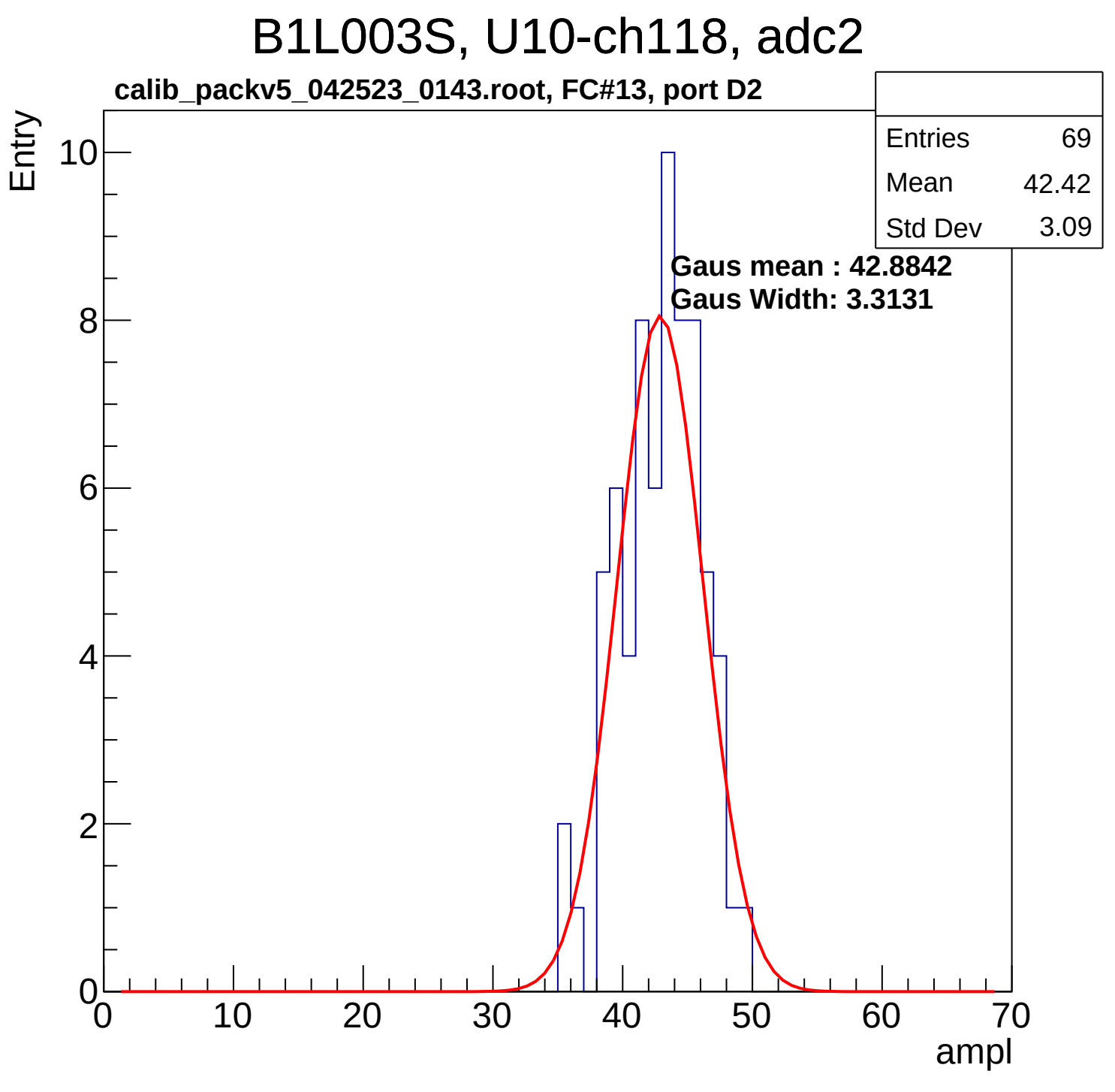
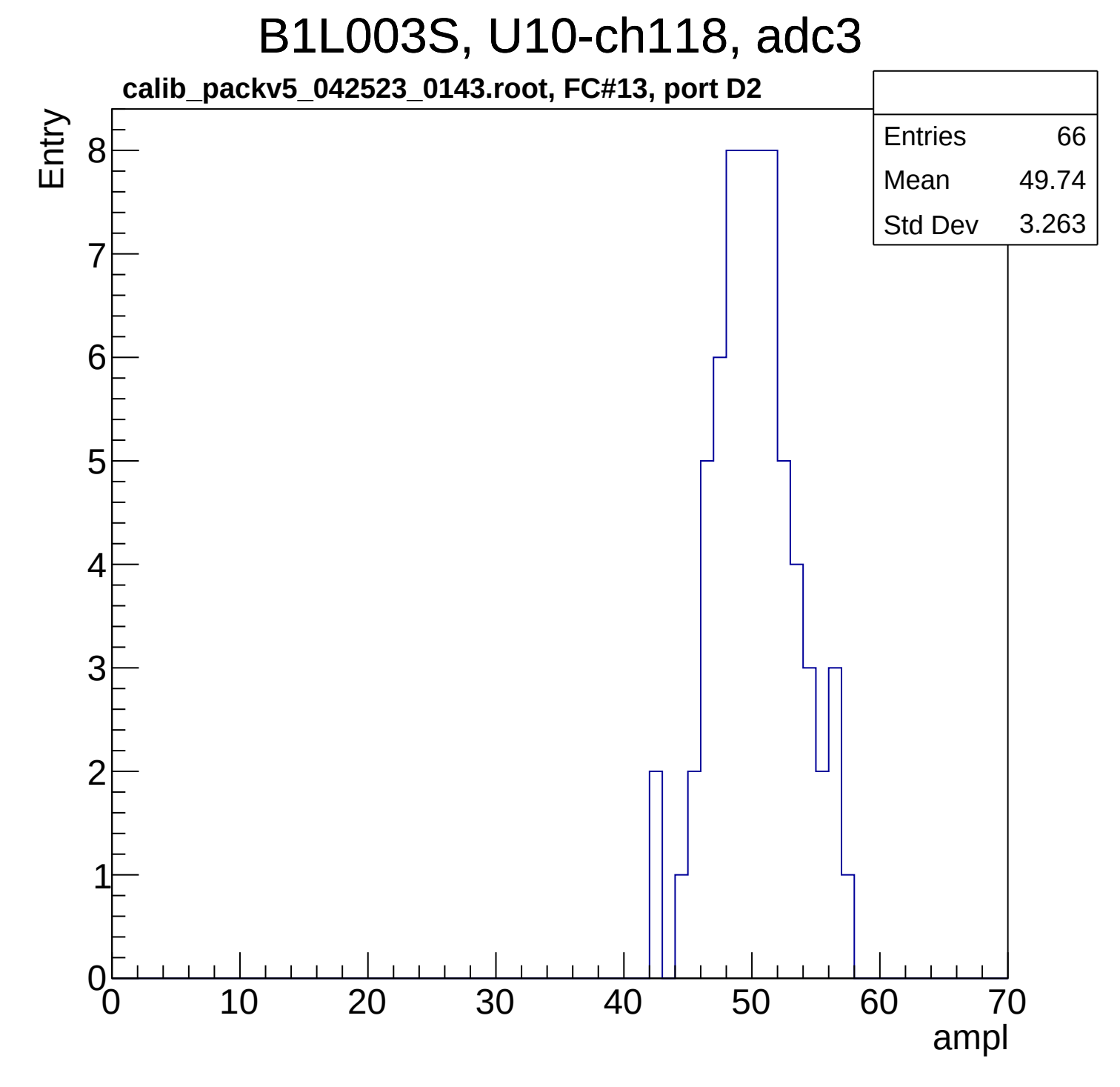
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch118, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.88
Std Dev 3.407
Gaus mean : 30.3321
Gaus Width: 3.6854
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch118, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 35.72
Std Dev 3.693
Gaus mean : 36.4641
Gaus Width: 4.0986
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch118, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 42.42
Std Dev 3.09
Gaus mean : 42.8842
Gaus Width: 3.3131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch118, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.74
Std Dev 3.263
Entry
ampl
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1
2
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4
5
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7
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0
10
20
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40
50
60
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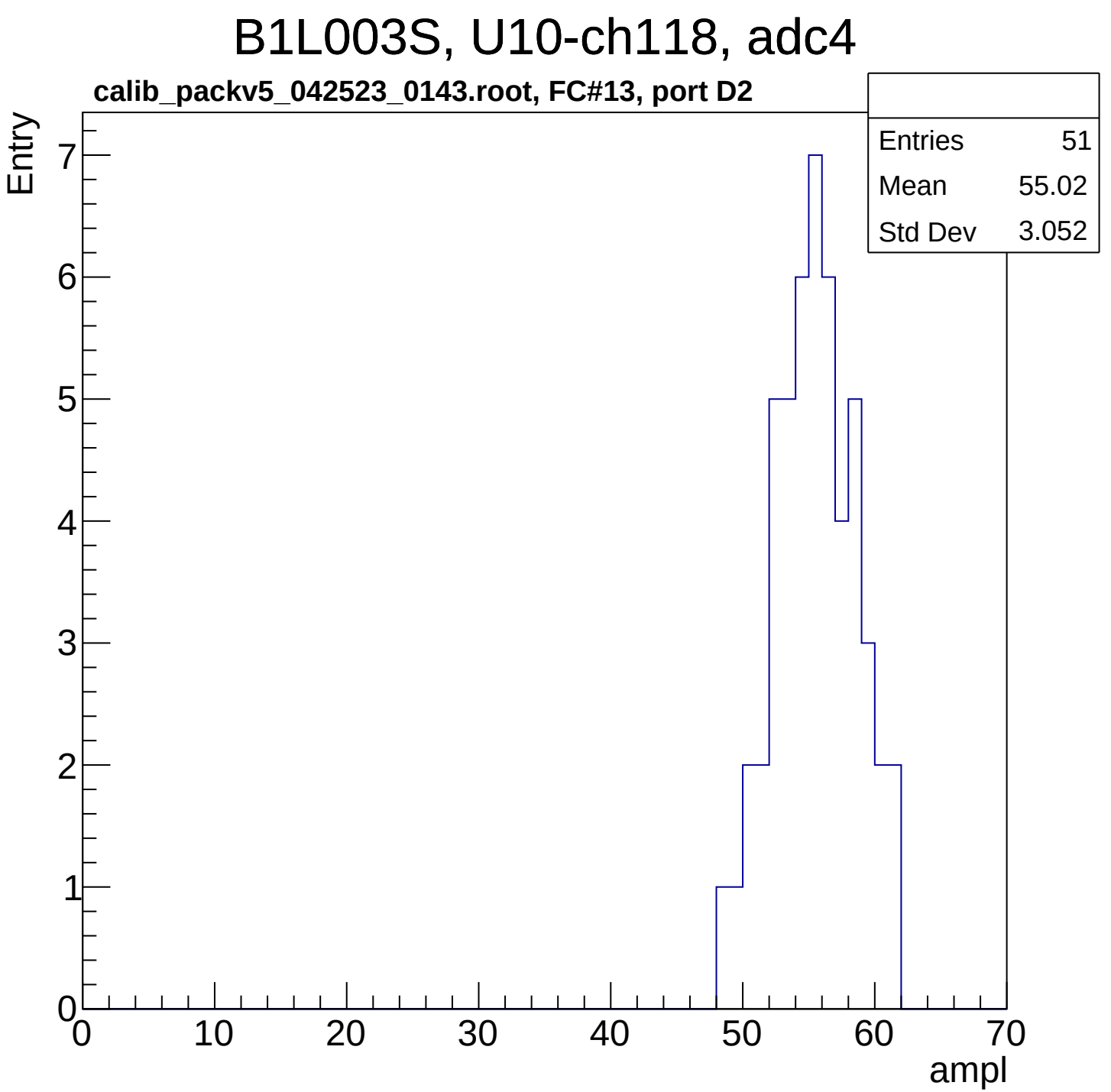
B1L003S, U10-ch118, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.02
Std Dev 3.052
Entry
ampl
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4
5
6
7
0
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20
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40
50
60
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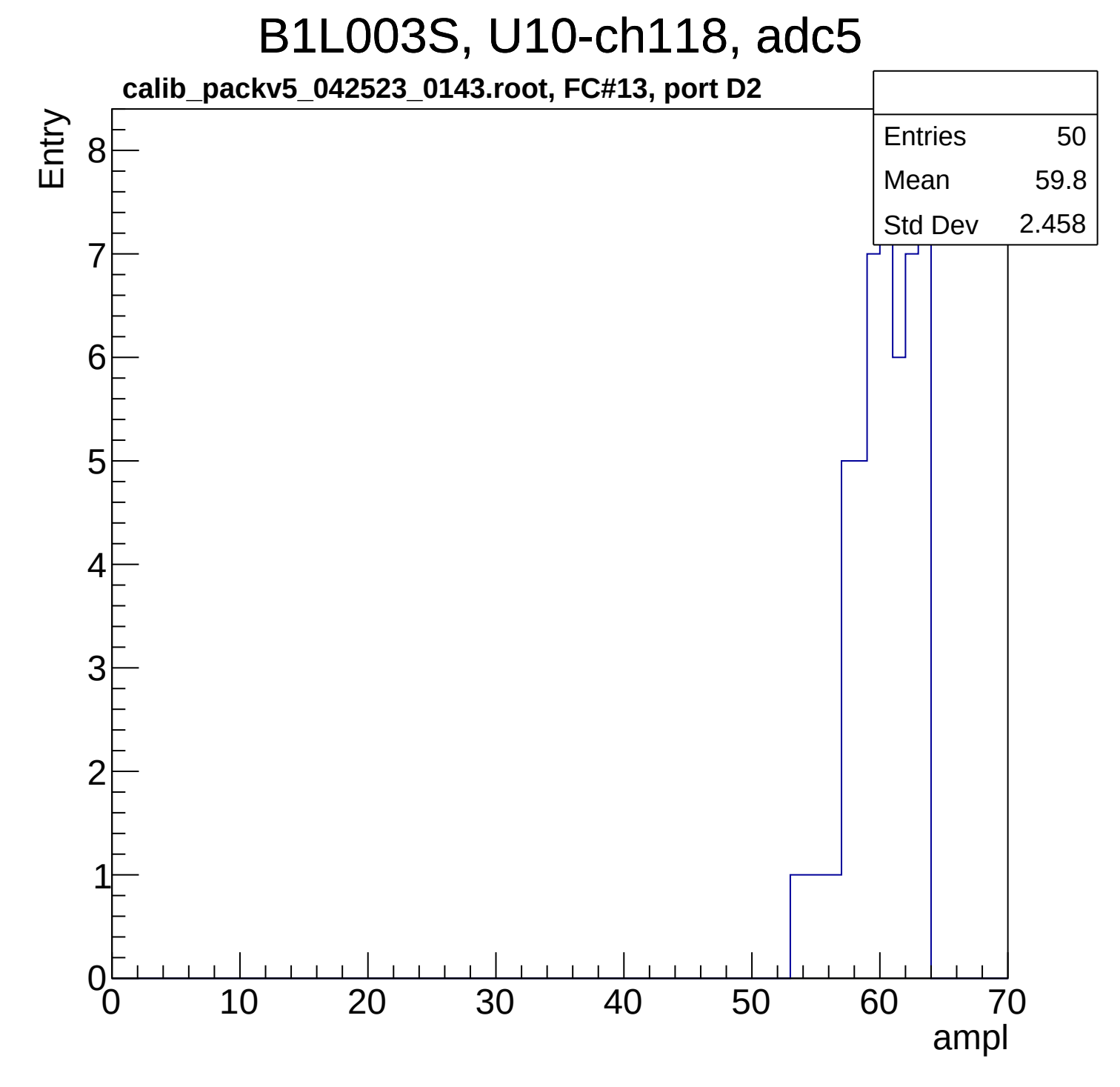
B1L003S, U10-ch118, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.8
Std Dev 2.458
Entry
ampl
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1
2
3
4
5
6
7
8
0
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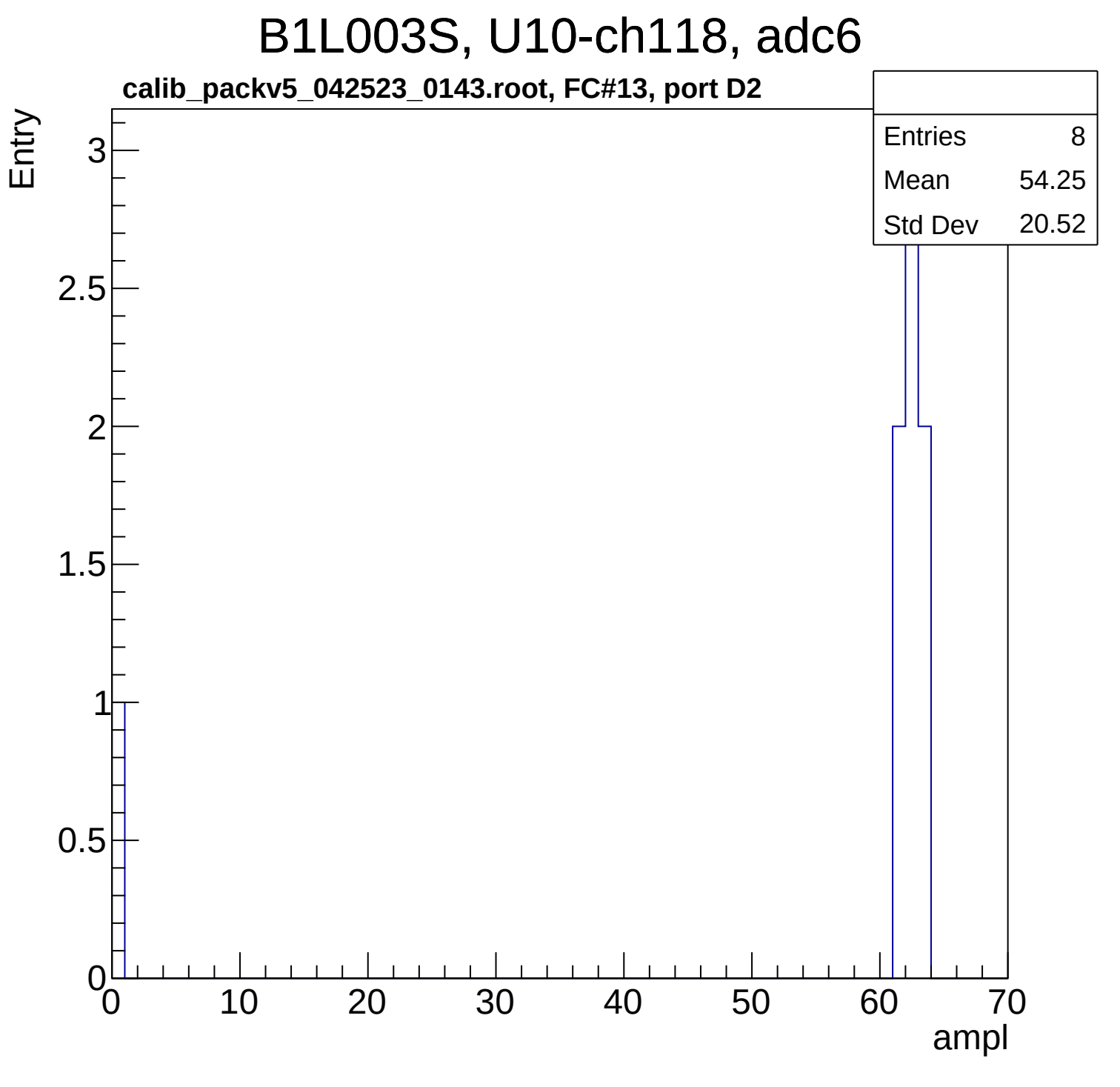
20
30
40
50
60
70


B1L003S, U10-ch118, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.25
Std Dev 20.52
Entry
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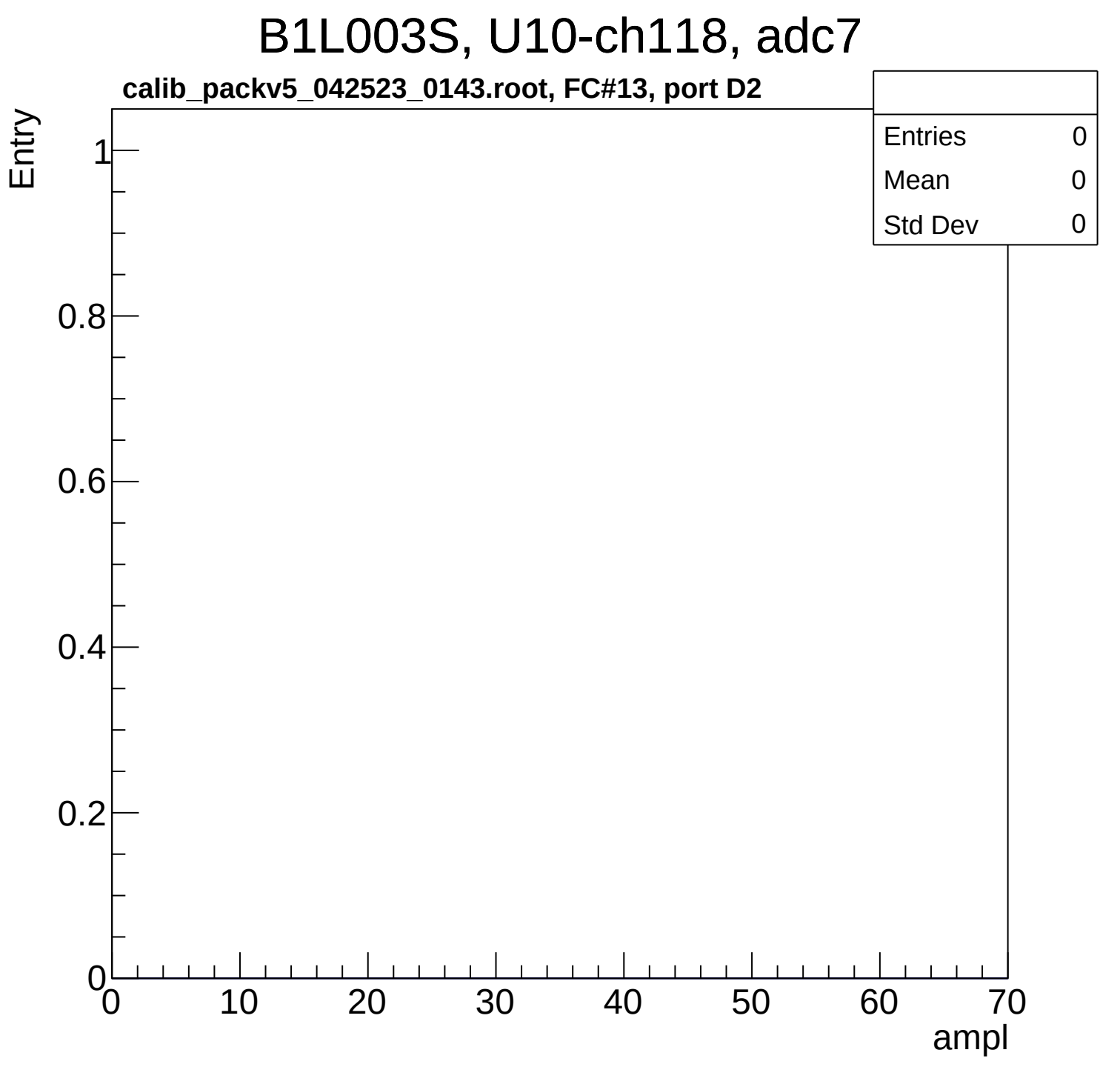
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch118, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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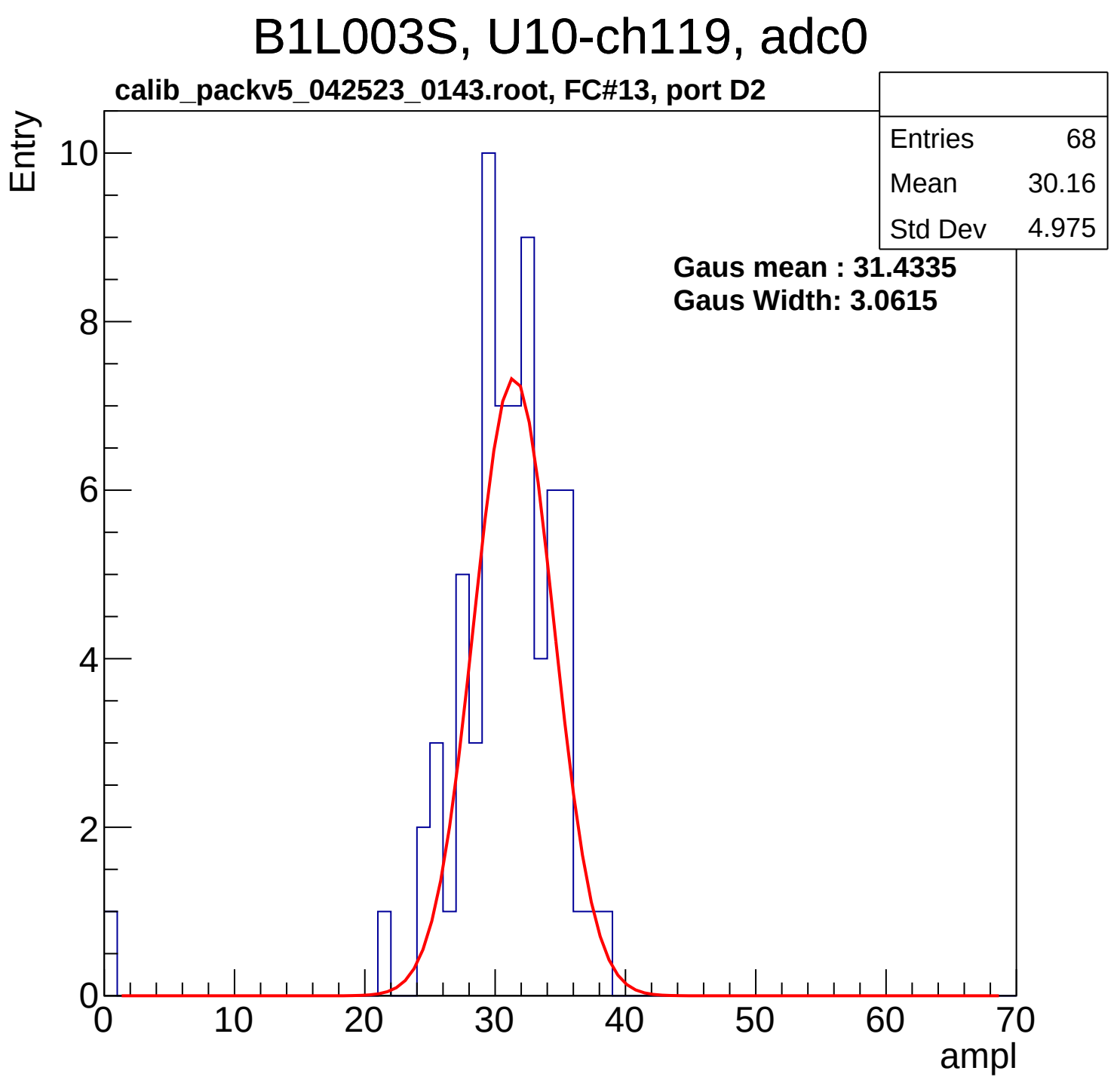
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch119, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.16
Std Dev 4.975
Gaus mean : 31.4335
Gaus Width: 3.0615
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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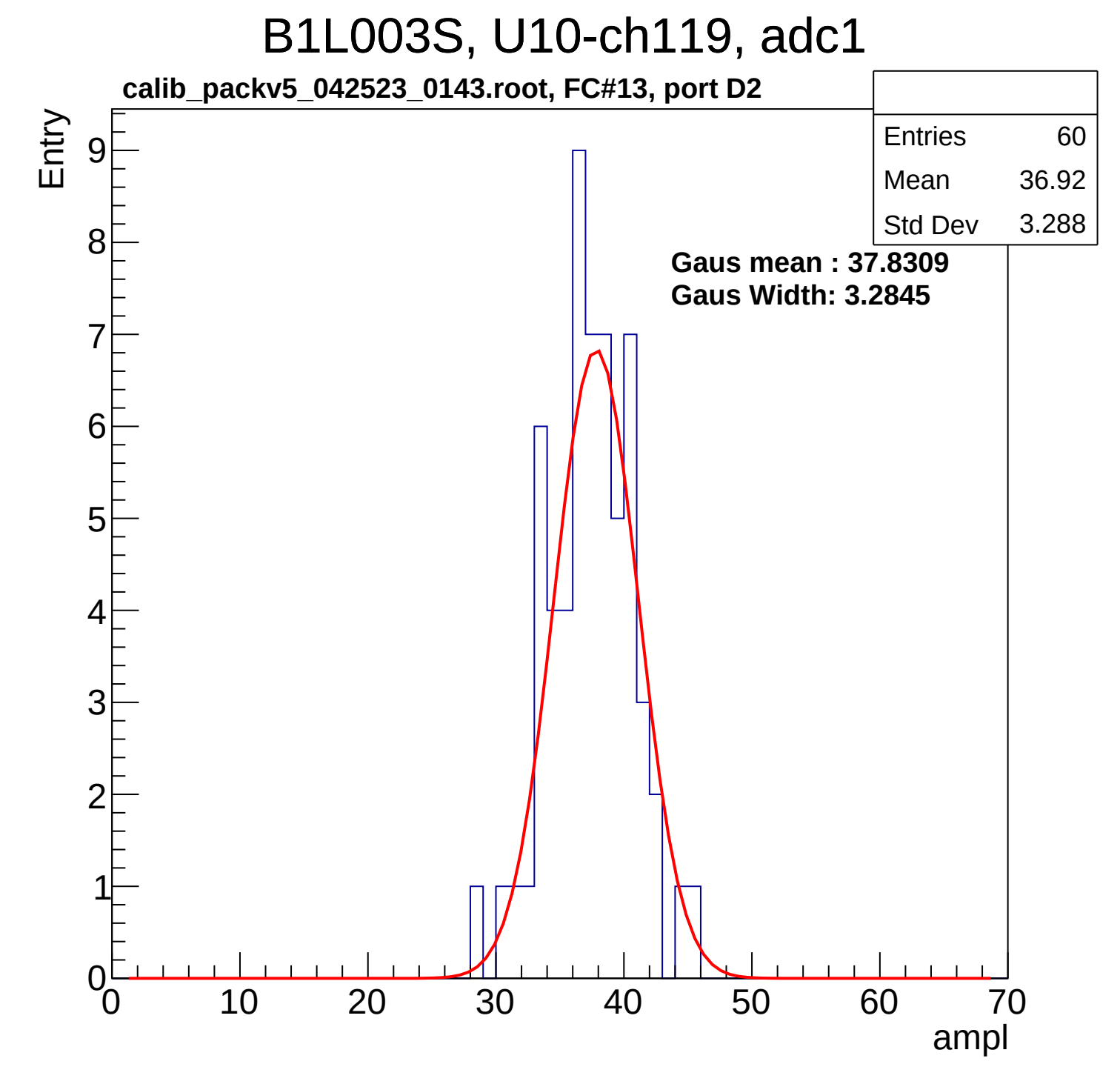
B1L003S, U10-ch119, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.92
Std Dev 3.288
Gaus mean : 37.8309
Gaus Width: 3.2845
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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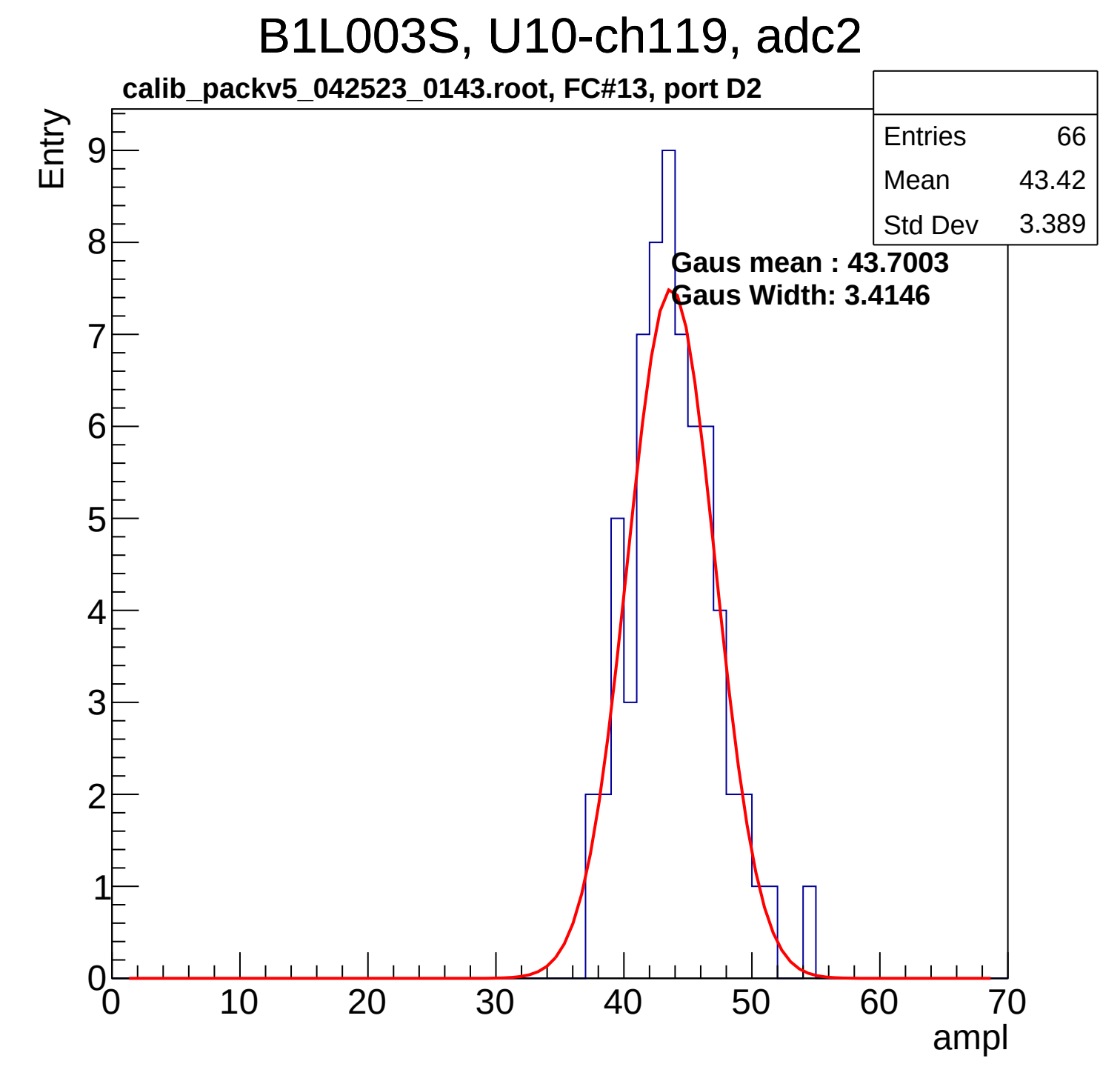
B1L003S, U10-ch119, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.42
Std Dev 3.389
Gaus mean : 43.7003
Gaus Width: 3.4146
Entry
ampl
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2
3
4
5
6
7
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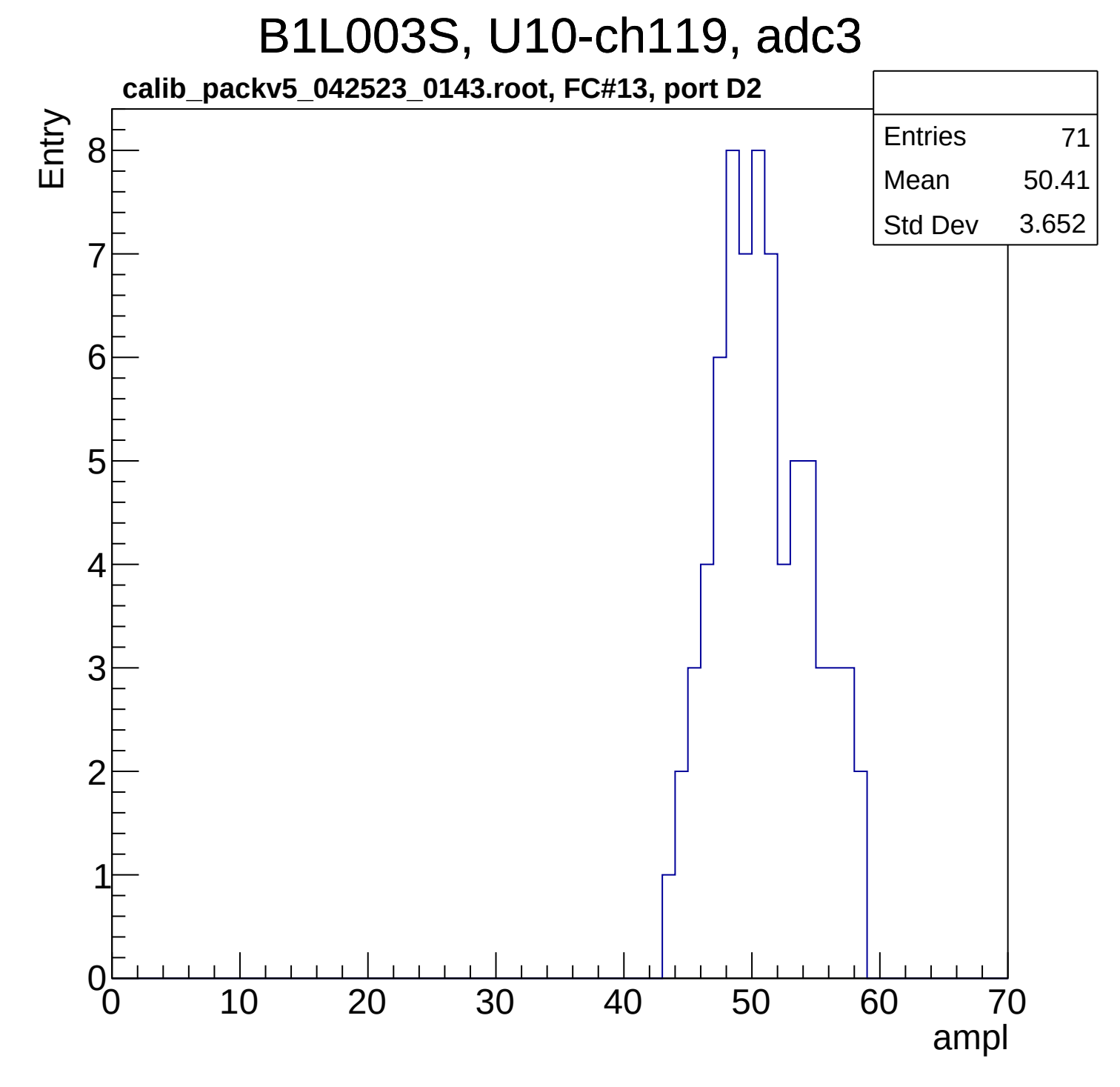
B1L003S, U10-ch119, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 50.41
Std Dev 3.652
Entry
ampl
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3
4
5
6
7
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0
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40
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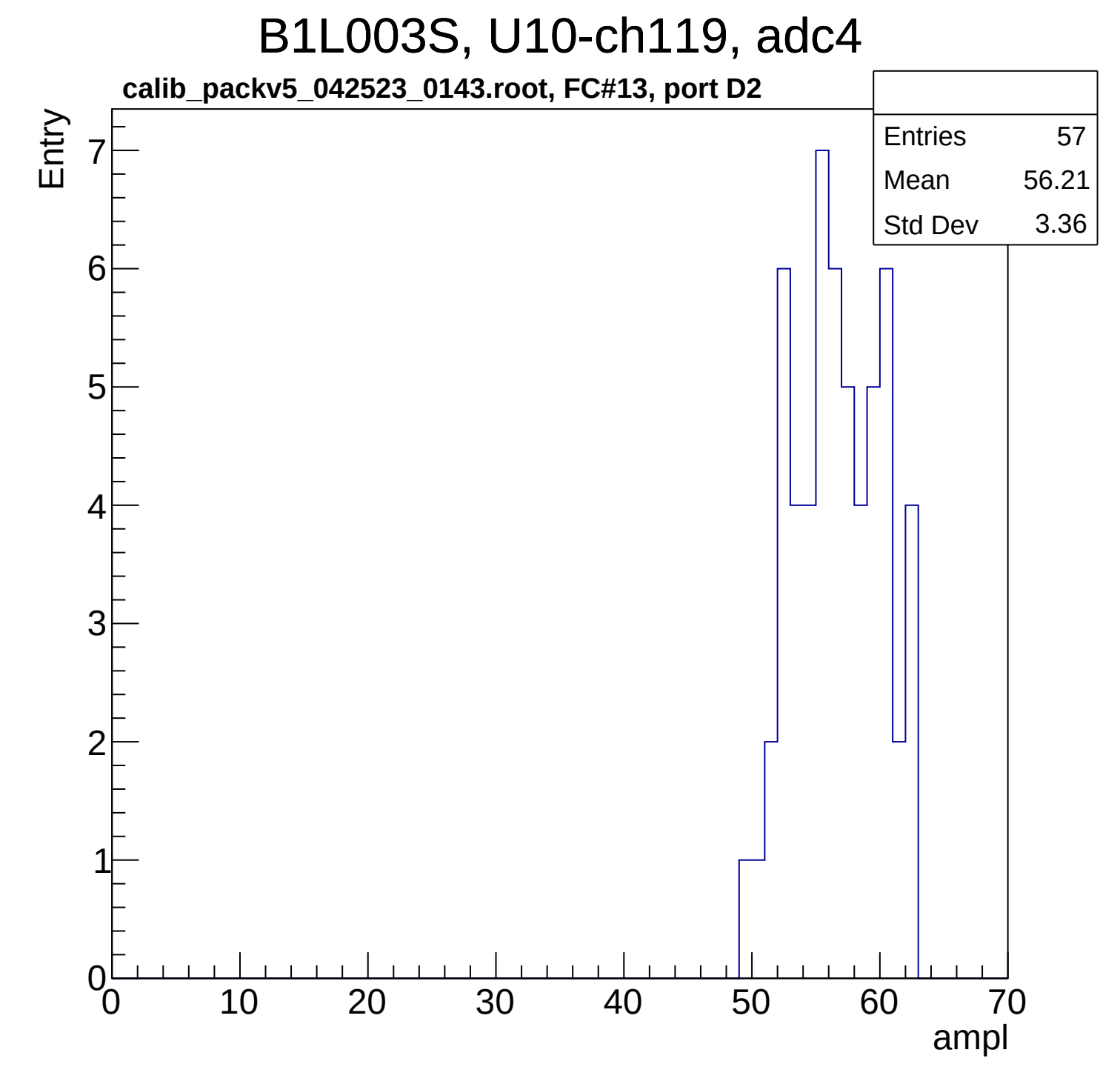
B1L003S, U10-ch119, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 56.21
Std Dev 3.36
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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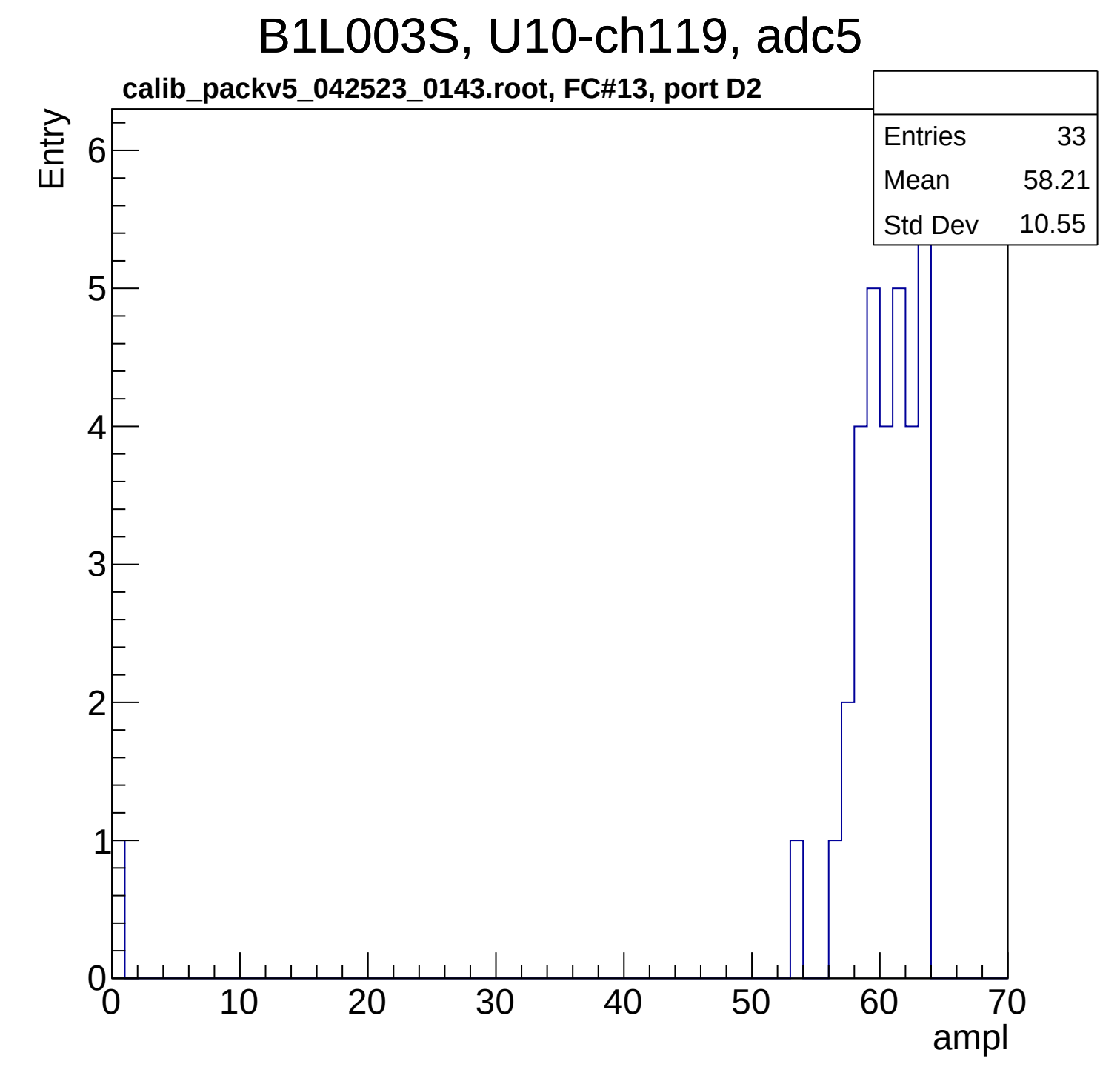
B1L003S, U10-ch119, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 58.21
Std Dev 10.55
Entry
ampl
0
1
2
3
4
5
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0
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60
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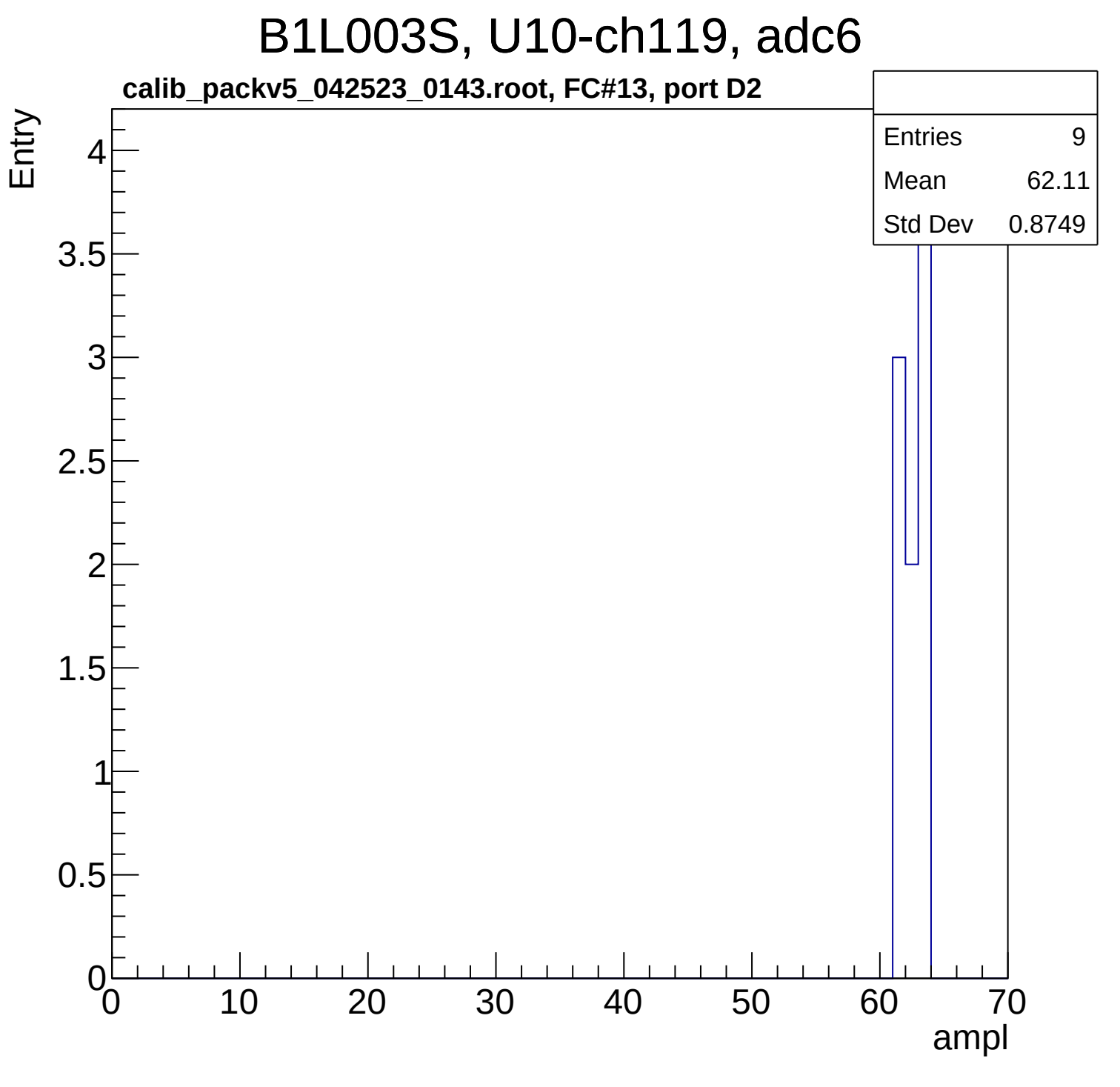
B1L003S, U10-ch119, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 0.8749
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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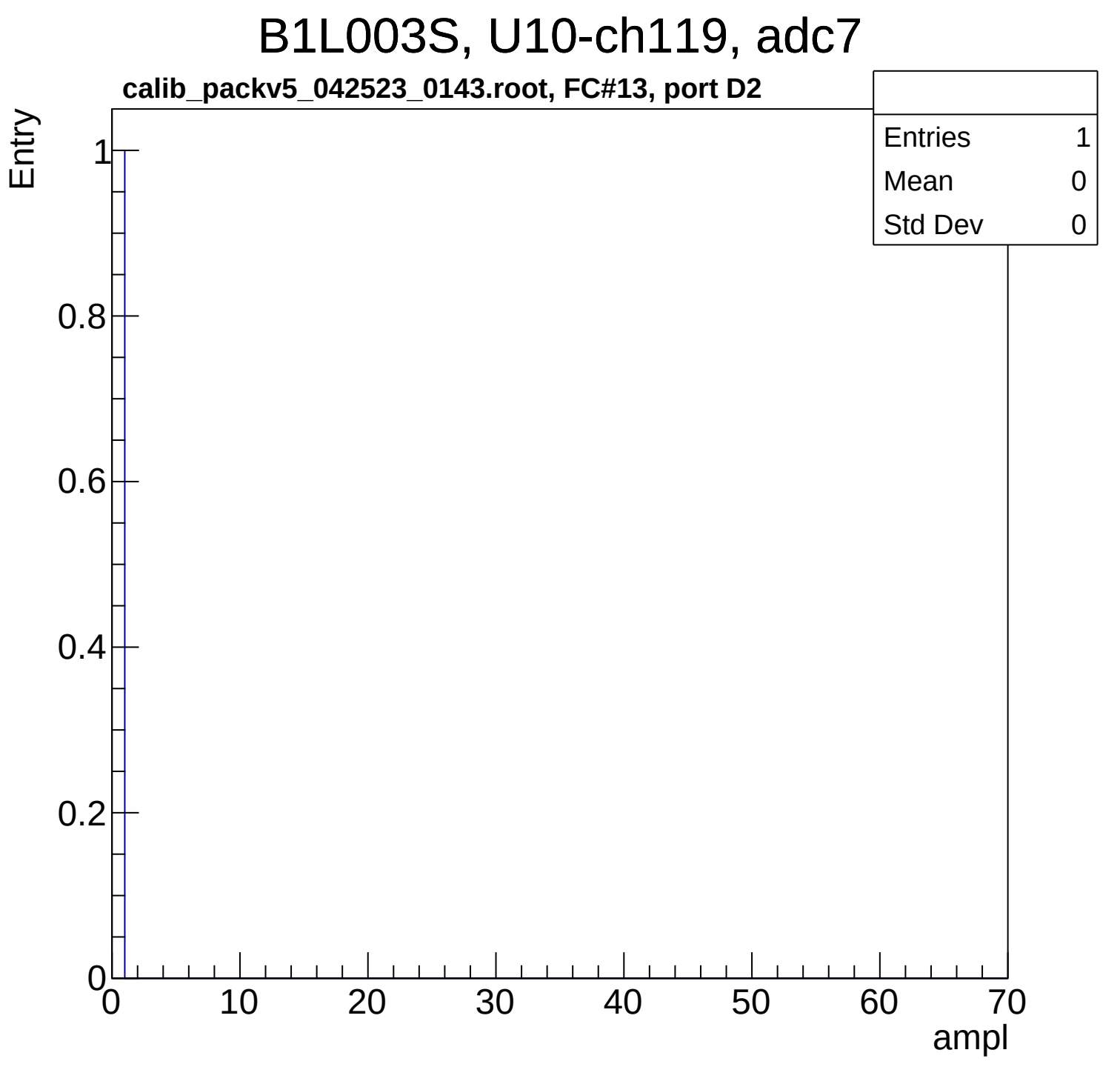
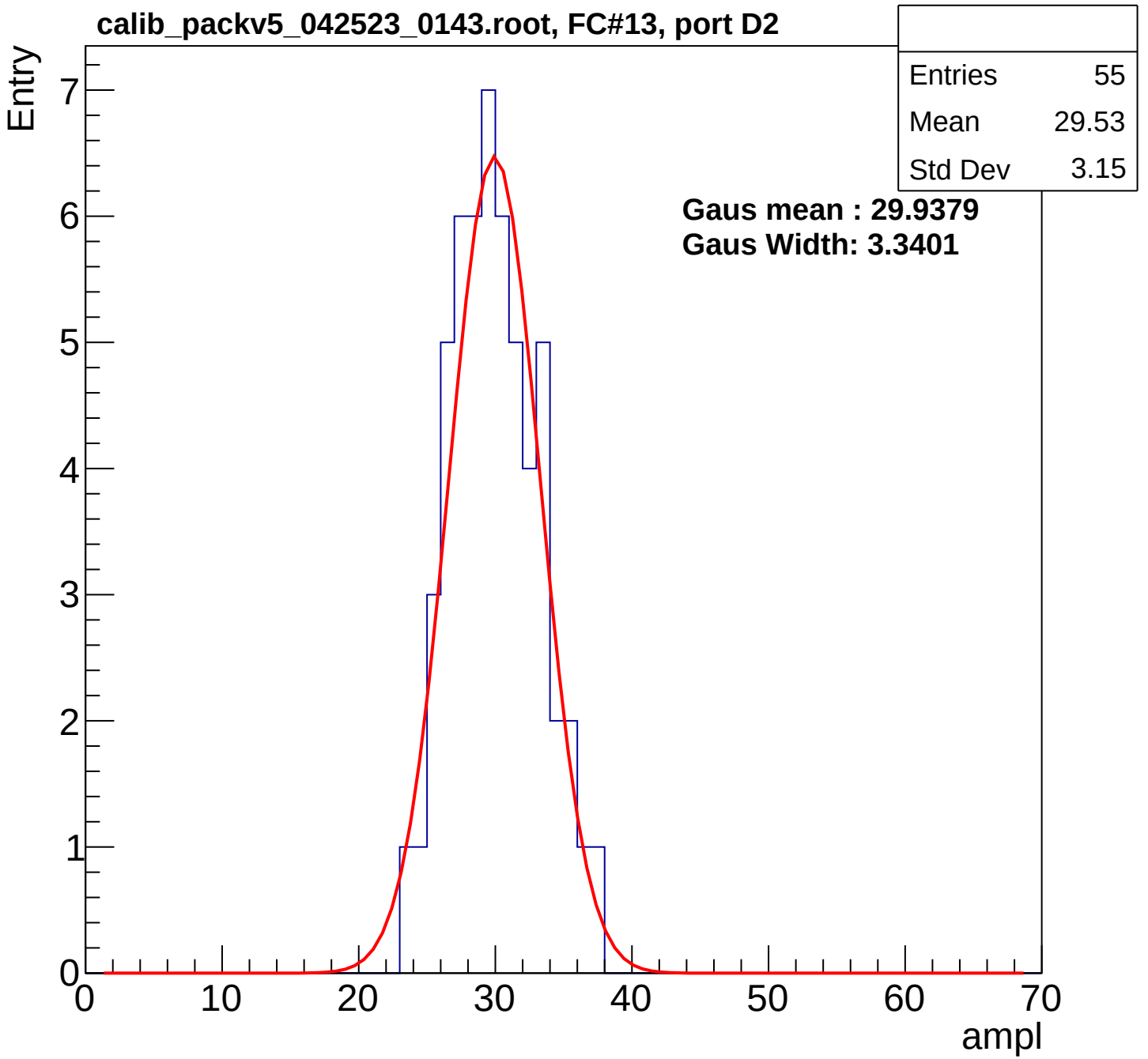

B1L003S, U10-ch119, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch120, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.53
Std Dev 3.15
Gaus mean : 29.9379
Gaus Width: 3.3401
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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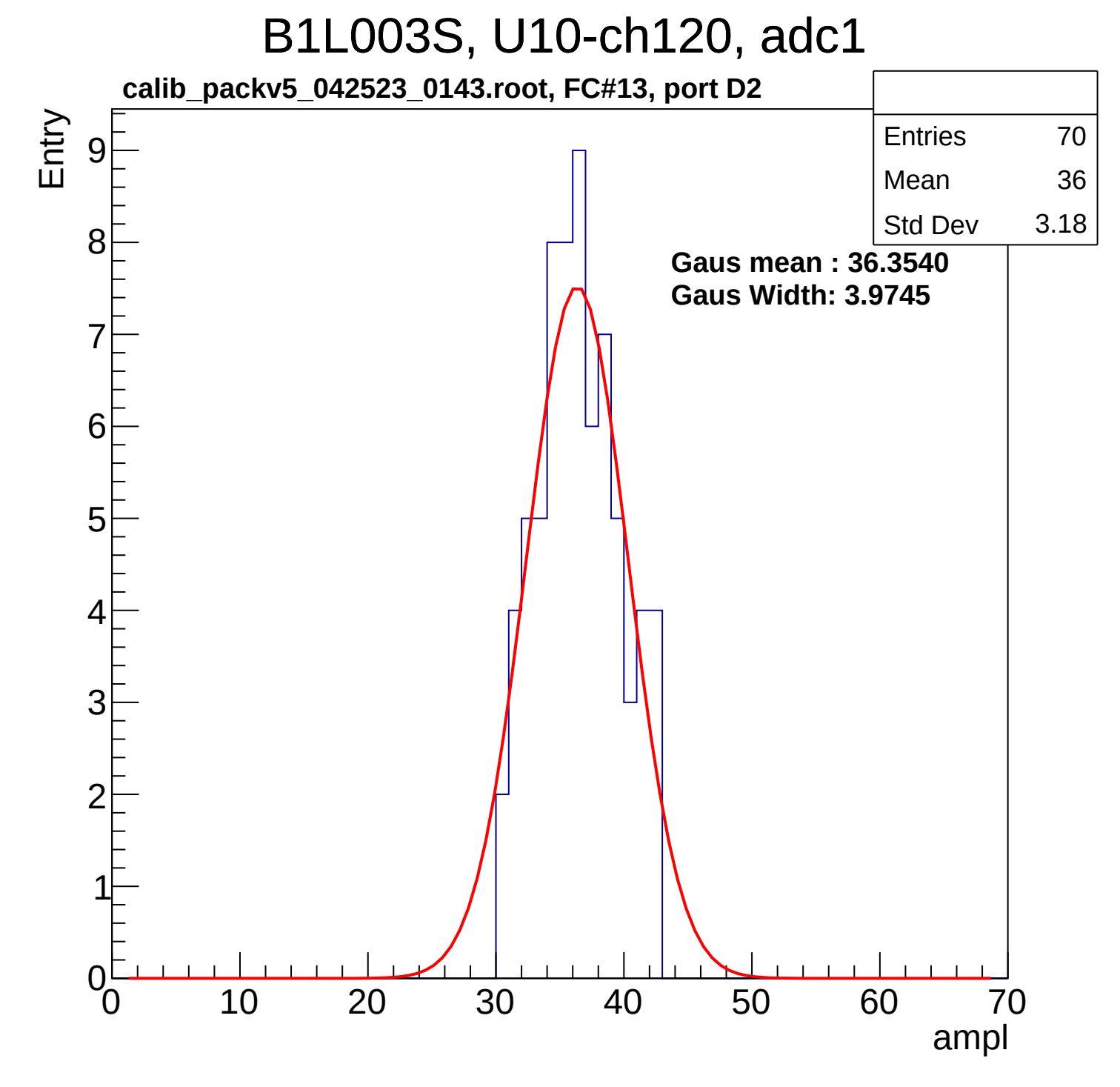
B1L003S, U10-ch120, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36
Std Dev 3.18
Gaus mean : 36.3540
Gaus Width: 3.9745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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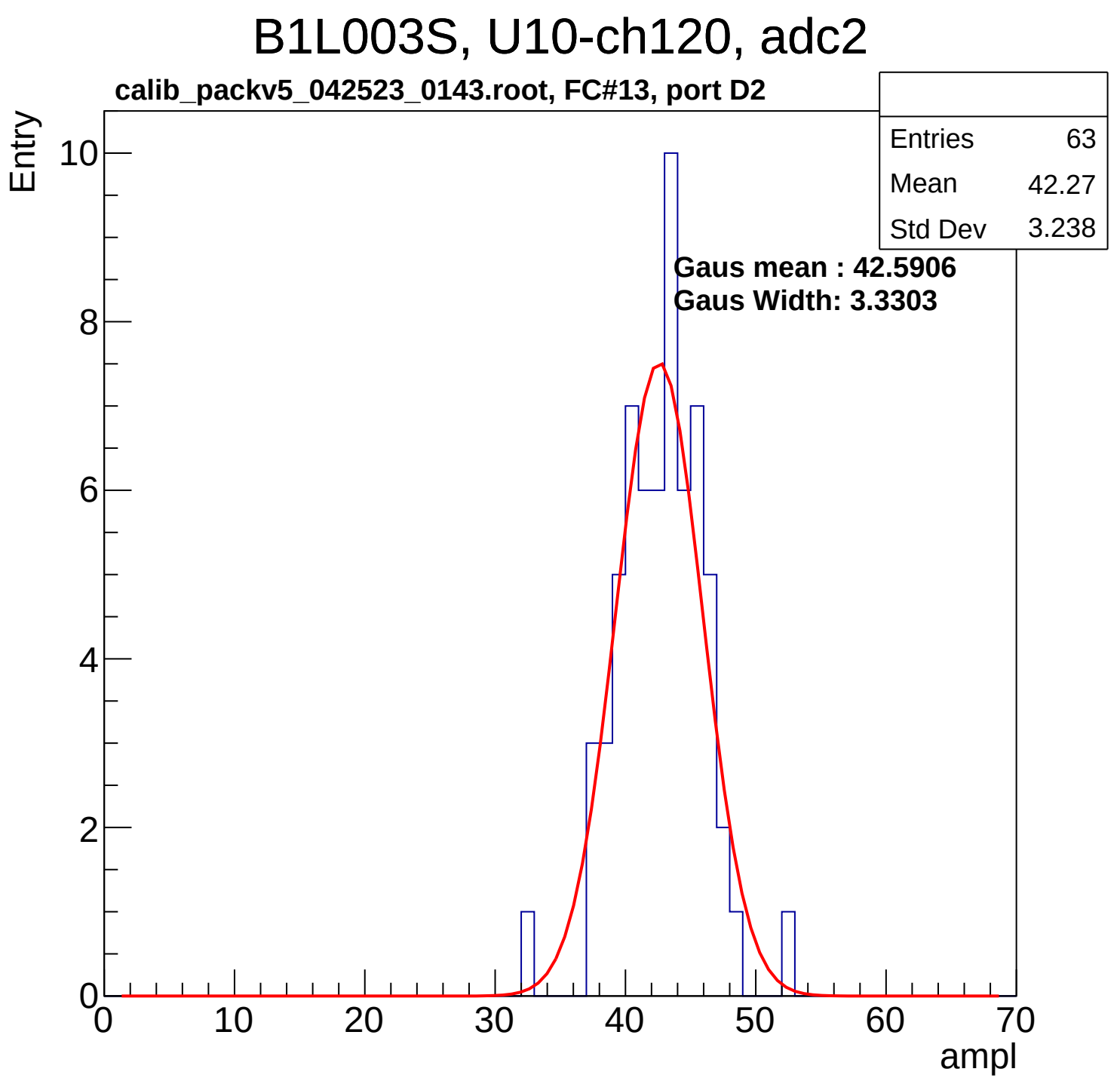
B1L003S, U10-ch120, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 42.27
Std Dev 3.238
Gaus mean : 42.5906
Gaus Width: 3.3303
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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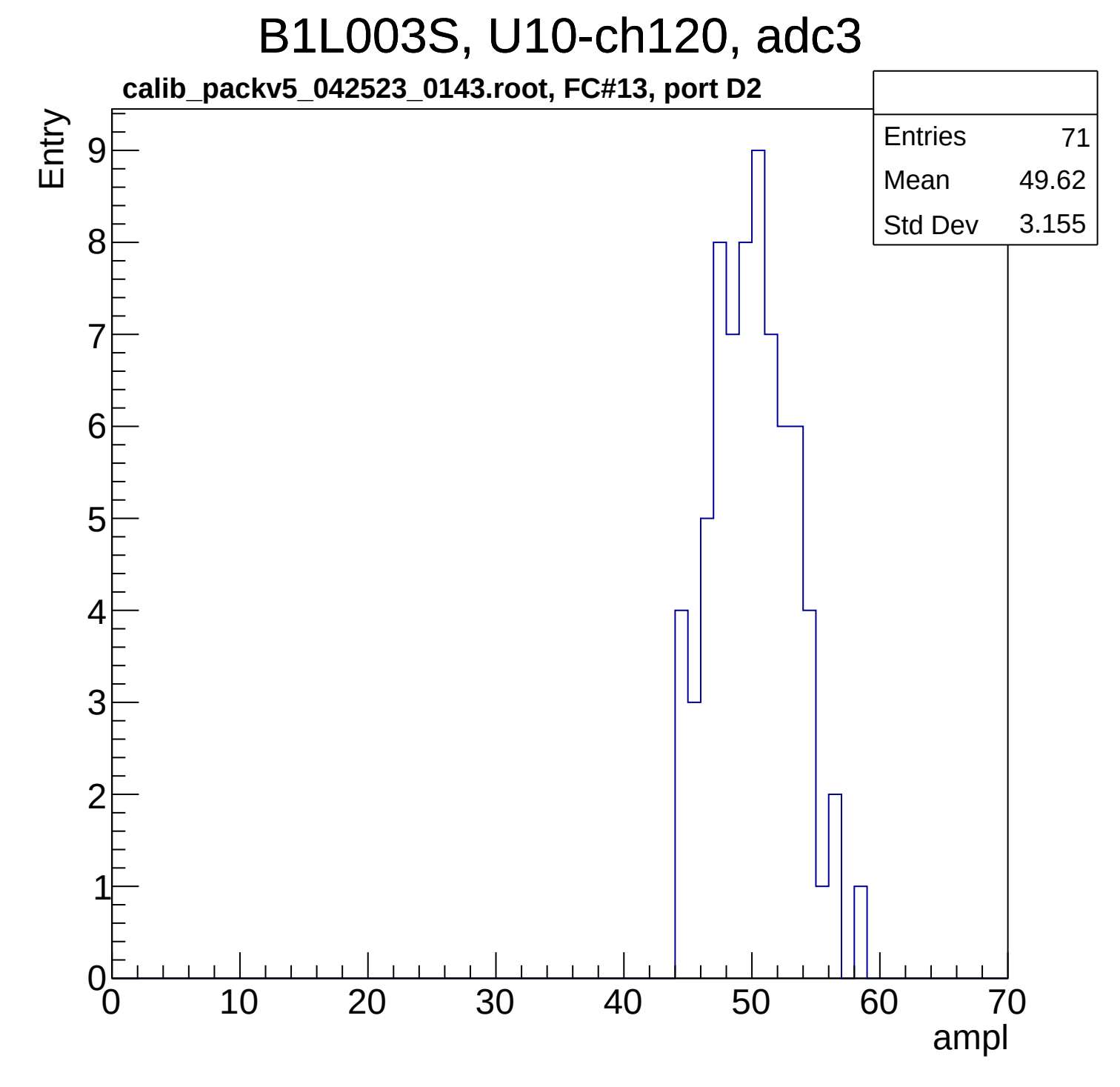
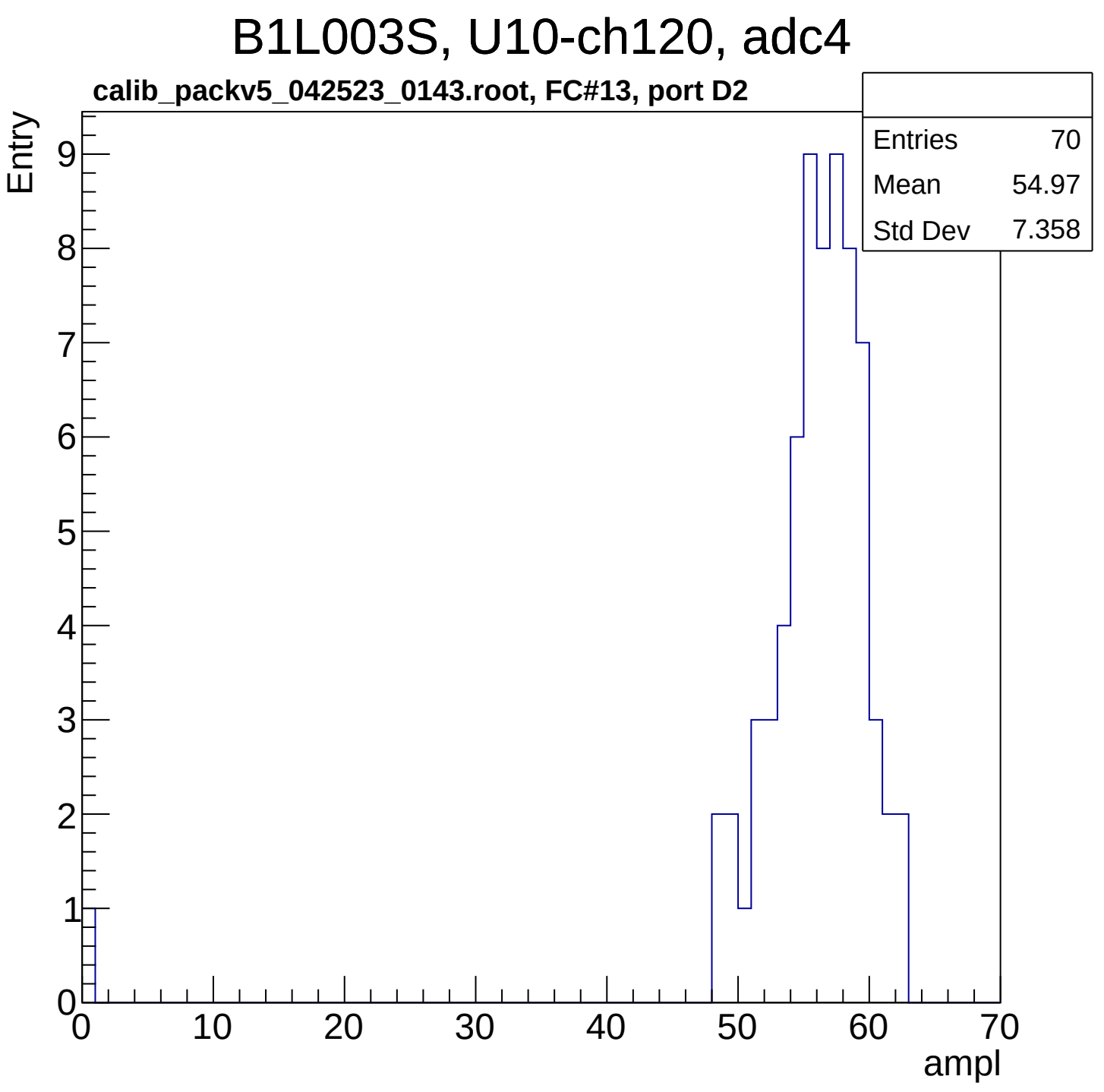

B1L003S, U10-ch120, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.62
Std Dev 3.155
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch120, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 54.97
Std Dev 7.358
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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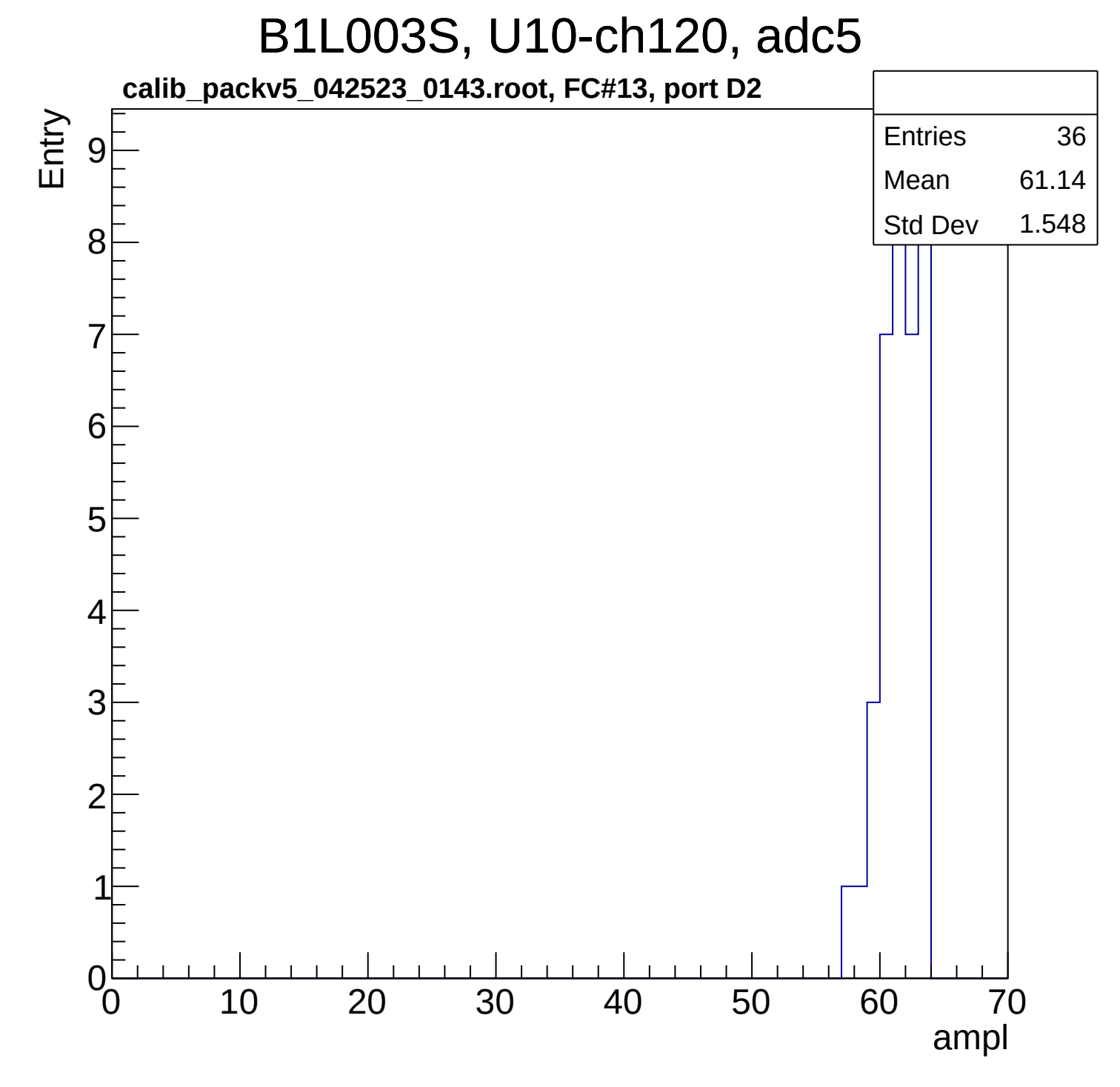
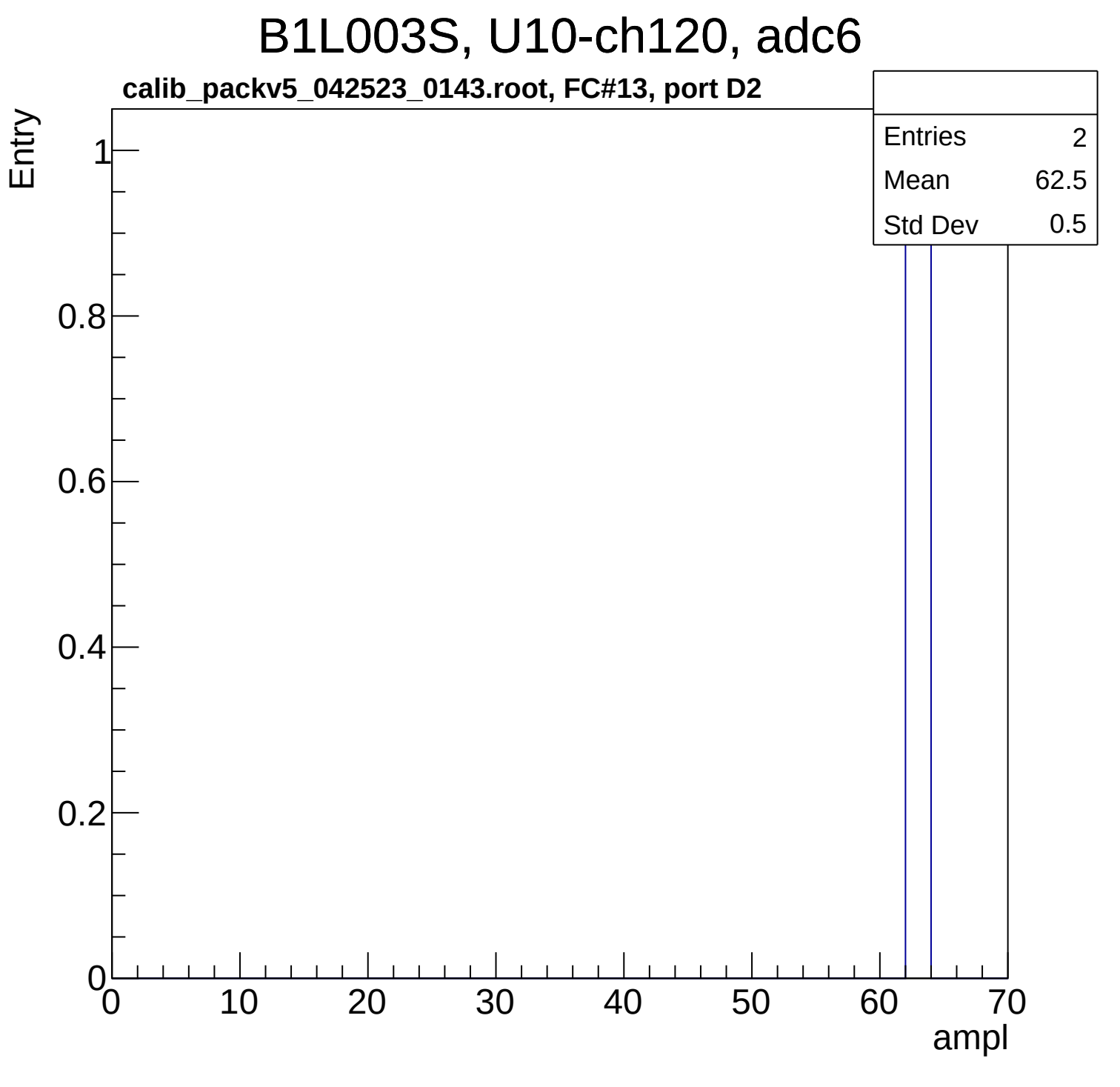

B1L003S, U10-ch120, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 61.14
Std Dev 1.548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch120, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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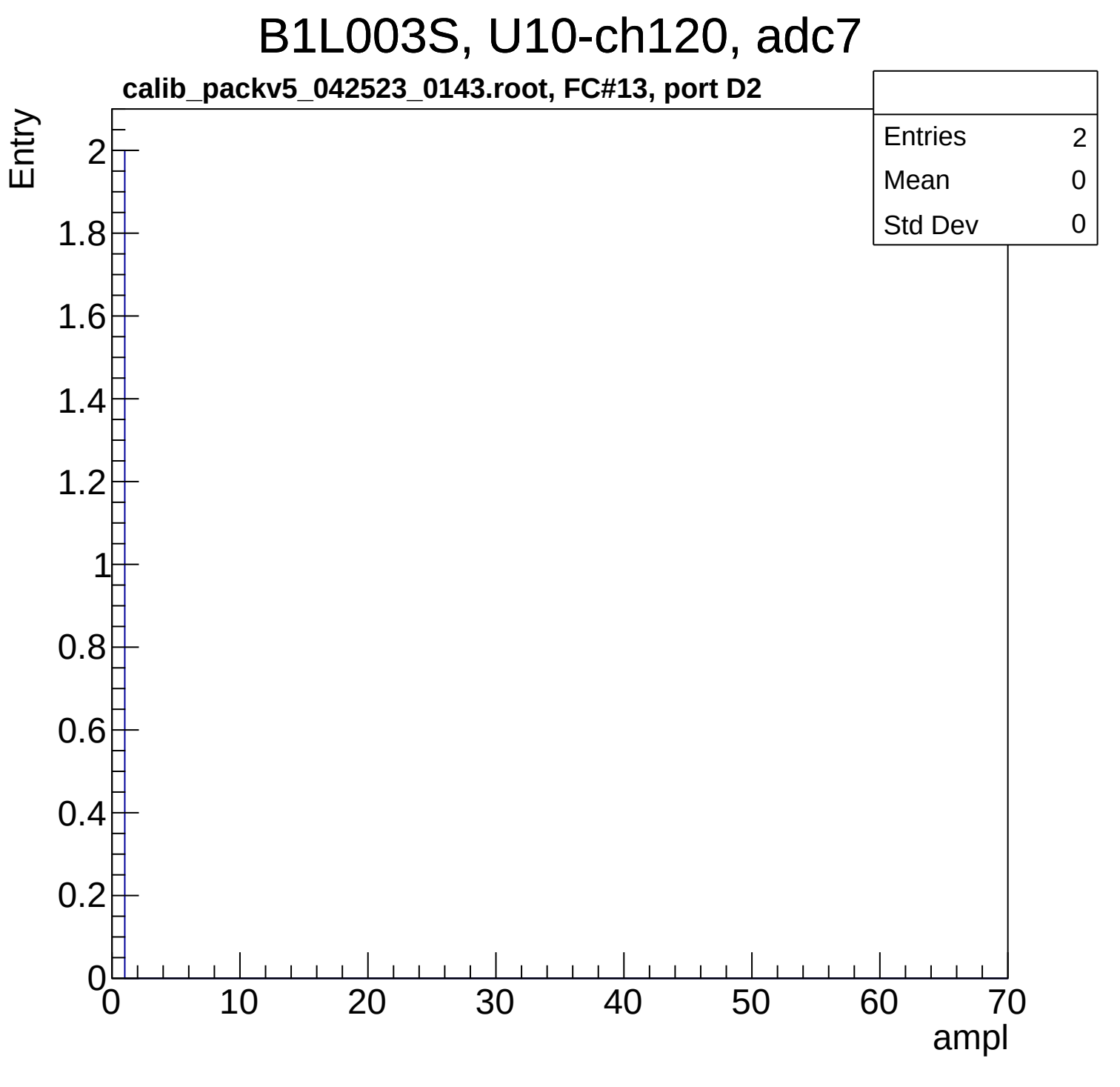
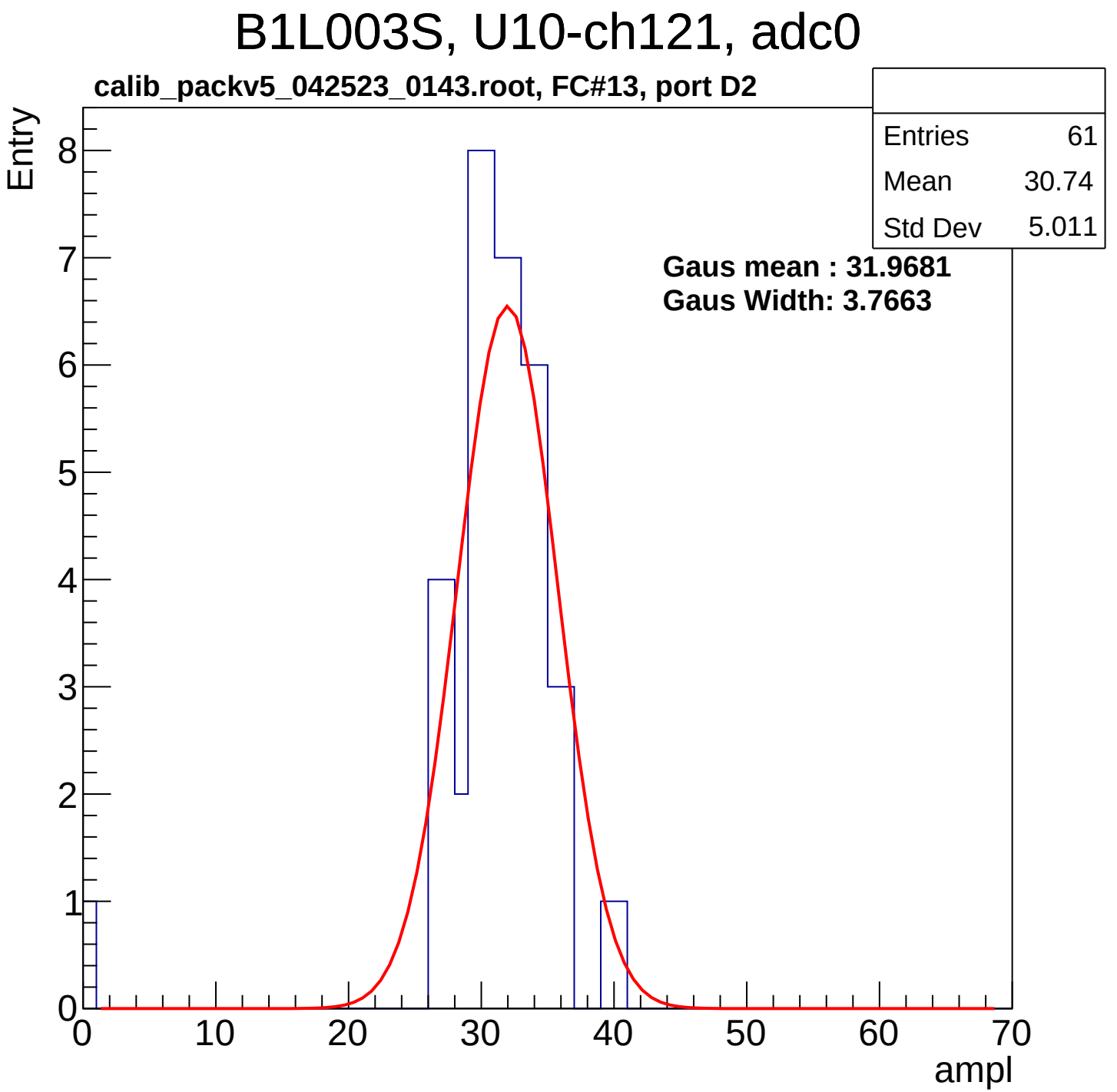
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch120, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch121, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 30.74
Std Dev 5.011
Gaus mean : 31.9681
Gaus Width: 3.7663
Entry
ampl
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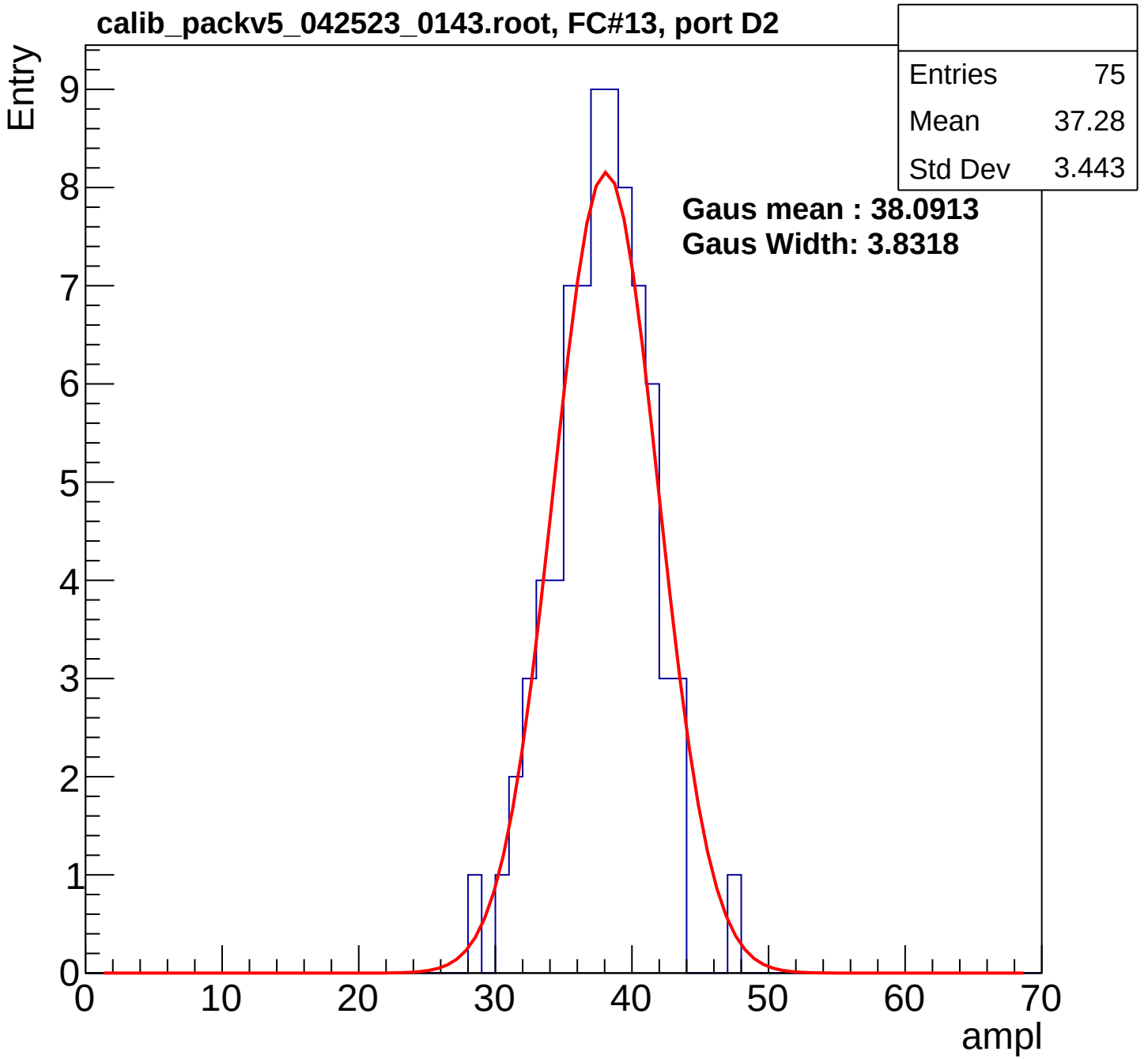
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch121, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 37.28
Std Dev 3.443
Gaus mean : 38.0913
Gaus Width: 3.8318
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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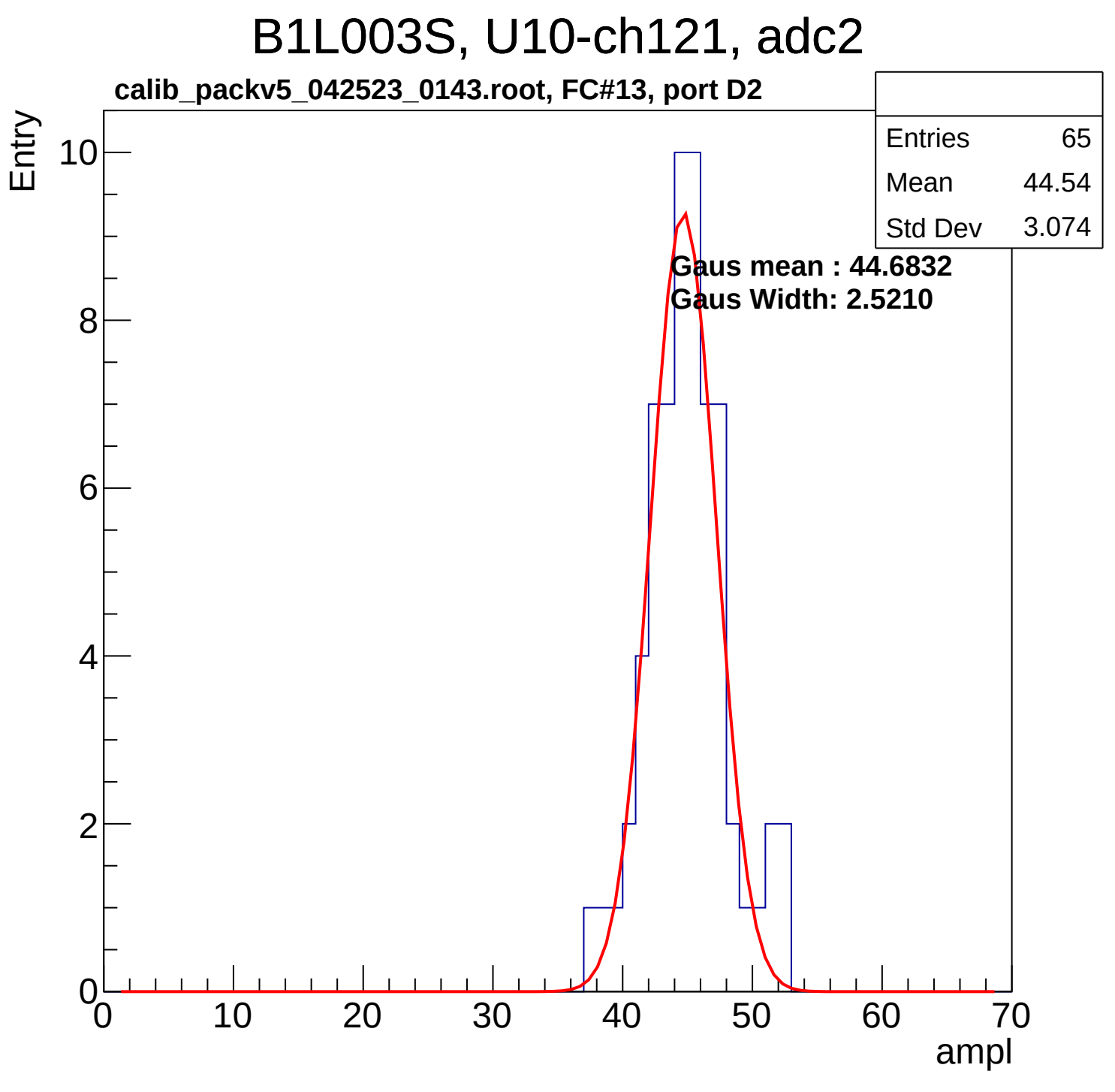
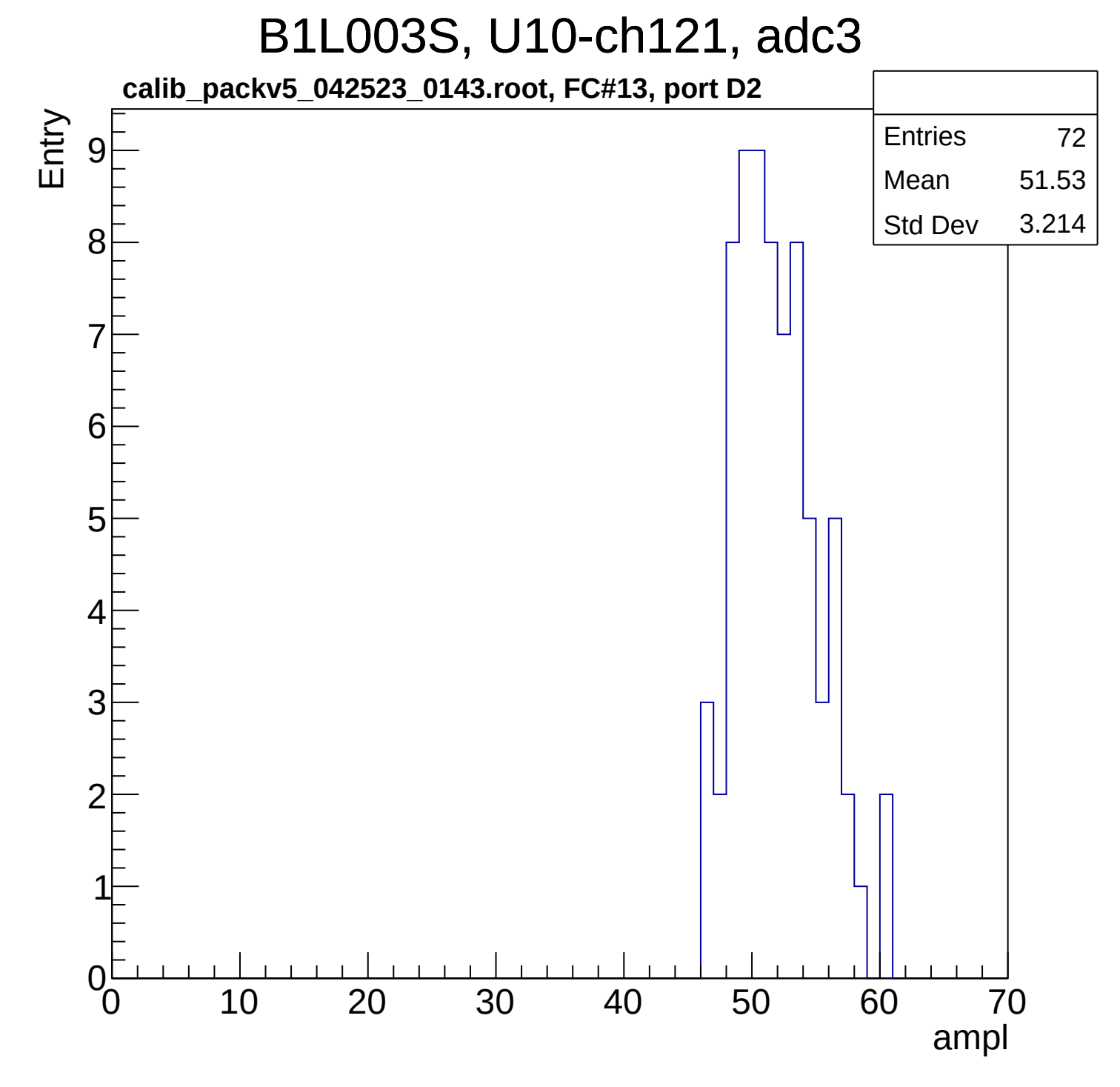

B1L003S, U10-ch121, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 44.54
Std Dev 3.074
Gaus mean : 44.6832
Gaus Width: 2.5210
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch121, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 51.53
Std Dev 3.214
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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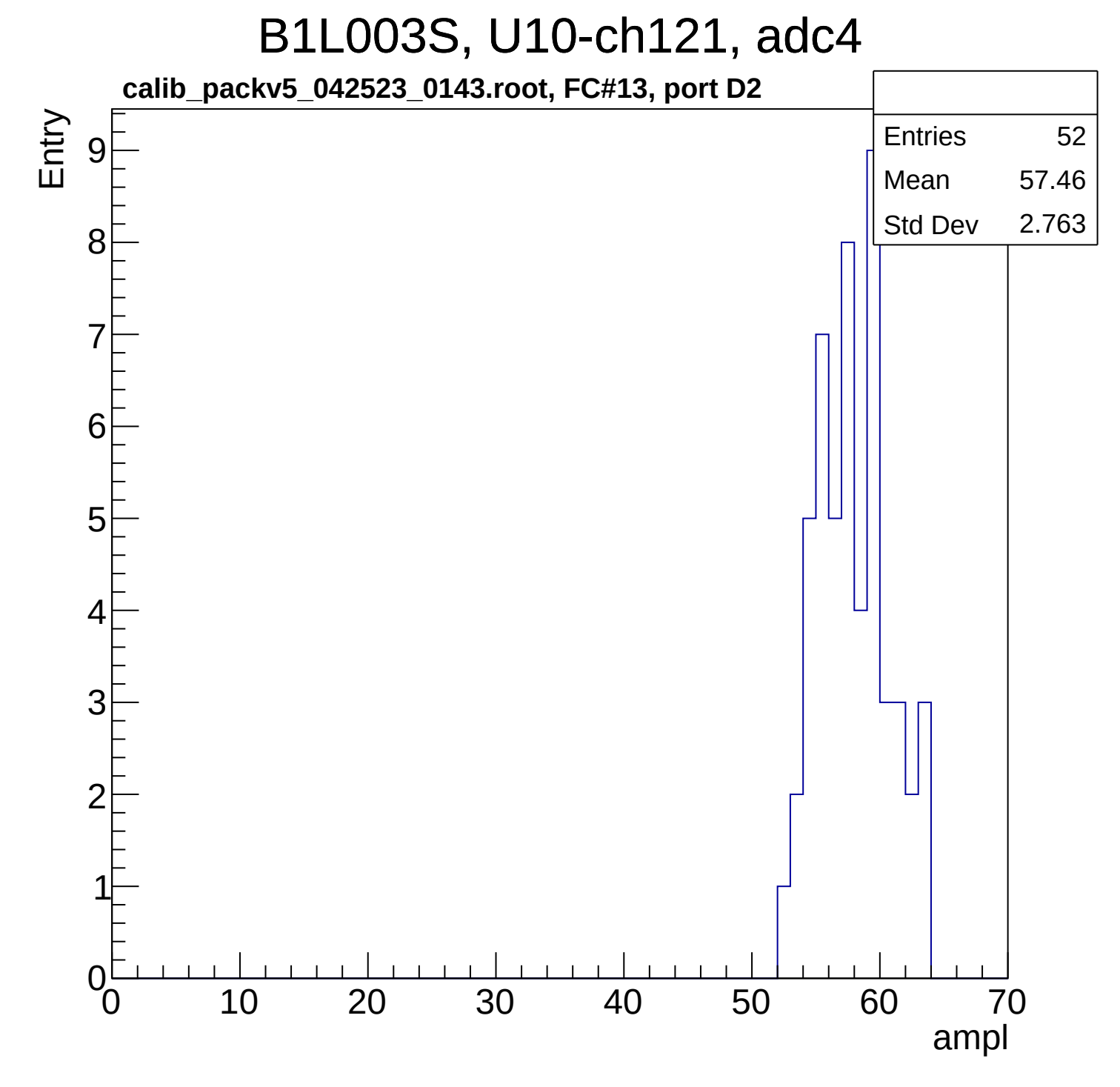
B1L003S, U10-ch121, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 57.46
Std Dev 2.763
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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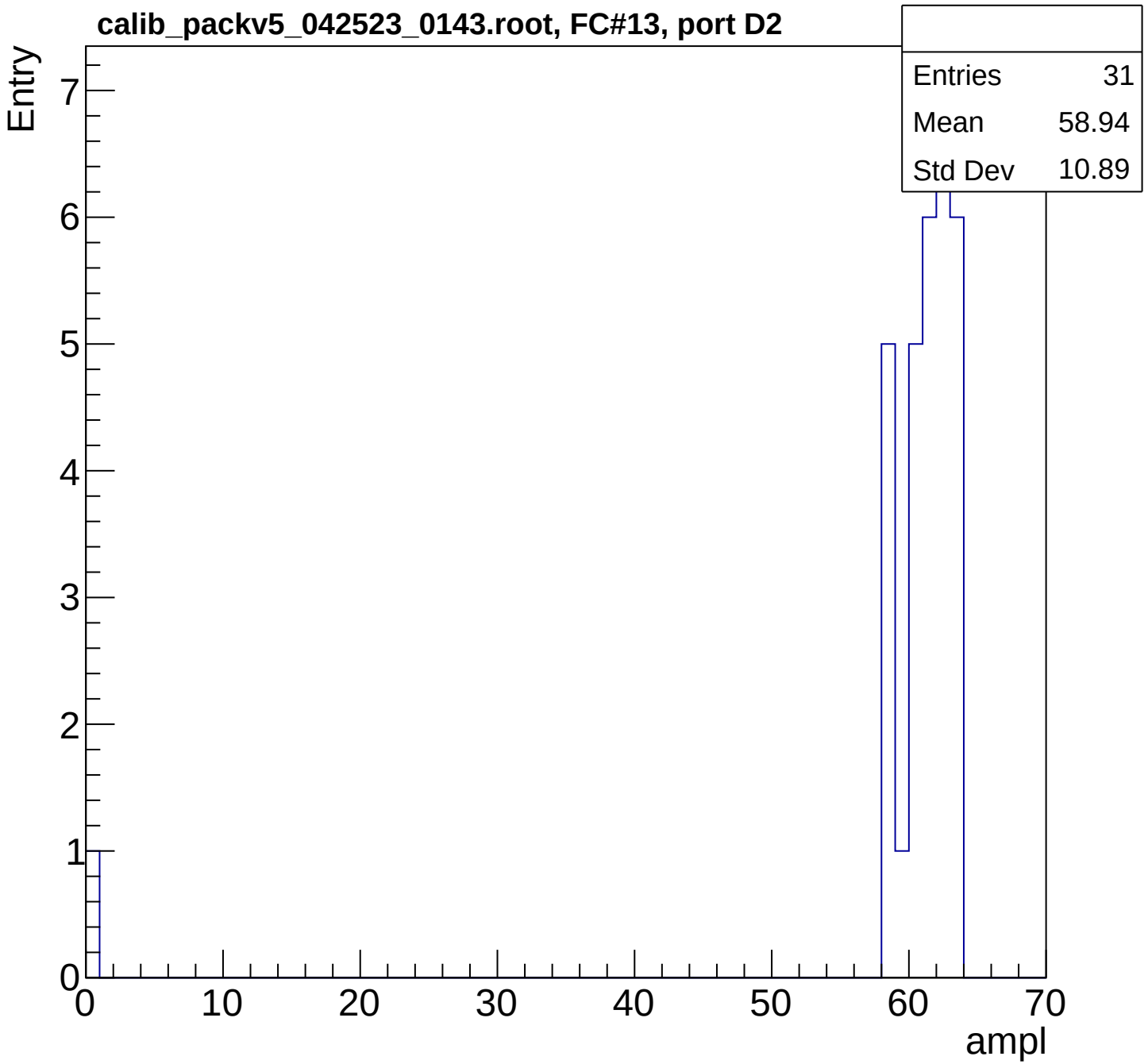
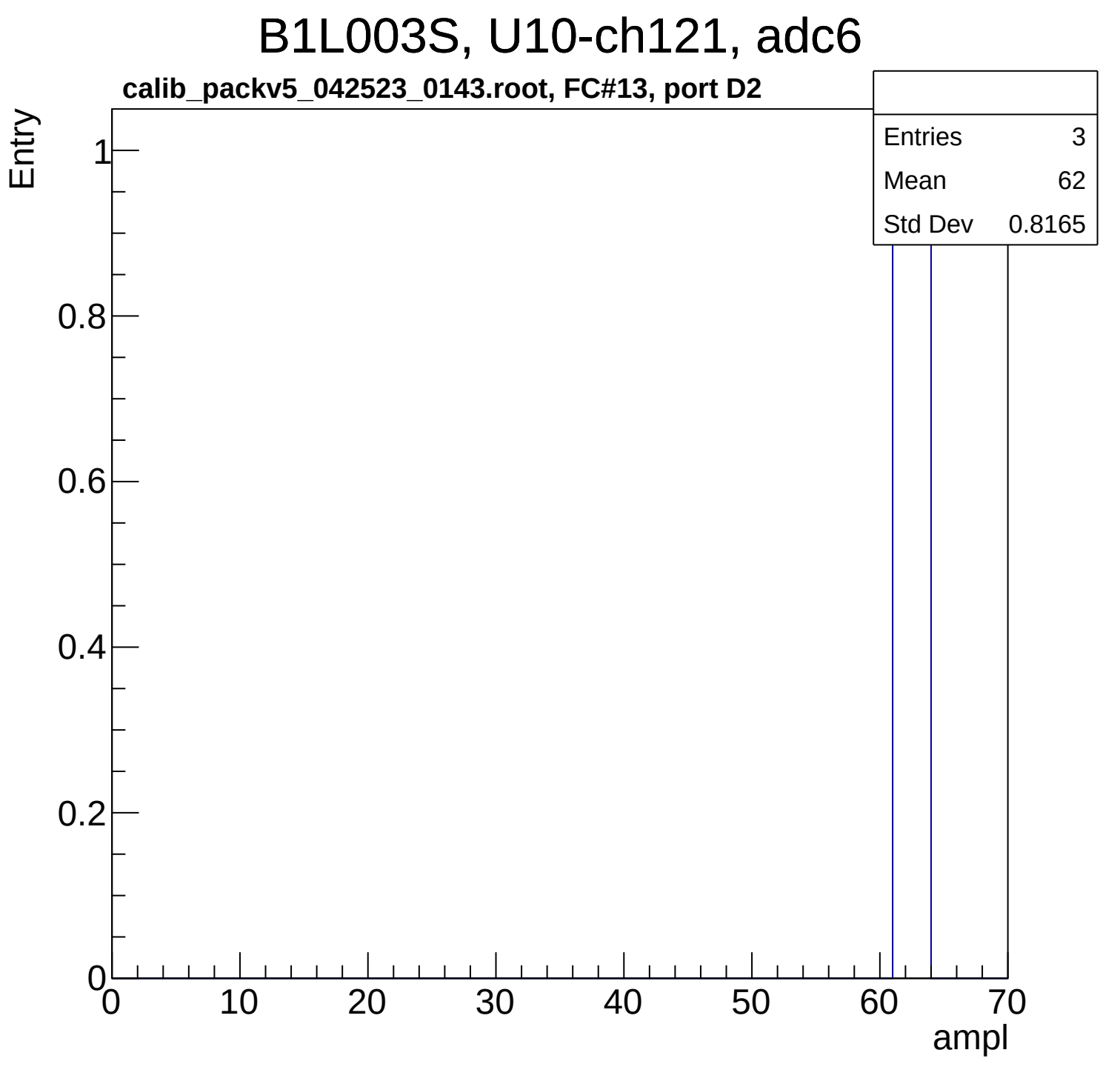

B1L003S, U10-ch121, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 31
Mean 58.94
Std Dev 10.89
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch121, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
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0.6
0.4
0.2
0
0
10
20
30
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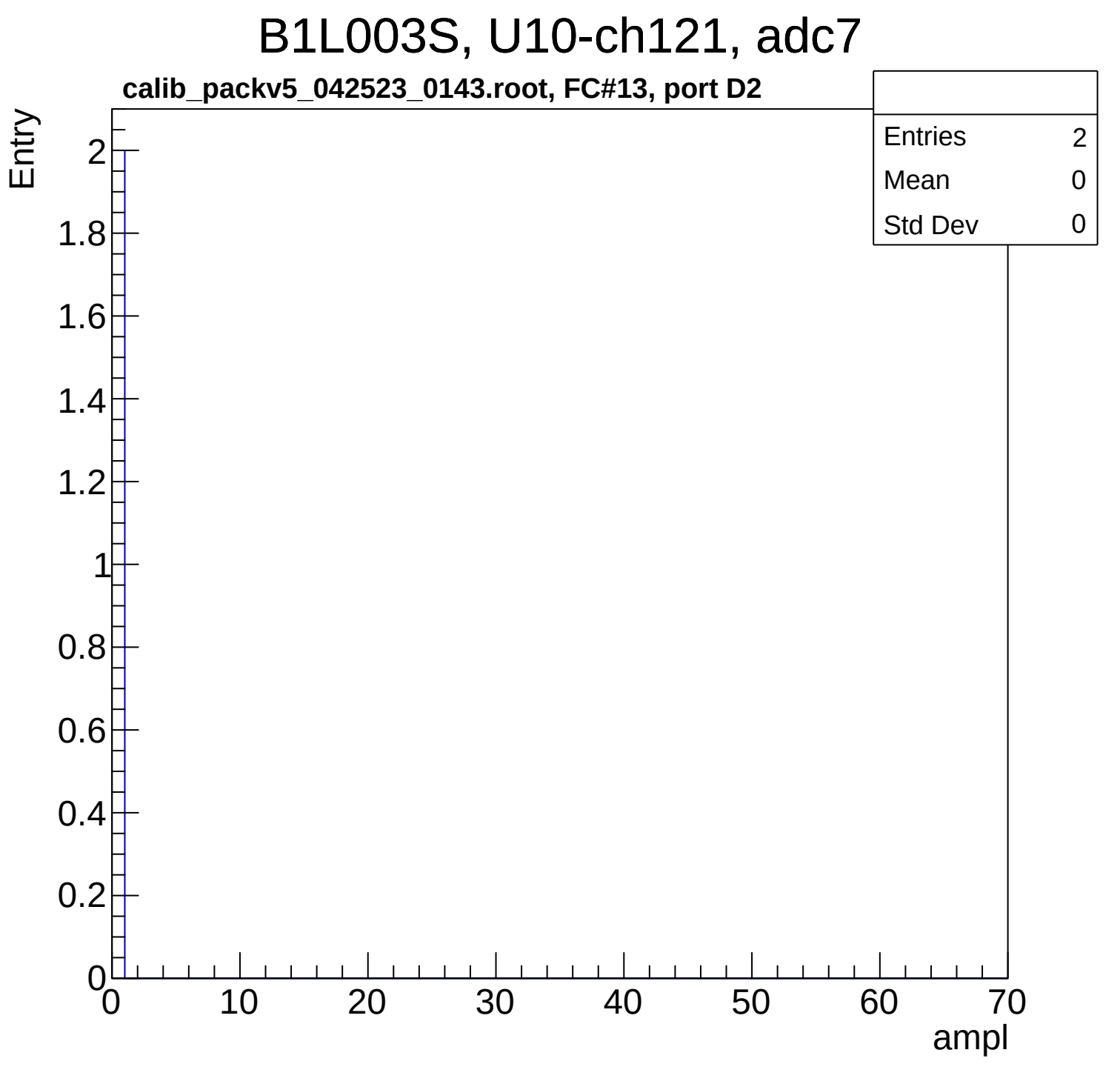
50
60
70
ampl


B1L003S, U10-ch121, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
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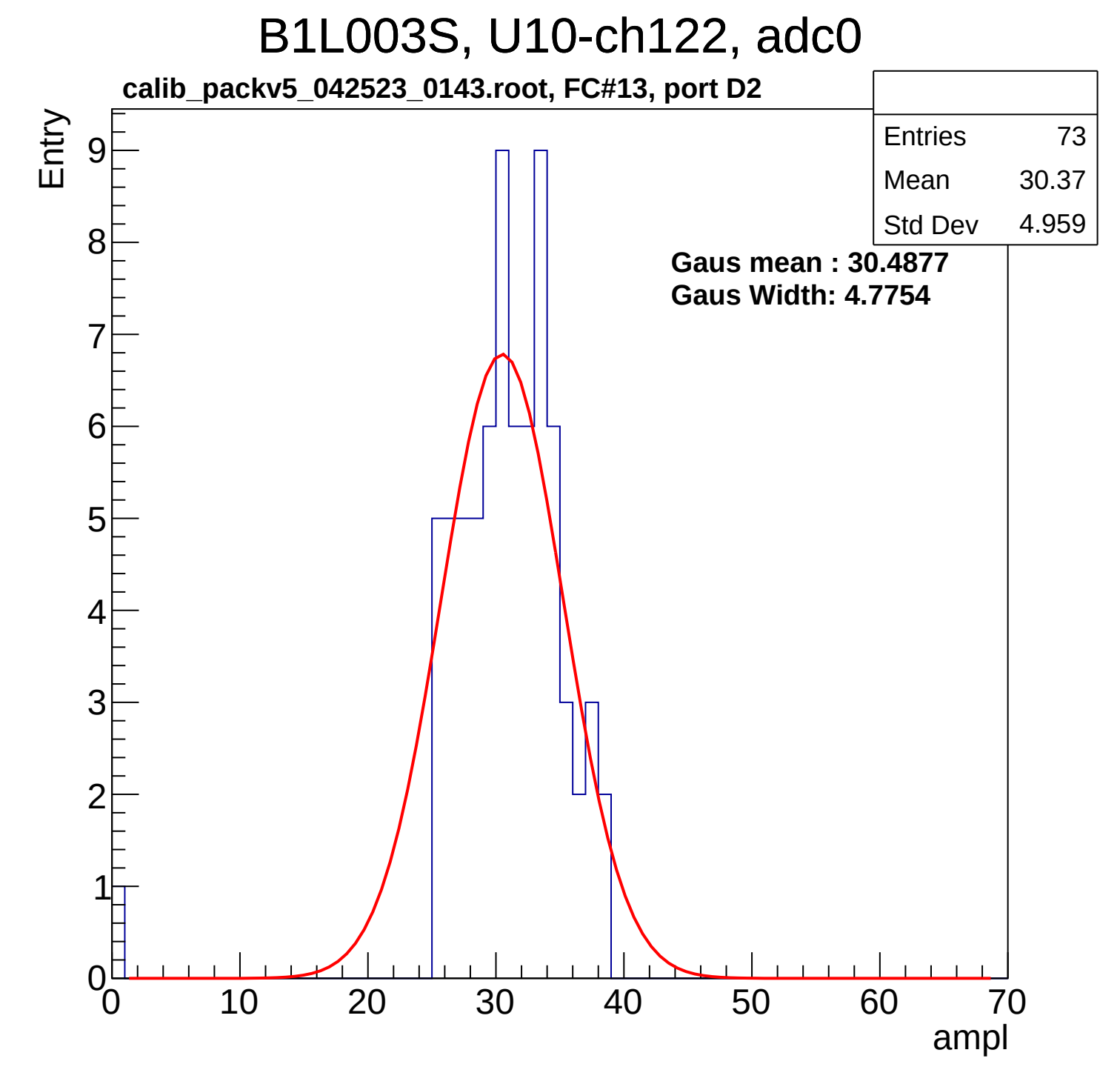
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch122, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 30.37
Std Dev 4.959
Gaus mean : 30.4877
Gaus Width: 4.7754
Entry
ampl
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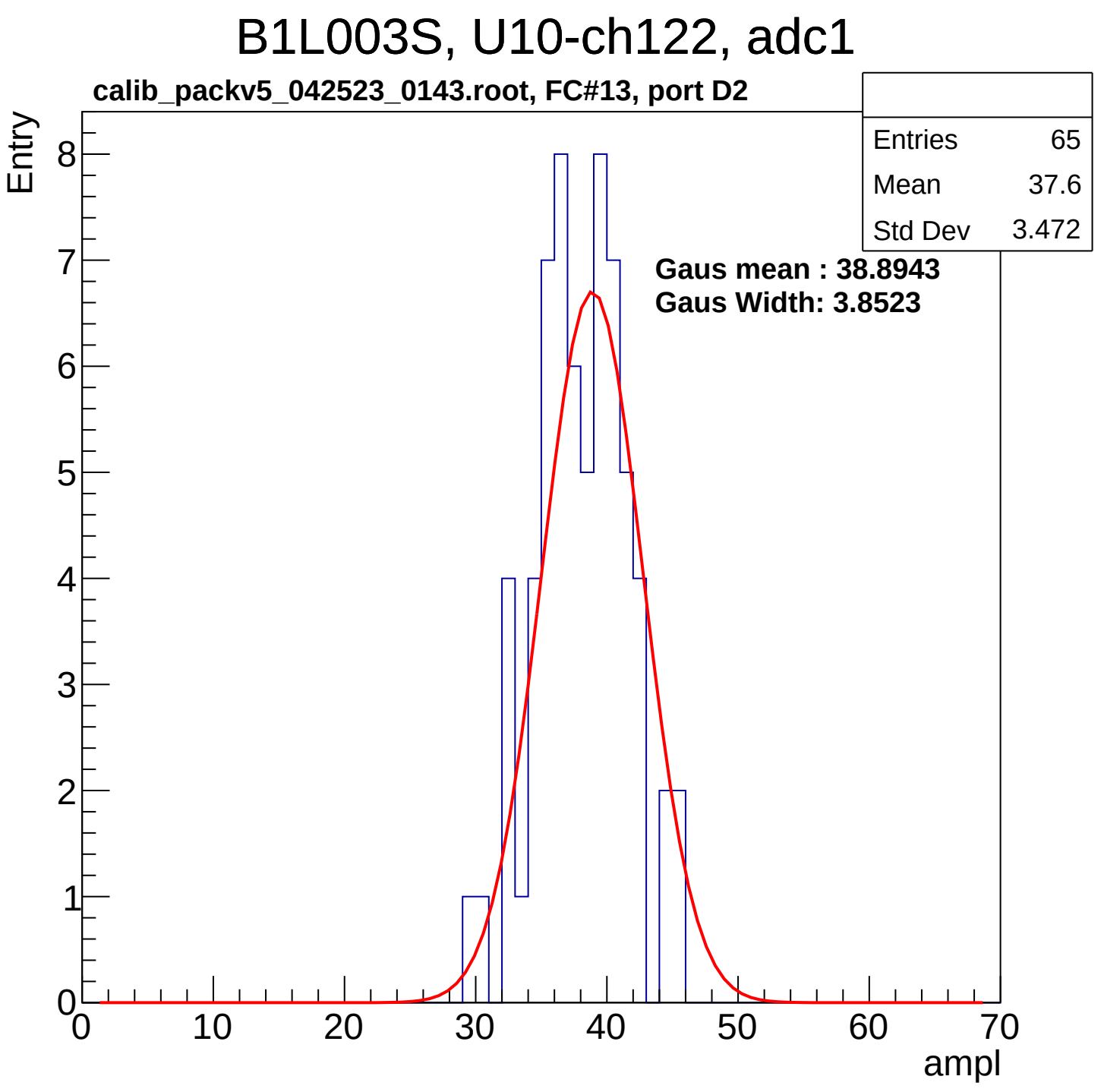
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch122, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 37.6
Std Dev 3.472
Gaus mean : 38.8943
Gaus Width: 3.8523
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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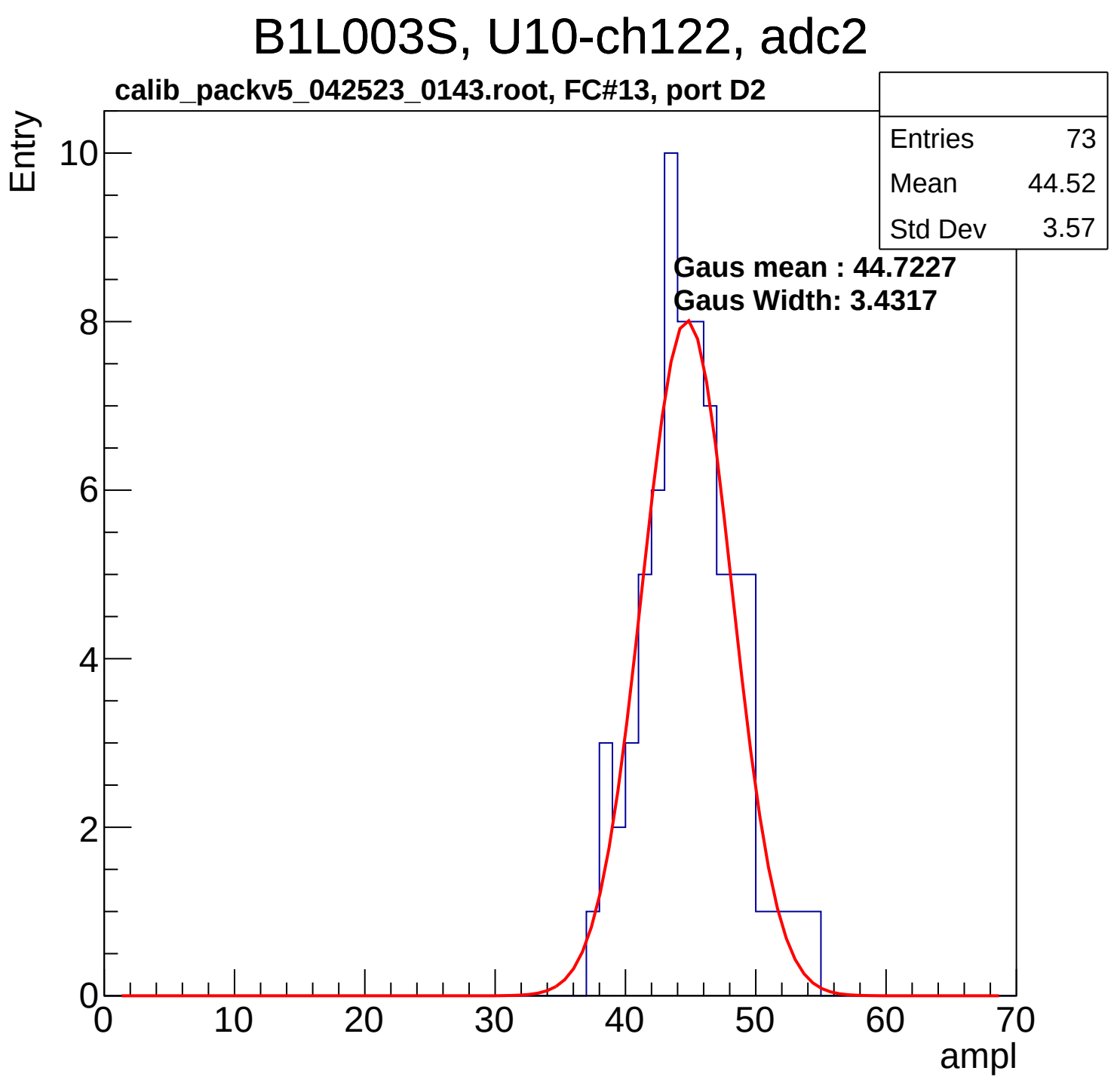
40
50
60
70


B1L003S, U10-ch122, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 44.52
Std Dev 3.57
Gaus mean : 44.7227
Gaus Width: 3.4317
Entry
ampl
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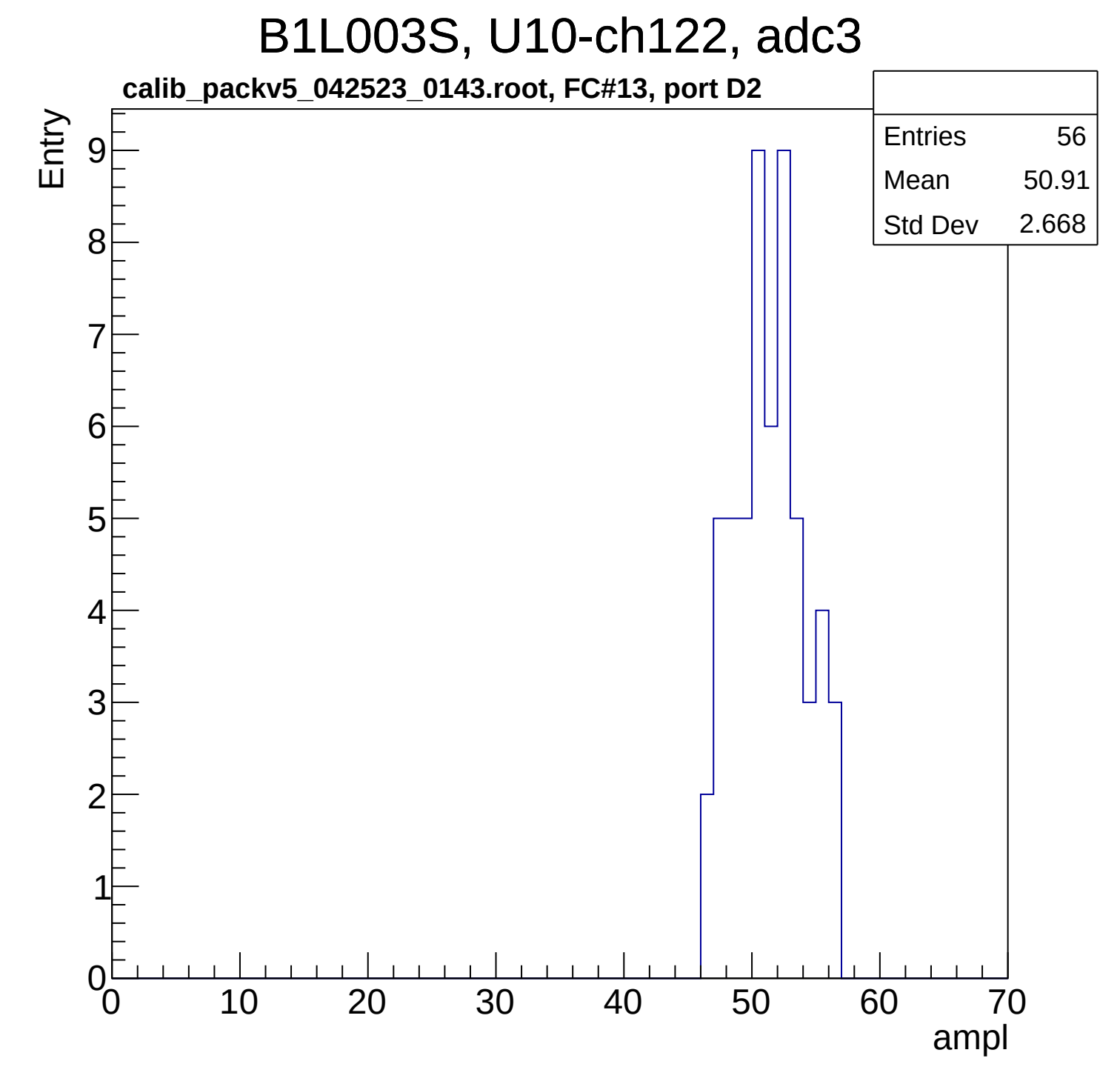
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U10-ch122, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 50.91
Std Dev 2.668
Entry
ampl
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3
4
5
6
7
8
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0
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60
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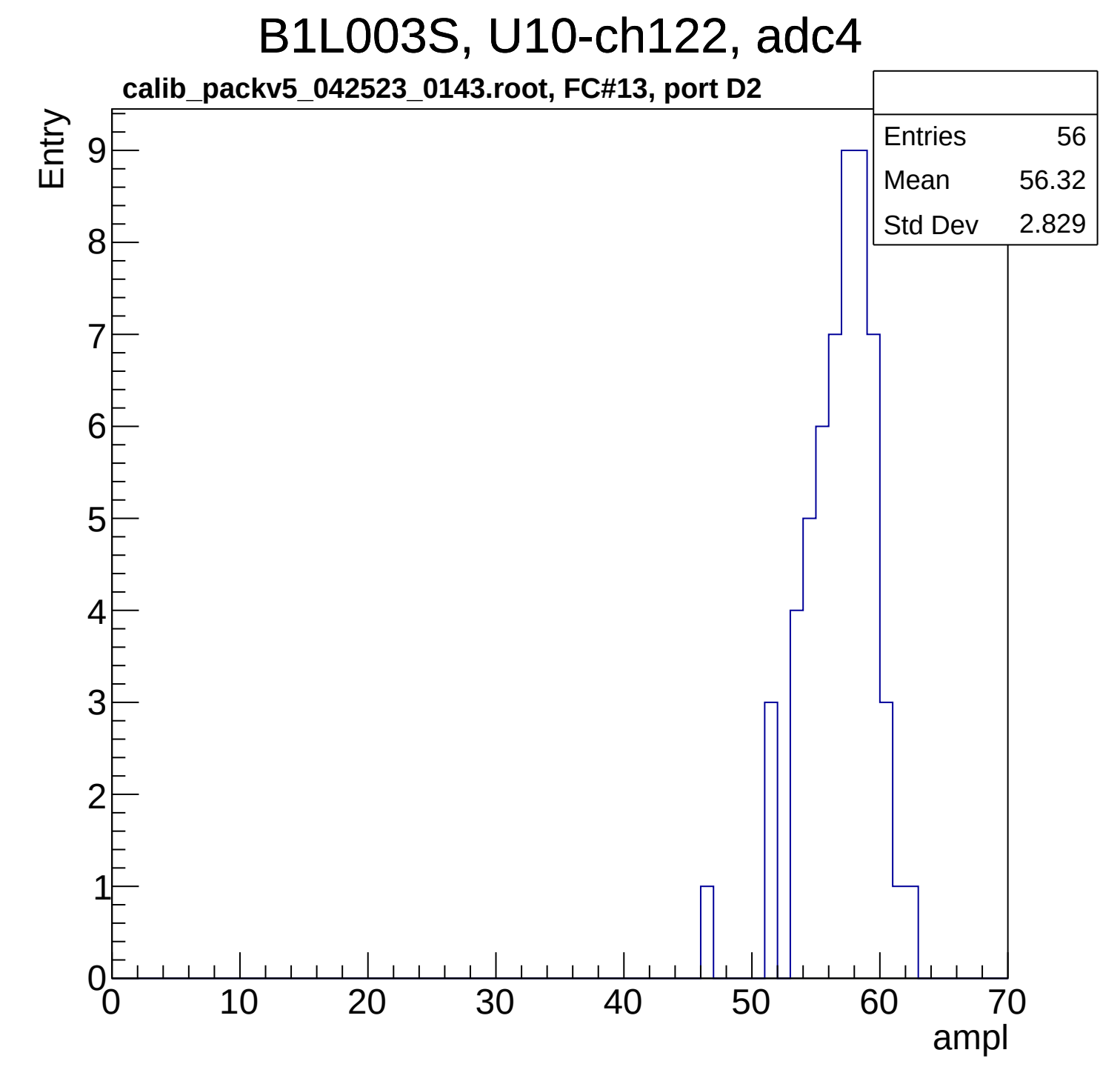
B1L003S, U10-ch122, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 56.32
Std Dev 2.829
Entry
ampl
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0
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60
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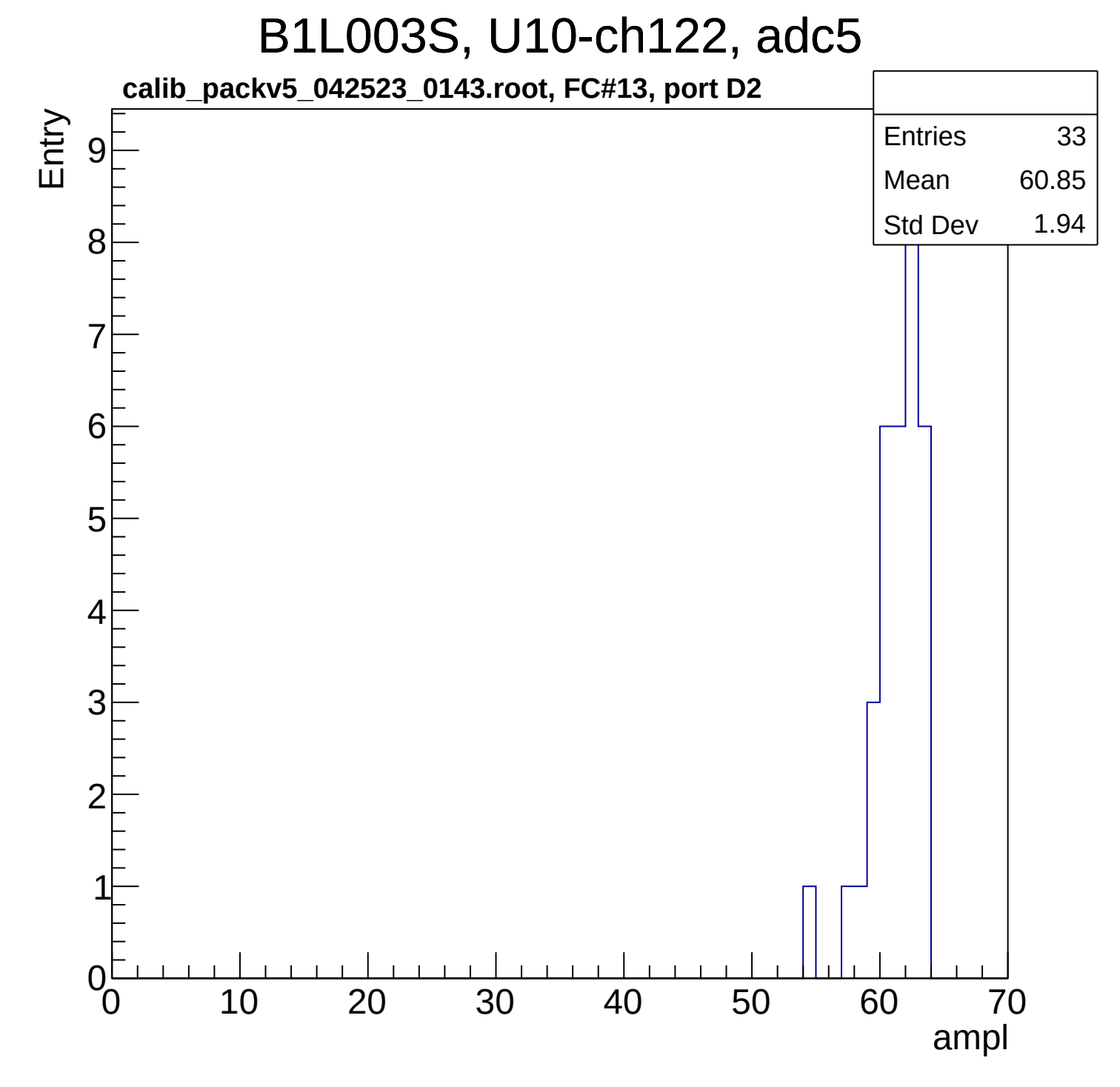
B1L003S, U10-ch122, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 60.85
Std Dev 1.94
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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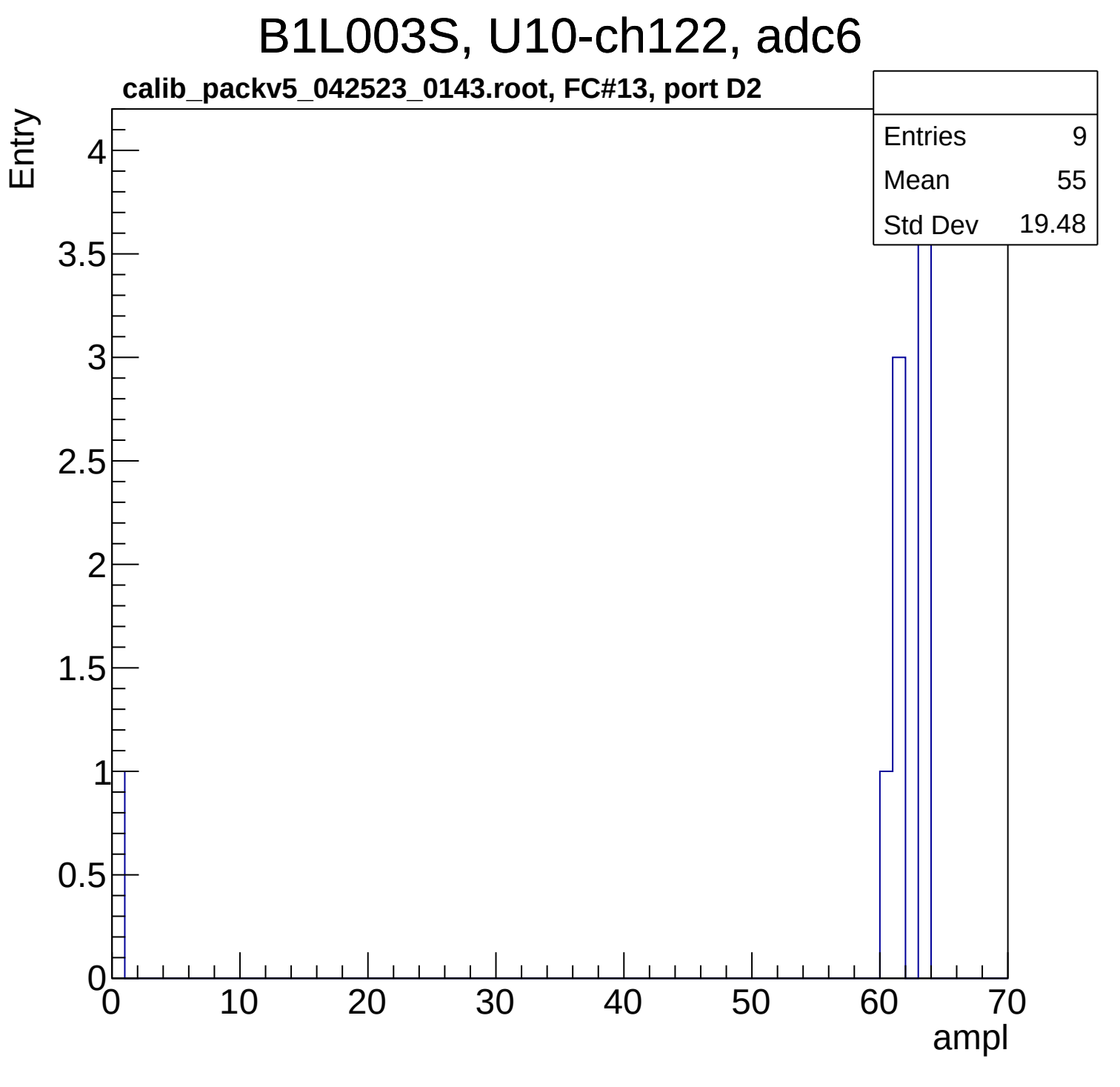
B1L003S, U10-ch122, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55
Std Dev 19.48
Entry
ampl
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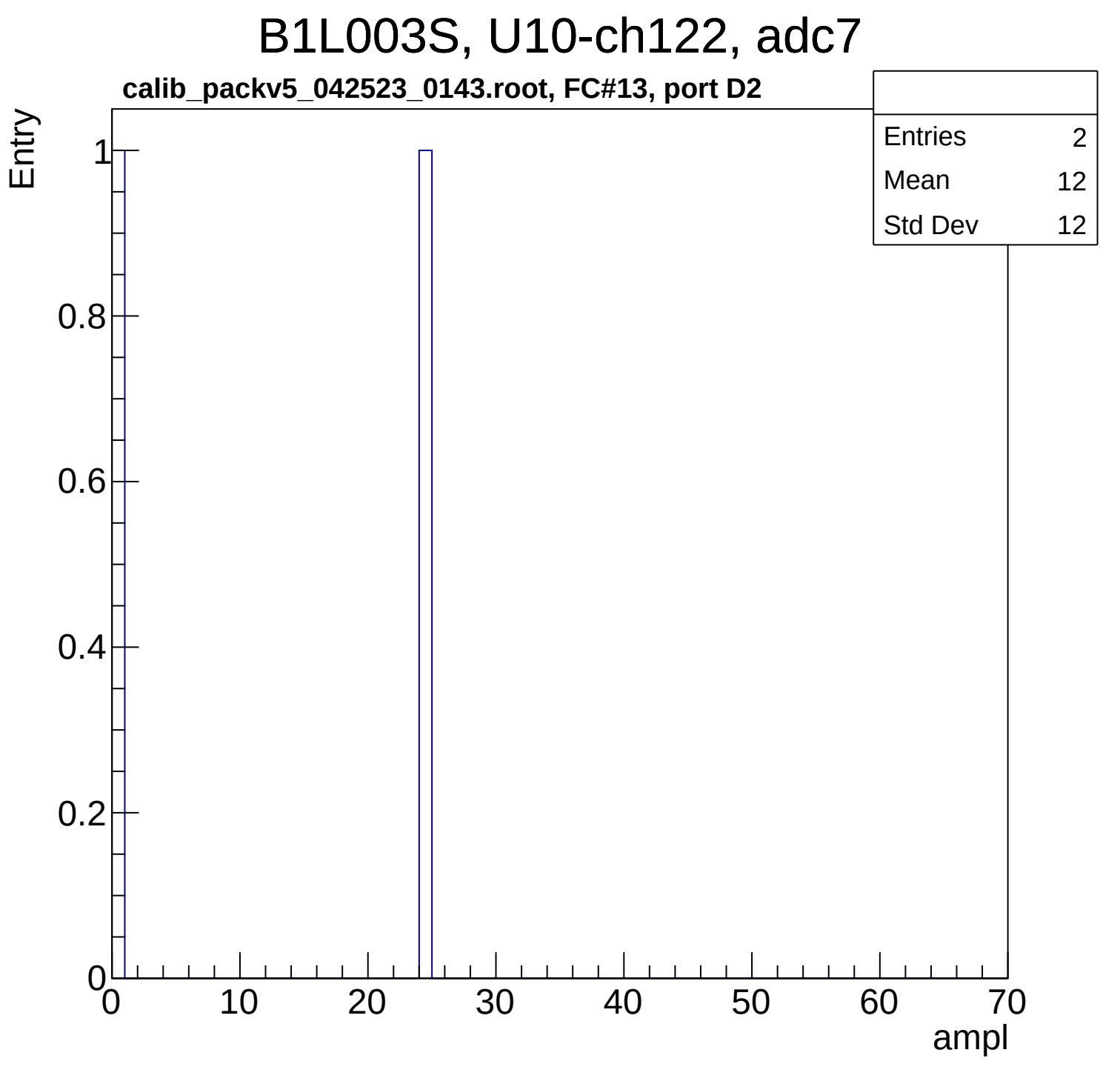
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch122, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12
Std Dev 12
Entry
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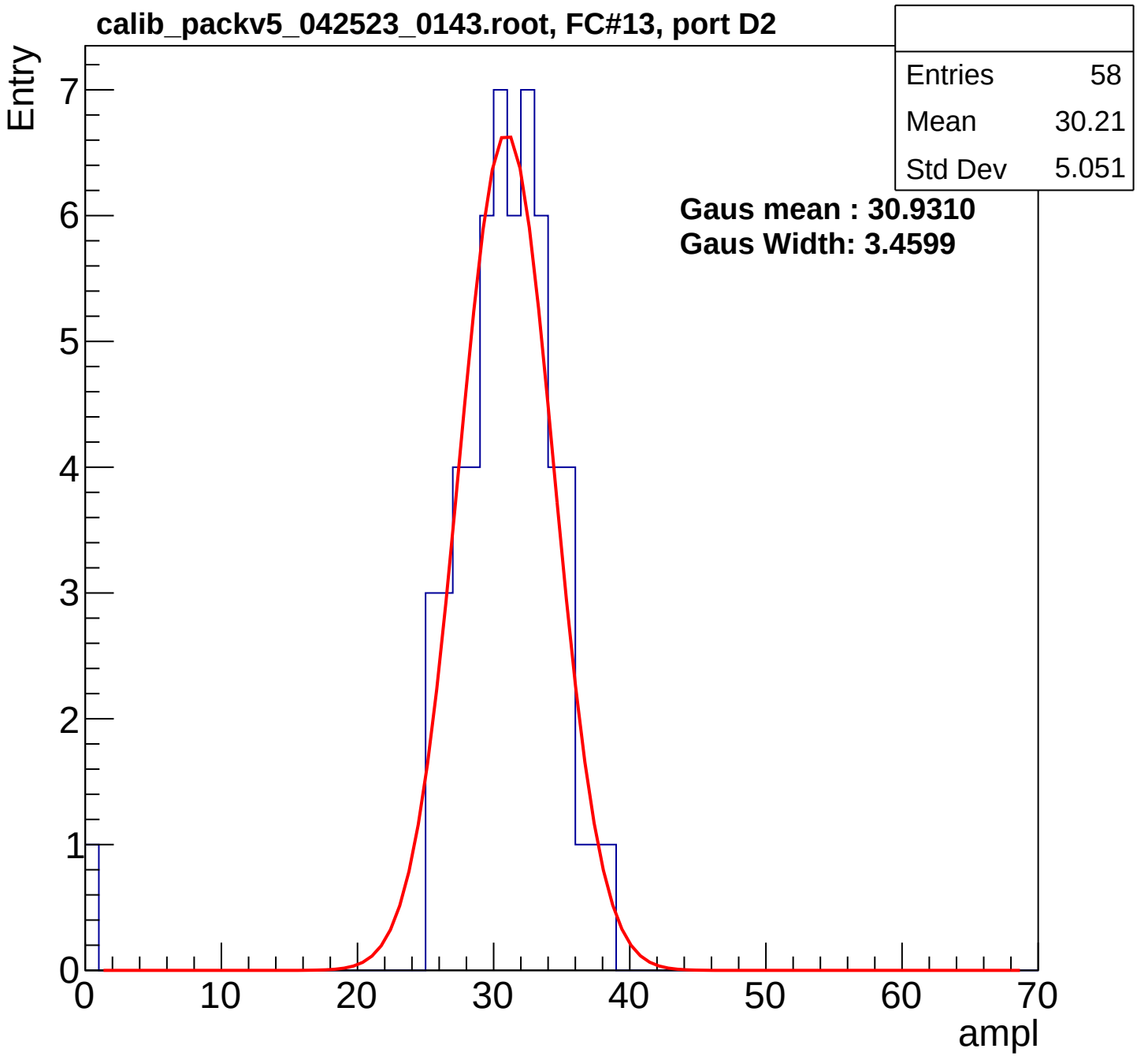
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch123, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 30.21
Std Dev 5.051
Gaus mean : 30.9310
Gaus Width: 3.4599
Entry
ampl
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1
2
3
4
5
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0
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60
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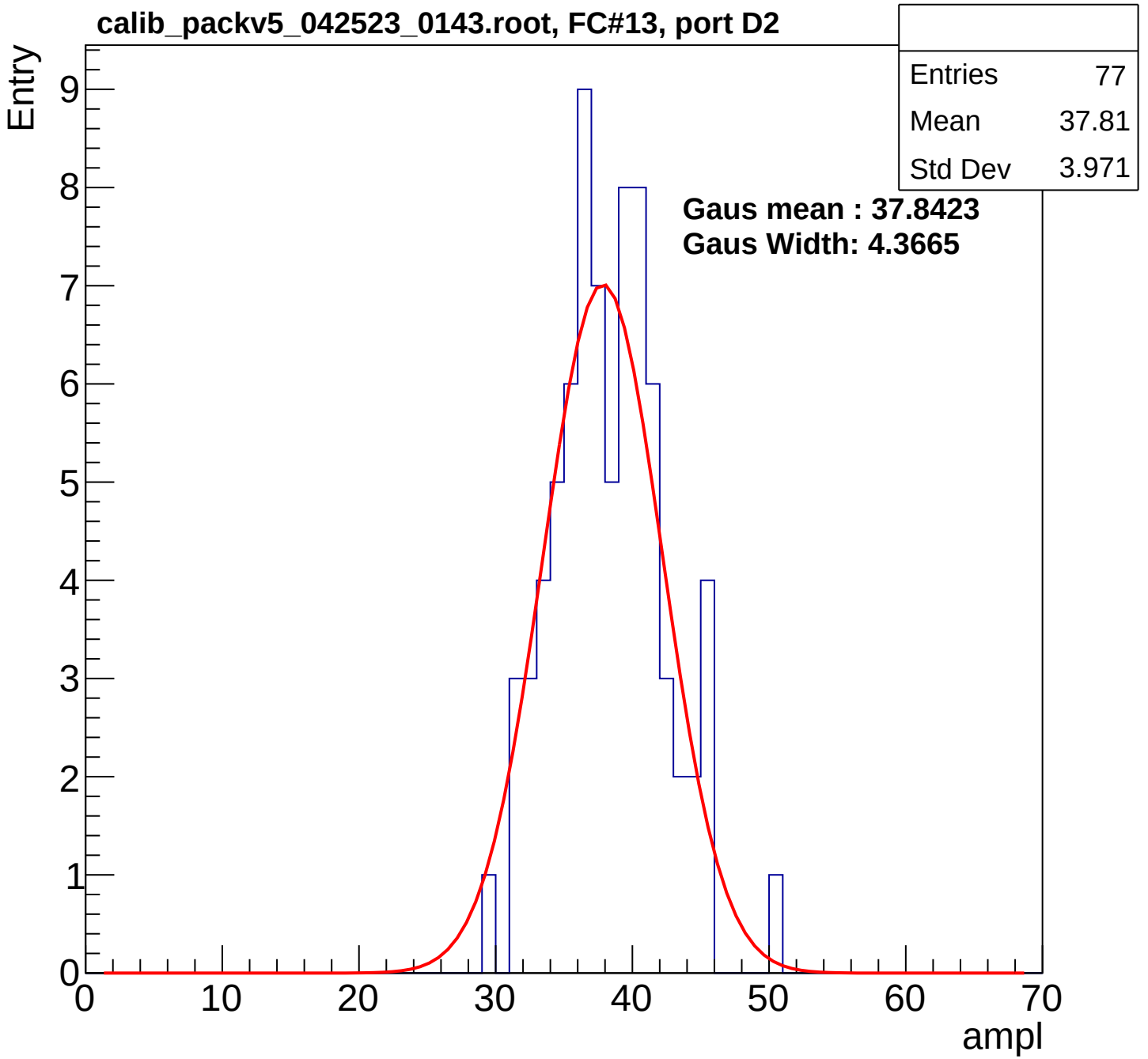
B1L003S, U10-ch123, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 37.81
Std Dev 3.971
Gaus mean : 37.8423
Gaus Width: 4.3665
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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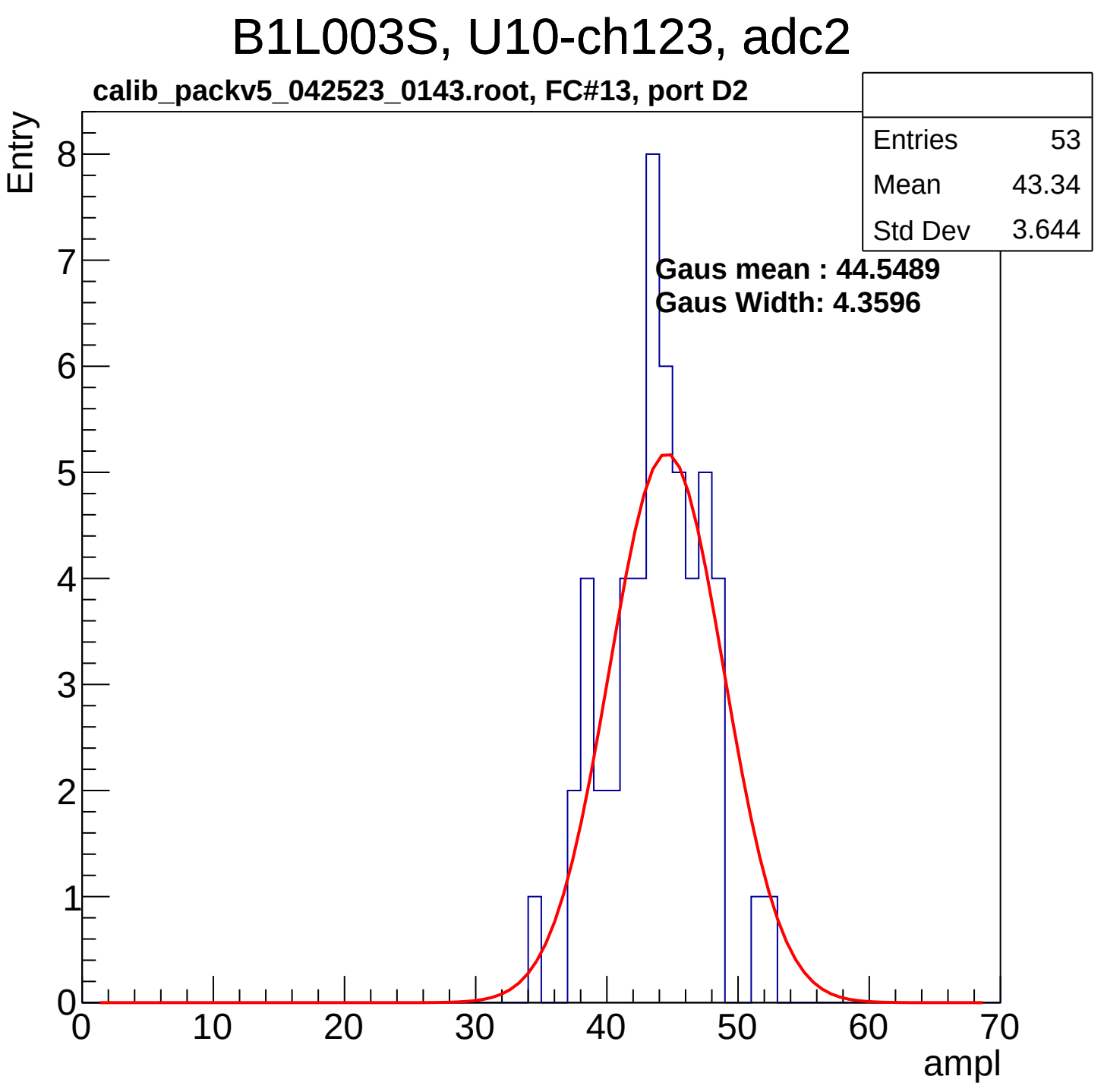
B1L003S, U10-ch123, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 43.34
Std Dev 3.644
Gaus mean : 44.5489
Gaus Width: 4.3596
Entry
ampl
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1
2
3
4
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0
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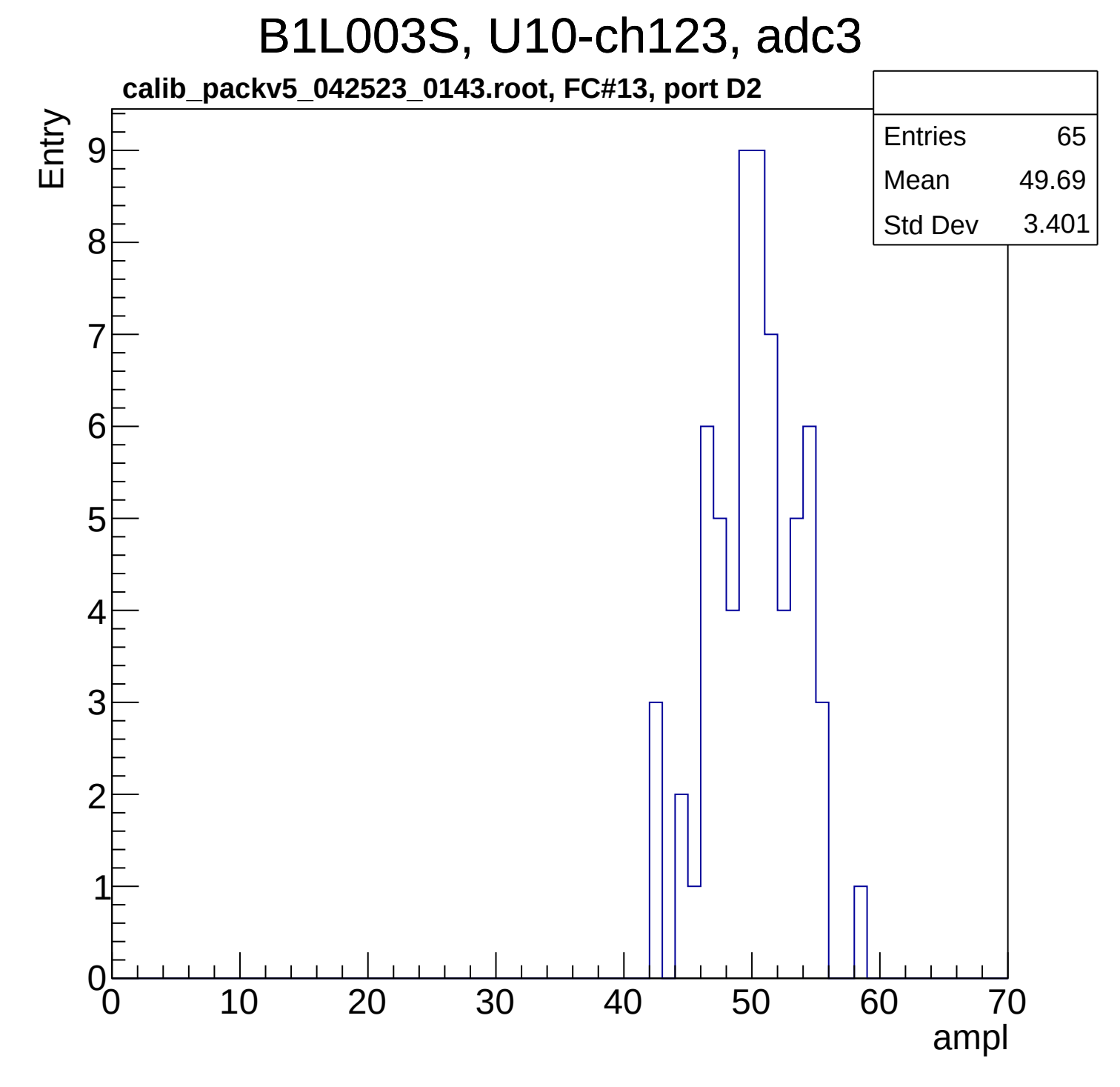
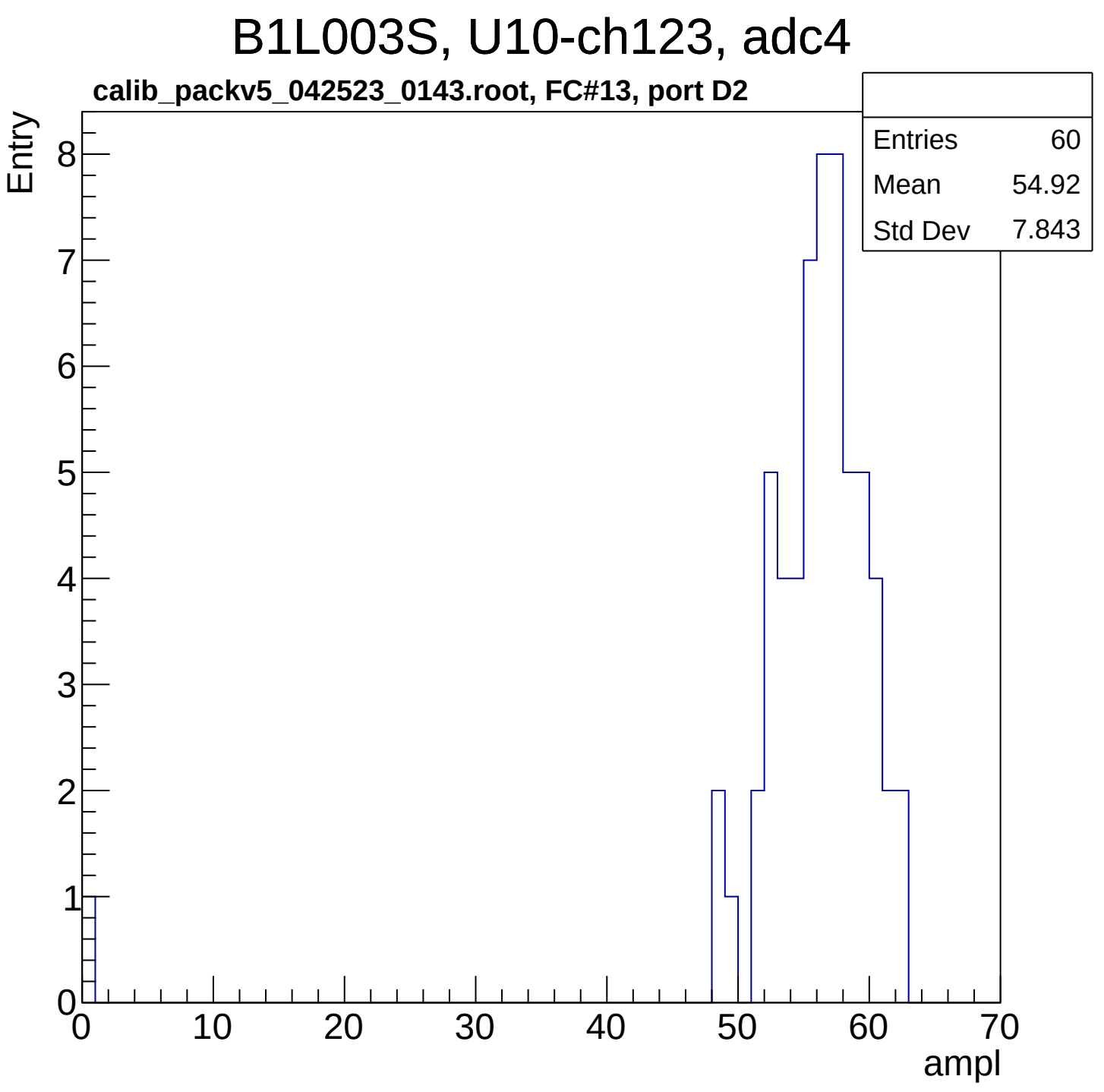

B1L003S, U10-ch123, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 49.69
Std Dev 3.401
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U10-ch123, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.92
Std Dev 7.843
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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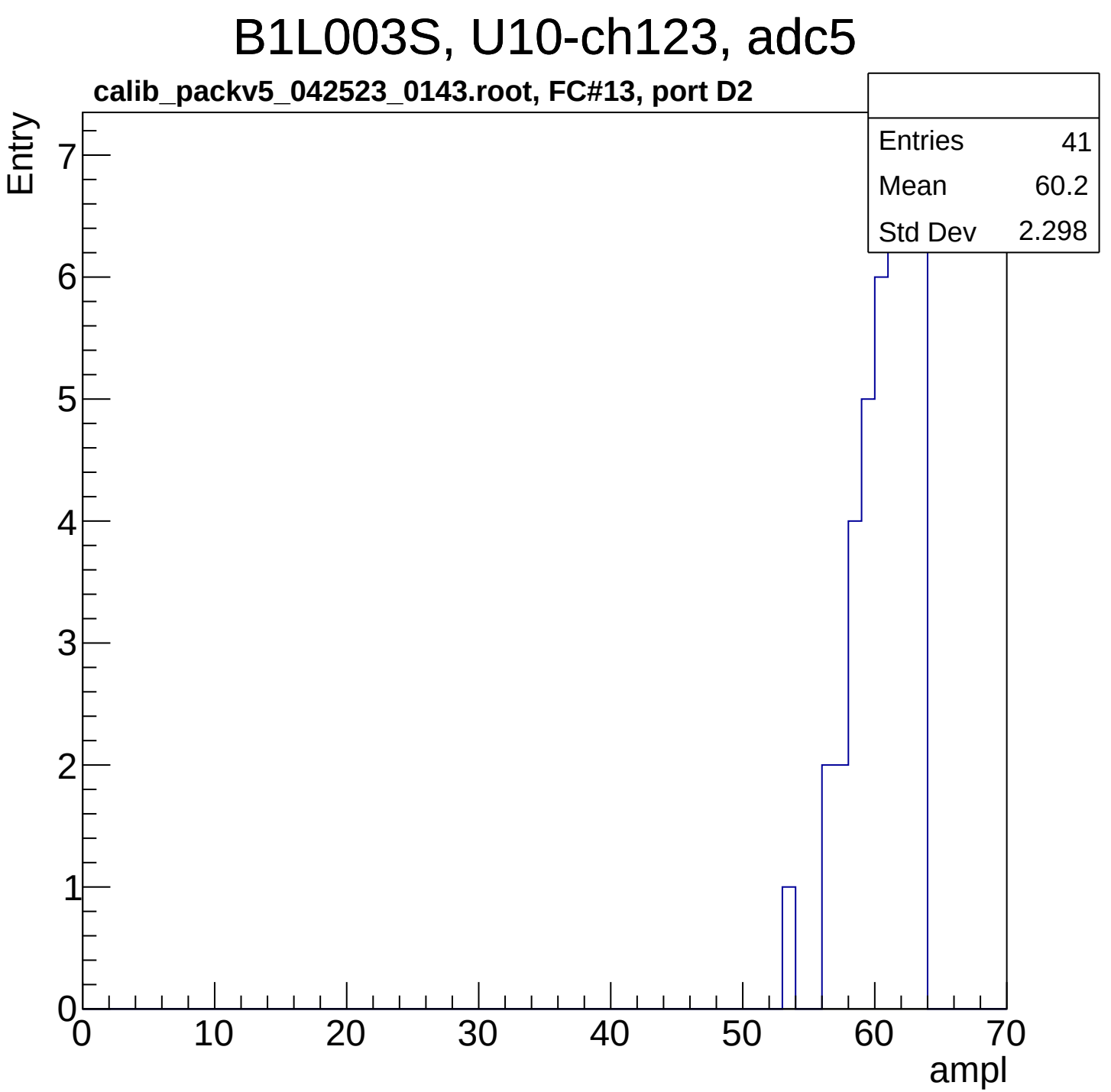
B1L003S, U10-ch123, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 60.2
Std Dev 2.298
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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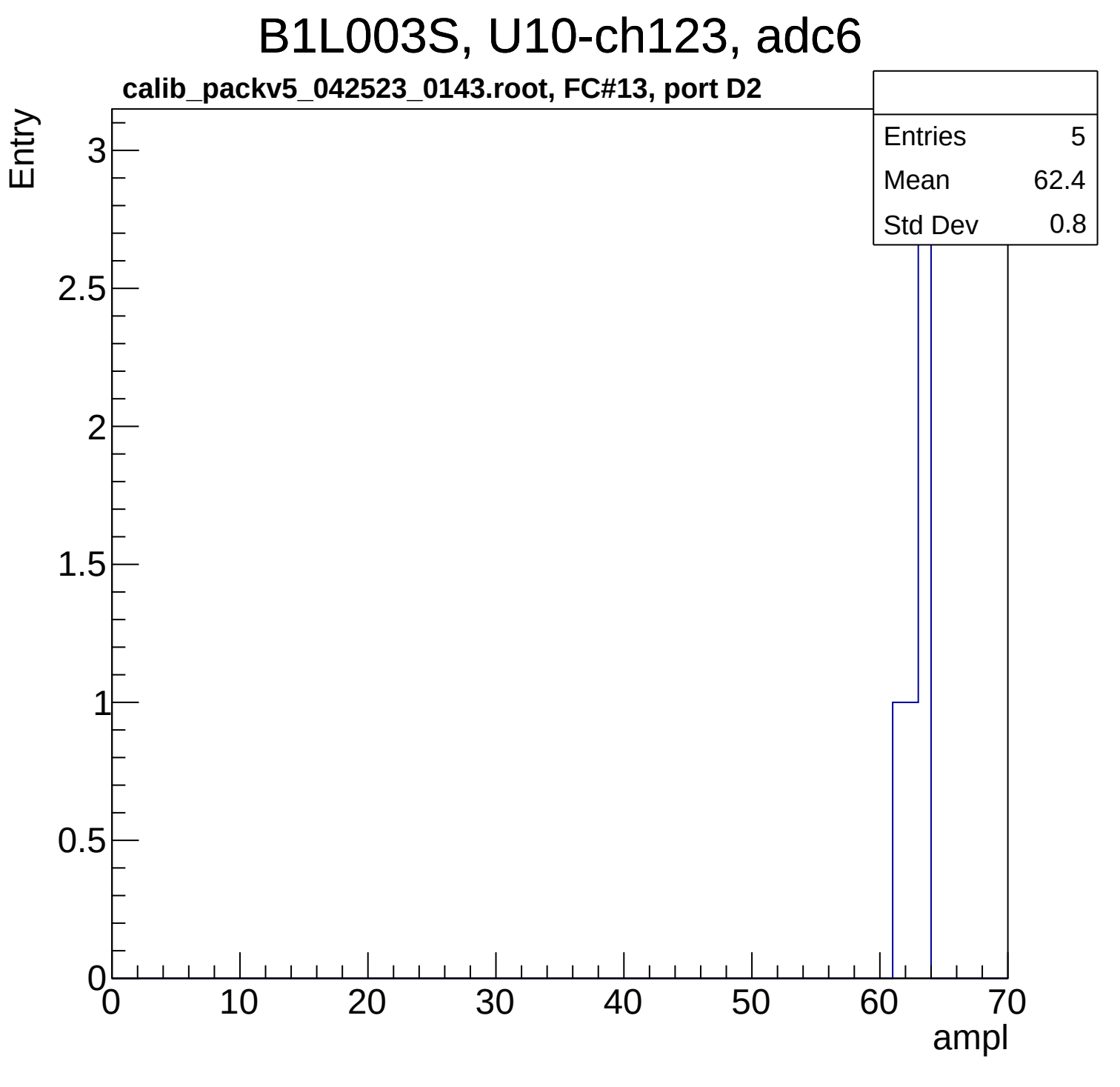
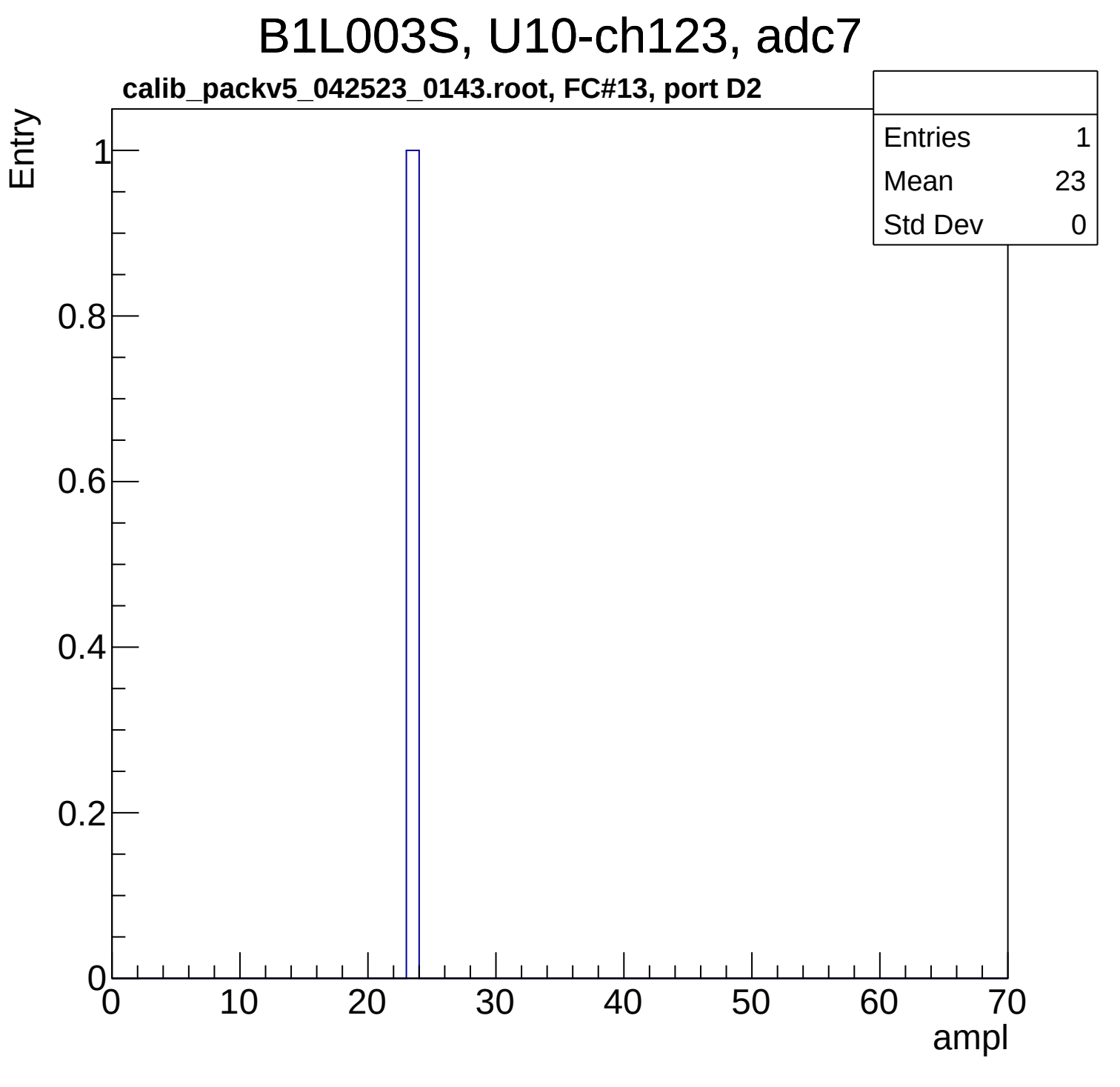
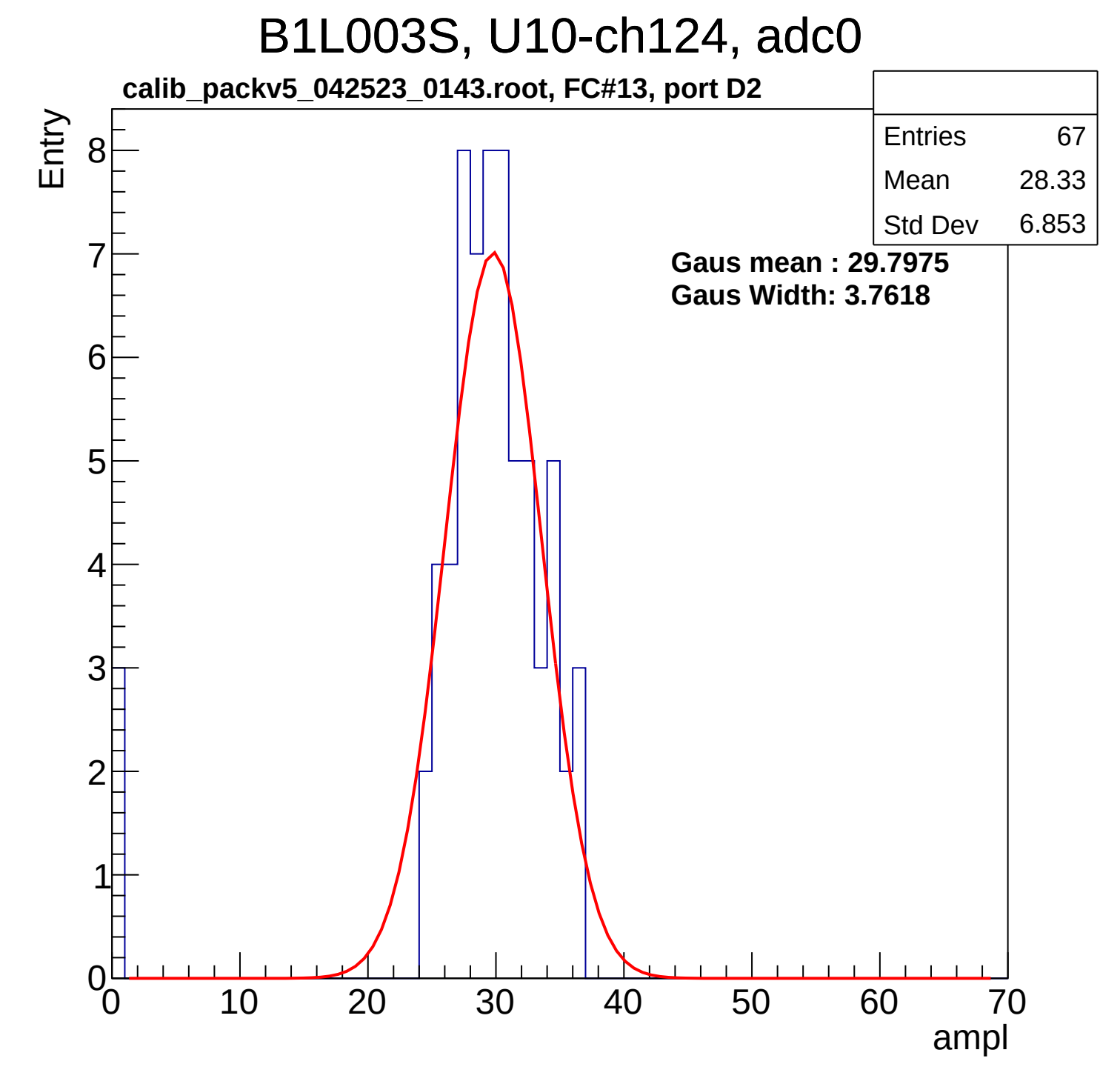

B1L003S, U10-ch123, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch123, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch124, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 28.33
Std Dev 6.853
Gaus mean : 29.7975
Gaus Width: 3.7618
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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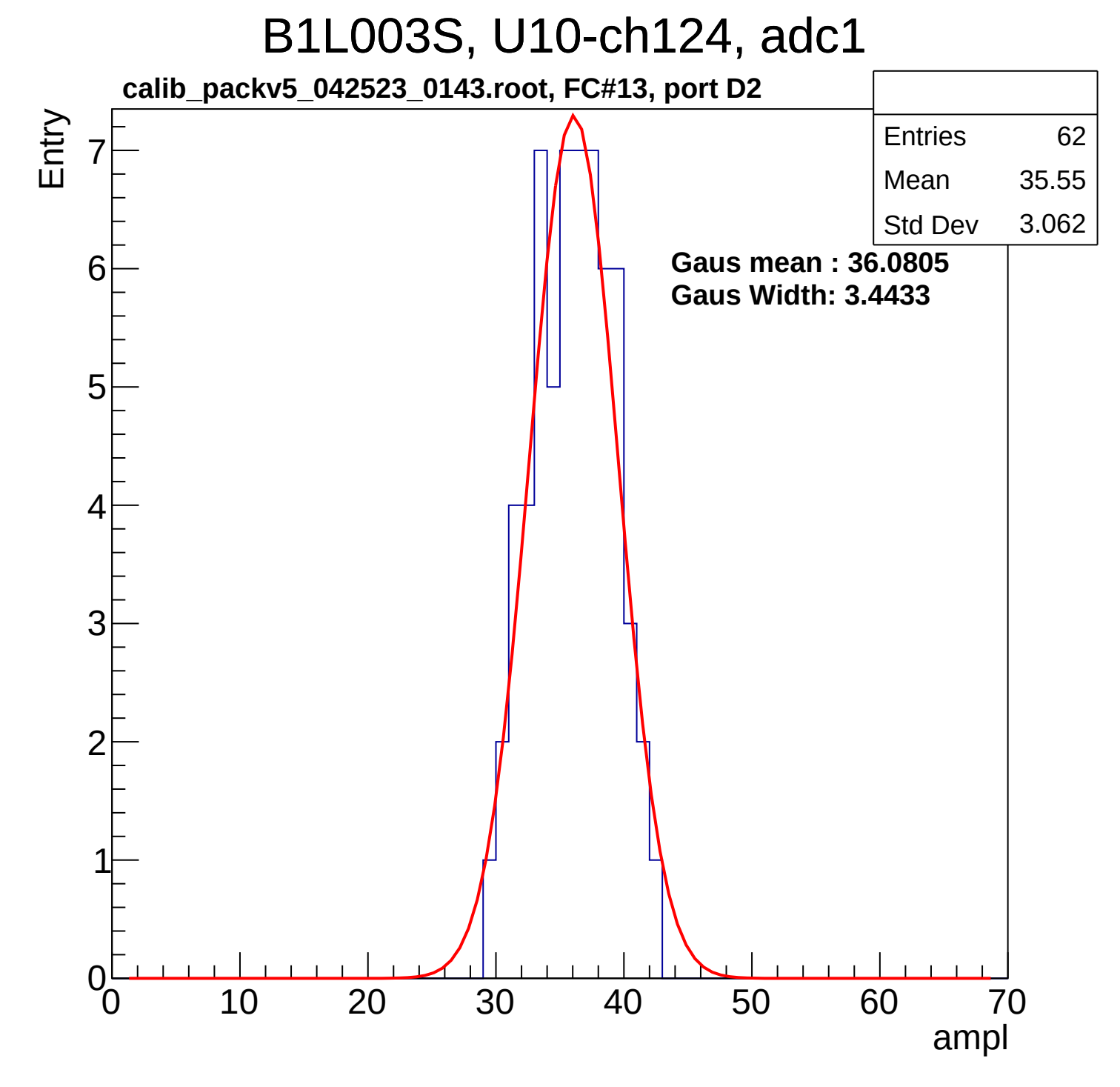
B1L003S, U10-ch124, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.55
Std Dev 3.062
Gaus mean : 36.0805
Gaus Width: 3.4433
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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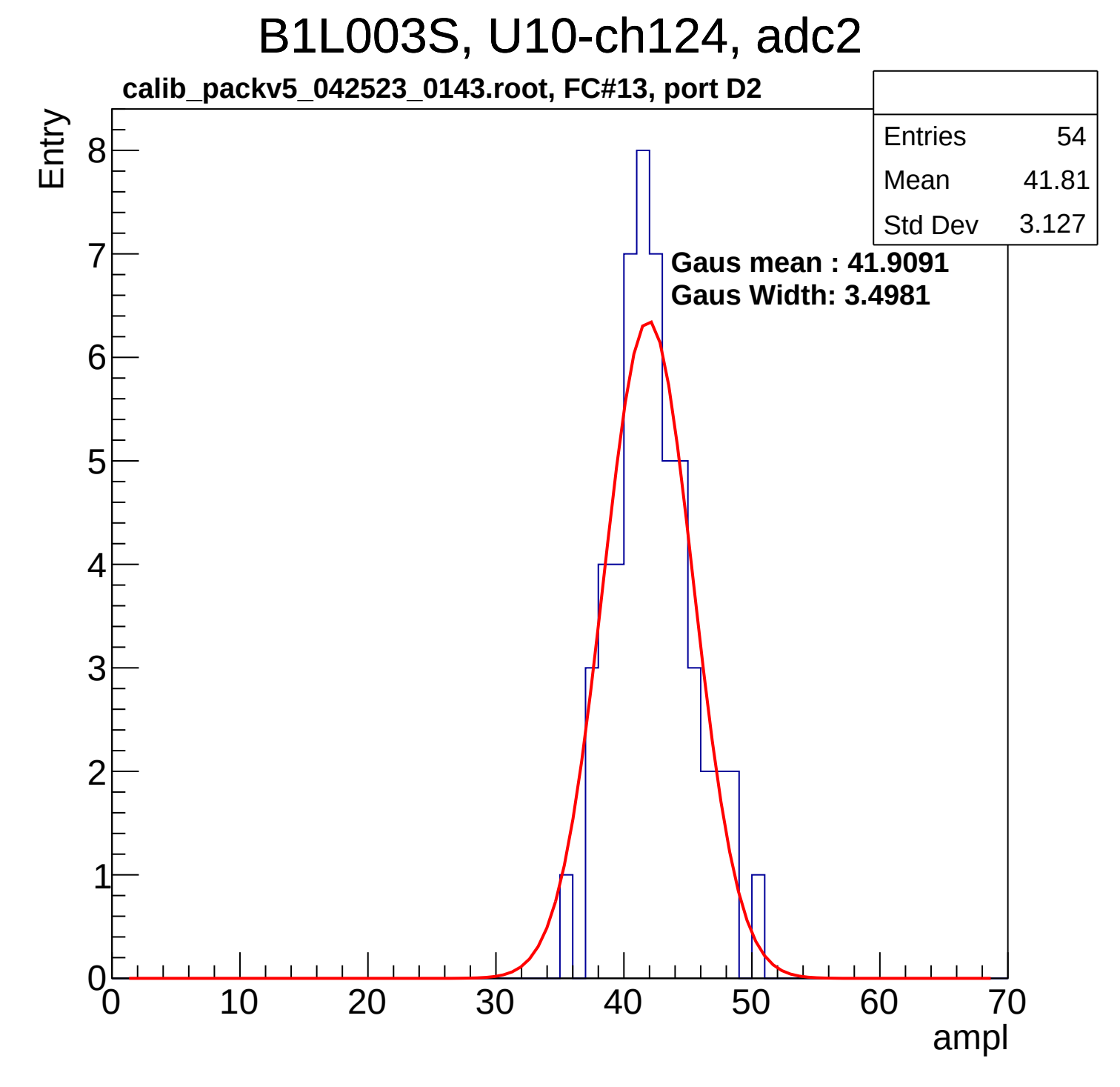
B1L003S, U10-ch124, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41.81
Std Dev 3.127
Gaus mean : 41.9091
Gaus Width: 3.4981
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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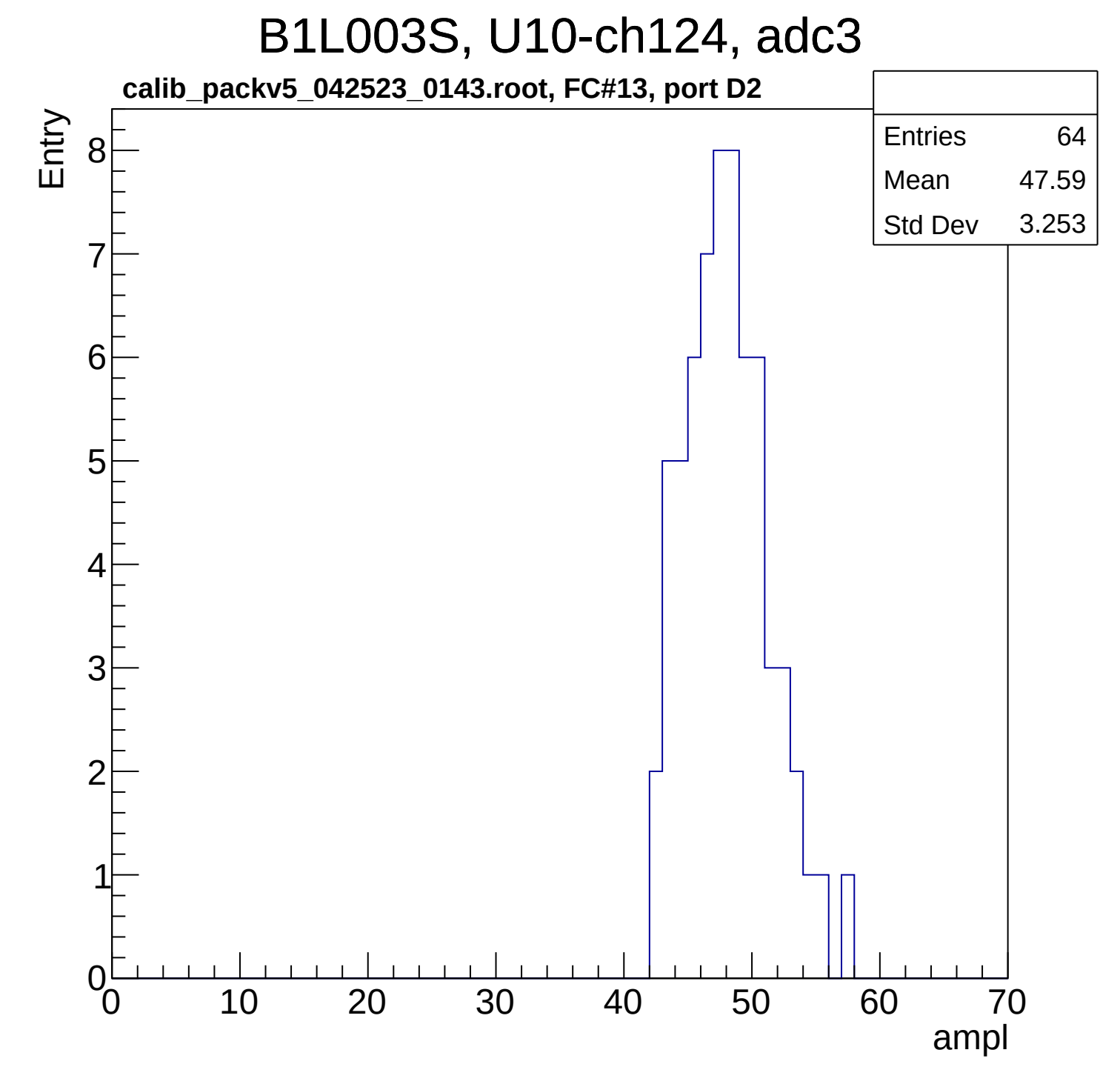
B1L003S, U10-ch124, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.59
Std Dev 3.253
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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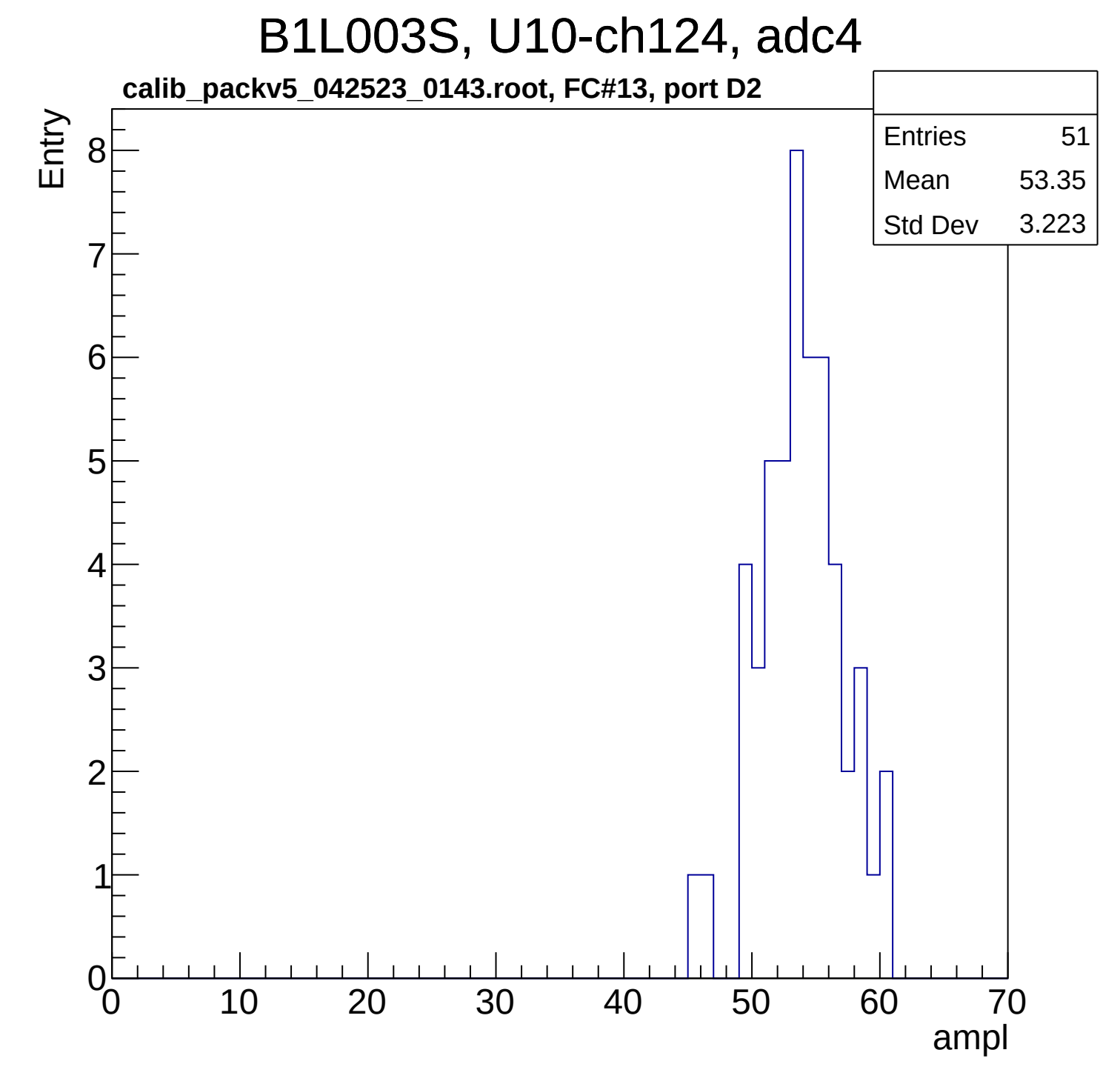
B1L003S, U10-ch124, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 53.35
Std Dev 3.223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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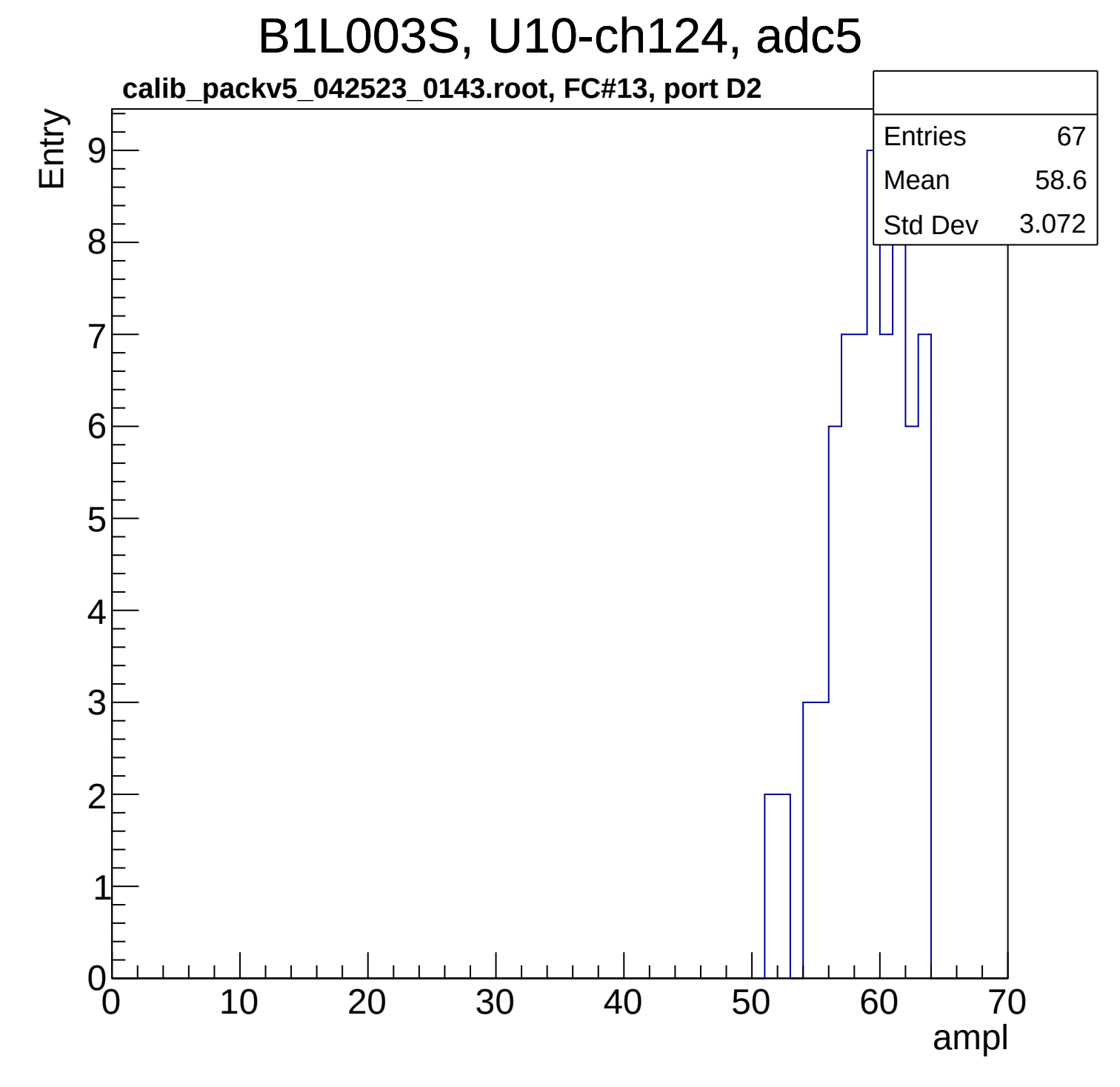
B1L003S, U10-ch124, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 58.6
Std Dev 3.072
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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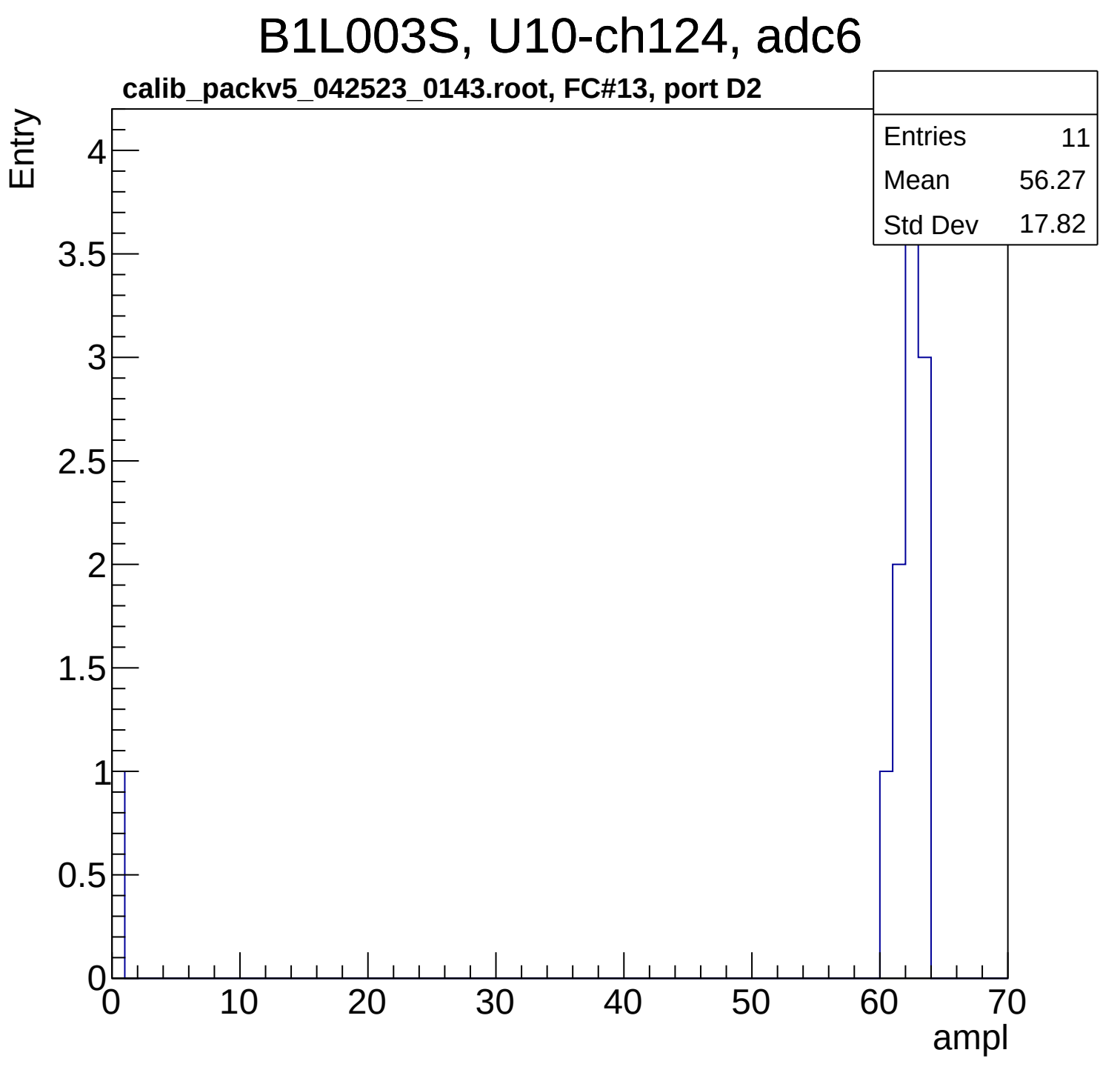
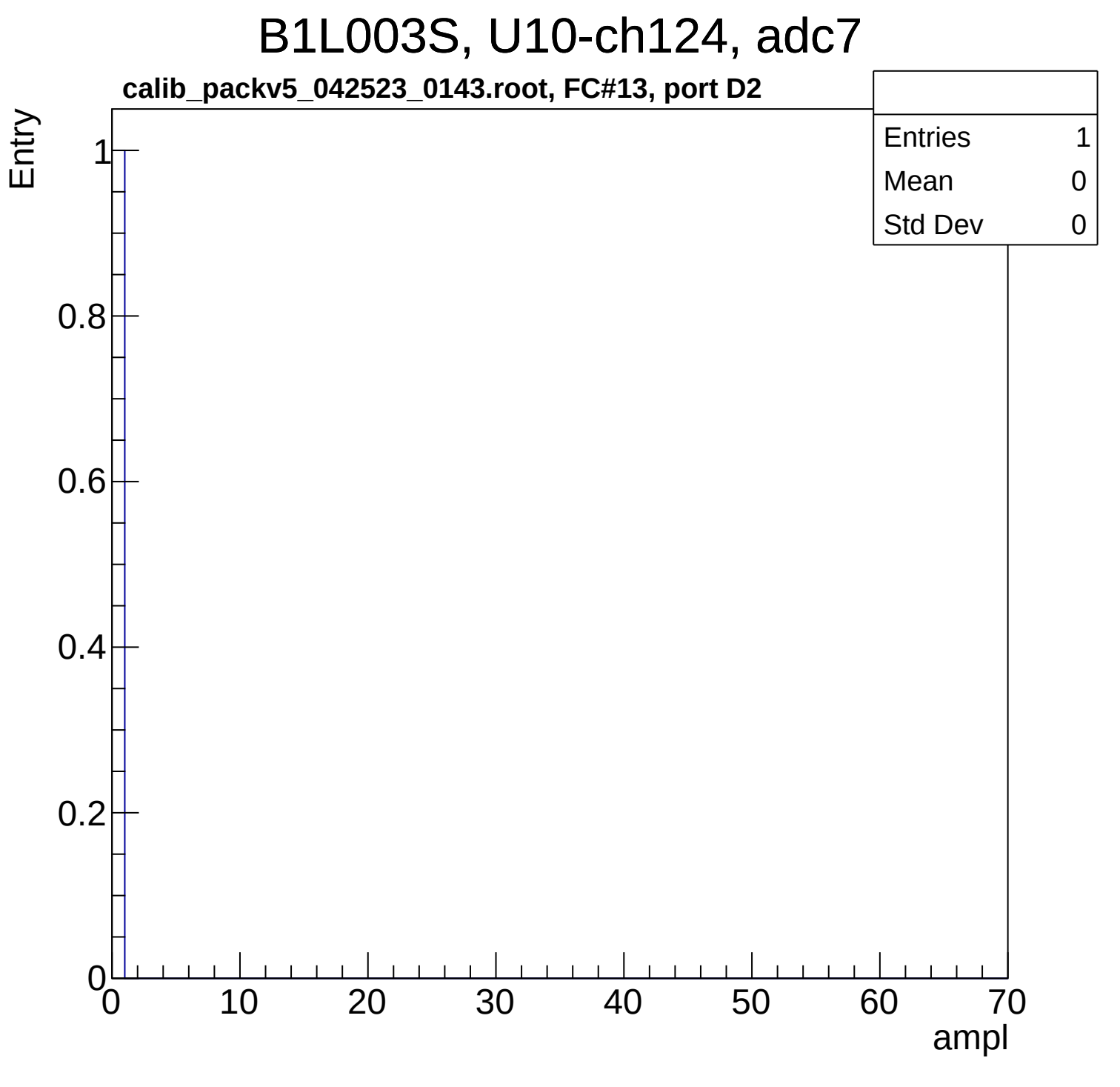
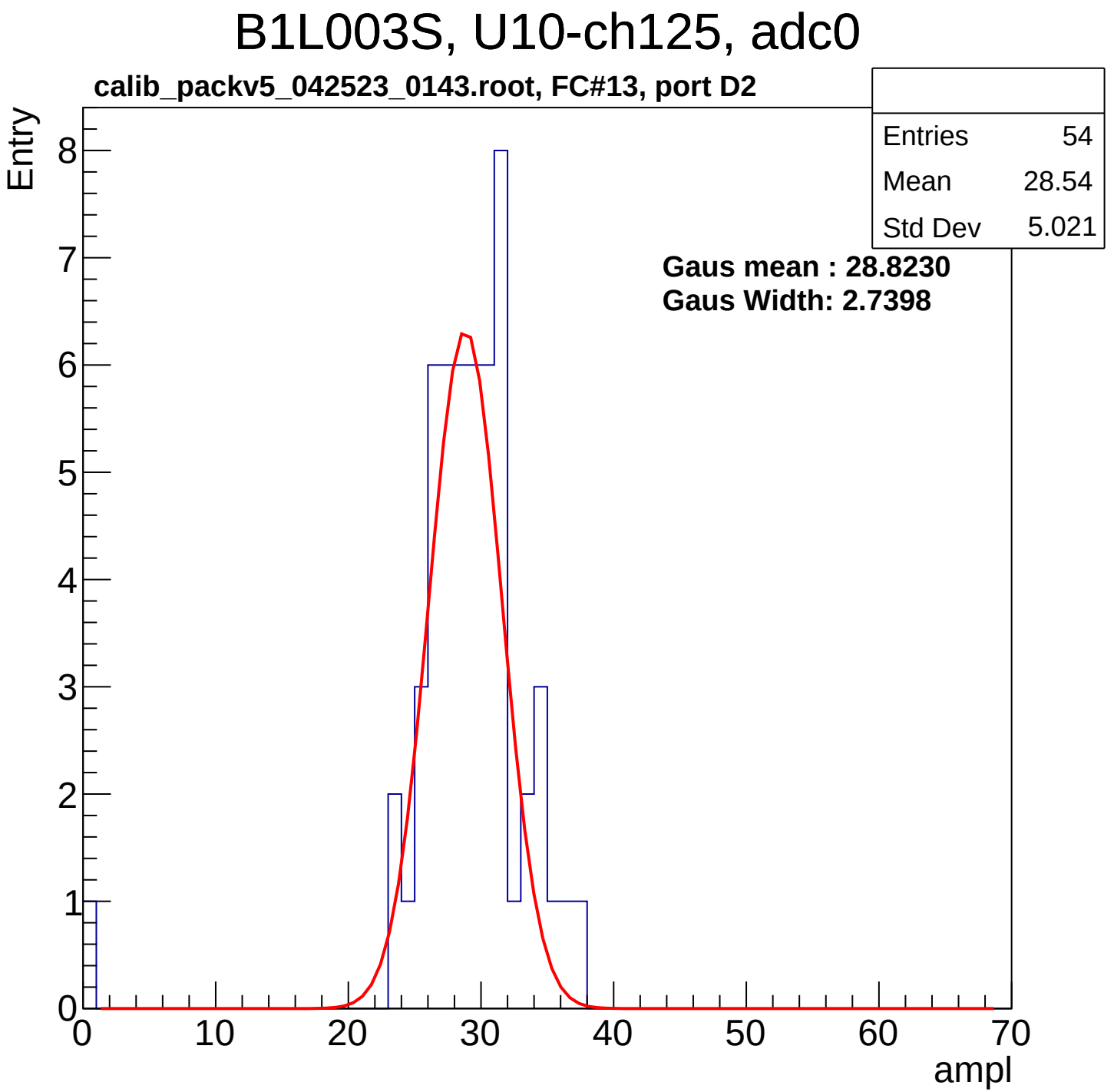

B1L003S, U10-ch124, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.27
Std Dev 17.82
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U10-ch124, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch125, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 28.54
Std Dev 5.021
Gaus mean : 28.8230
Gaus Width: 2.7398
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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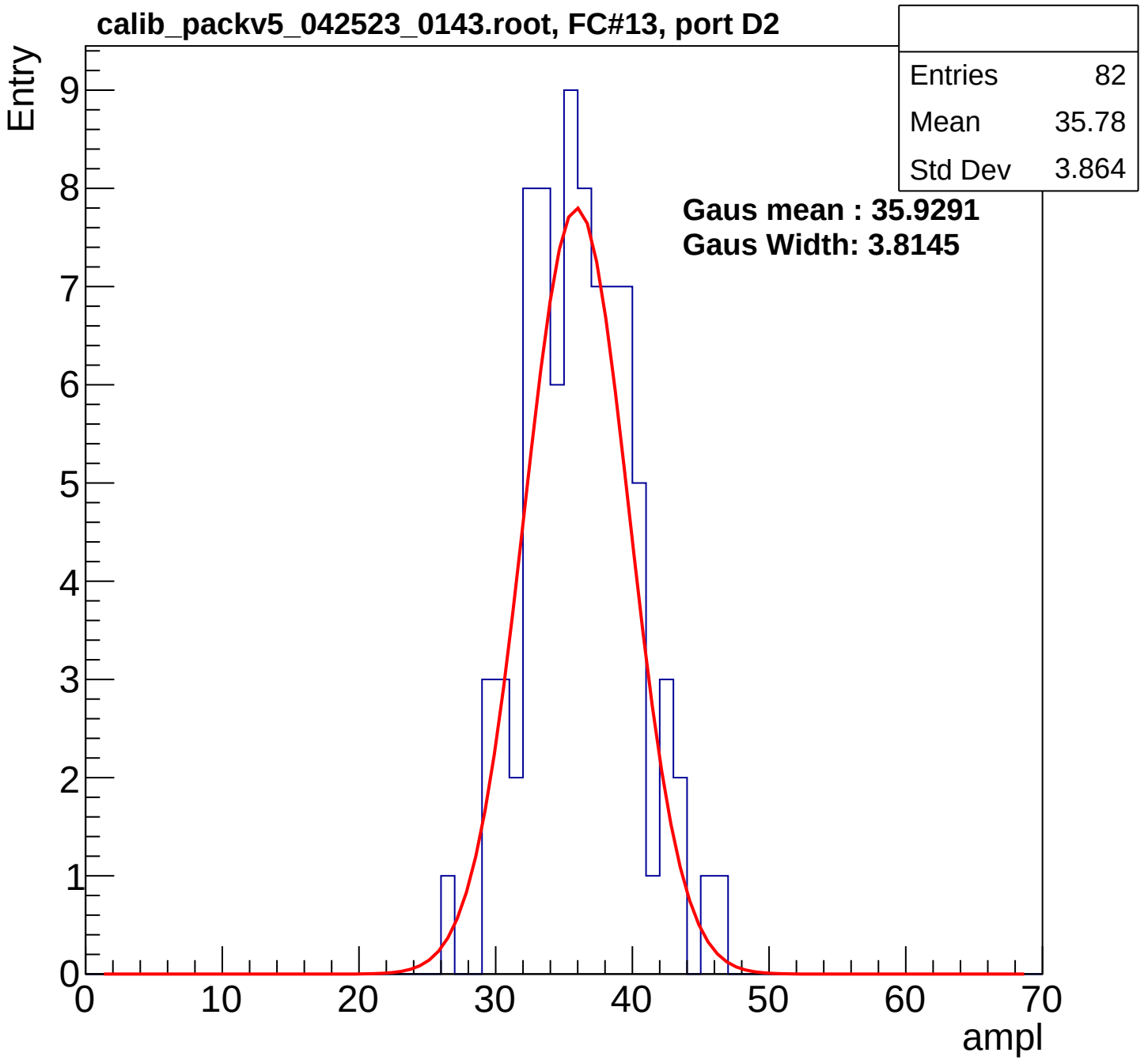
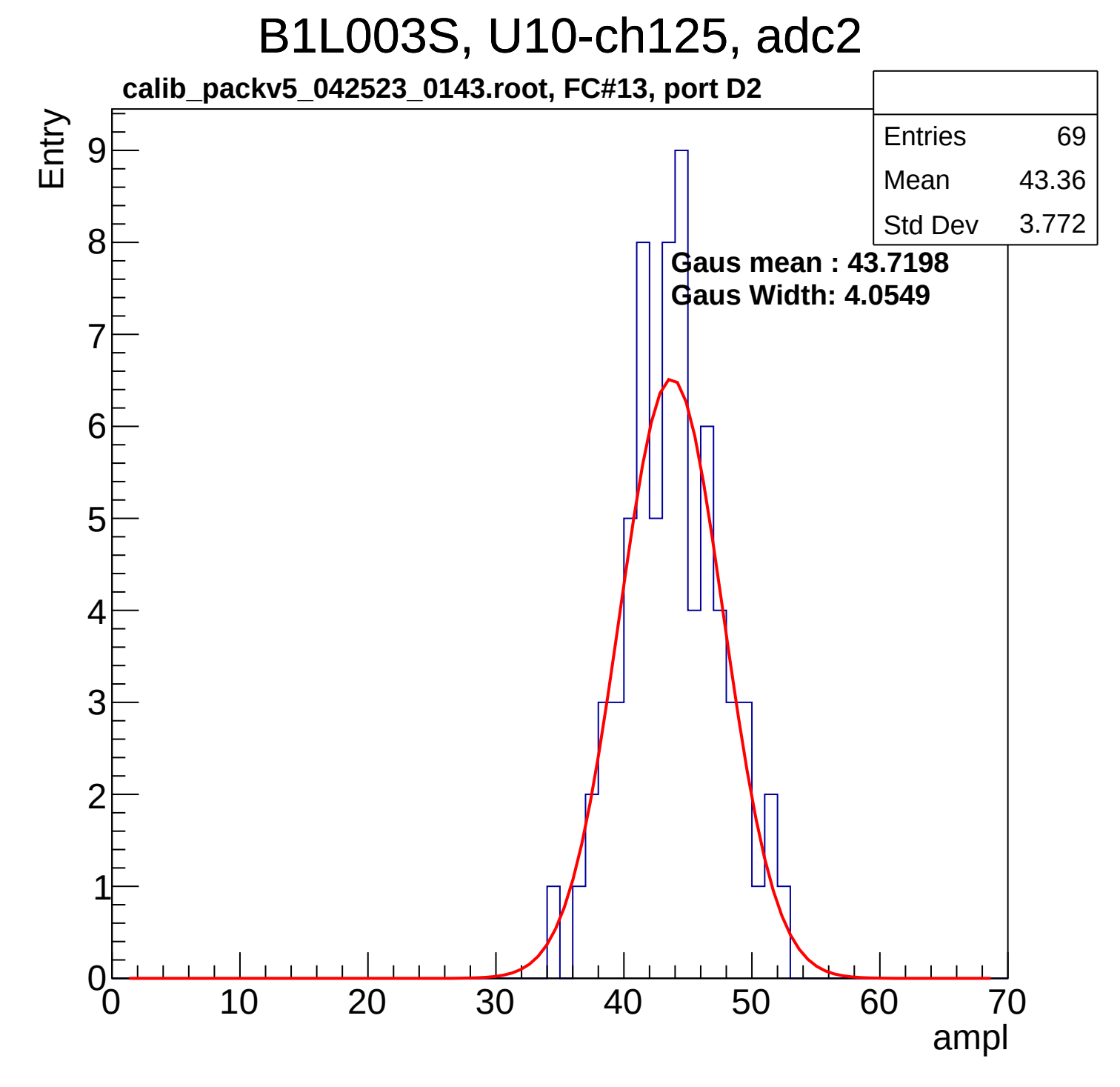

B1L003S, U10-ch125, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 35.78
Std Dev 3.864
Gaus mean : 35.9291
Gaus Width: 3.8145
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U10-ch125, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 43.36
Std Dev 3.772
Gaus mean : 43.7198
Gaus Width: 4.0549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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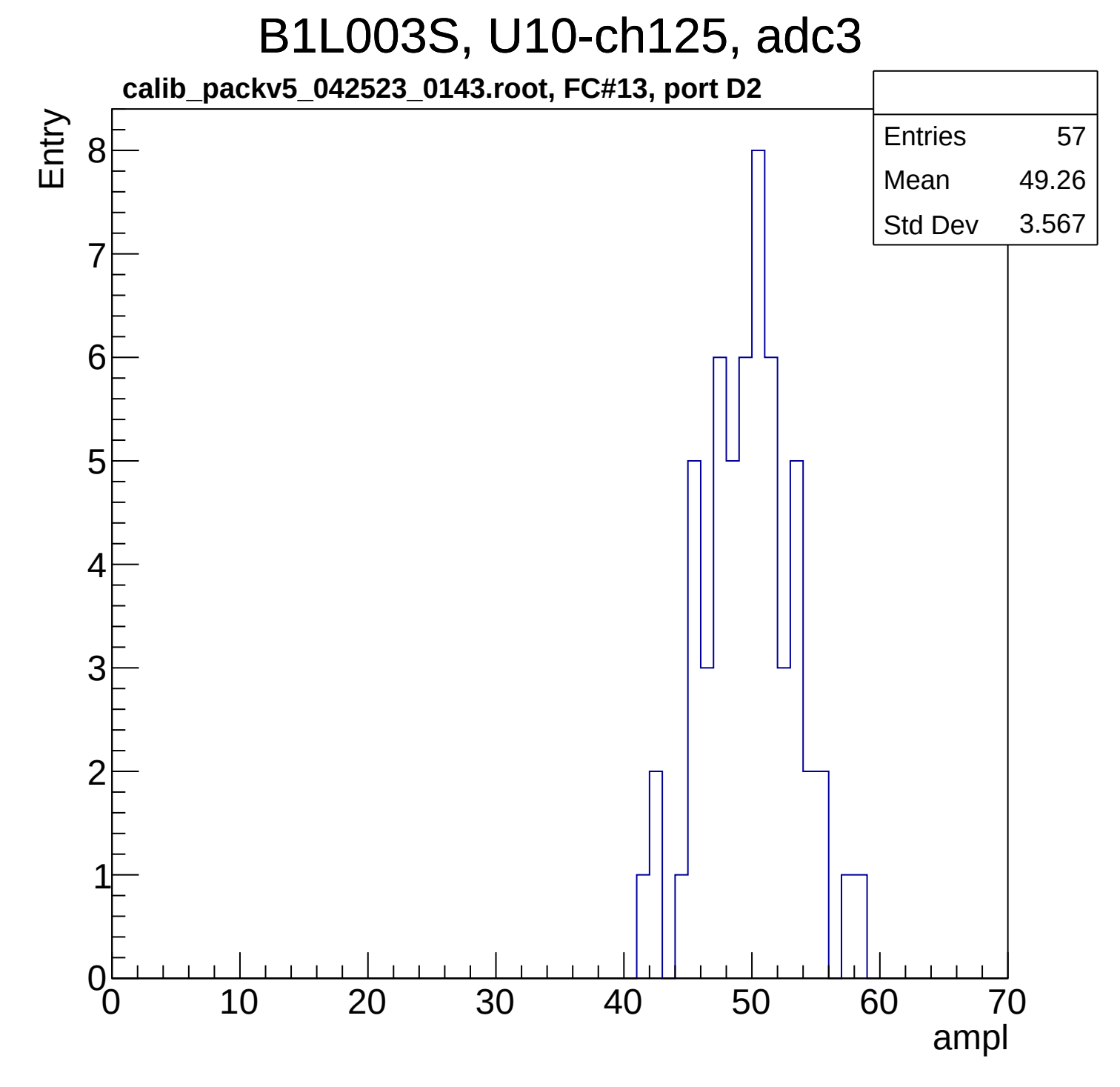
B1L003S, U10-ch125, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 49.26
Std Dev 3.567
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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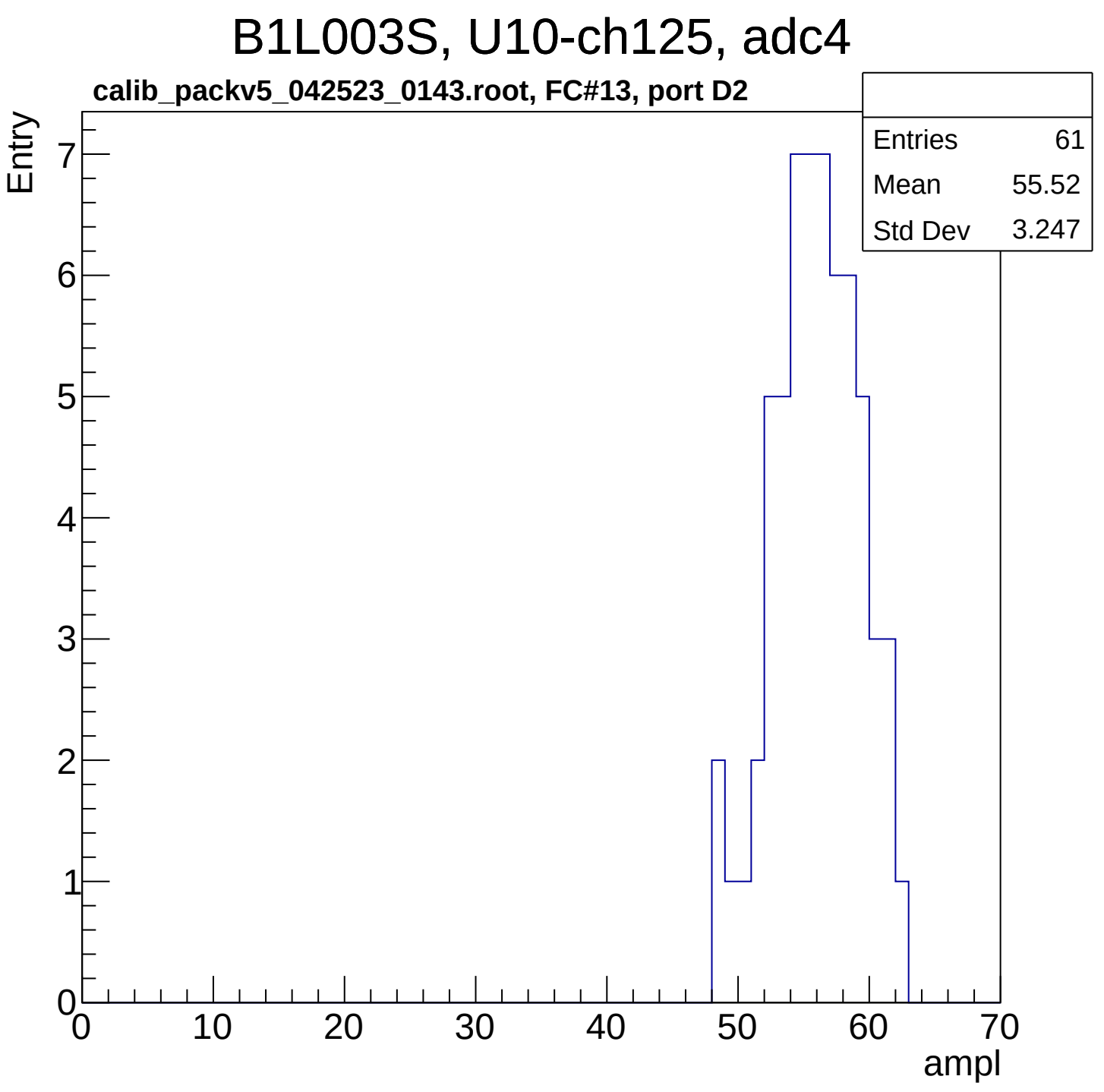
B1L003S, U10-ch125, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 55.52
Std Dev 3.247
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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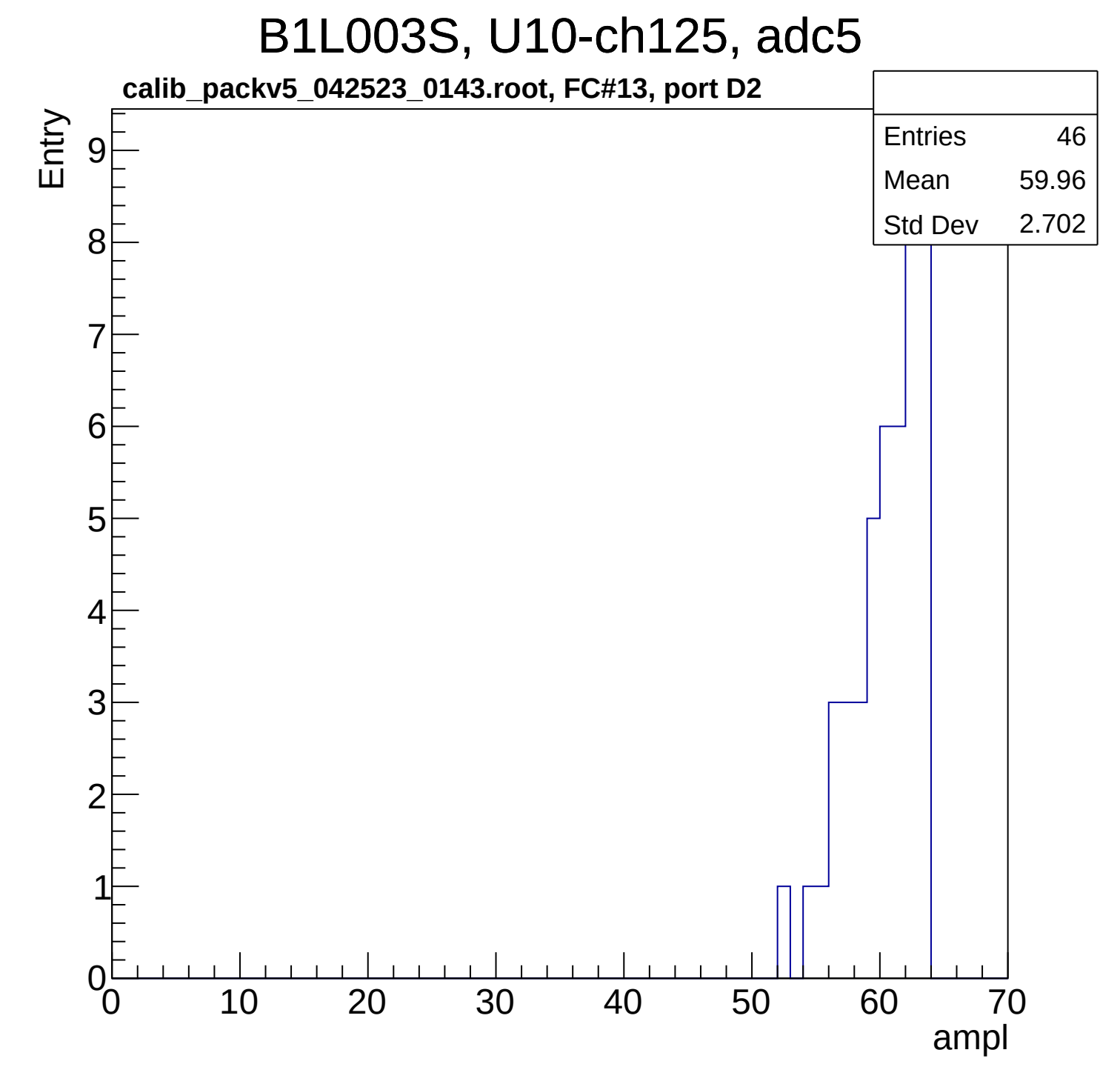
B1L003S, U10-ch125, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.96
Std Dev 2.702
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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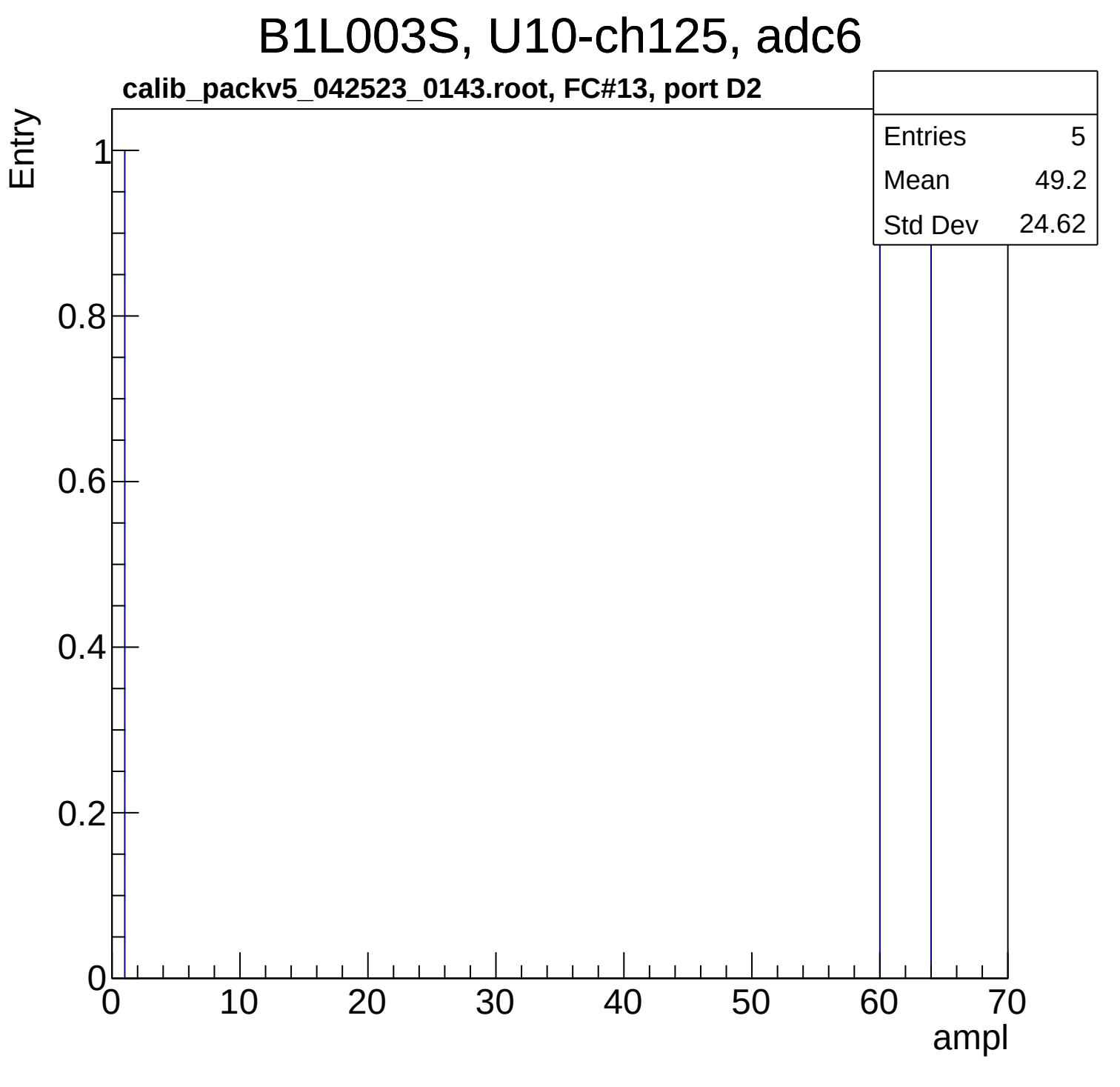
B1L003S, U10-ch125, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 49.2
Std Dev 24.62
Entry
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0.6
0.4
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0
10
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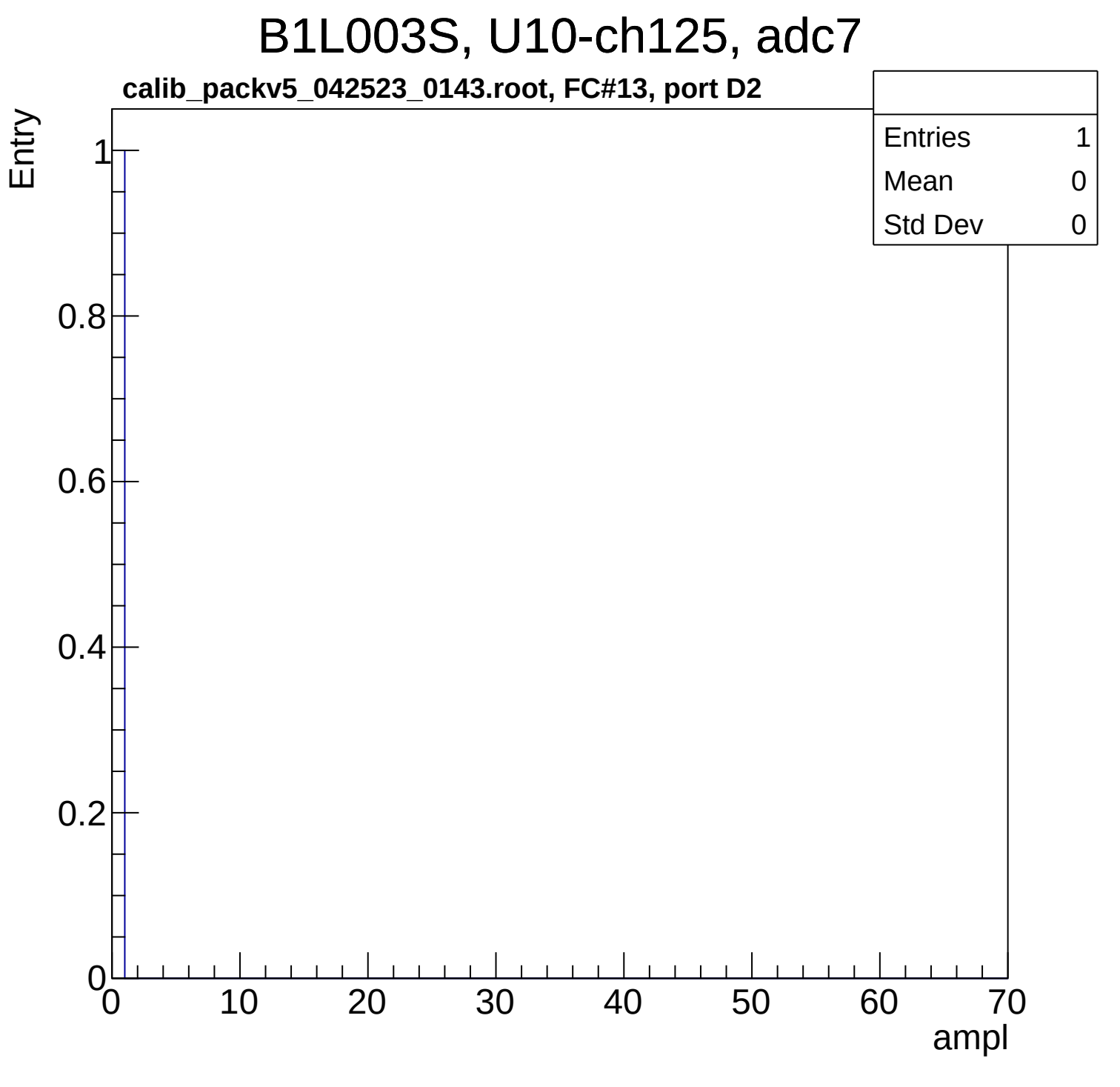
50
60
70
ampl


B1L003S, U10-ch125, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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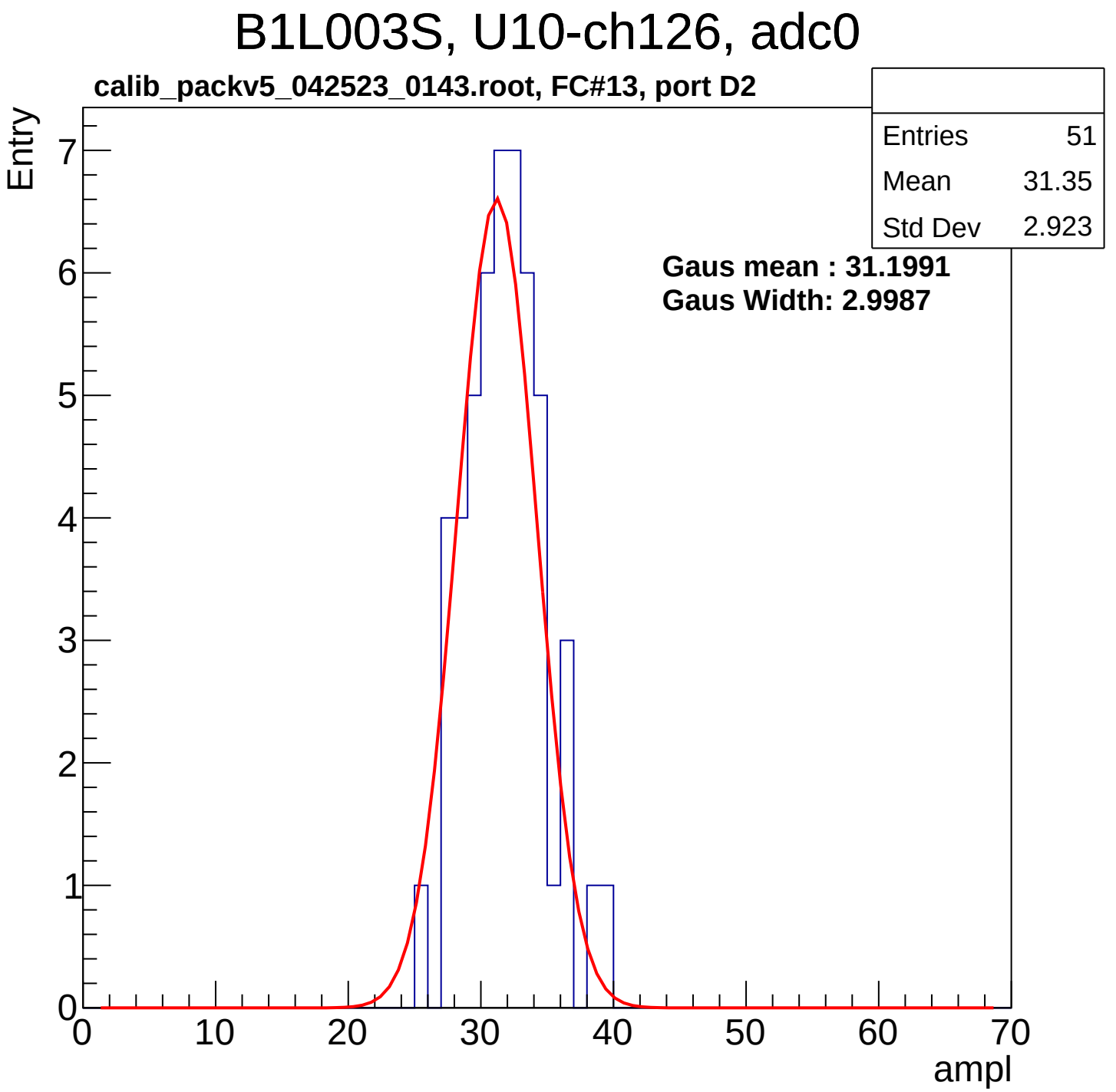
50
60
70
ampl


B1L003S, U10-ch126, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 31.35
Std Dev 2.923
Gaus mean : 31.1991
Gaus Width: 2.9987
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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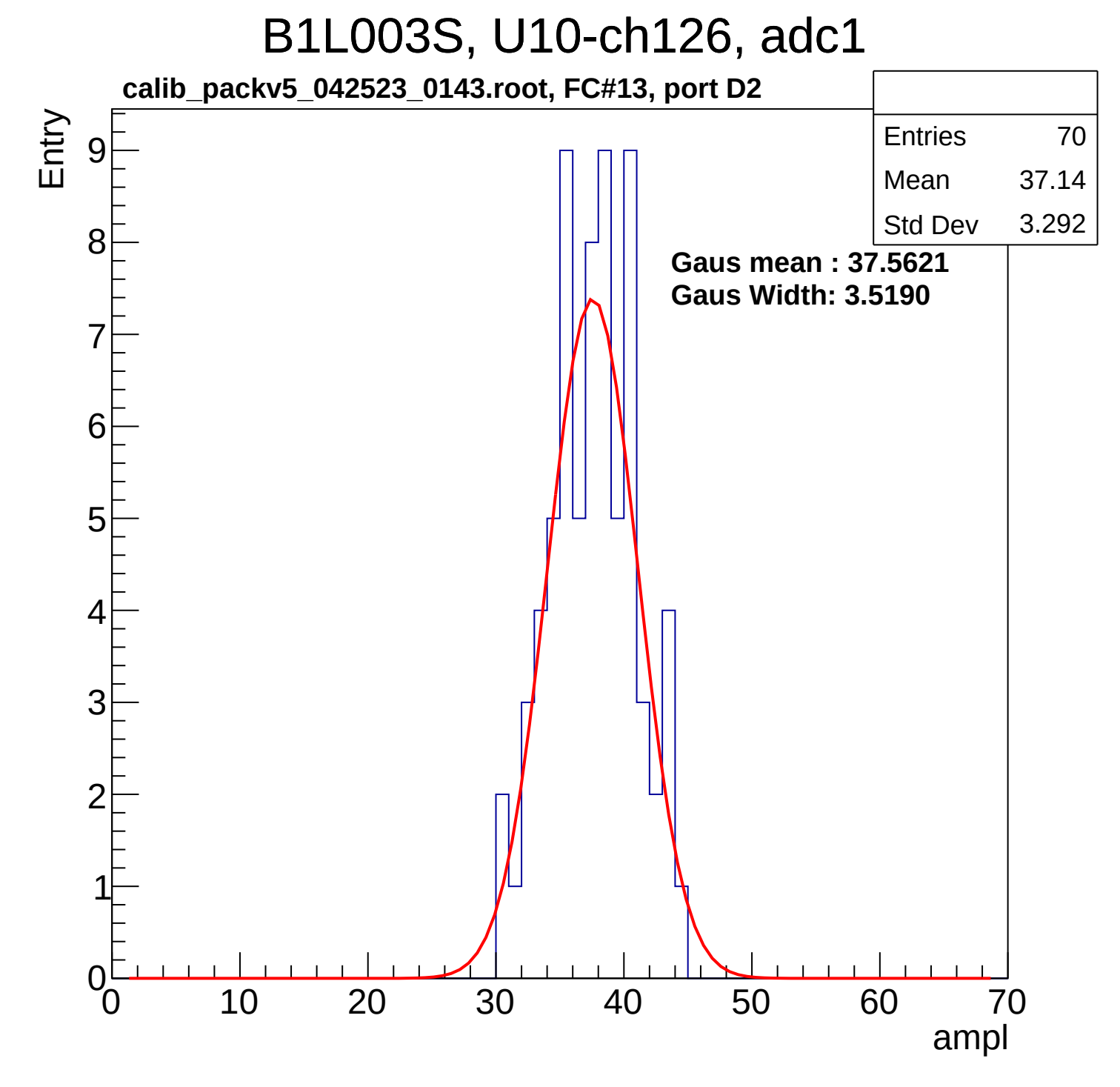
40
50
60
70


B1L003S, U10-ch126, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 37.14
Std Dev 3.292
Gaus mean : 37.5621
Gaus Width: 3.5190
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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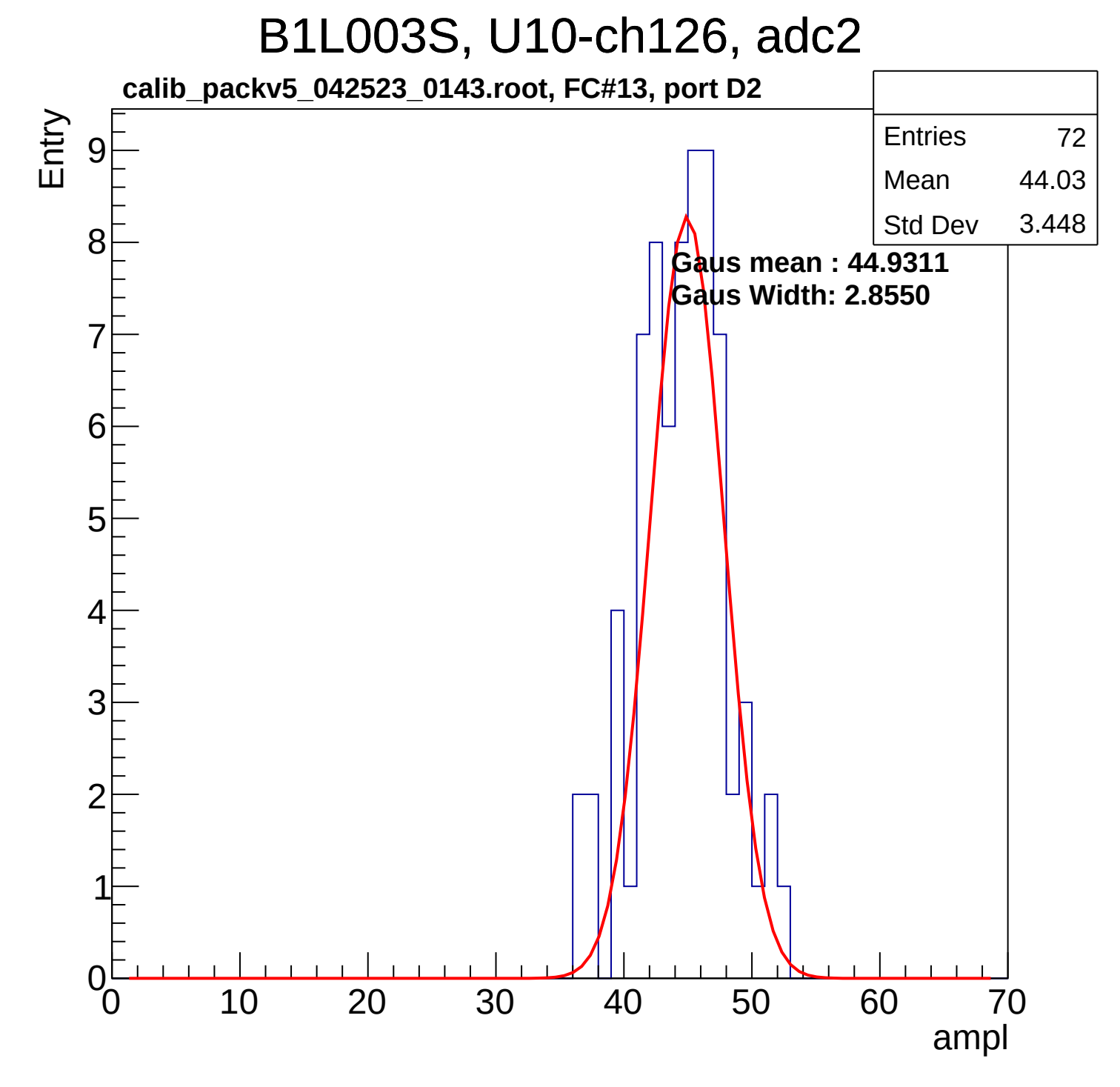
40
50
60
70


B1L003S, U10-ch126, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 44.03
Std Dev 3.448
Gaus mean : 44.9311
Gaus Width: 2.8550
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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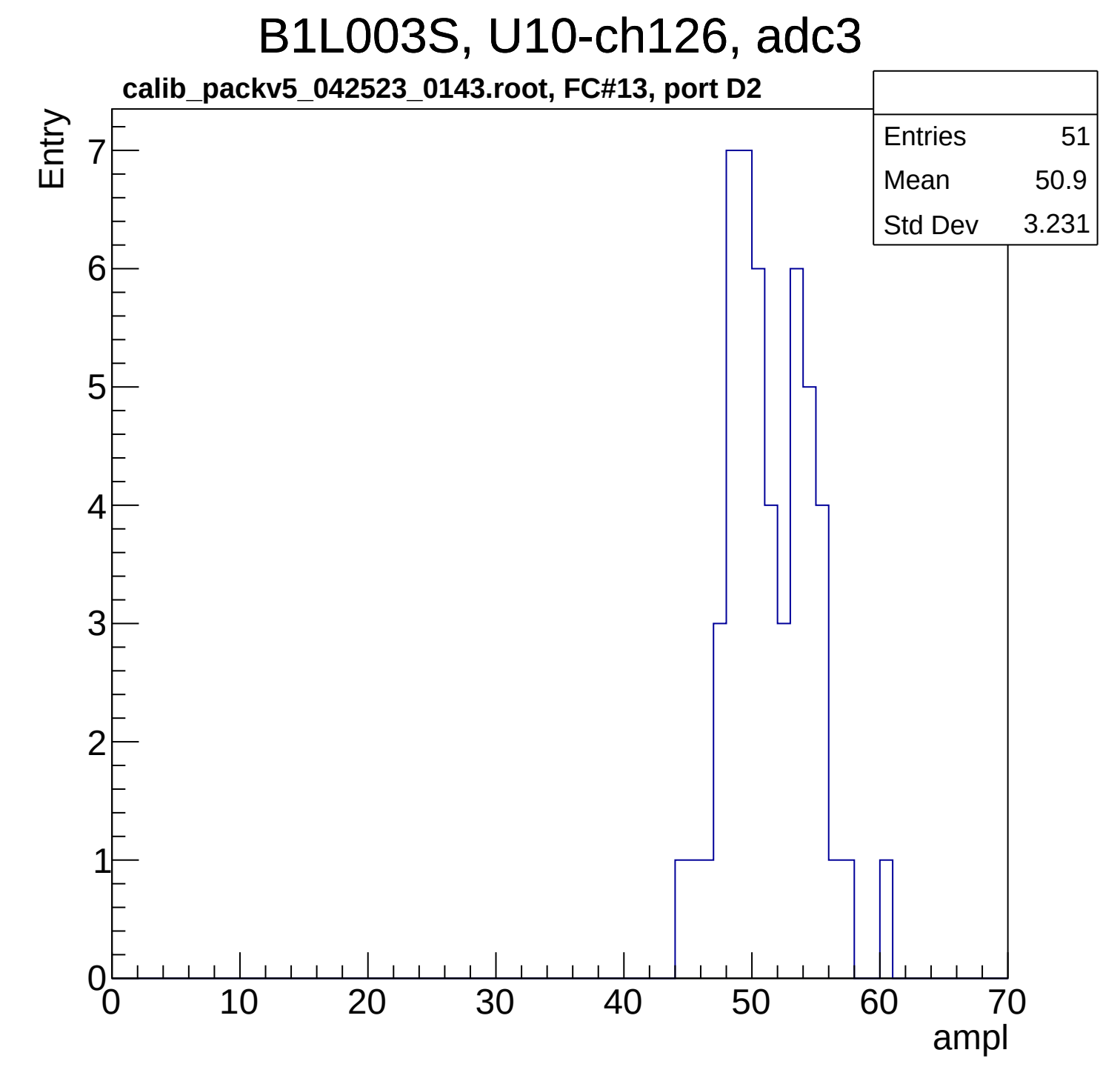
B1L003S, U10-ch126, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 50.9
Std Dev 3.231
Entry
ampl
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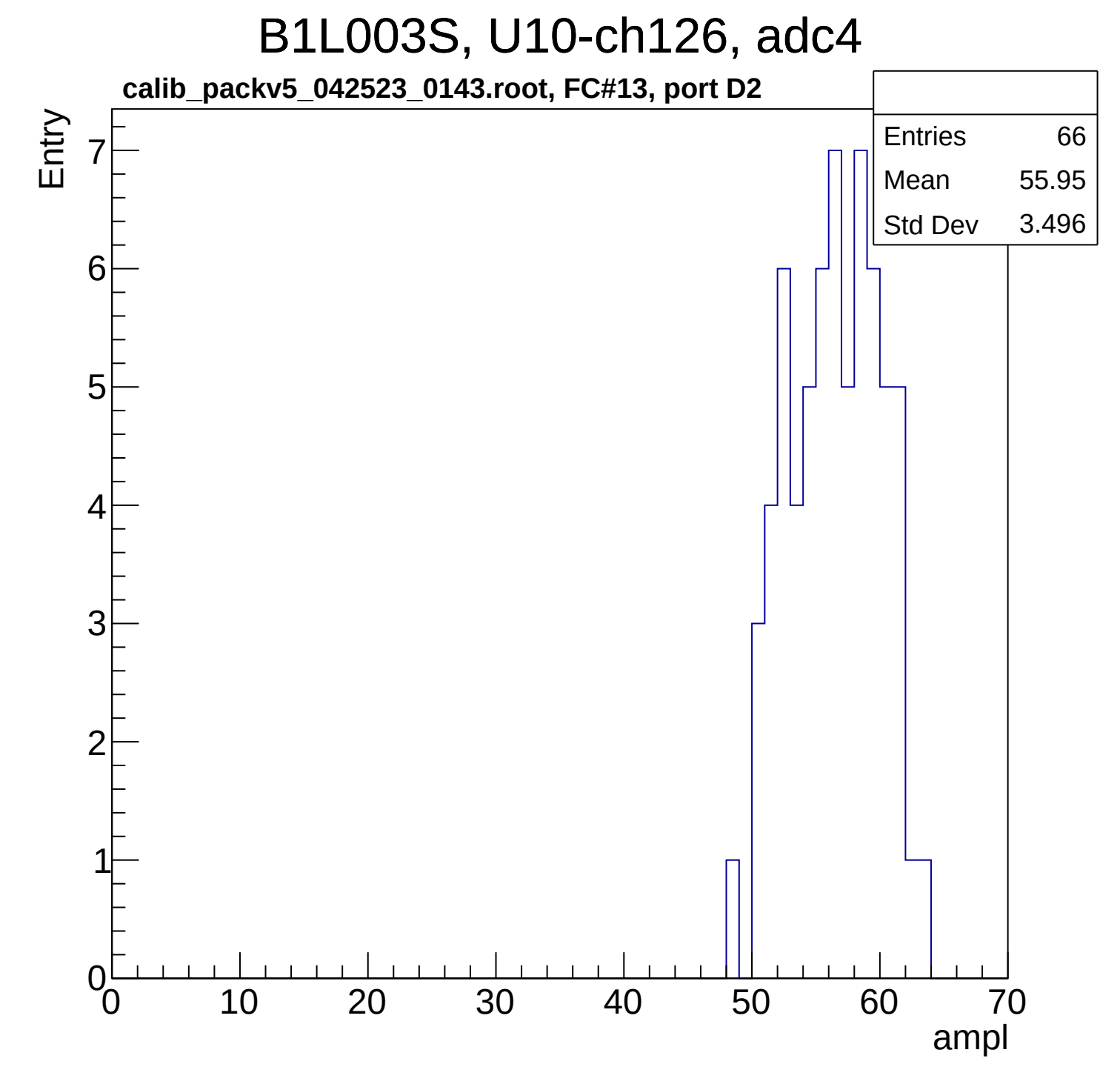
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U10-ch126, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 55.95
Std Dev 3.496
Entry
ampl
0
1
2
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4
5
6
7
0
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20
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40
50
60
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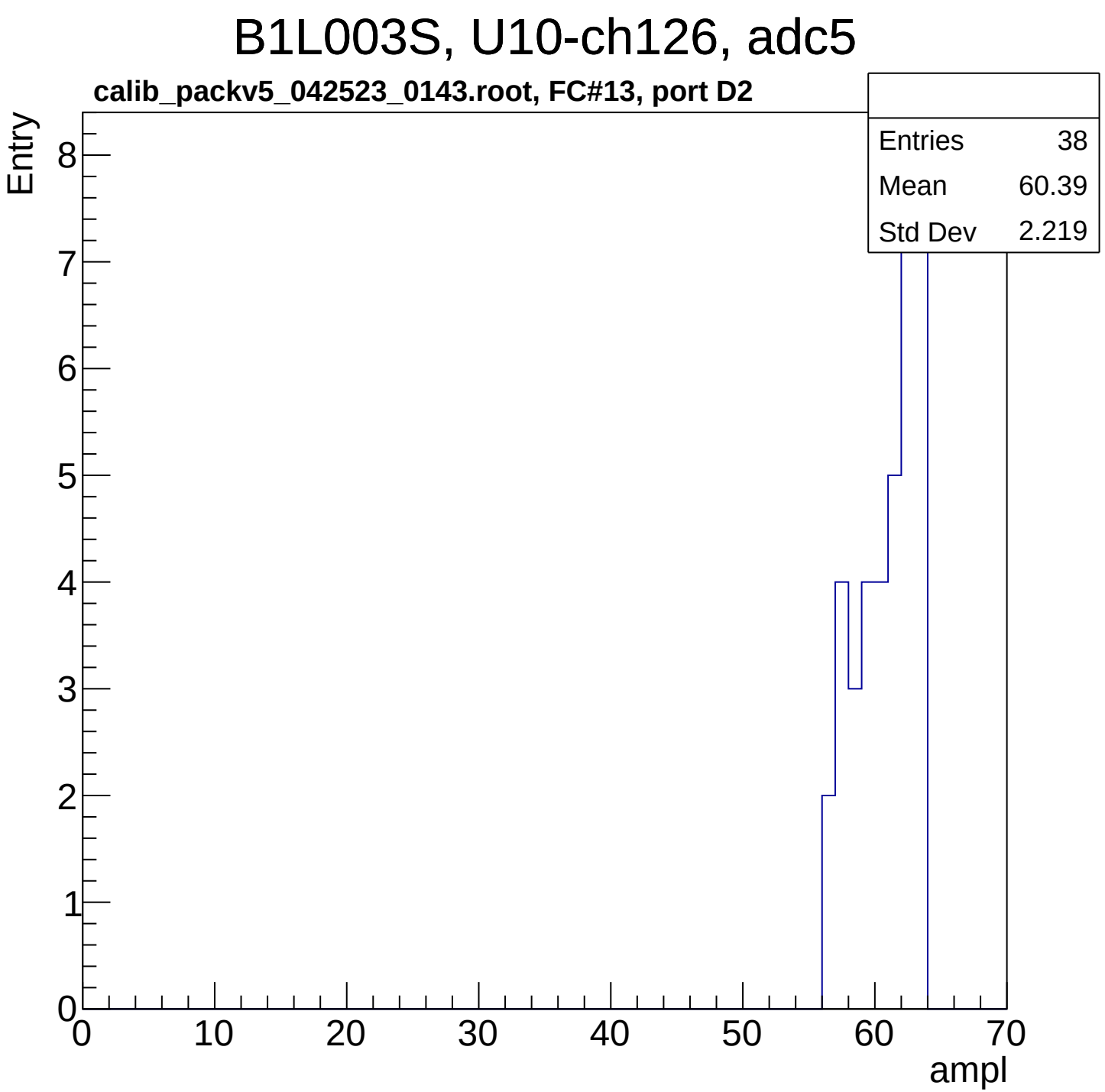
B1L003S, U10-ch126, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 60.39
Std Dev 2.219
Entry
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2
3
4
5
6
7
8
0
10
20
30
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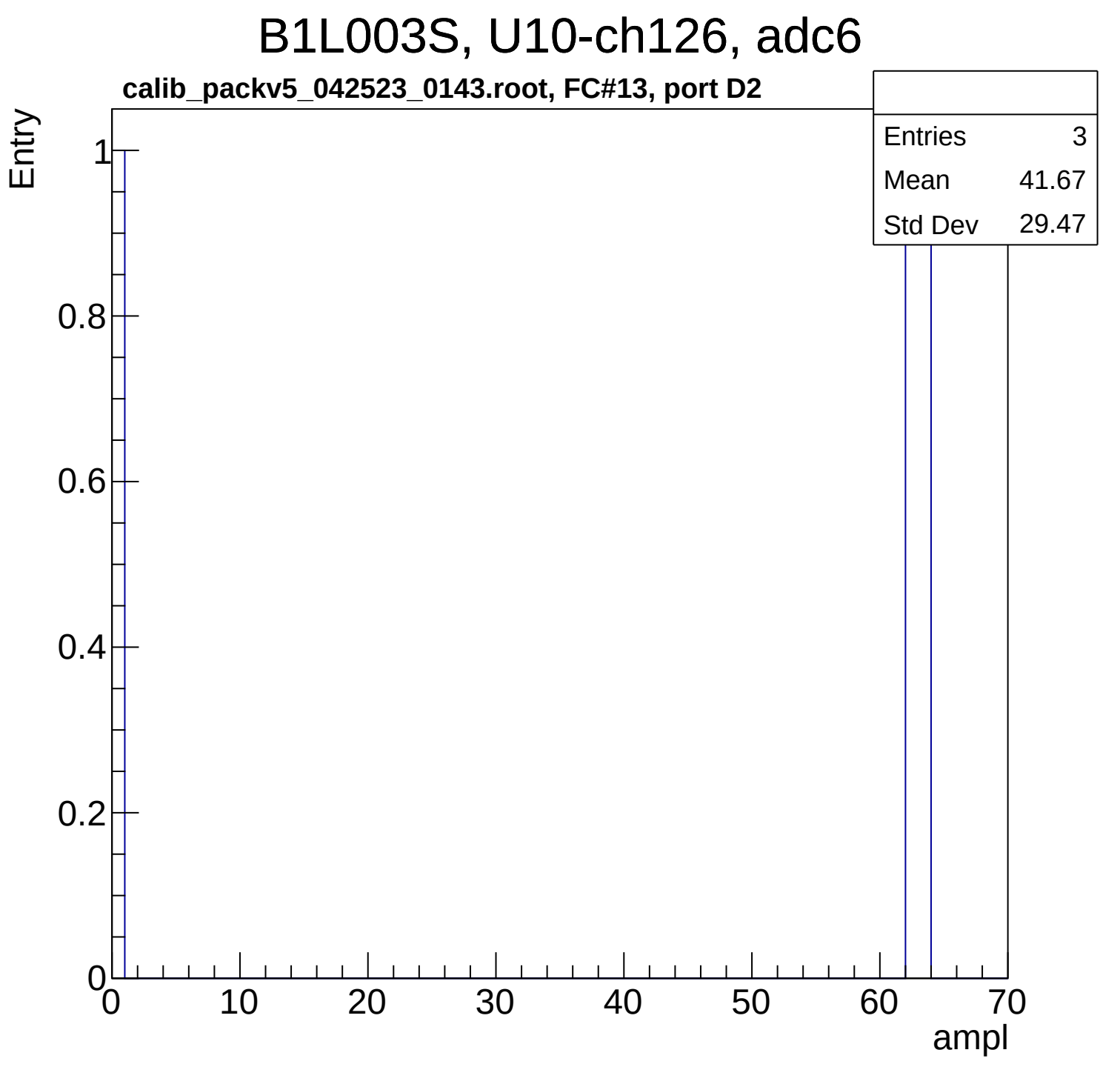
50
60
70
ampl


B1L003S, U10-ch126, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
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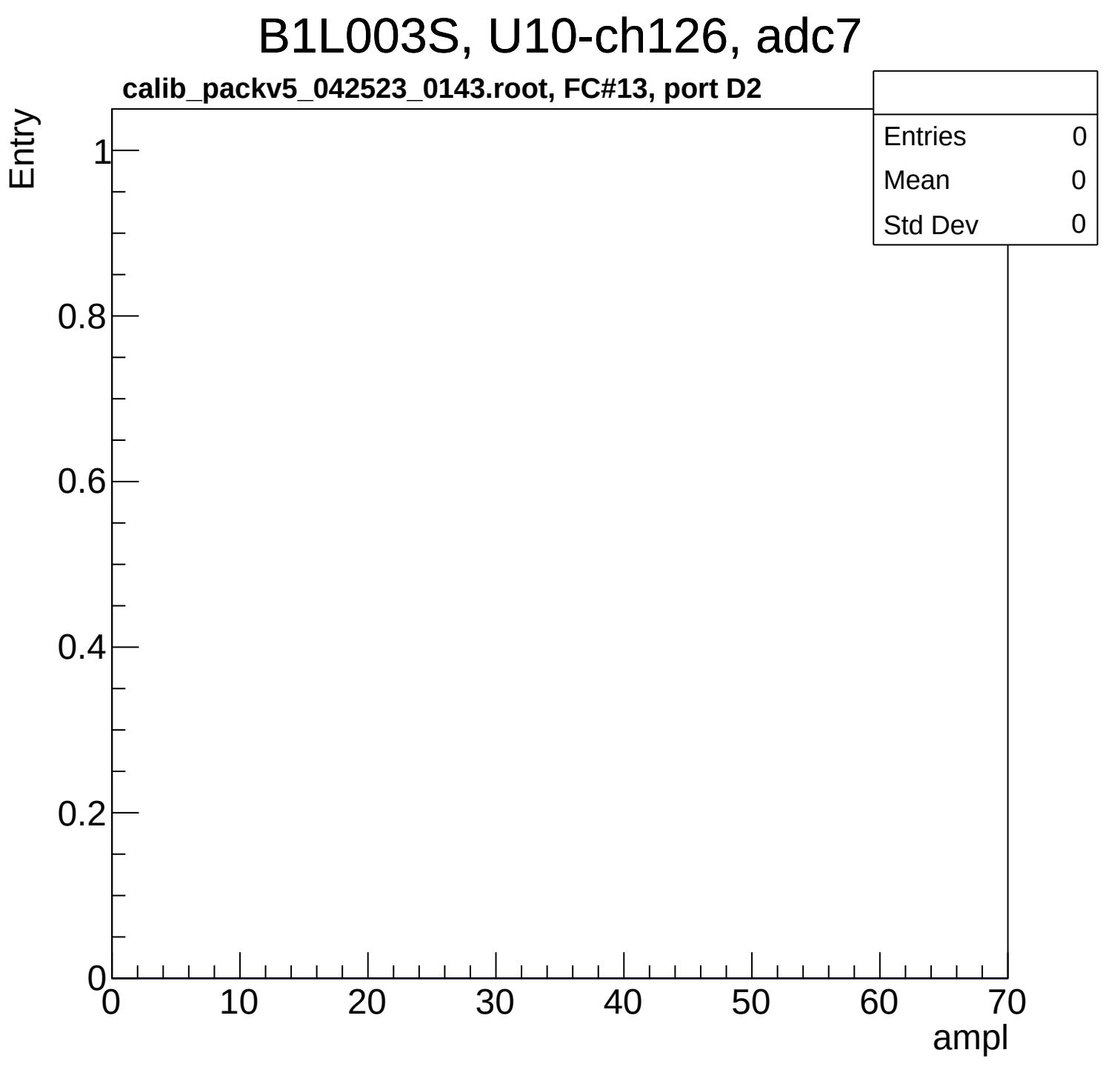
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch126, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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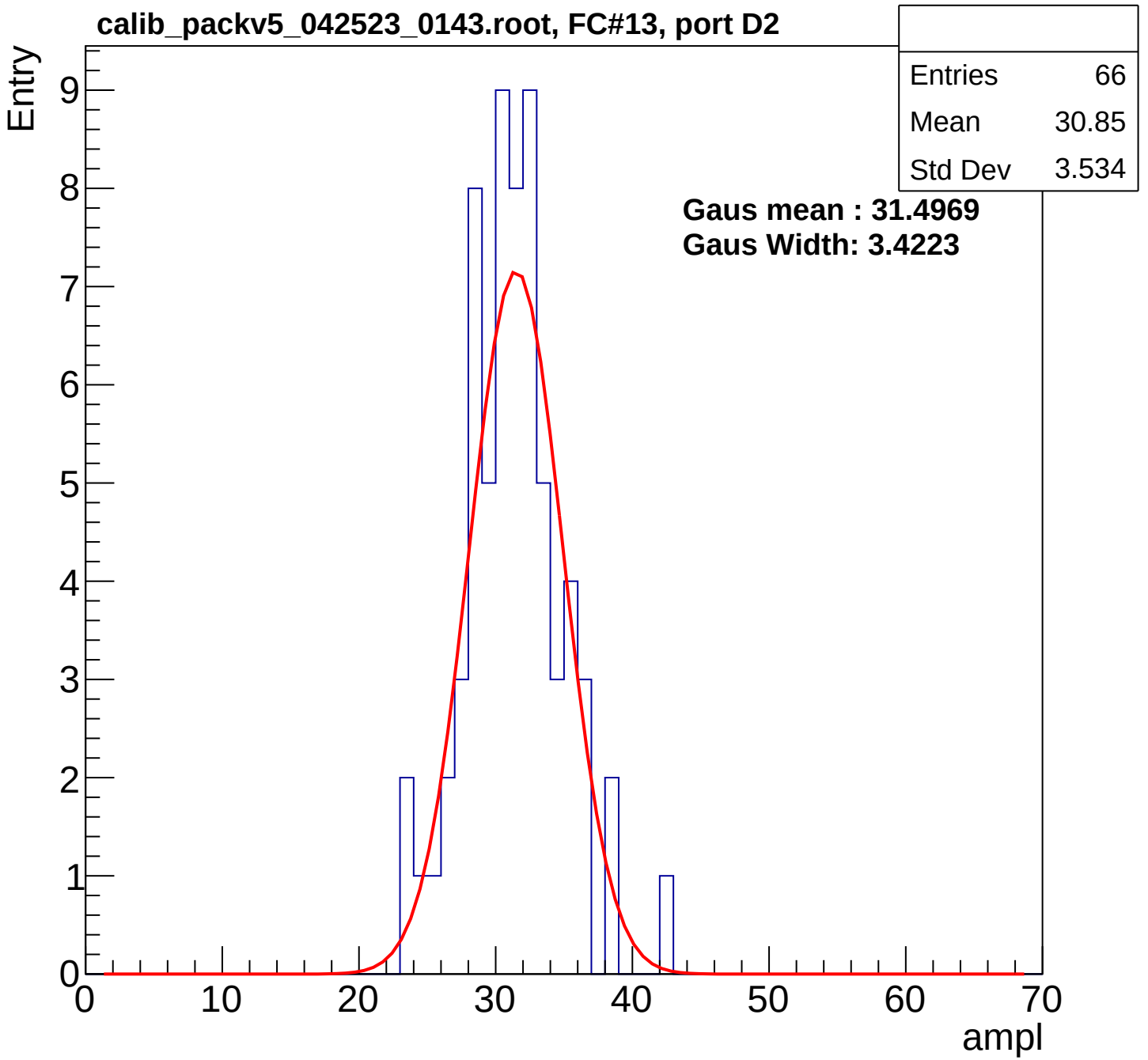
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch127, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.85
Std Dev 3.534
Gaus mean : 31.4969
Gaus Width: 3.4223
Entry
ampl
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2
3
4
5
6
7
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0
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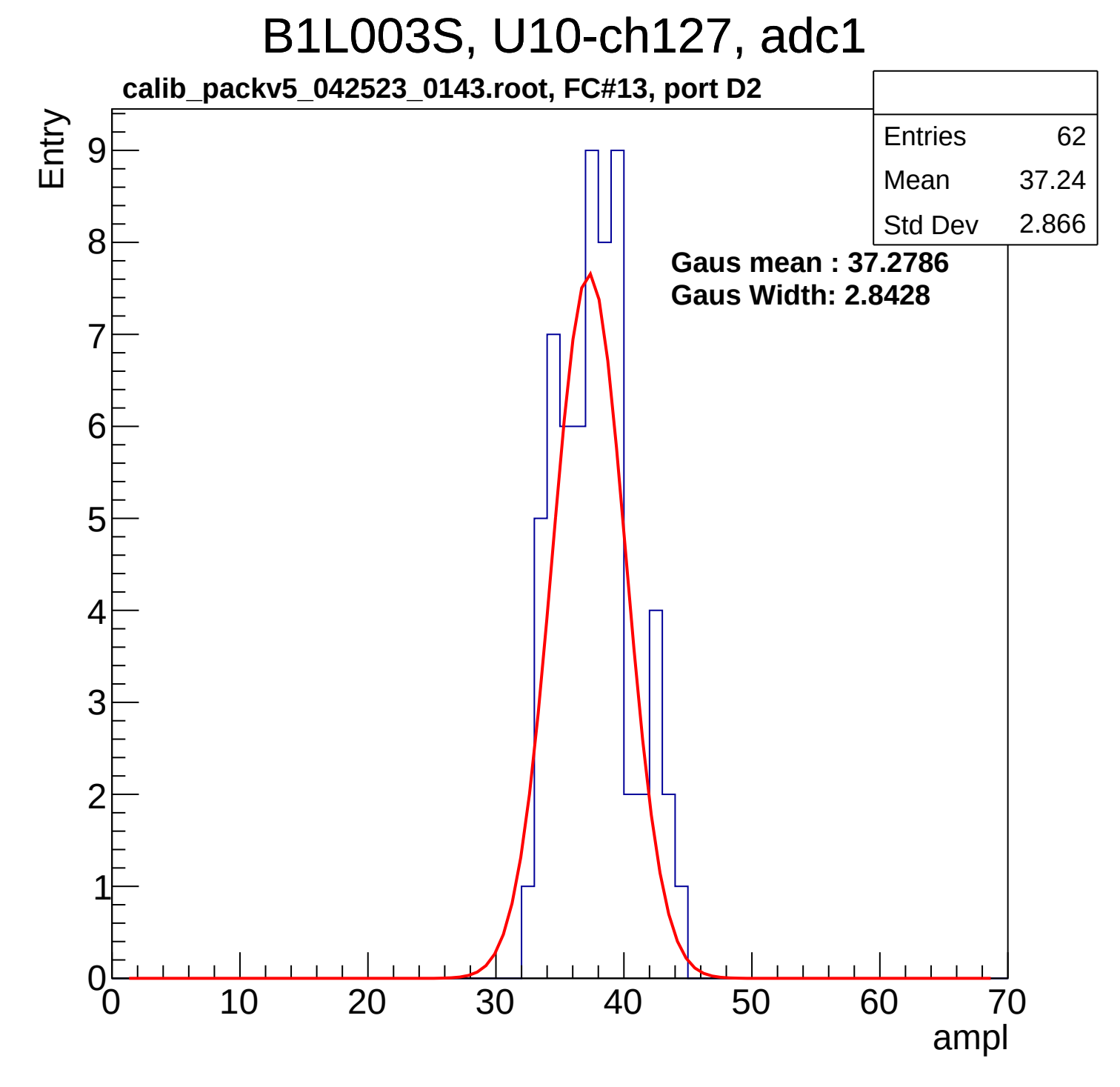
B1L003S, U10-ch127, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.24
Std Dev 2.866
Gaus mean : 37.2786
Gaus Width: 2.8428
Entry
ampl
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2
3
4
5
6
7
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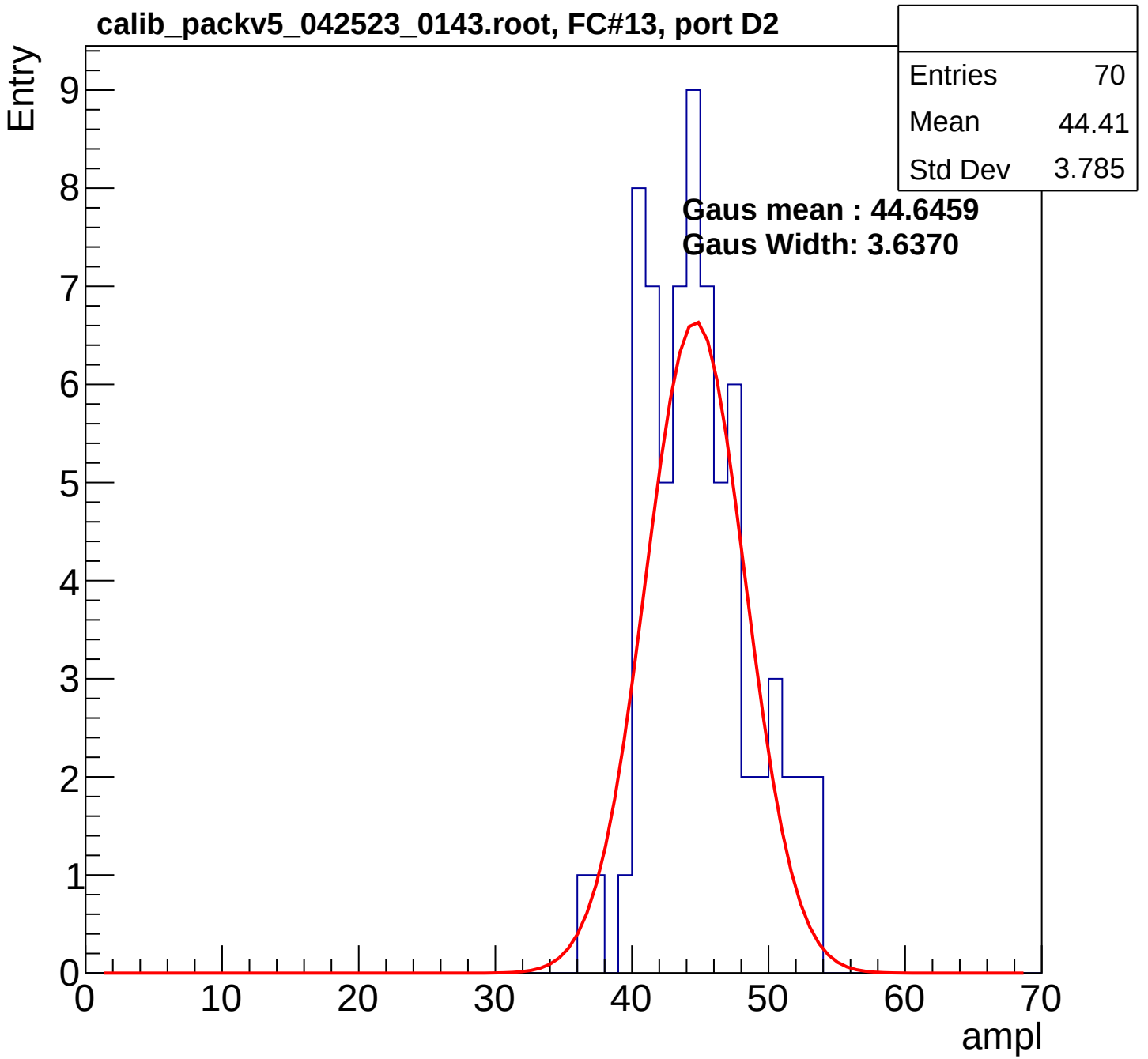
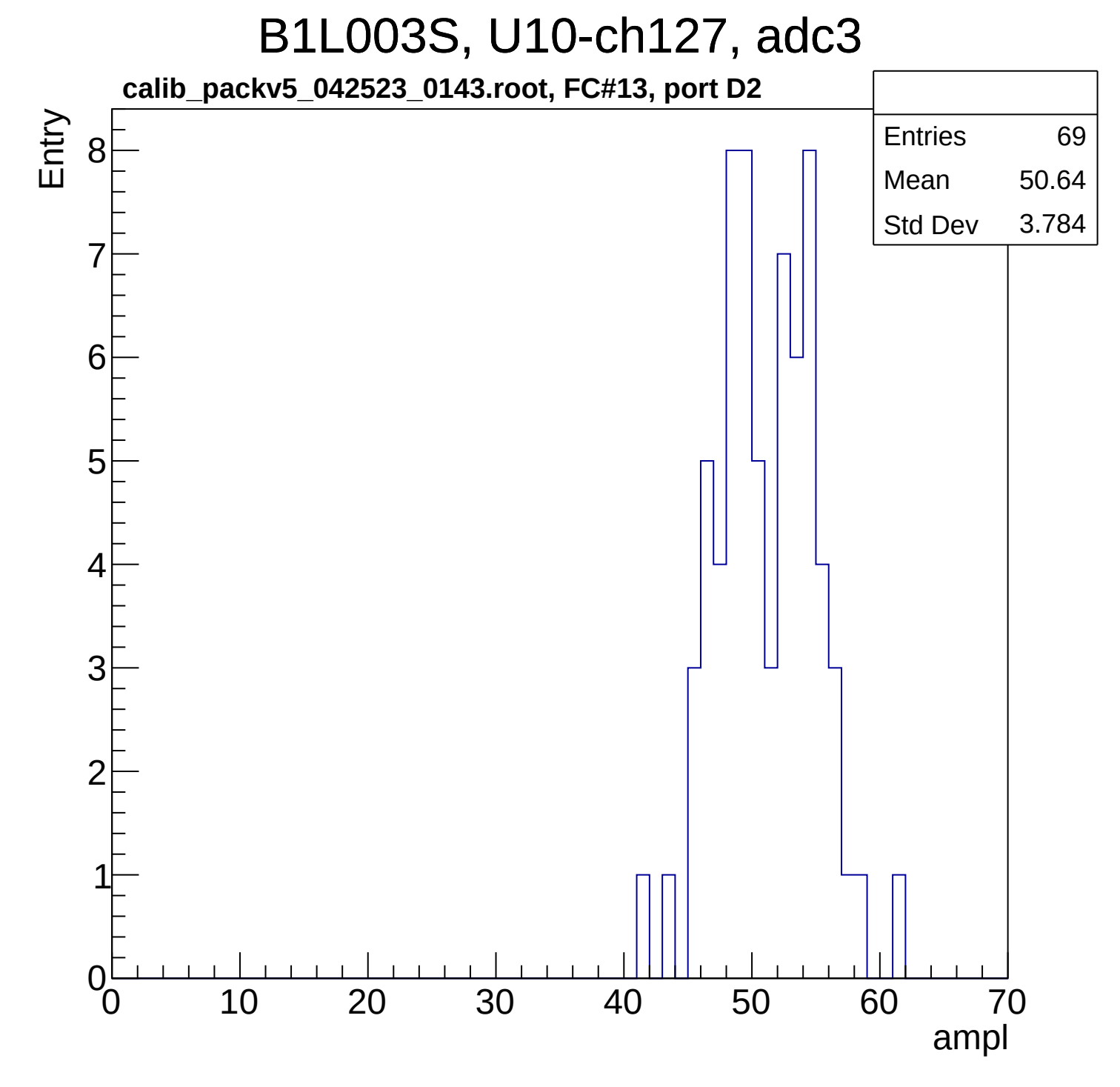
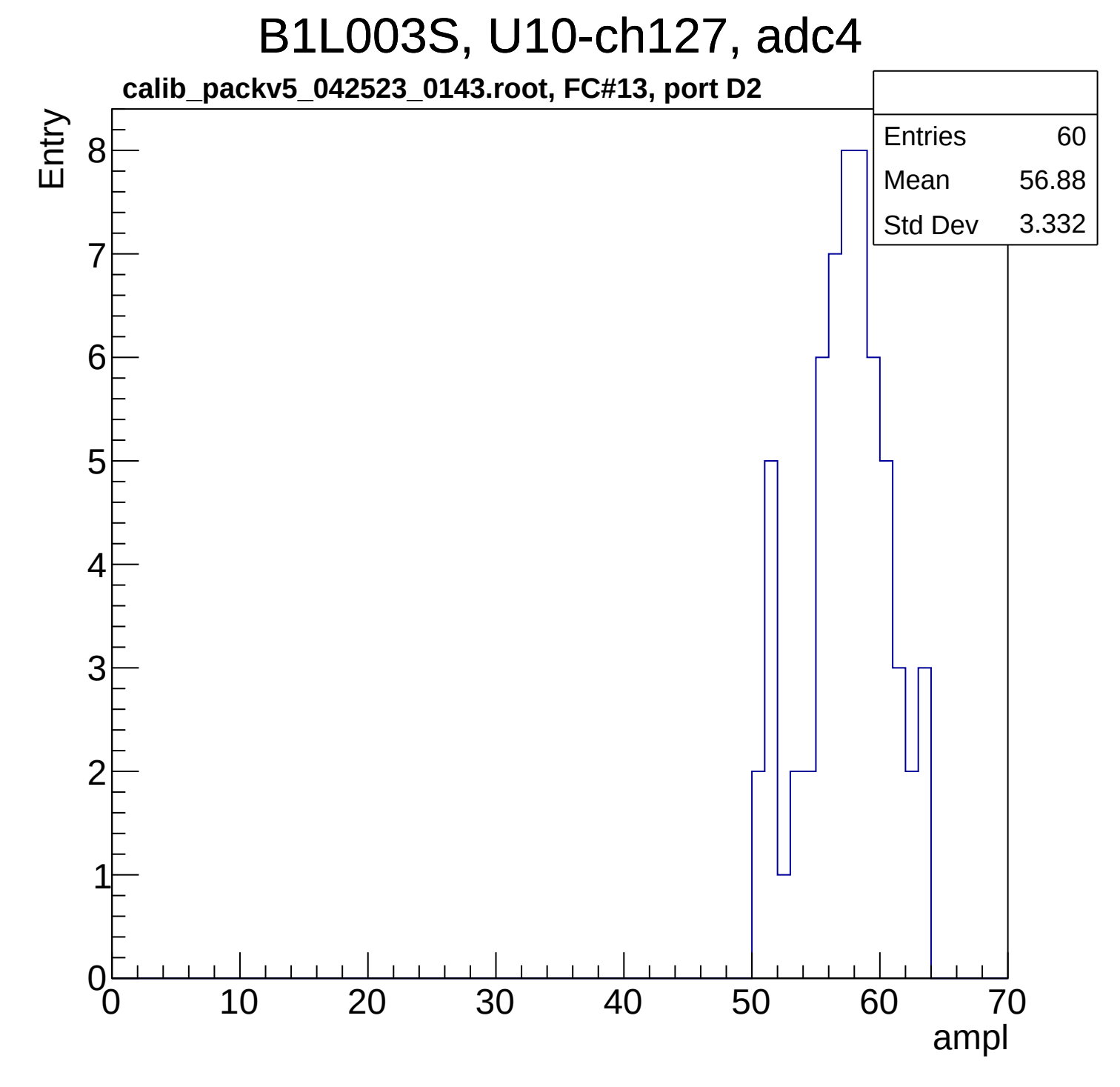
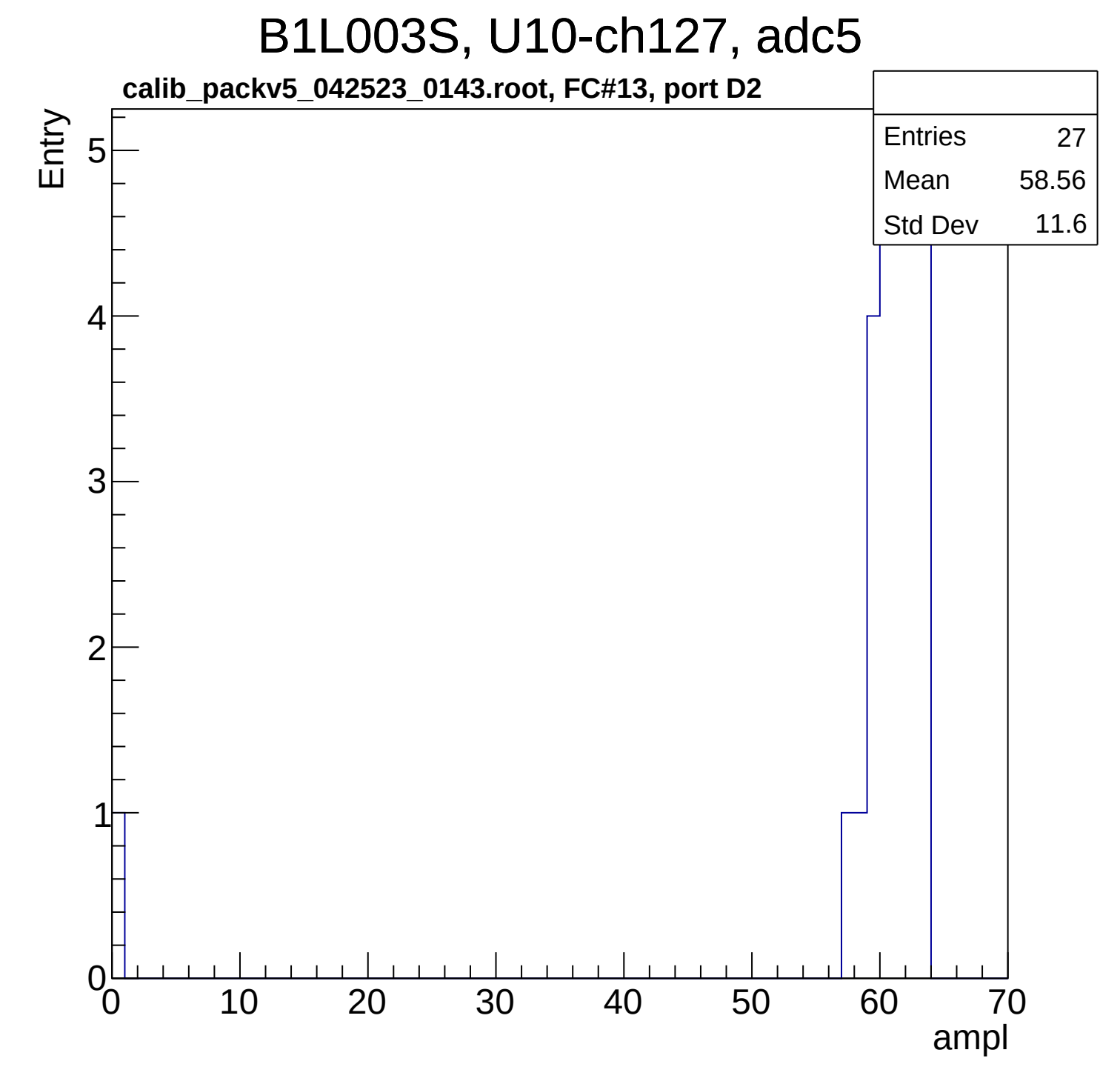
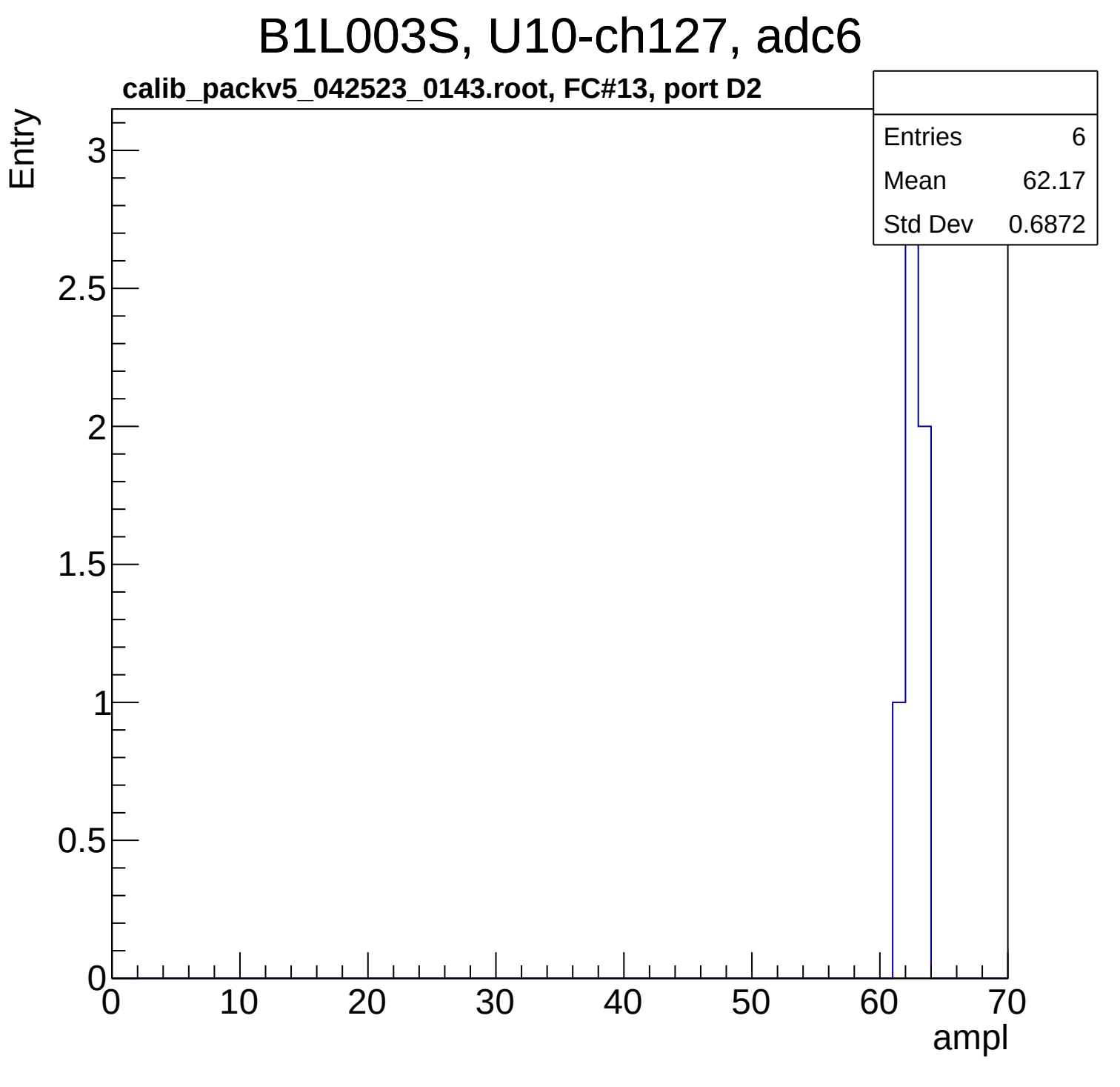
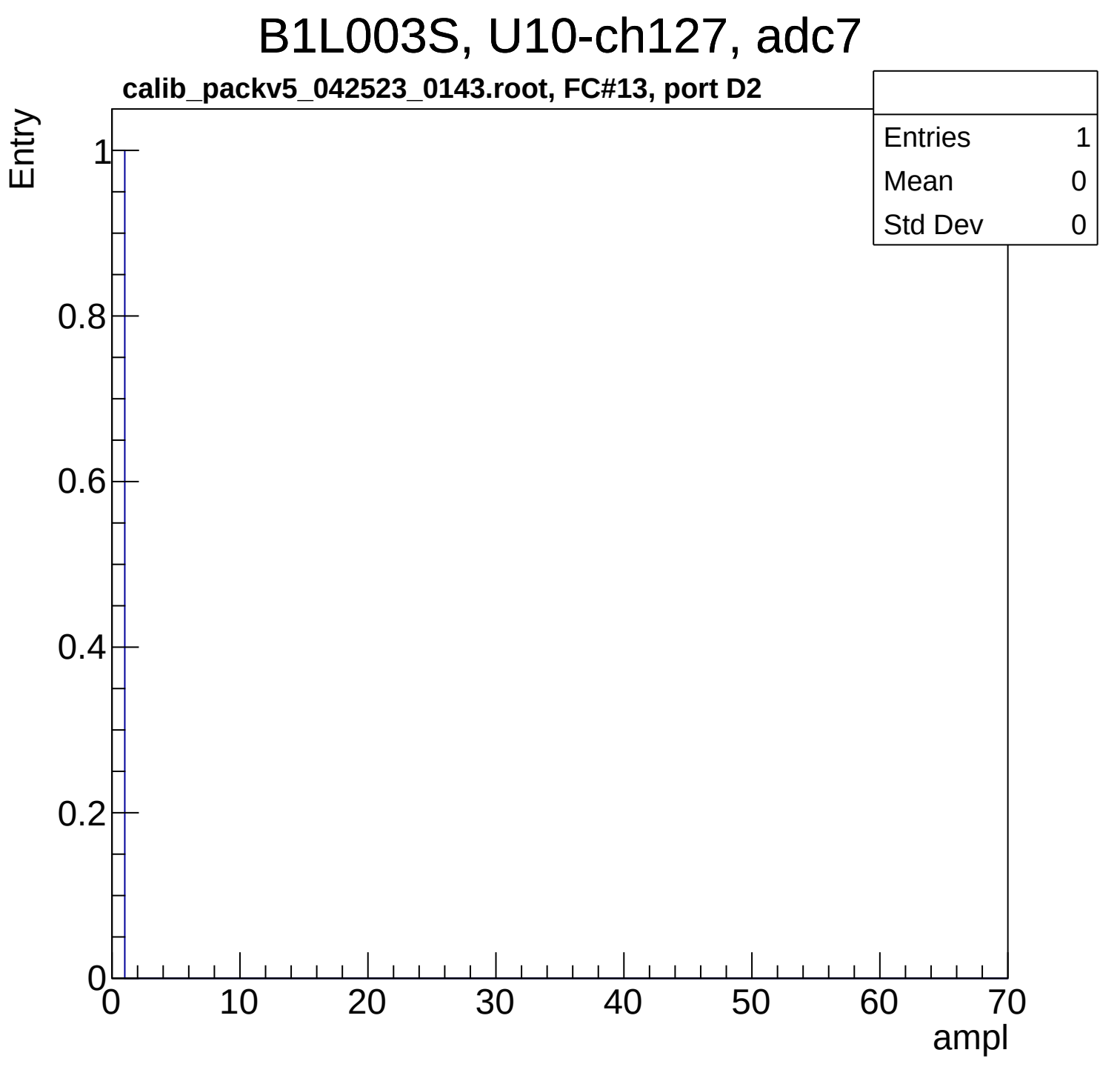

B1L003S, U10-ch127, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 44.41
Std Dev 3.785
Gaus mean : 44.6459
Gaus Width: 3.6370
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

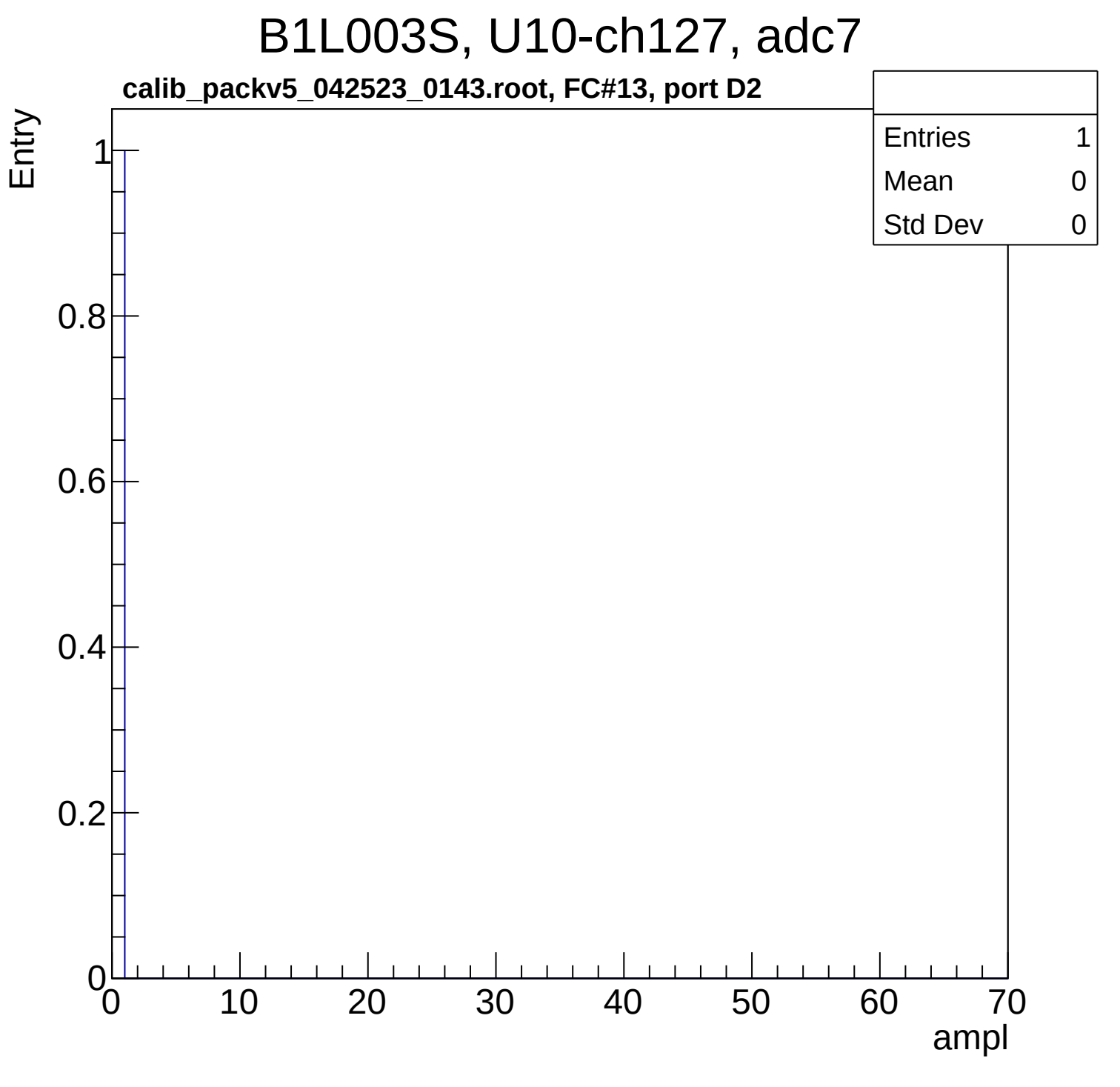

B1L003S, U10-ch127, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.64
Std Dev 3.784
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch127, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 56.88
Std Dev 3.332
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U10-ch127, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 27
Mean 58.56
Std Dev 11.6
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch127, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U10-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U10-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl