

B1L001S, U24-ch0

calib_packv5_042523_0143.root, FC#2, port C2

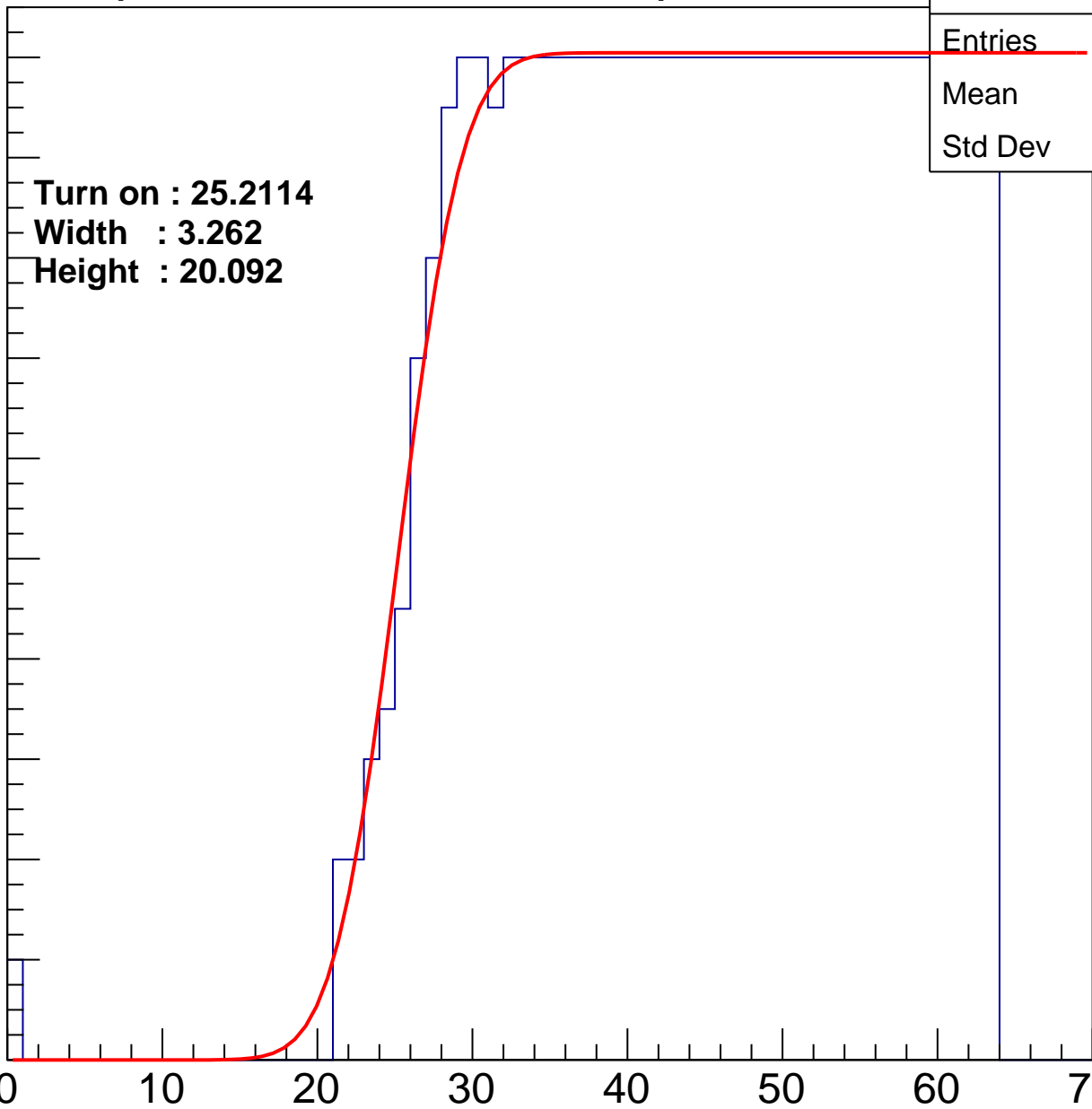
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2114
Width : 3.262
Height : 20.092

Entries	780
Mean	43.85
Std Dev	11.58

ampl



B1L001S, U24-ch1

calib_packv5_042523_0143.root, FC#2, port C2

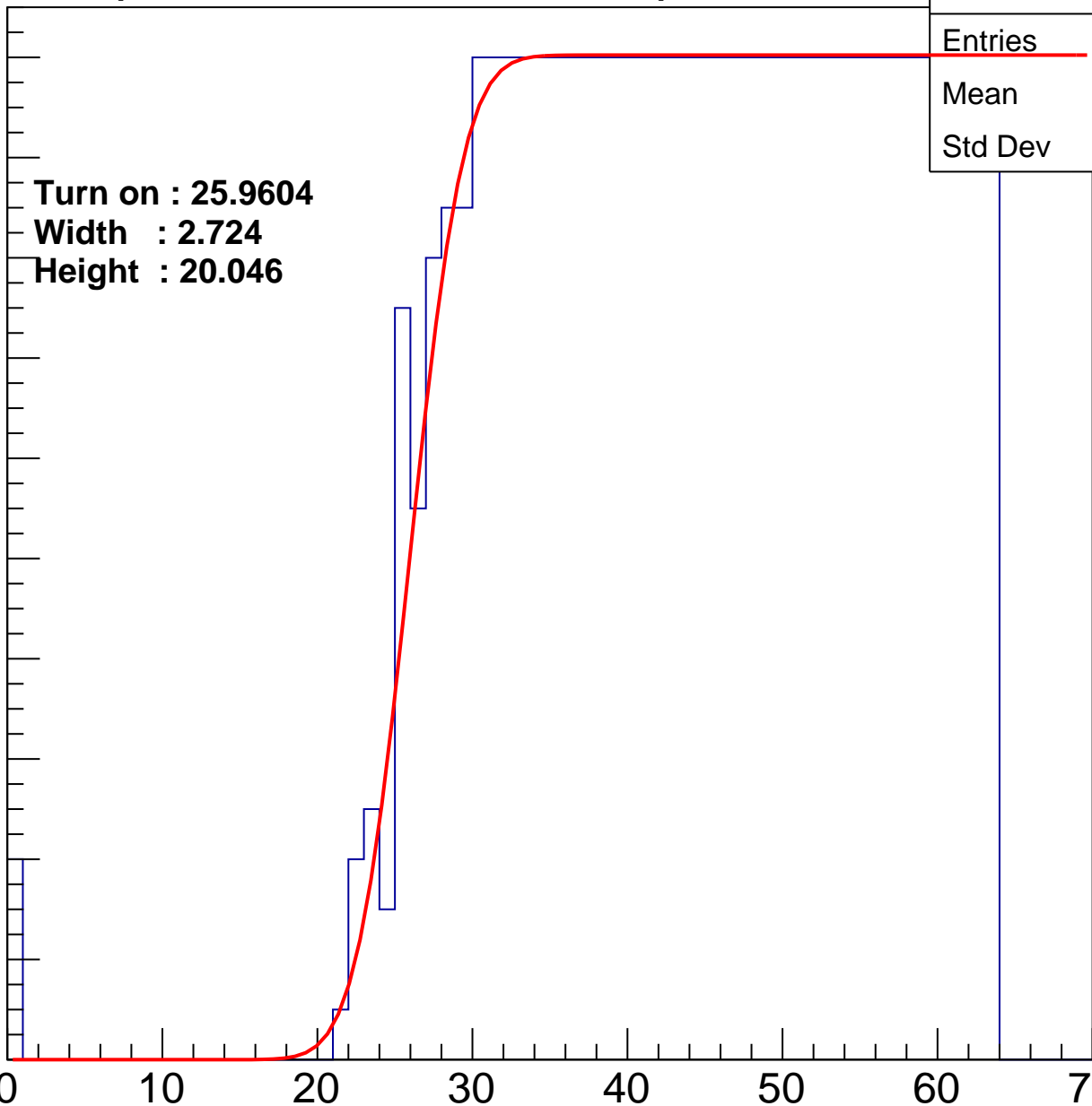
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9604
Width : 2.724
Height : 20.046

Entries	773
Mean	43.96
Std Dev	11.66

ampl



B1L001S, U24-ch2

calib_packv5_042523_0143.root, FC#2, port C2

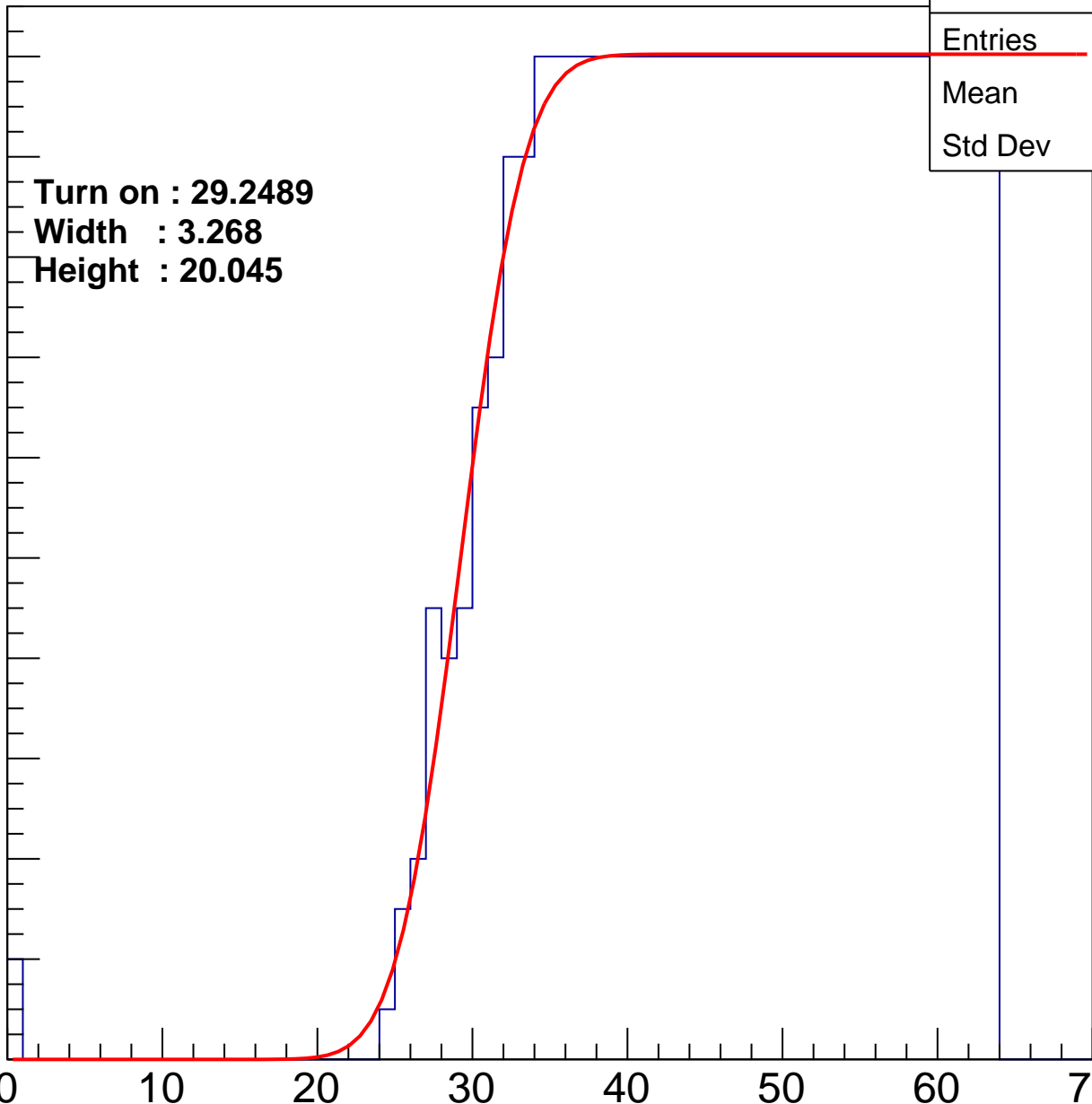
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.2489
Width : 3.268
Height : 20.045

Entries	699
Mean	45.82
Std Dev	10.55

ampl



B1L001S, U24-ch3

calib_packv5_042523_0143.root, FC#2, port C2

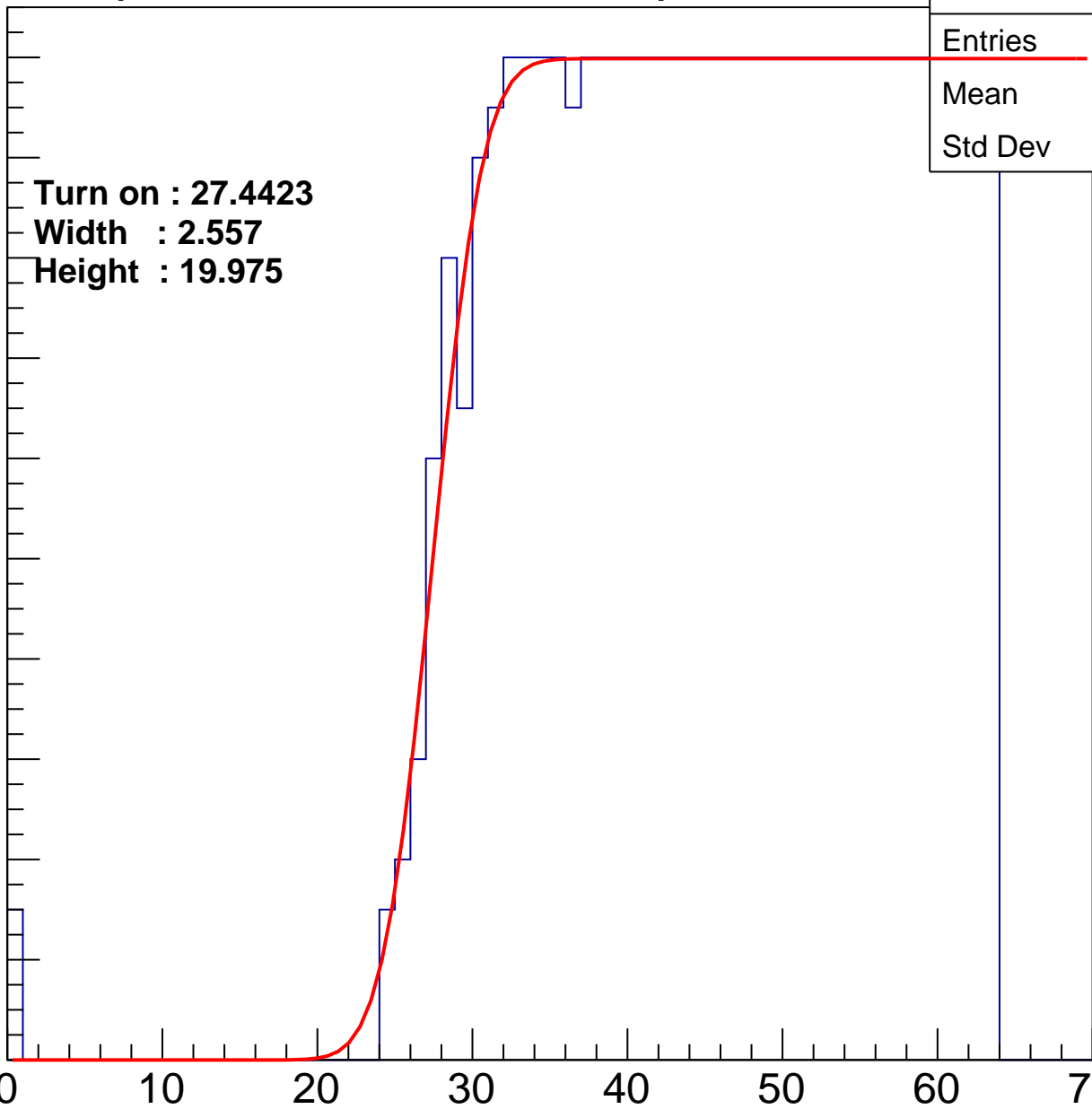
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4423
Width : 2.557
Height : 19.975

Entries	733
Mean	44.98
Std Dev	11.03

ampl



B1L001S, U24-ch4

calib_packv5_042523_0143.root, FC#2, port C2

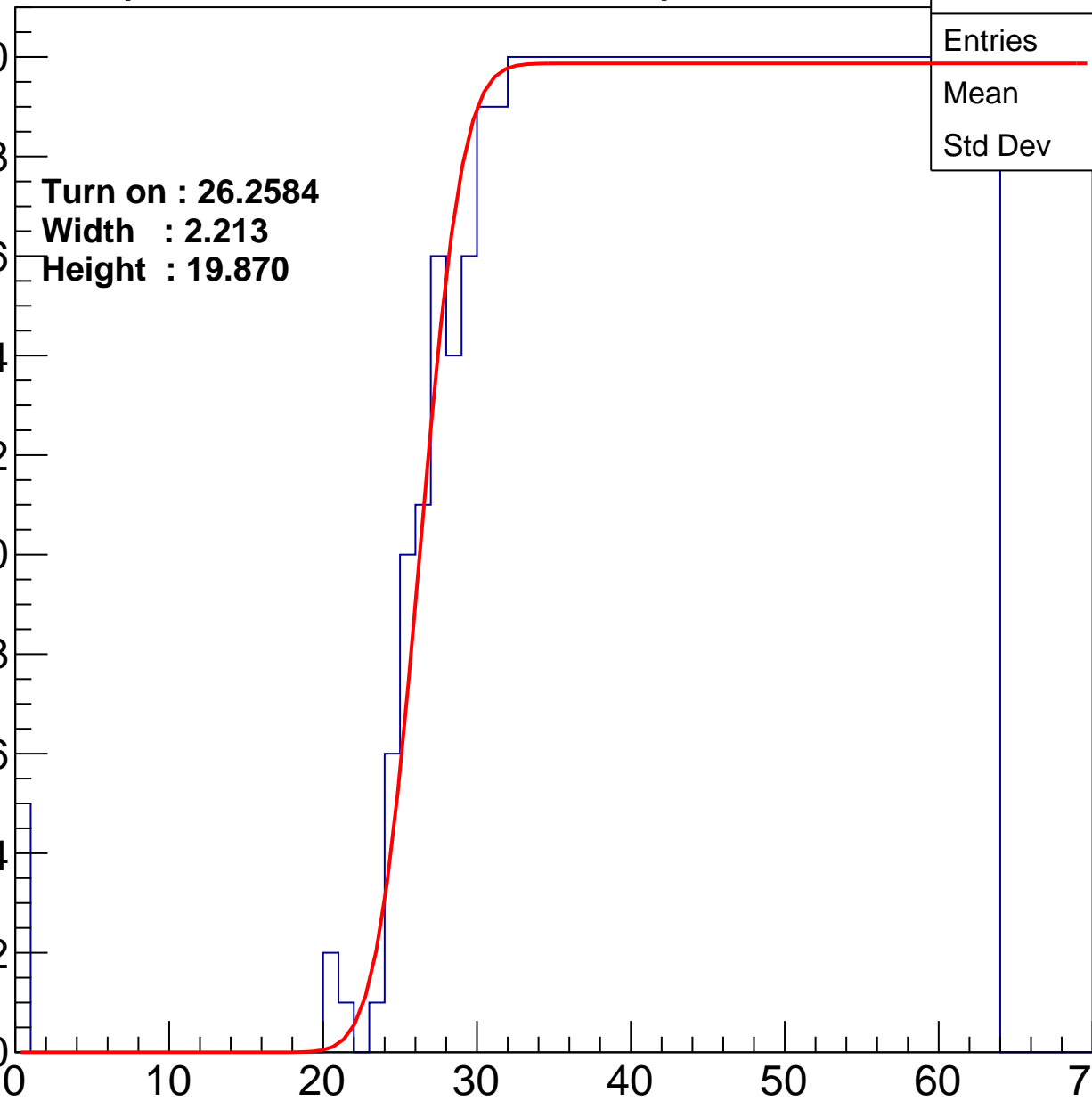
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2584
Width : 2.213
Height : 19.870

Entries	760
Mean	44.23
Std Dev	11.61

ampl



B1L001S, U24-ch5

calib_packv5_042523_0143.root, FC#2, port C2

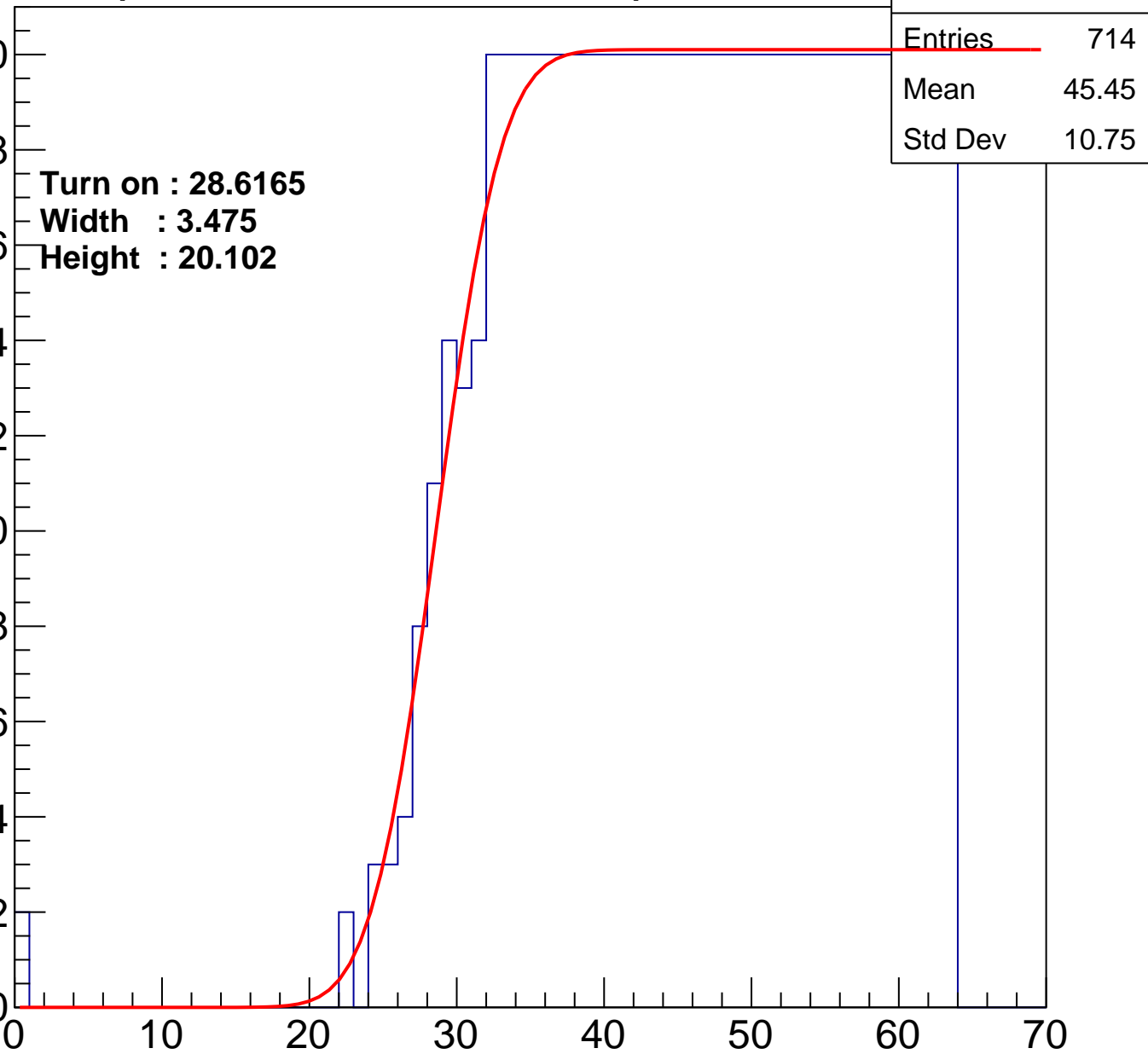
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6165
Width : 3.475
Height : 20.102

Entries	714
Mean	45.45
Std Dev	10.75

ampl



B1L001S, U24-ch6

calib_packv5_042523_0143.root, FC#2, port C2

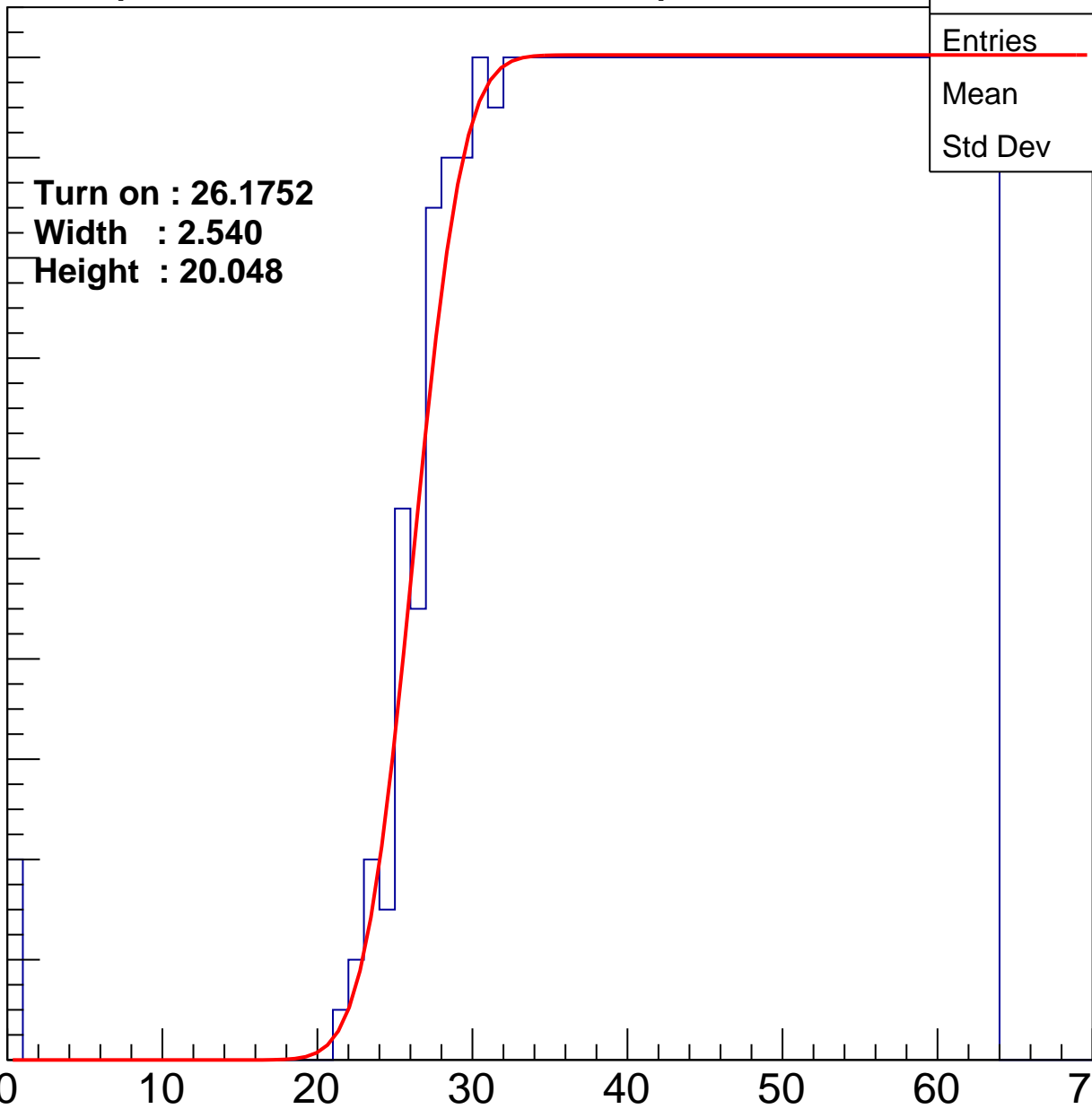
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1752
Width : 2.540
Height : 20.048

Entries	766
Mean	44.14
Std Dev	11.55

ampl



B1L001S, U24-ch7

calib_packv5_042523_0143.root, FC#2, port C2

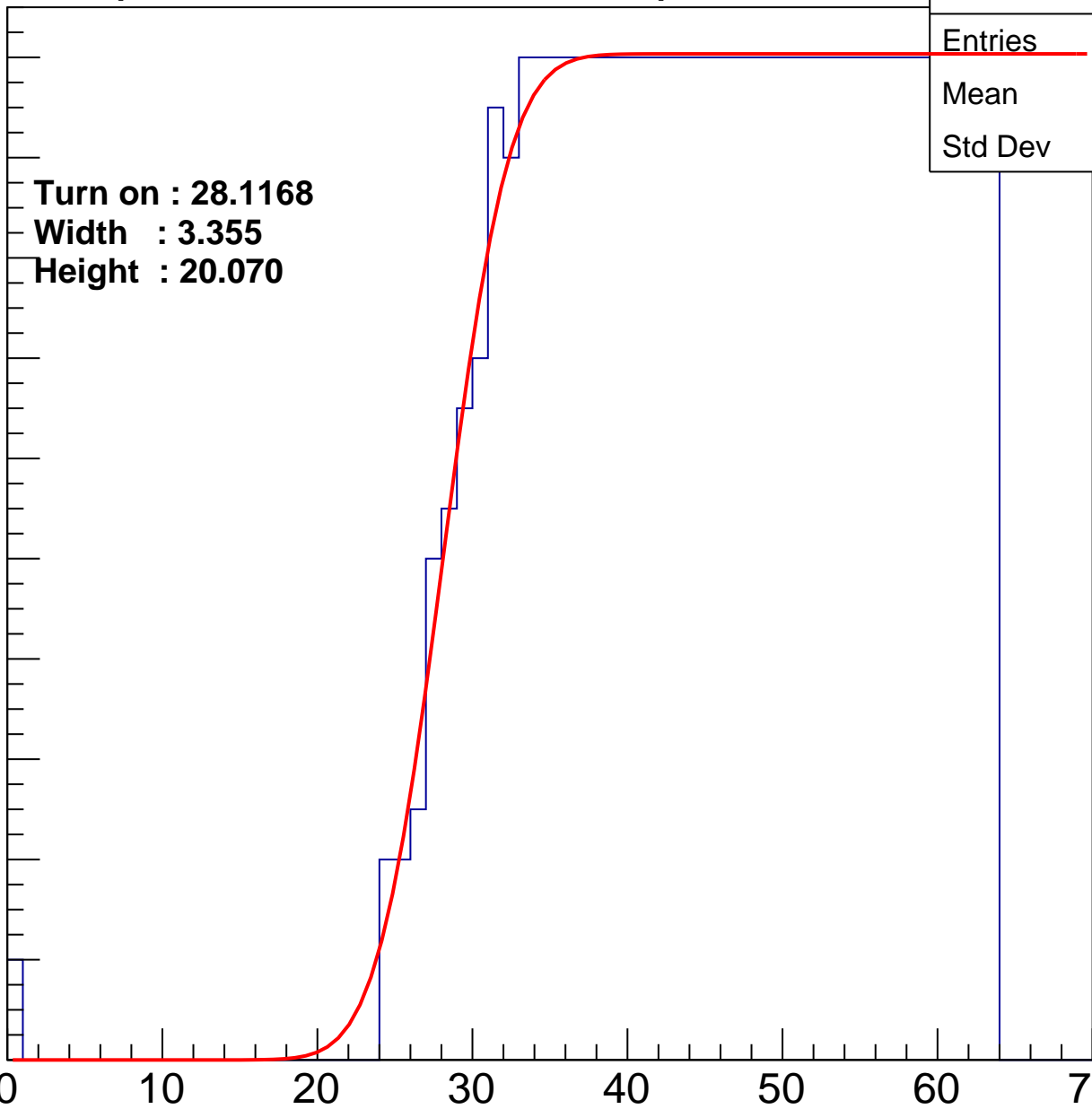
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1168
Width : 3.355
Height : 20.070

Entries	720
Mean	45.31
Std Dev	10.8

ampl



B1L001S, U24-ch8

calib_packv5_042523_0143.root, FC#2, port C2

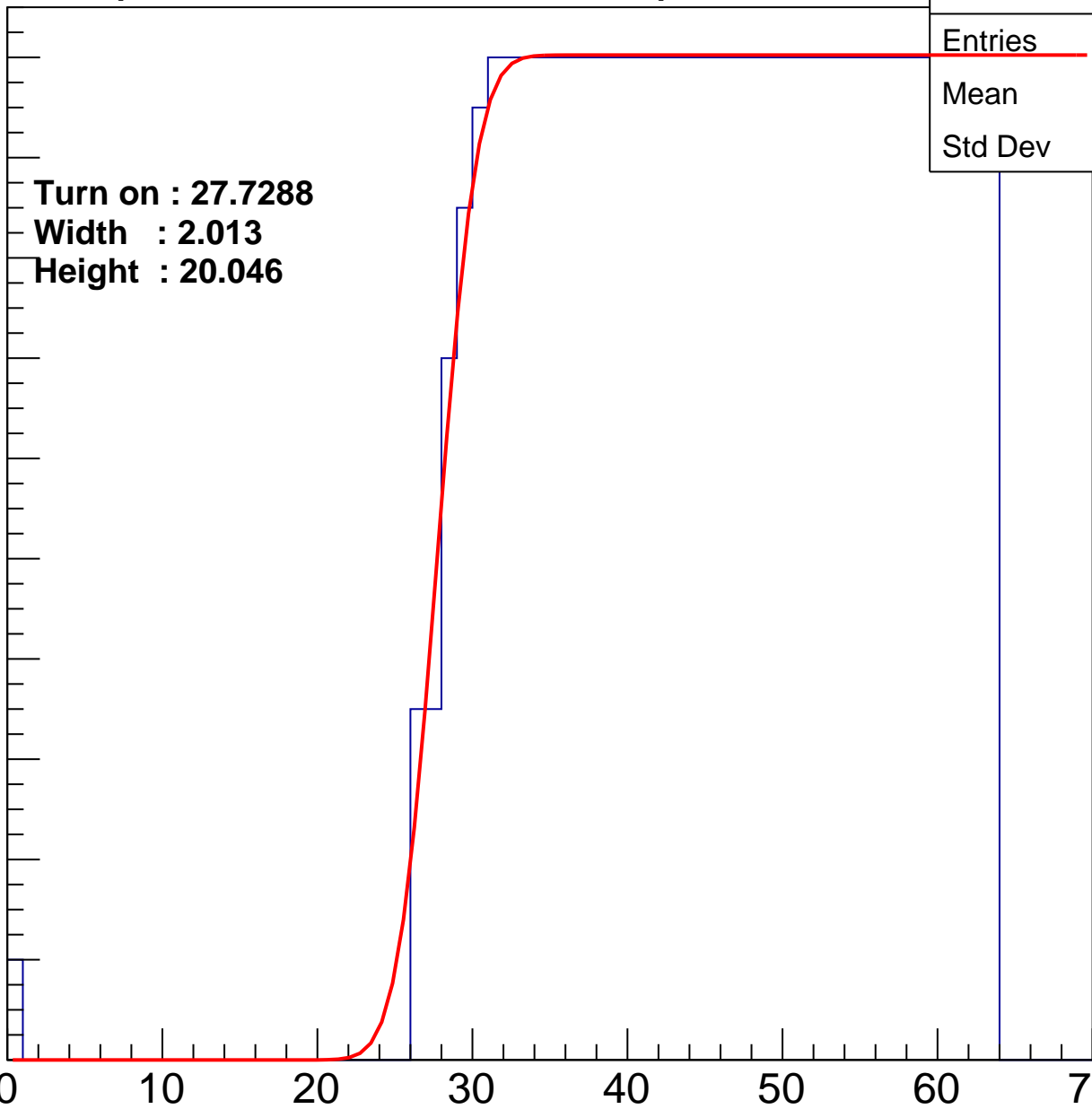
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7288
Width : 2.013
Height : 20.046

Entries	726
Mean	45.24
Std Dev	10.75

ampl



B1L001S, U24-ch9

calib_packv5_042523_0143.root, FC#2, port C2

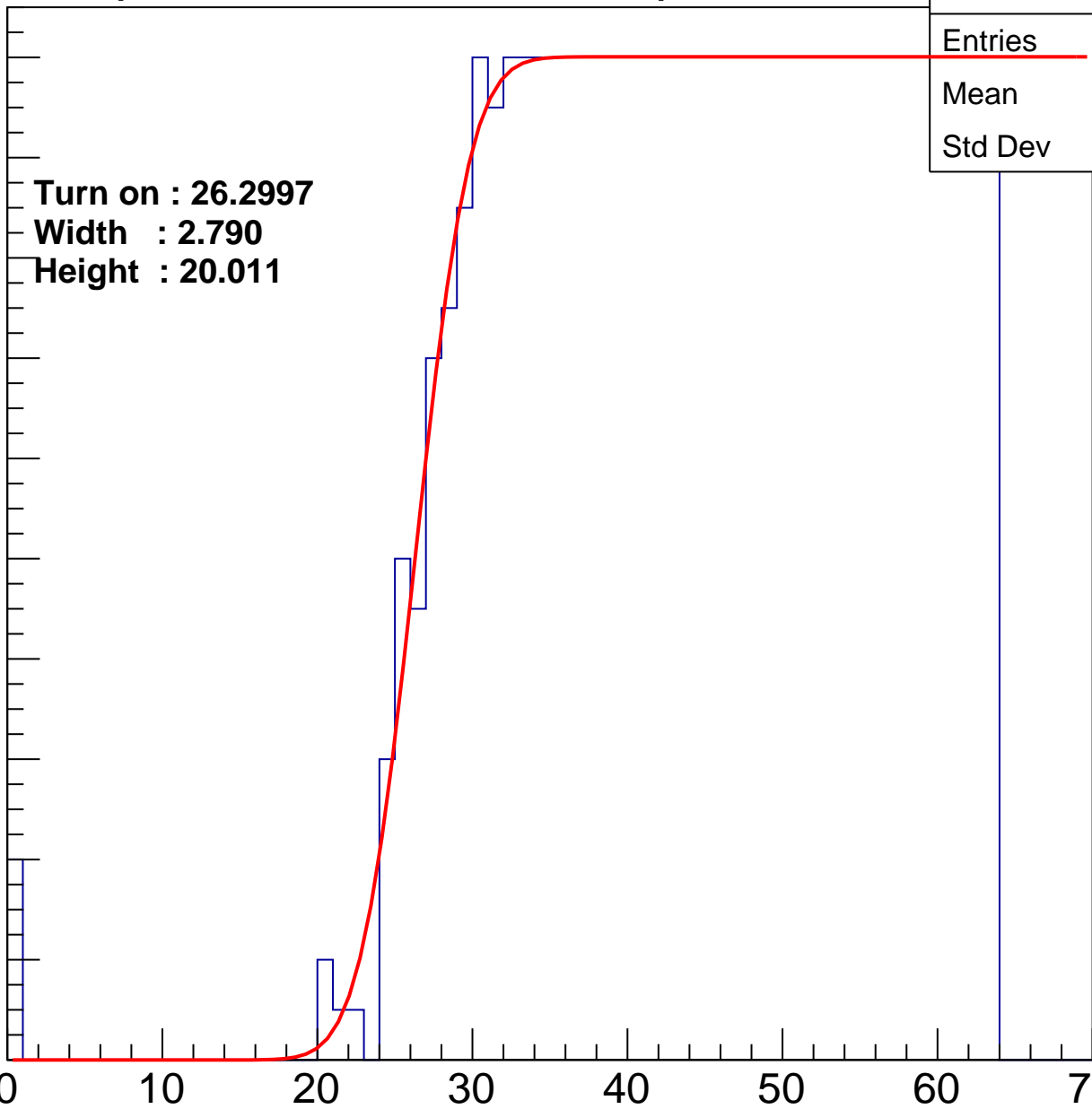
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2997
Width : 2.790
Height : 20.011

Entries	758
Mean	44.32
Std Dev	11.49

ampl



B1L001S, U24-ch10

calib_packv5_042523_0143.root, FC#2, port C2

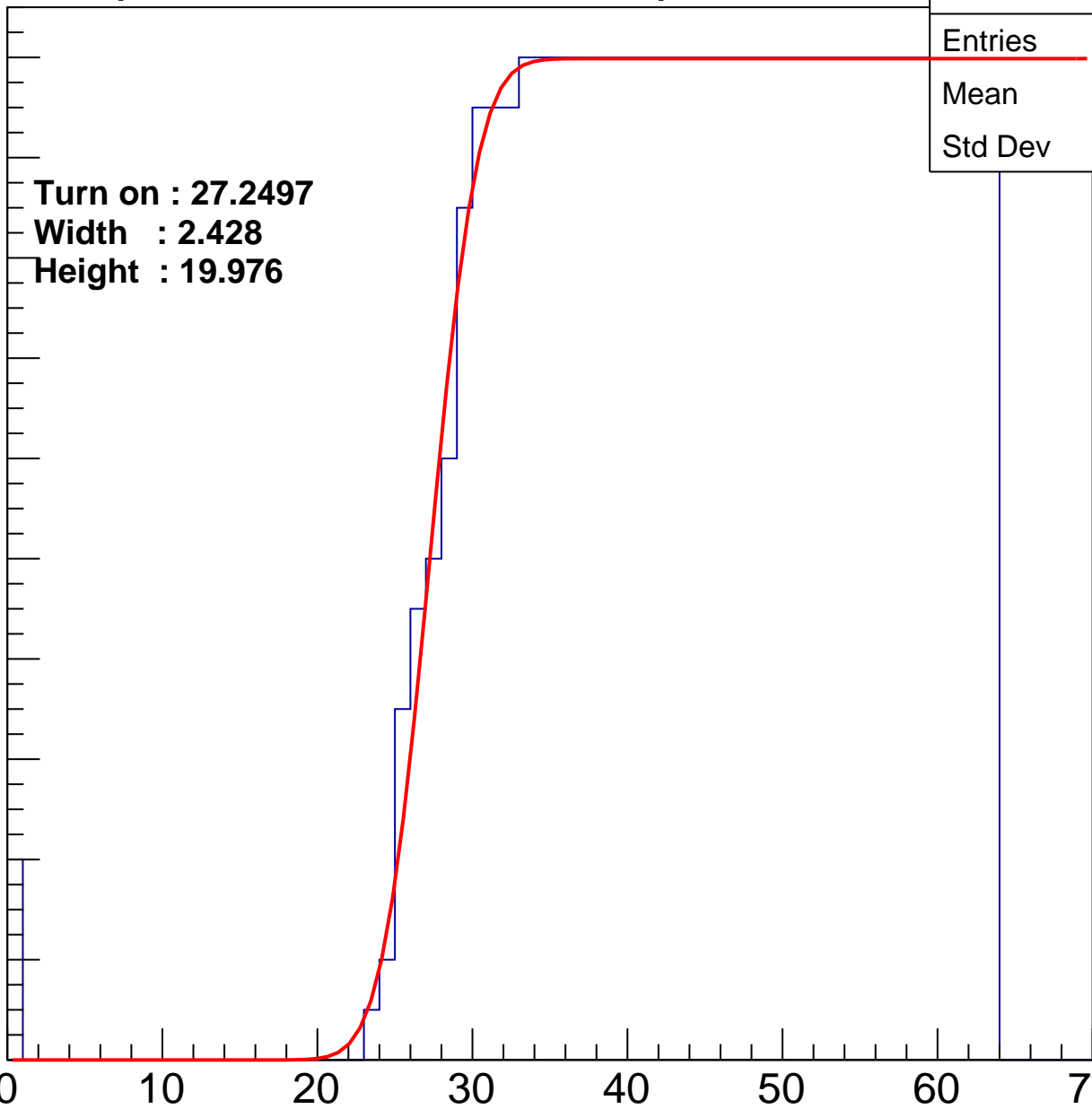
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2497
Width : 2.428
Height : 19.976

Entries	739
Mean	44.8
Std Dev	11.21

ampl



B1L001S, U24-ch11

calib_packv5_042523_0143.root, FC#2, port C2

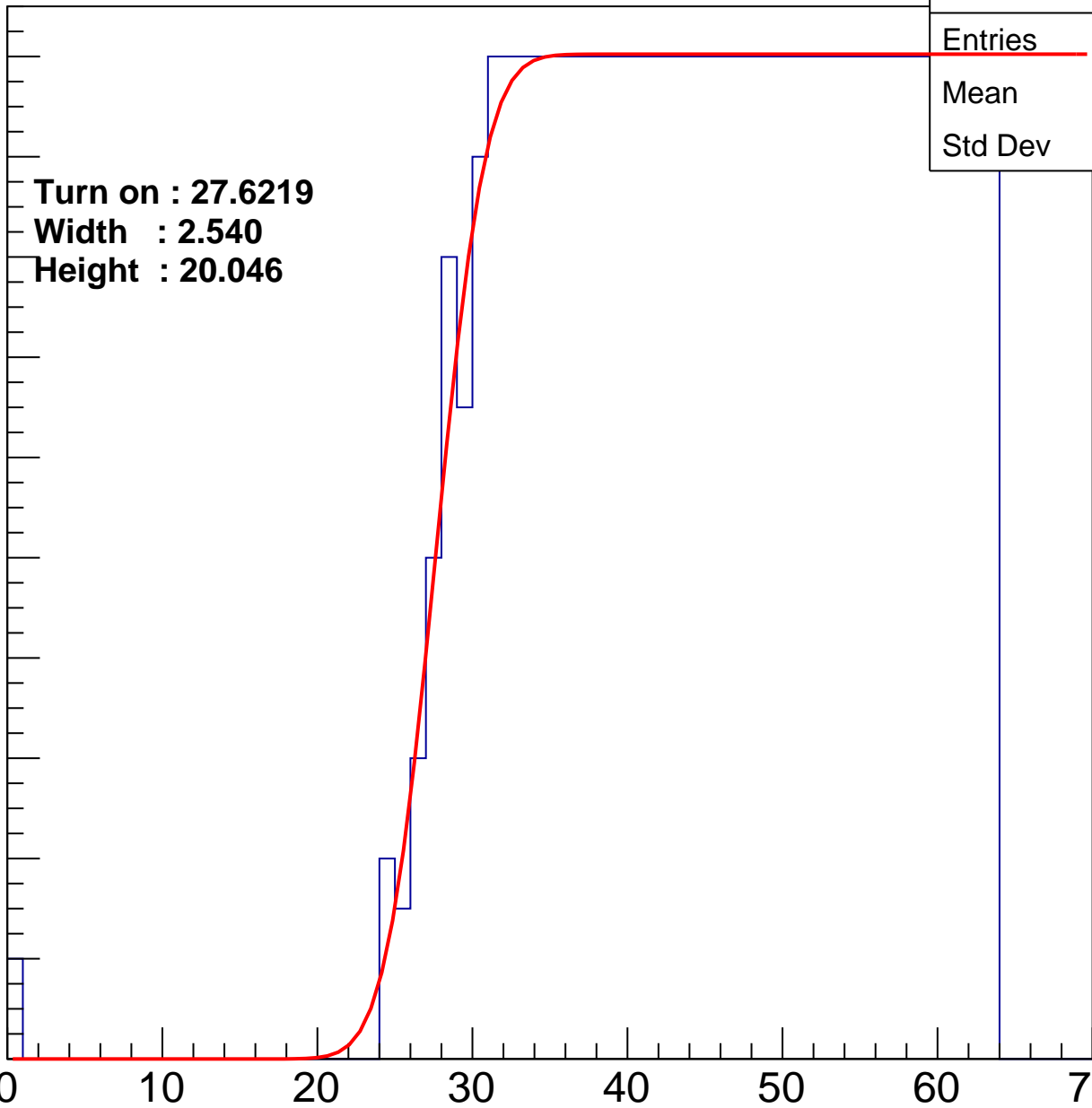
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6219
Width : 2.540
Height : 20.046

Entries	732
Mean	45.06
Std Dev	10.9

ampl



B1L001S, U24-ch12

calib_packv5_042523_0143.root, FC#2, port C2

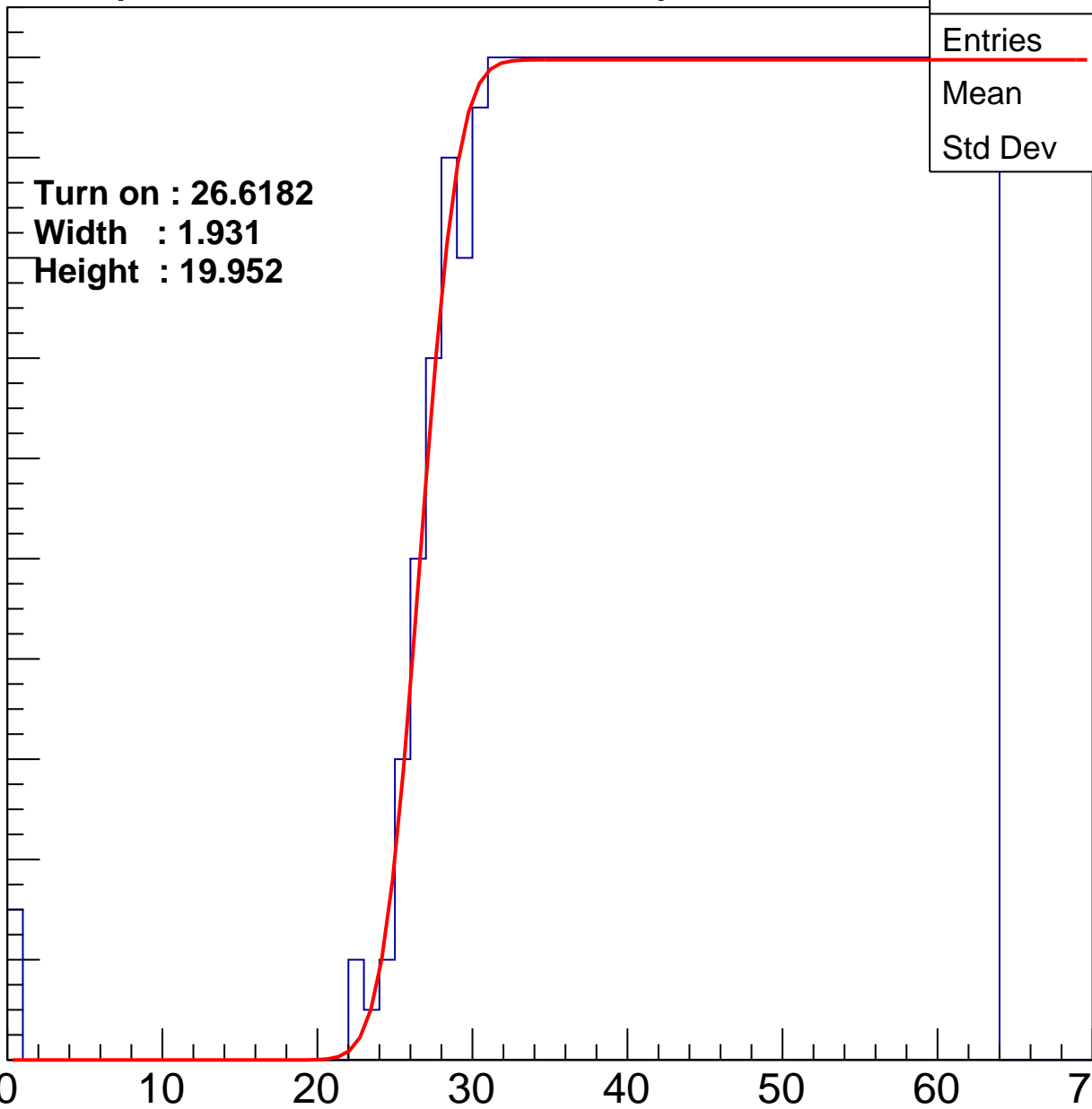
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6182
Width : 1.931
Height : 19.952

Entries	751
Mean	44.56
Std Dev	11.24

ampl



B1L001S, U24-ch13

calib_packv5_042523_0143.root, FC#2, port C2

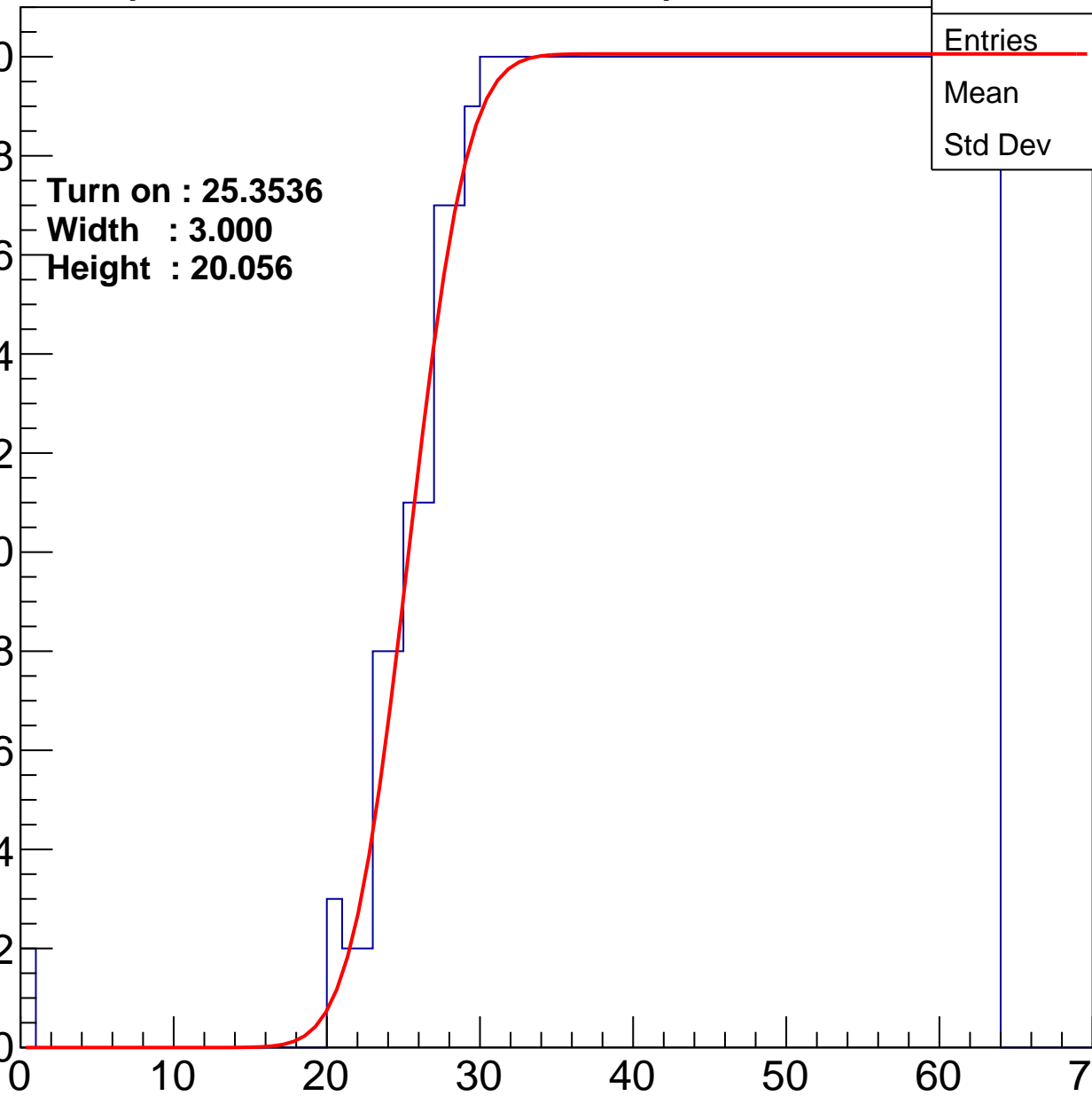
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3536
Width : 3.000
Height : 20.056

Entries	780
Mean	43.83
Std Dev	11.61

ampl



B1L001S, U24-ch14

calib_packv5_042523_0143.root, FC#2, port C2

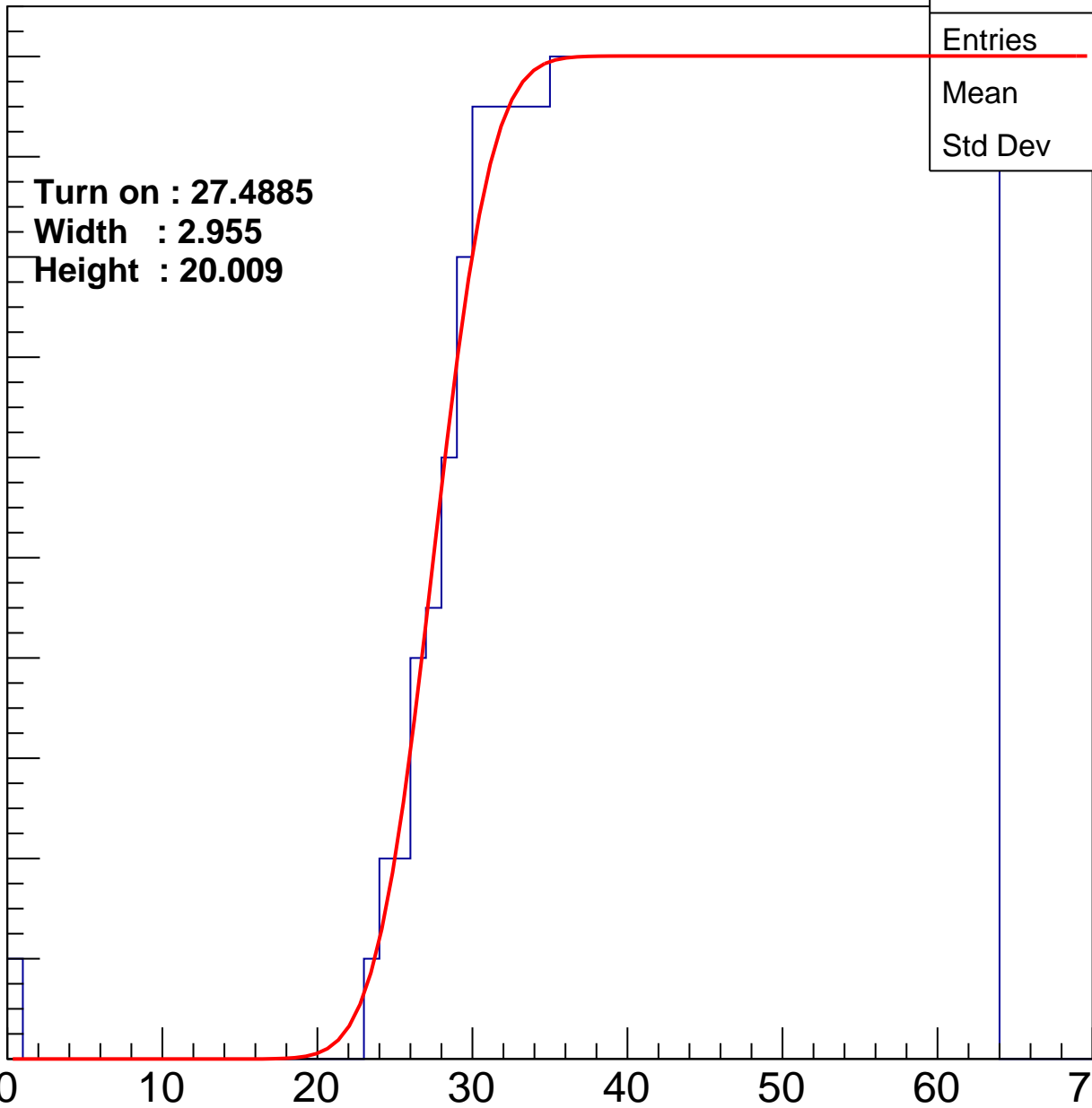
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4885
Width : 2.955
Height : 20.009

Entries	732
Mean	45.02
Std Dev	10.96

ampl



B1L001S, U24-ch15

calib_packv5_042523_0143.root, FC#2, port C2

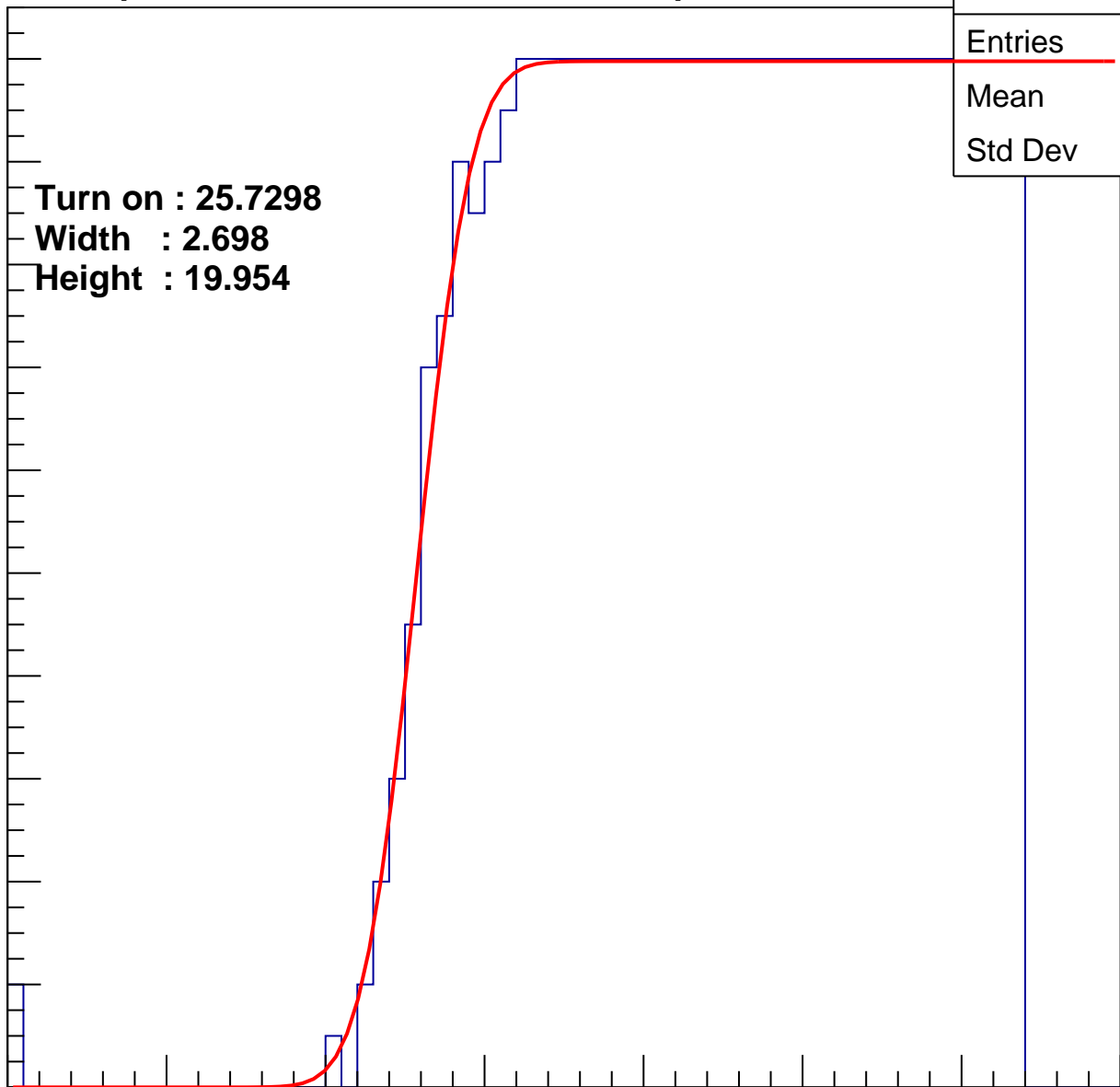
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7298
Width : 2.698
Height : 19.954

Entries	765
Mean	44.21
Std Dev	11.39

ampl



B1L001S, U24-ch16

calib_packv5_042523_0143.root, FC#2, port C2

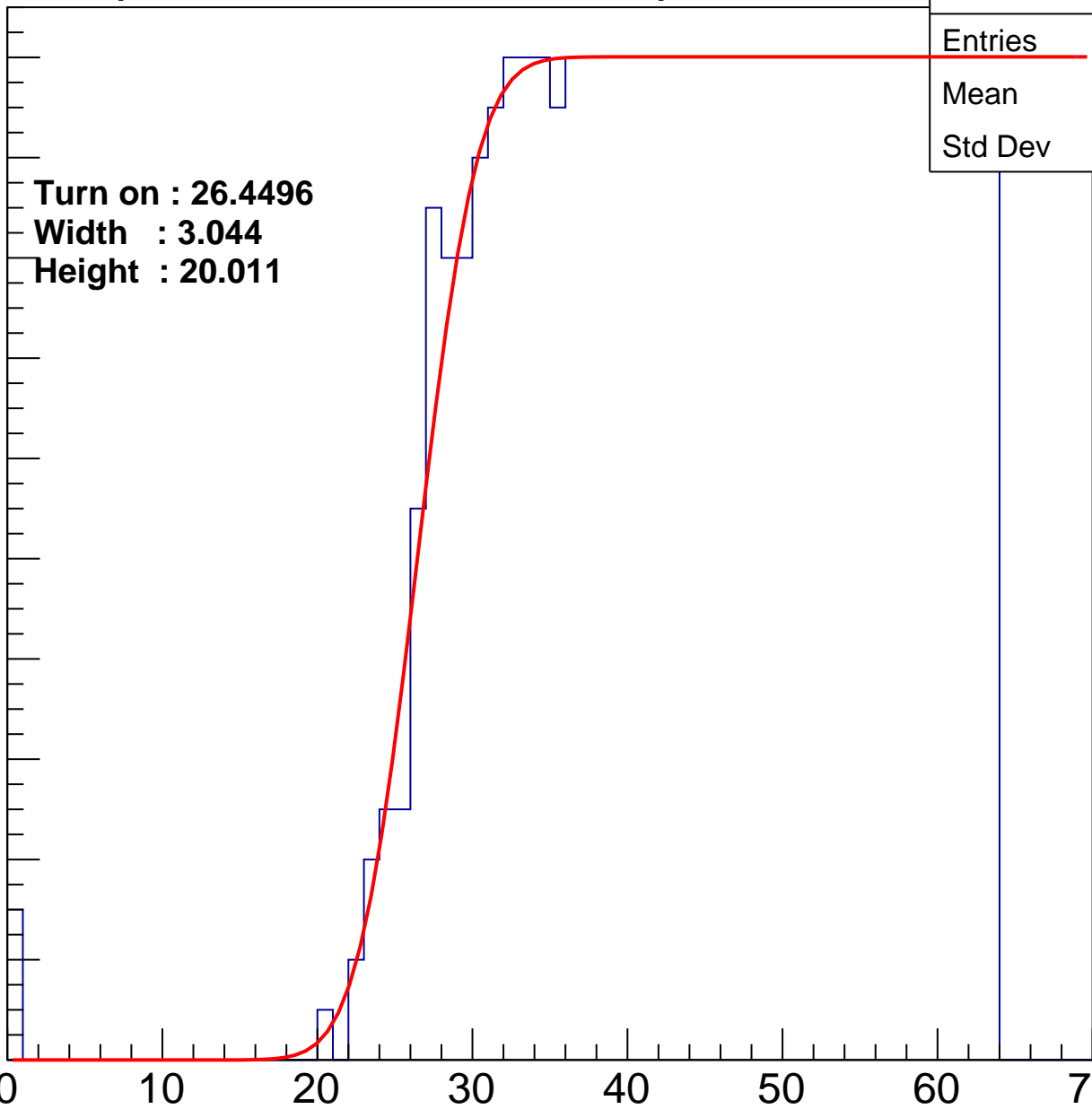
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4496
Width : 3.044
Height : 20.011

Entries	756
Mean	44.38
Std Dev	11.39

ampl



B1L001S, U24-ch17

calib_packv5_042523_0143.root, FC#2, port C2

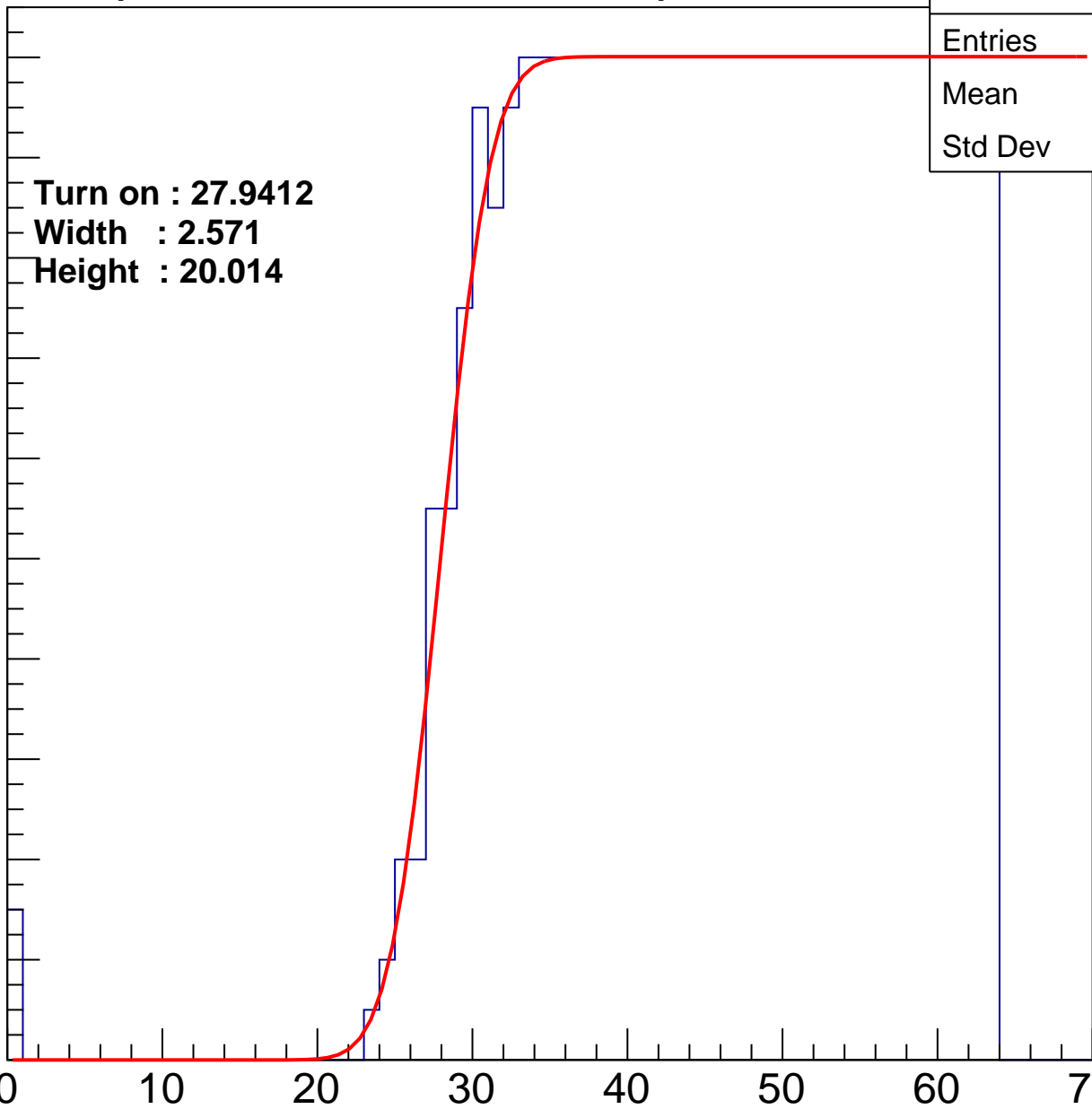
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9412
Width : 2.571
Height : 20.014

Entries	726
Mean	45.15
Std Dev	10.95

ampl



B1L001S, U24-ch18

calib_packv5_042523_0143.root, FC#2, port C2

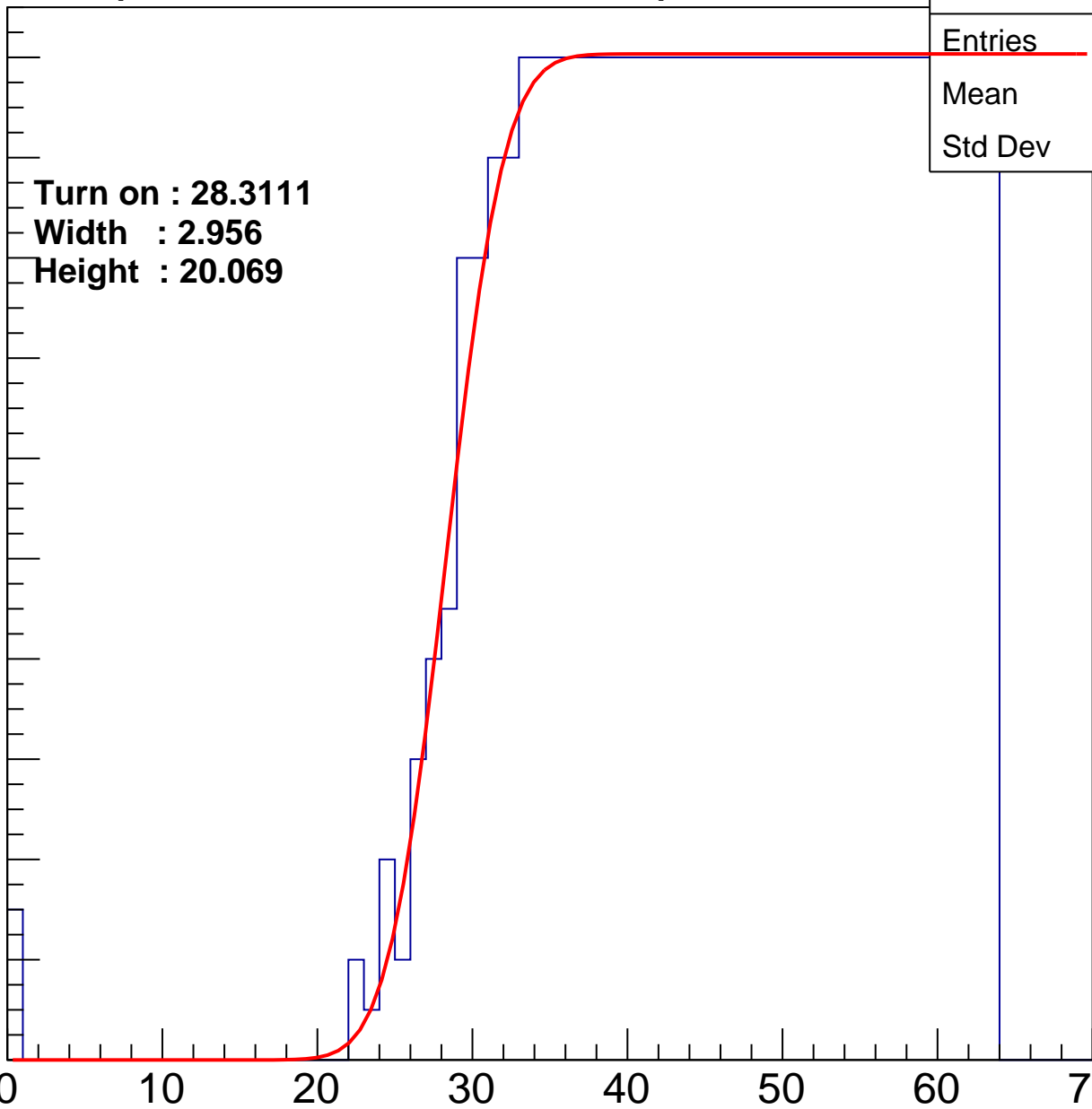
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3111
Width : 2.956
Height : 20.069

Entries	723
Mean	45.19
Std Dev	10.97

ampl



B1L001S, U24-ch19

calib_packv5_042523_0143.root, FC#2, port C2

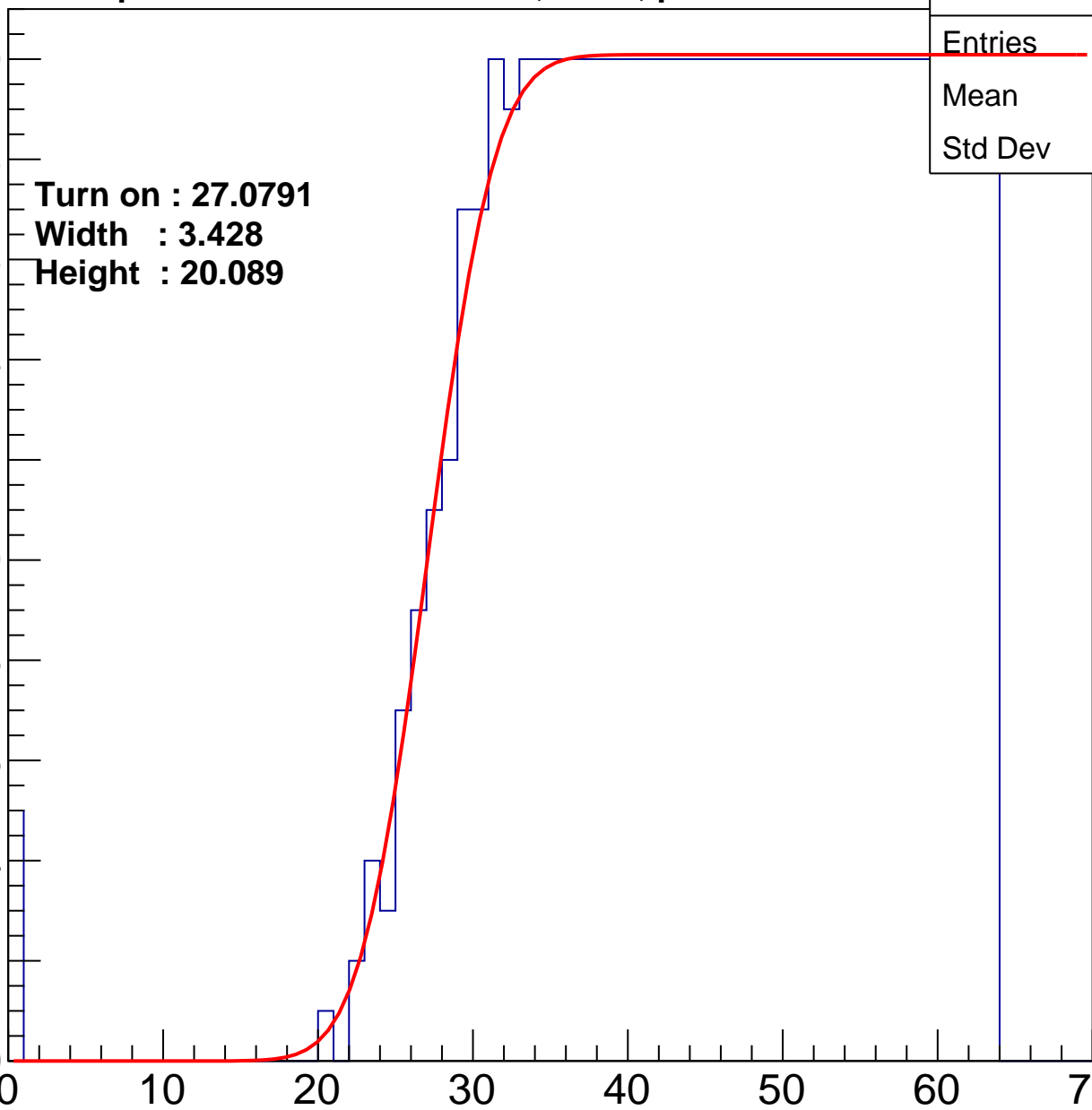
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0791
Width : 3.428
Height : 20.089

Entries	747
Mean	44.53
Std Dev	11.48

ampl



B1L001S, U24-ch20

calib_packv5_042523_0143.root, FC#2, port C2

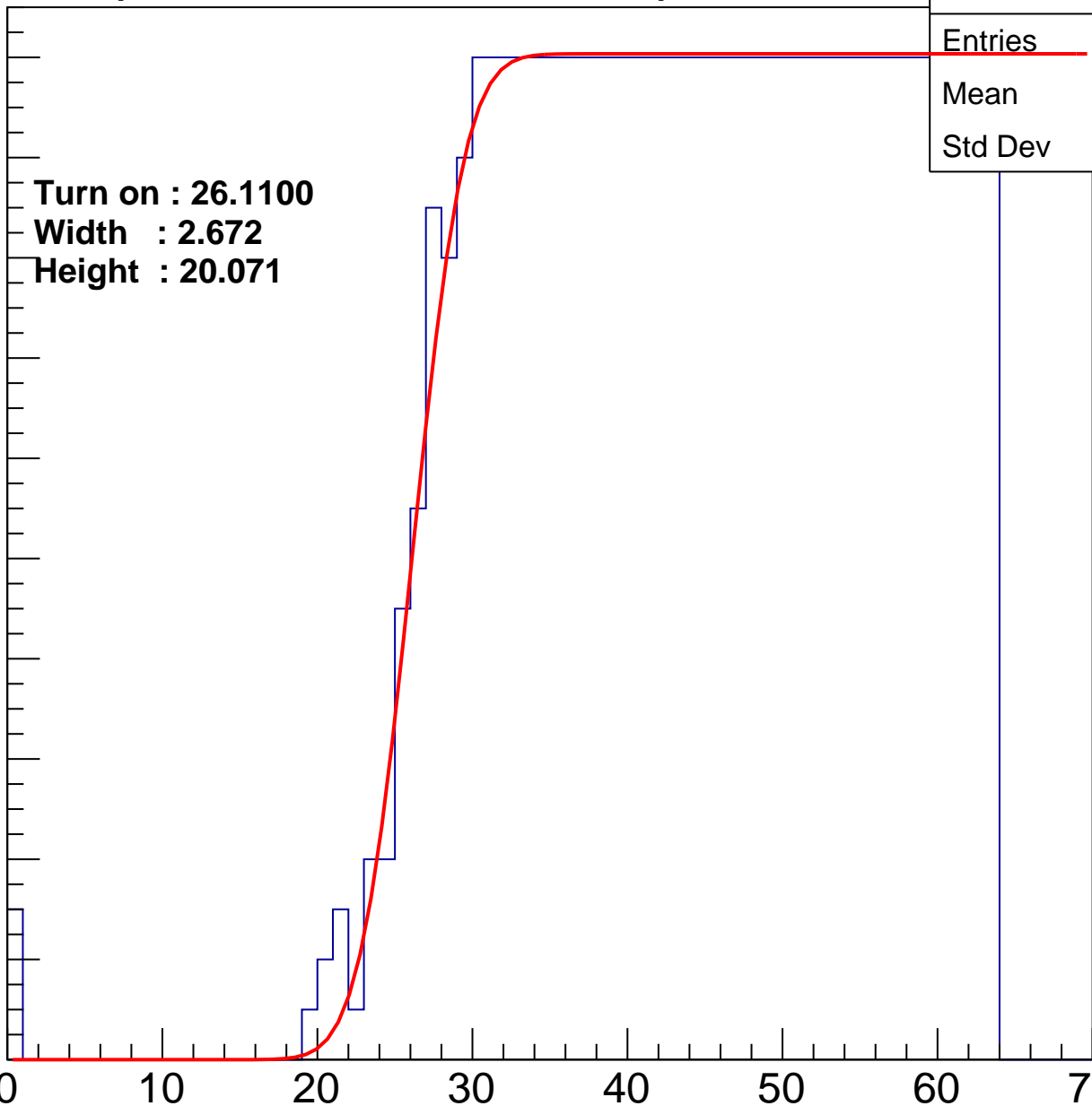
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1100
Width : 2.672
Height : 20.071

Entries	769
Mean	44.07
Std Dev	11.55

ampl



B1L001S, U24-ch21

calib_packv5_042523_0143.root, FC#2, port C2

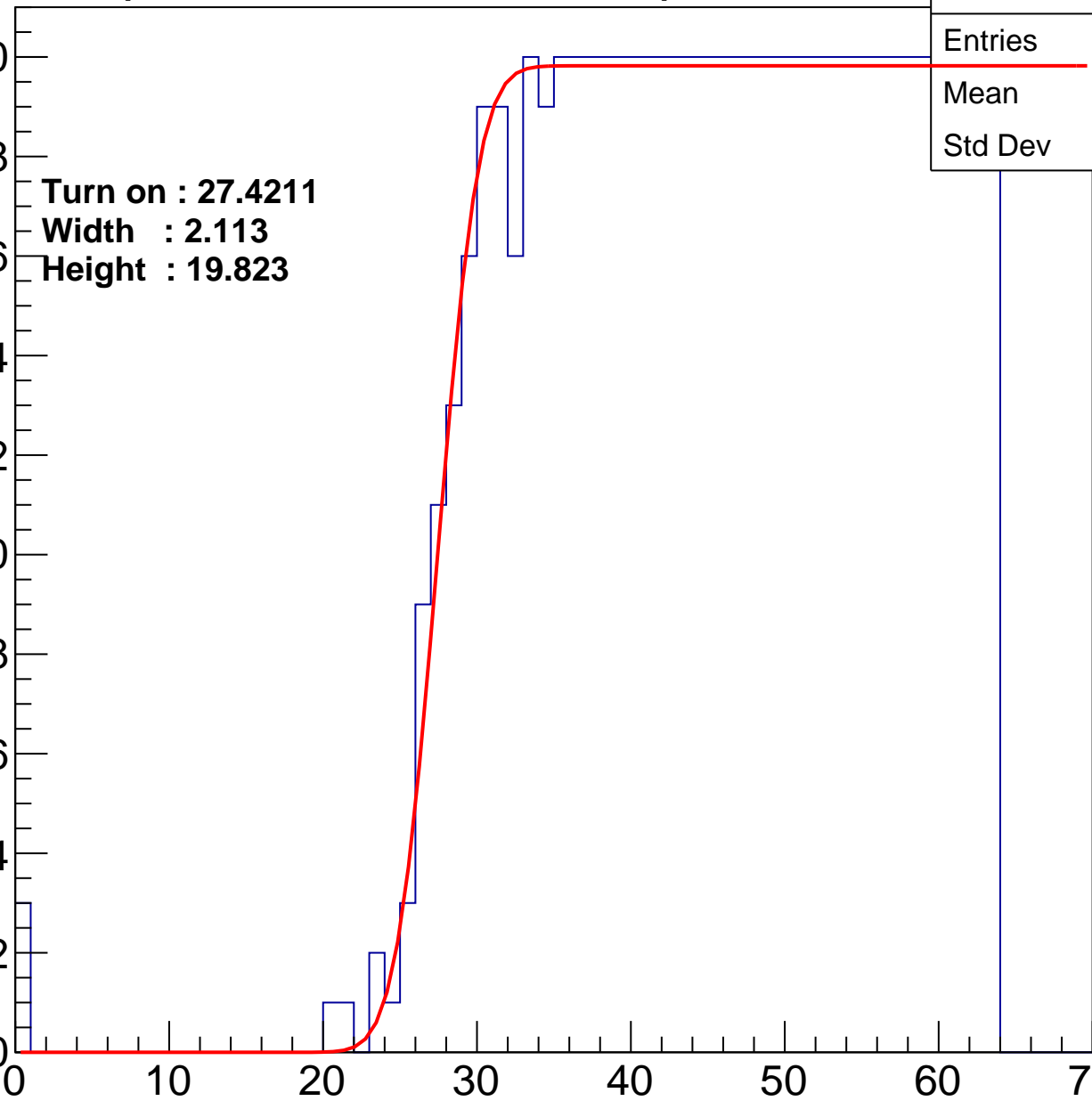
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4211
Width : 2.113
Height : 19.823

Entries	733
Mean	44.94
Std Dev	11.1

ampl



B1L001S, U24-ch22

calib_packv5_042523_0143.root, FC#2, port C2

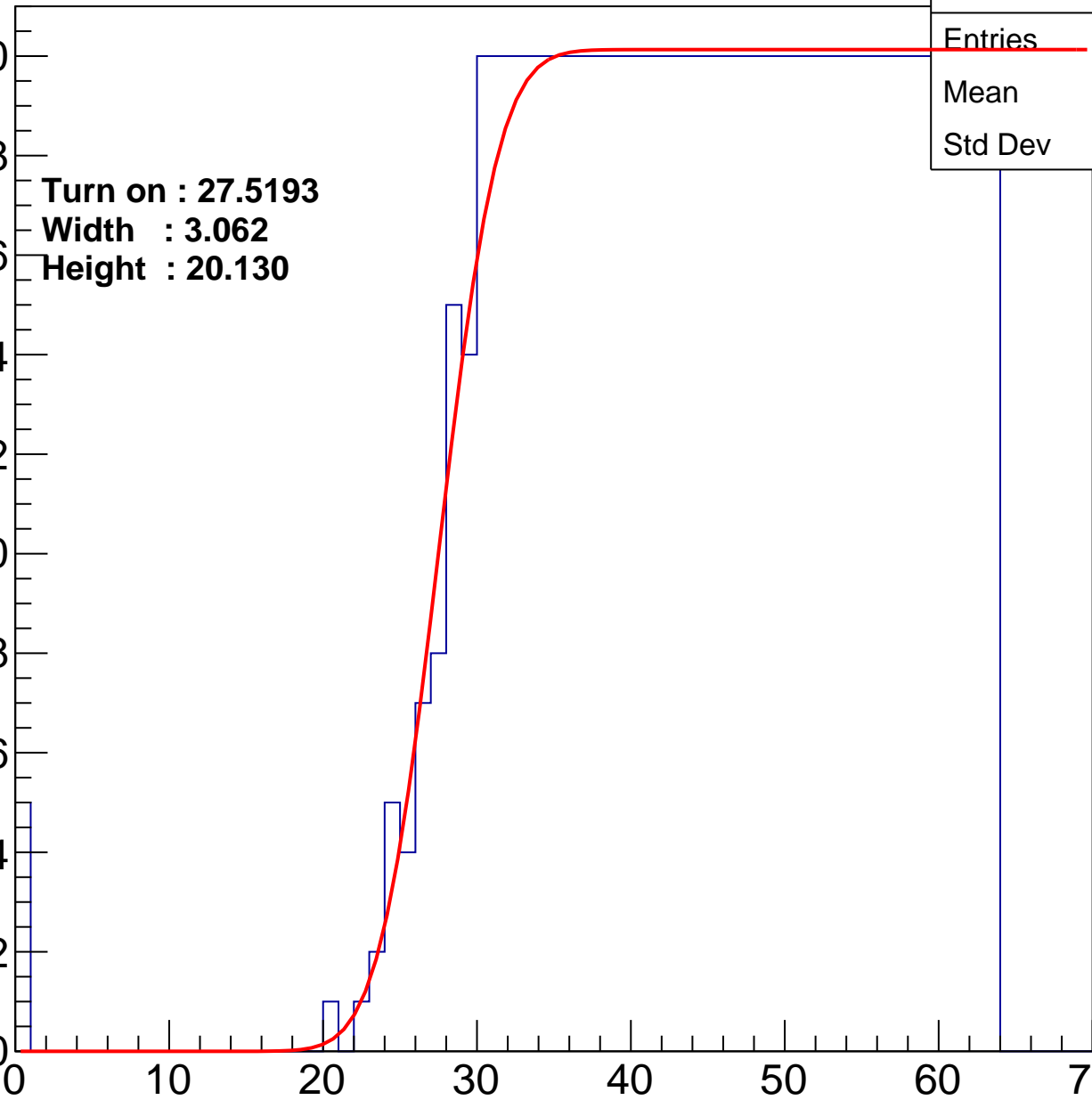
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5193
Width : 3.062
Height : 20.130

Entries	742
Mean	44.68
Std Dev	11.37

ampl



B1L001S, U24-ch23

calib_packv5_042523_0143.root, FC#2, port C2

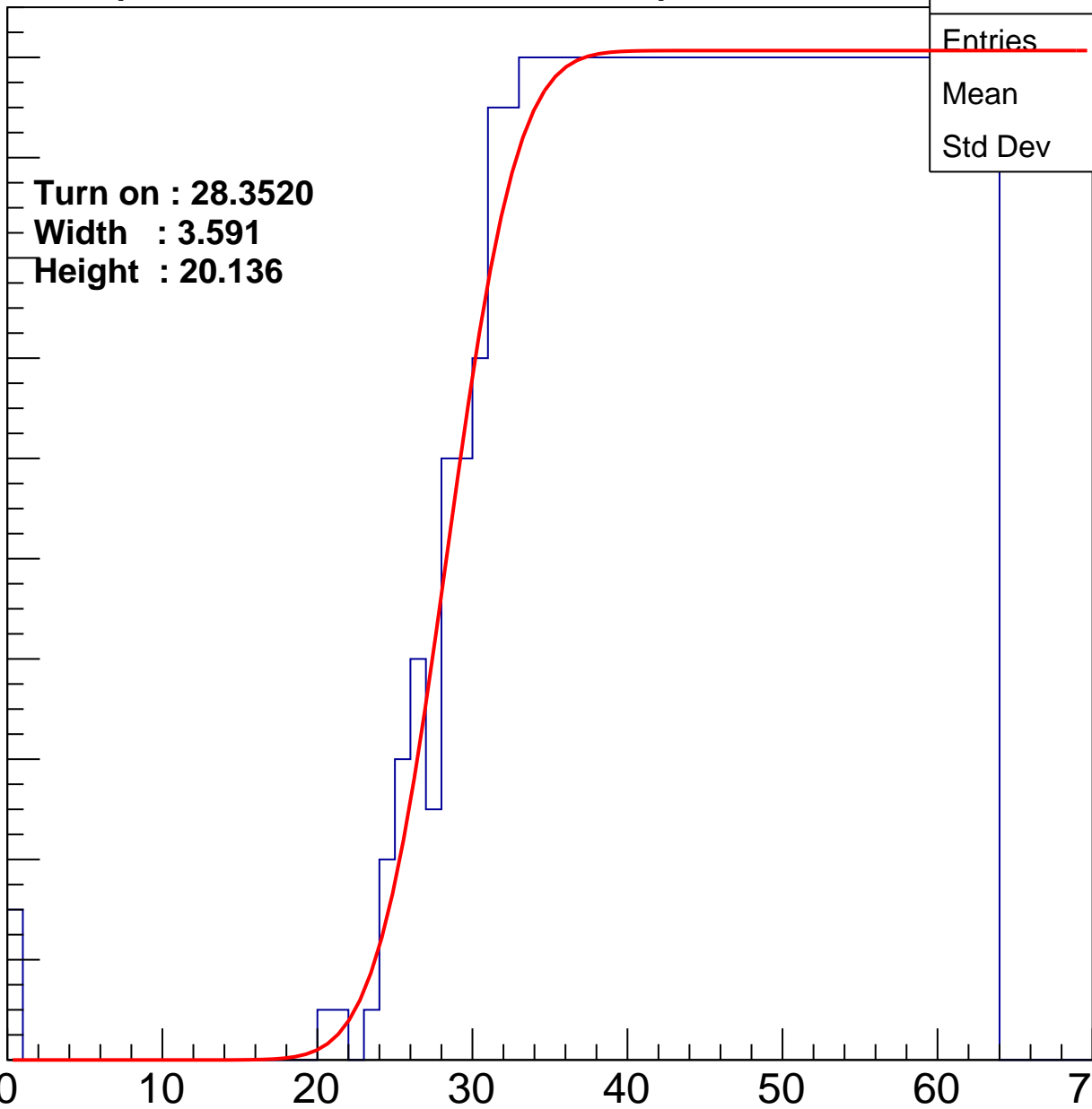
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3520
Width : 3.591
Height : 20.136

Entries	725
Mean	45.12
Std Dev	11.03

ampl



B1L001S, U24-ch24

calib_packv5_042523_0143.root, FC#2, port C2

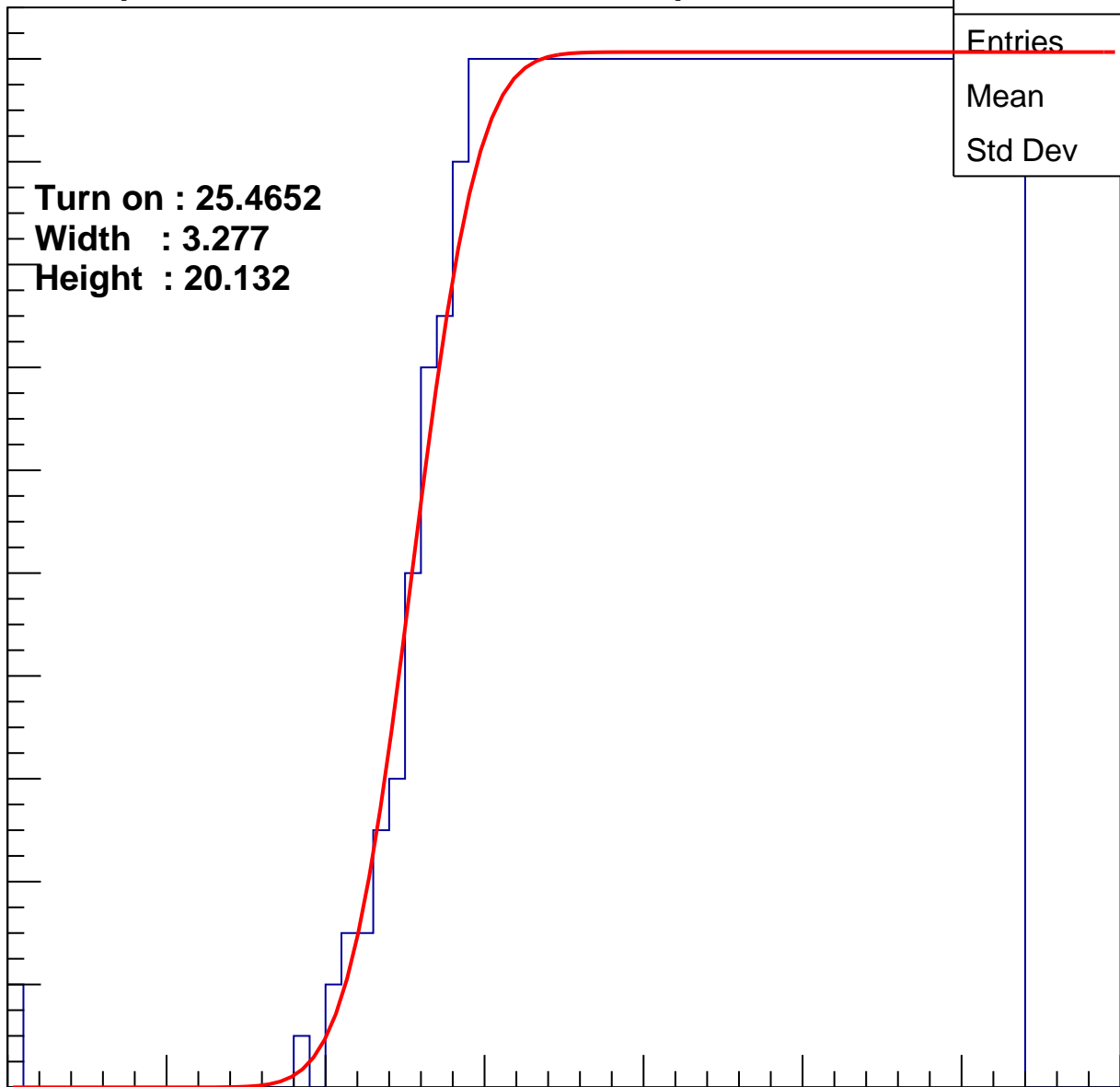
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4652
Width : 3.277
Height : 20.132

Entries	779
Mean	43.86
Std Dev	11.59

ampl



B1L001S, U24-ch25

calib_packv5_042523_0143.root, FC#2, port C2

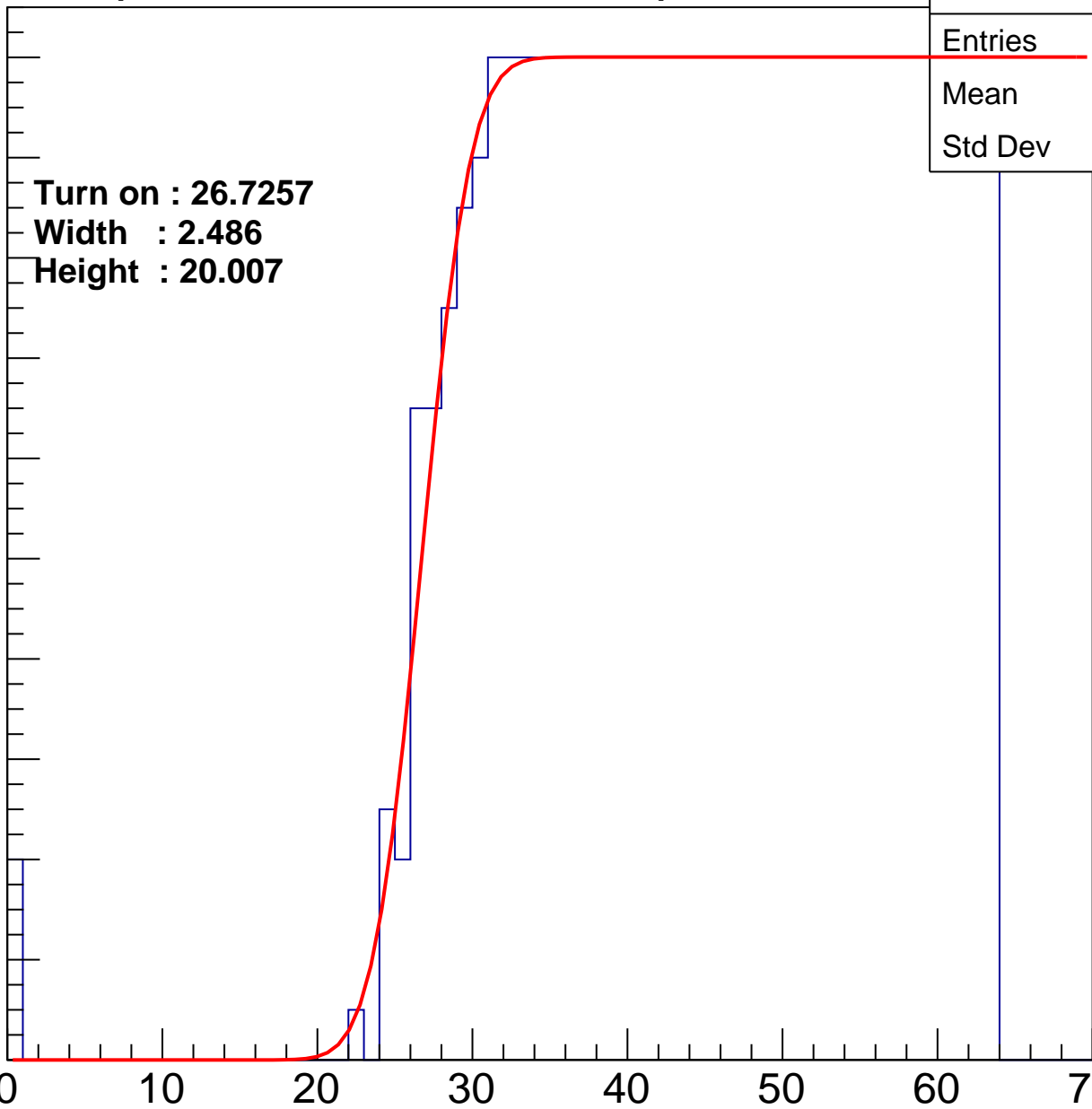
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7257
Width : 2.486
Height : 20.007

Entries	750
Mean	44.54
Std Dev	11.34

ampl



B1L001S, U24-ch26

calib_packv5_042523_0143.root, FC#2, port C2

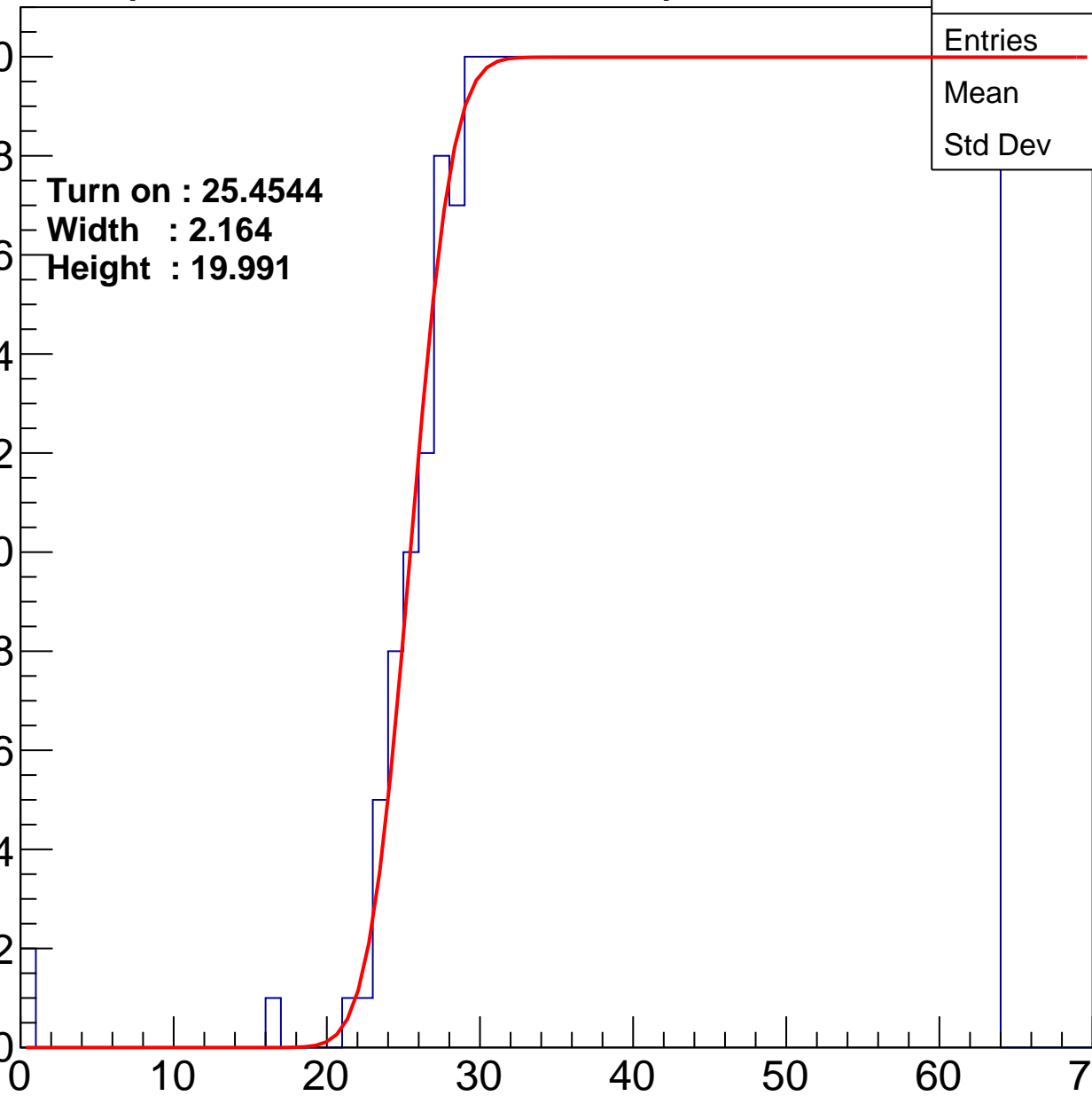
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4544
Width : 2.164
Height : 19.991

Entries	775
Mean	43.99
Std Dev	11.49

ampl



B1L001S, U24-ch27

calib_packv5_042523_0143.root, FC#2, port C2

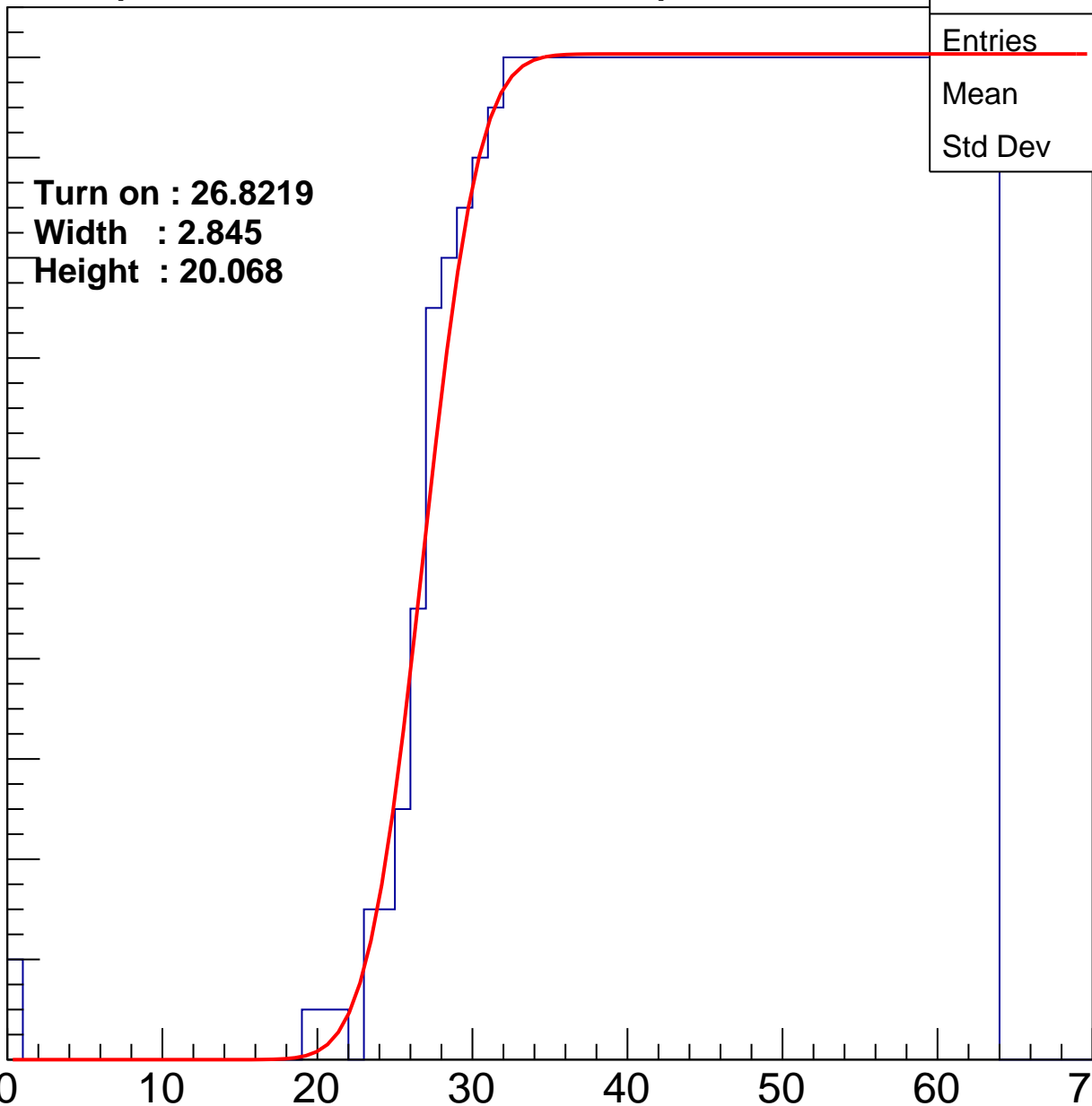
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8219
Width : 2.845
Height : 20.068

Entries	750
Mean	44.58
Std Dev	11.19

ampl



B1L001S, U24-ch28

calib_packv5_042523_0143.root, FC#2, port C2

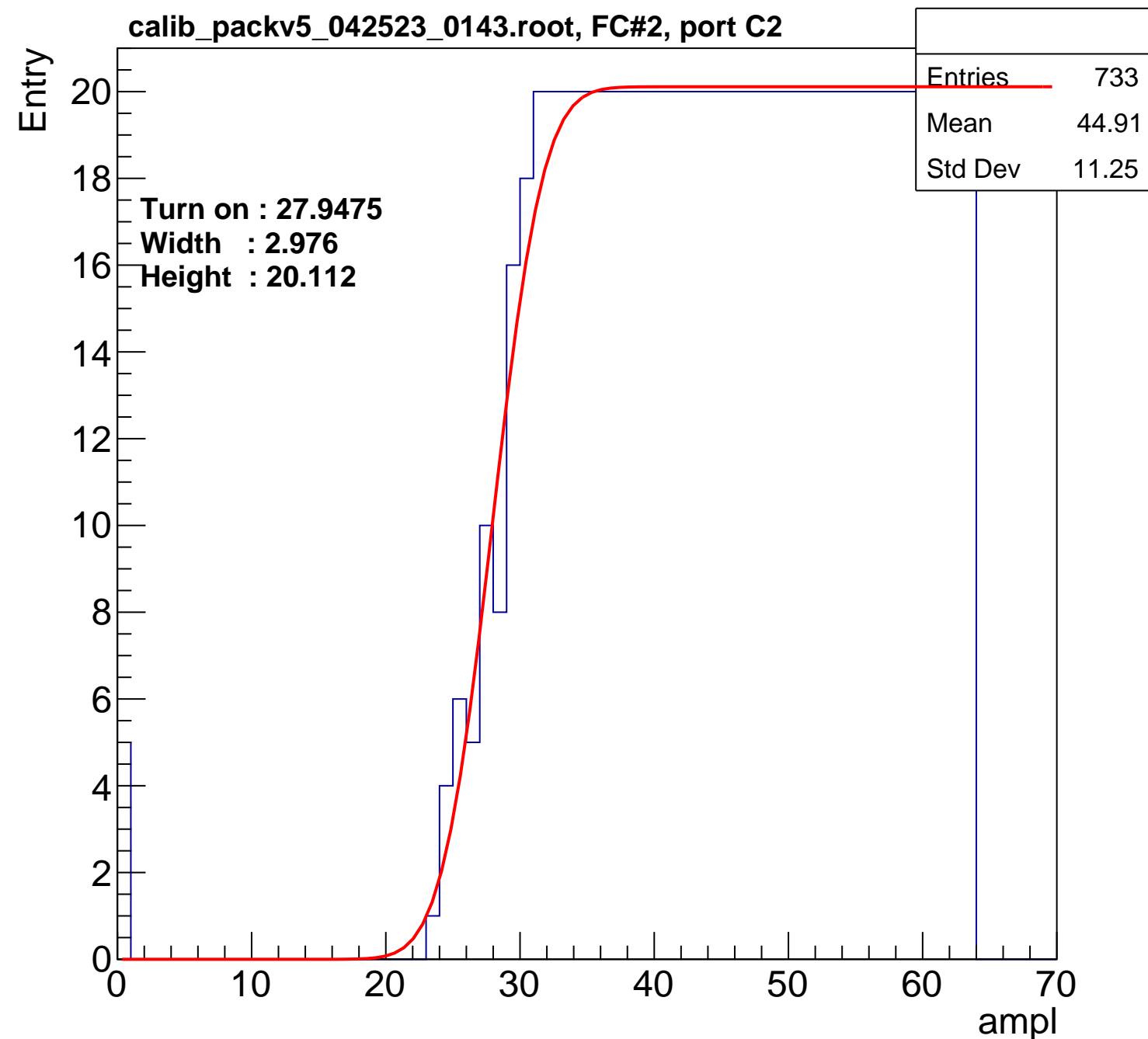
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9475
Width : 2.976
Height : 20.112

Entries	733
Mean	44.91
Std Dev	11.25

ampl



B1L001S, U24-ch29

calib_packv5_042523_0143.root, FC#2, port C2

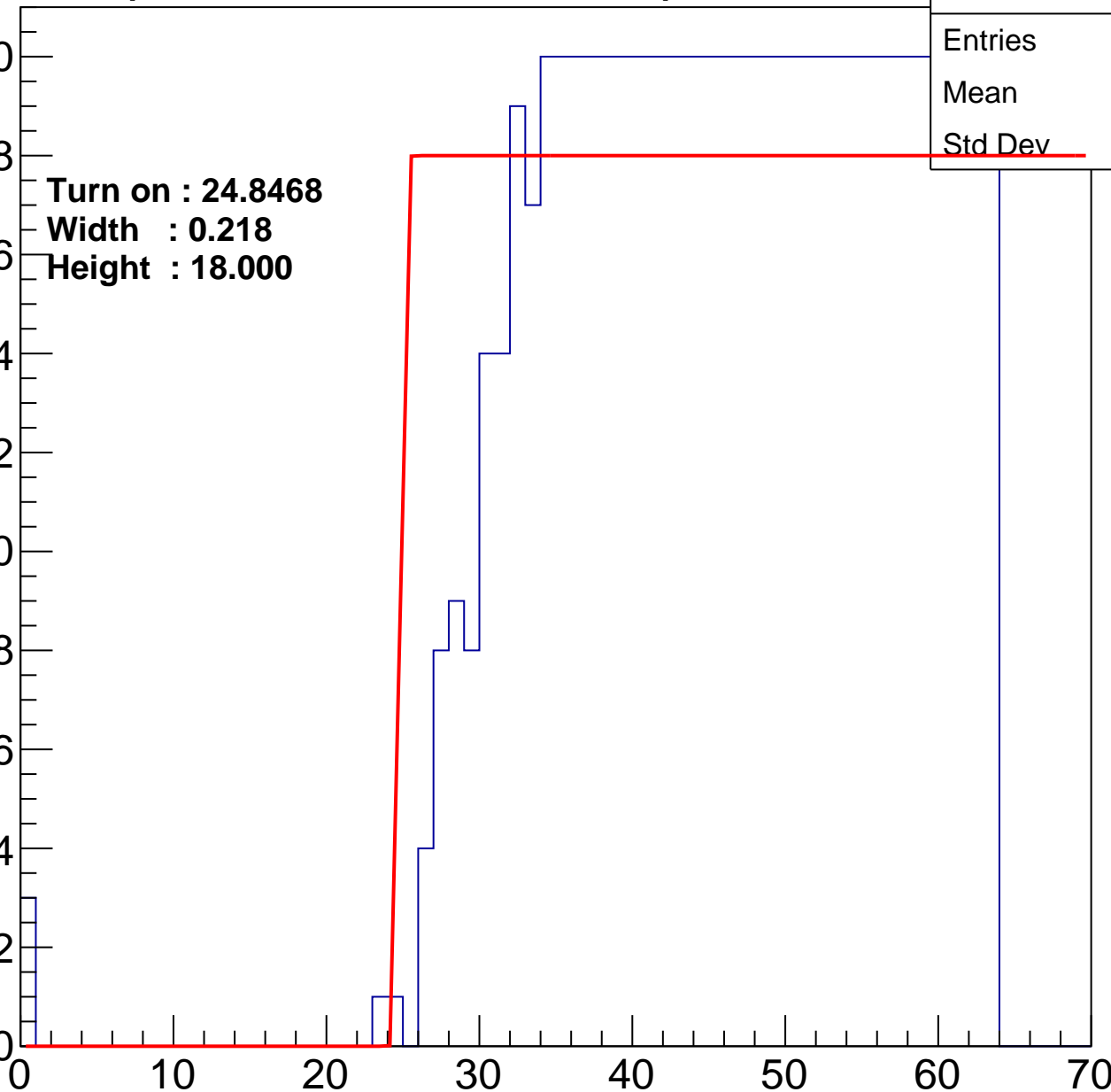
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8468
Width : 0.218
Height : 18.000

Entries	698
Mean	45.81
Std Dev	10.64

ampl



B1L001S, U24-ch30

calib_packv5_042523_0143.root, FC#2, port C2

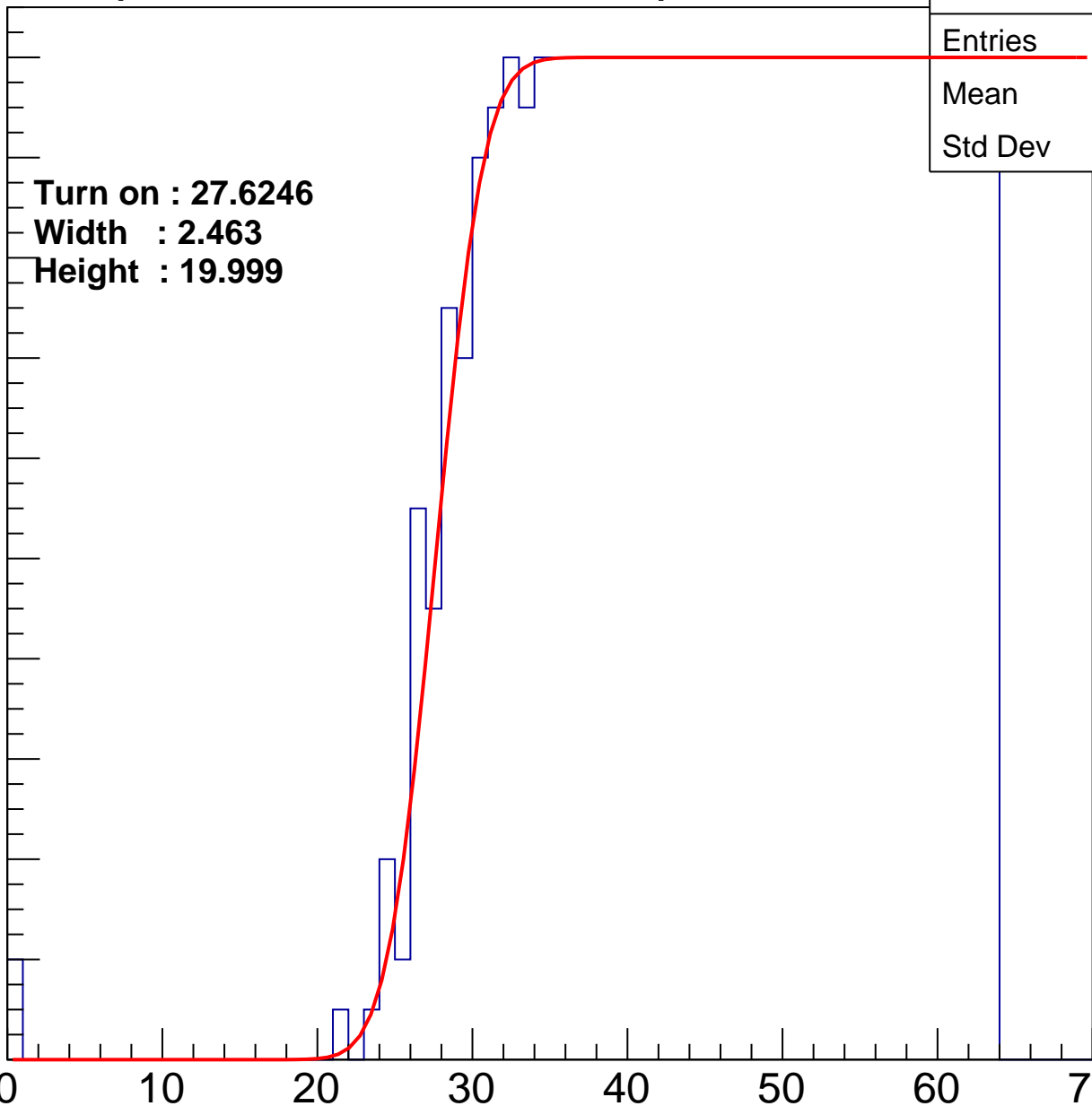
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6246
Width : 2.463
Height : 19.999

Entries	735
Mean	44.95
Std Dev	10.98

ampl



B1L001S, U24-ch31

calib_packv5_042523_0143.root, FC#2, port C2

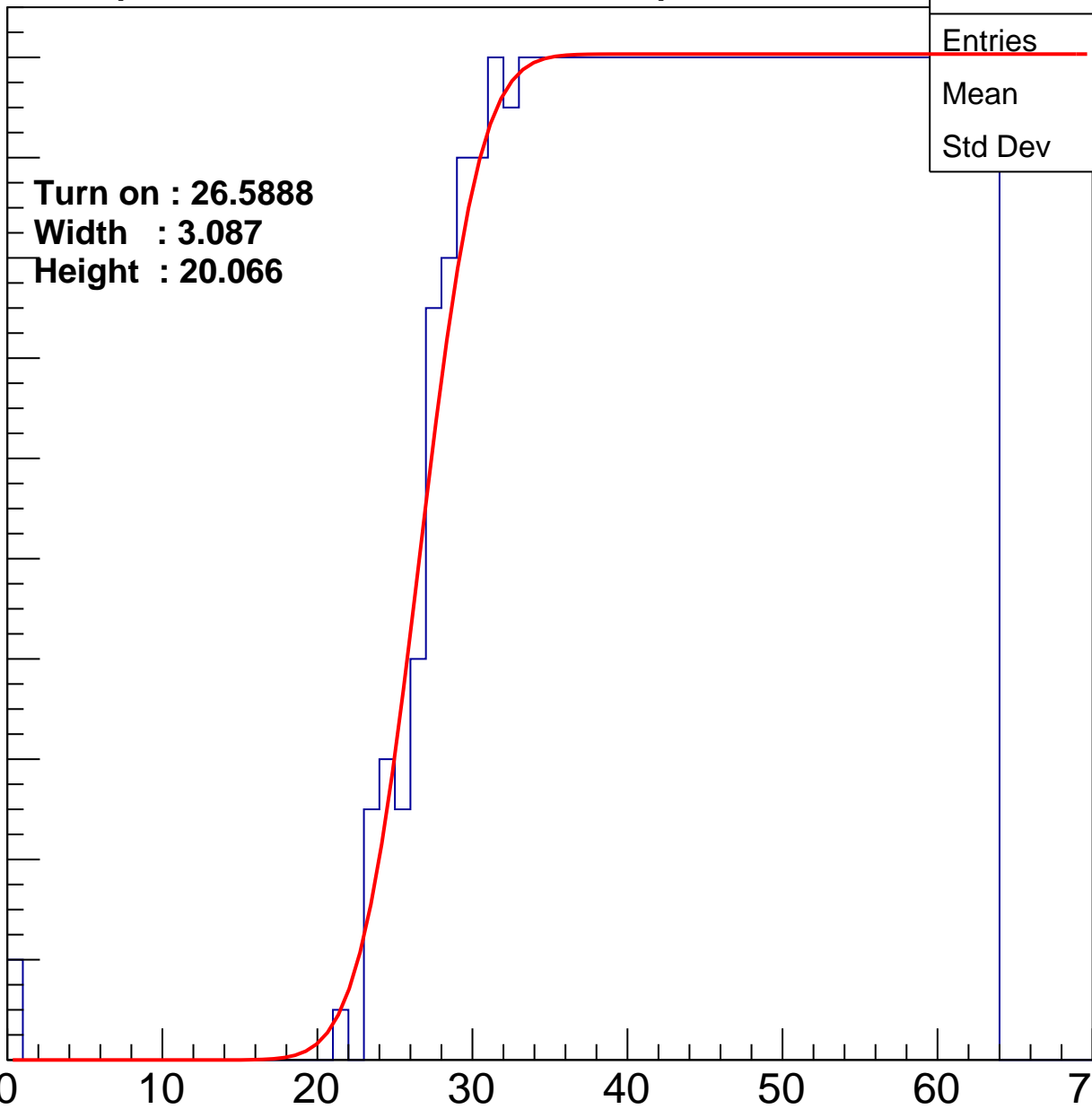
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5888
Width : 3.087
Height : 20.066

Entries	753
Mean	44.51
Std Dev	11.22

ampl



B1L001S, U24-ch32

calib_packv5_042523_0143.root, FC#2, port C2

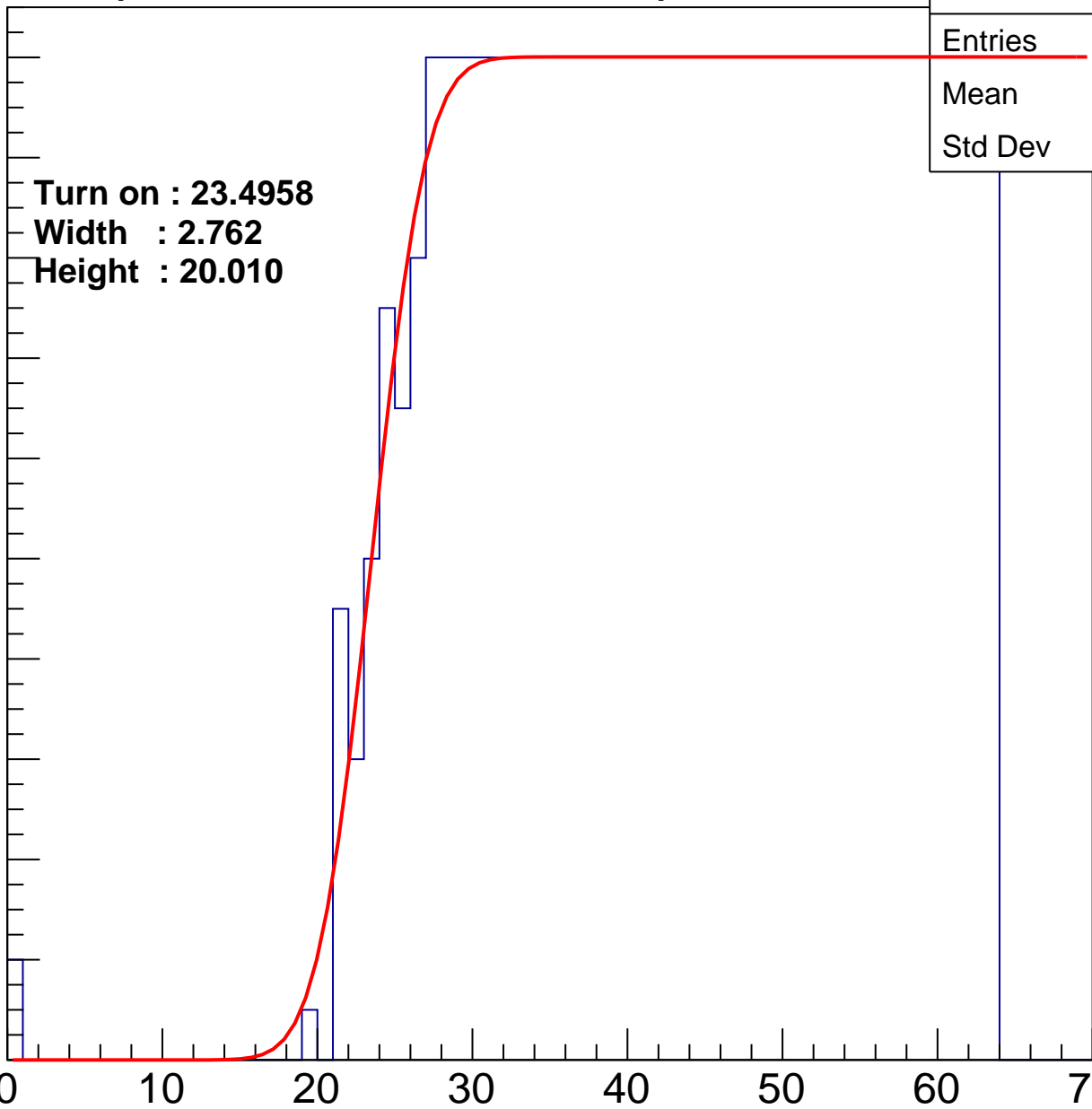
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 23.4958
Width : 2.762
Height : 20.010

Entries	812
Mean	43.07
Std Dev	12

ampl



B1L001S, U24-ch33

calib_packv5_042523_0143.root, FC#2, port C2

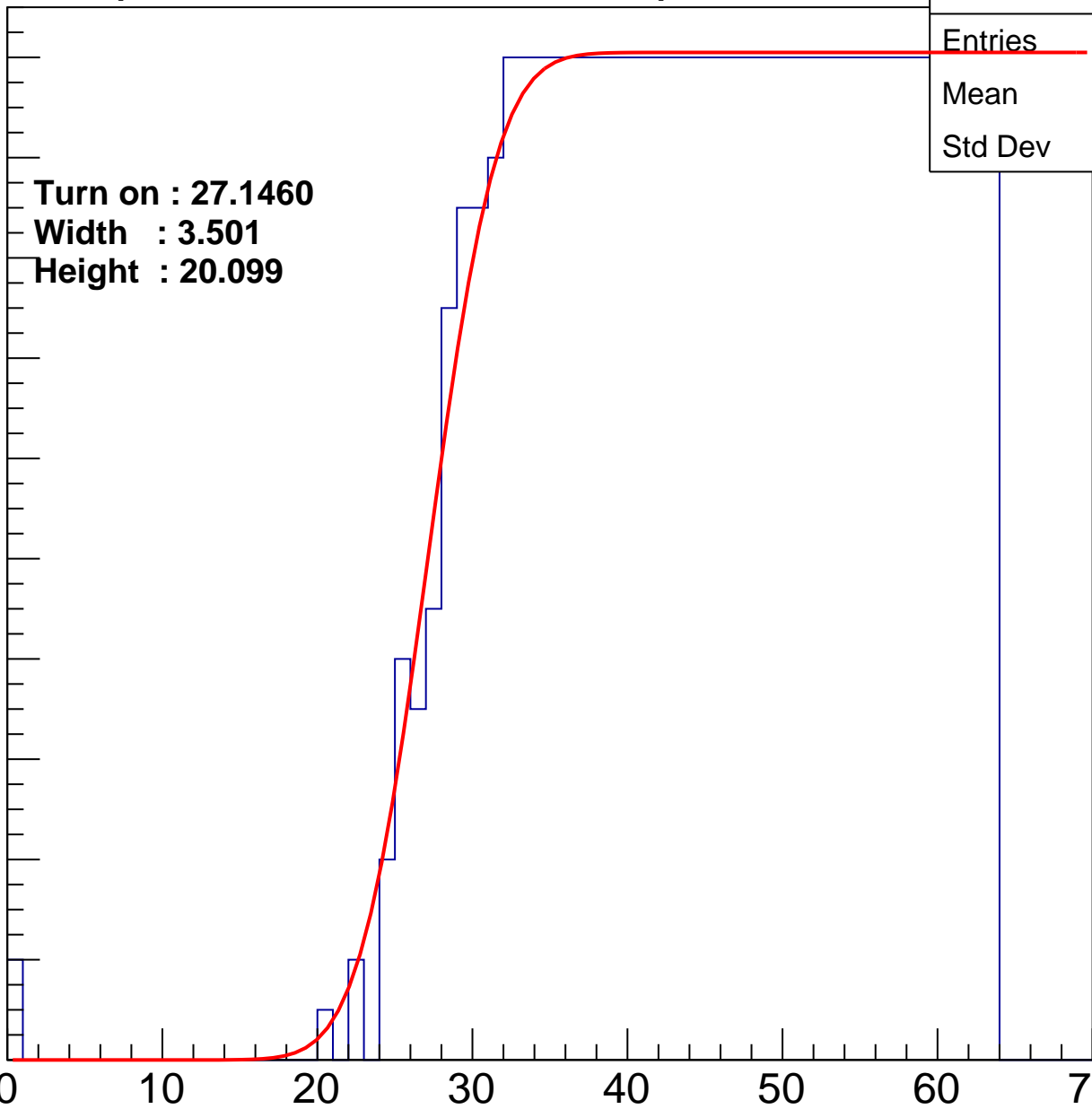
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1460
Width : 3.501
Height : 20.099

Entries	740
Mean	44.82
Std Dev	11.07

ampl



B1L001S, U24-ch34

calib_packv5_042523_0143.root, FC#2, port C2

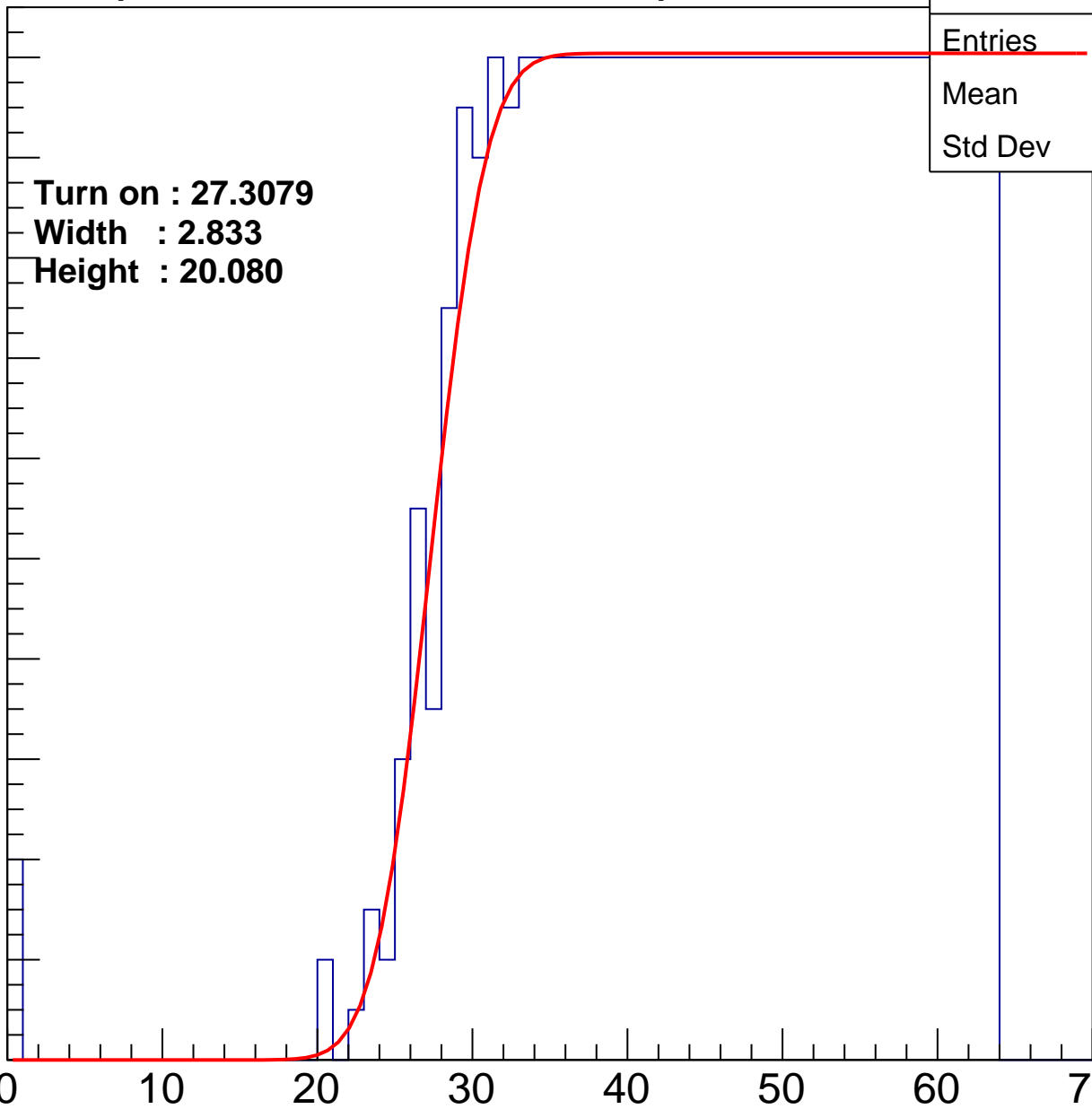
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3079
Width : 2.833
Height : 20.080

Entries	747
Mean	44.58
Std Dev	11.35

ampl



B1L001S, U24-ch35

calib_packv5_042523_0143.root, FC#2, port C2

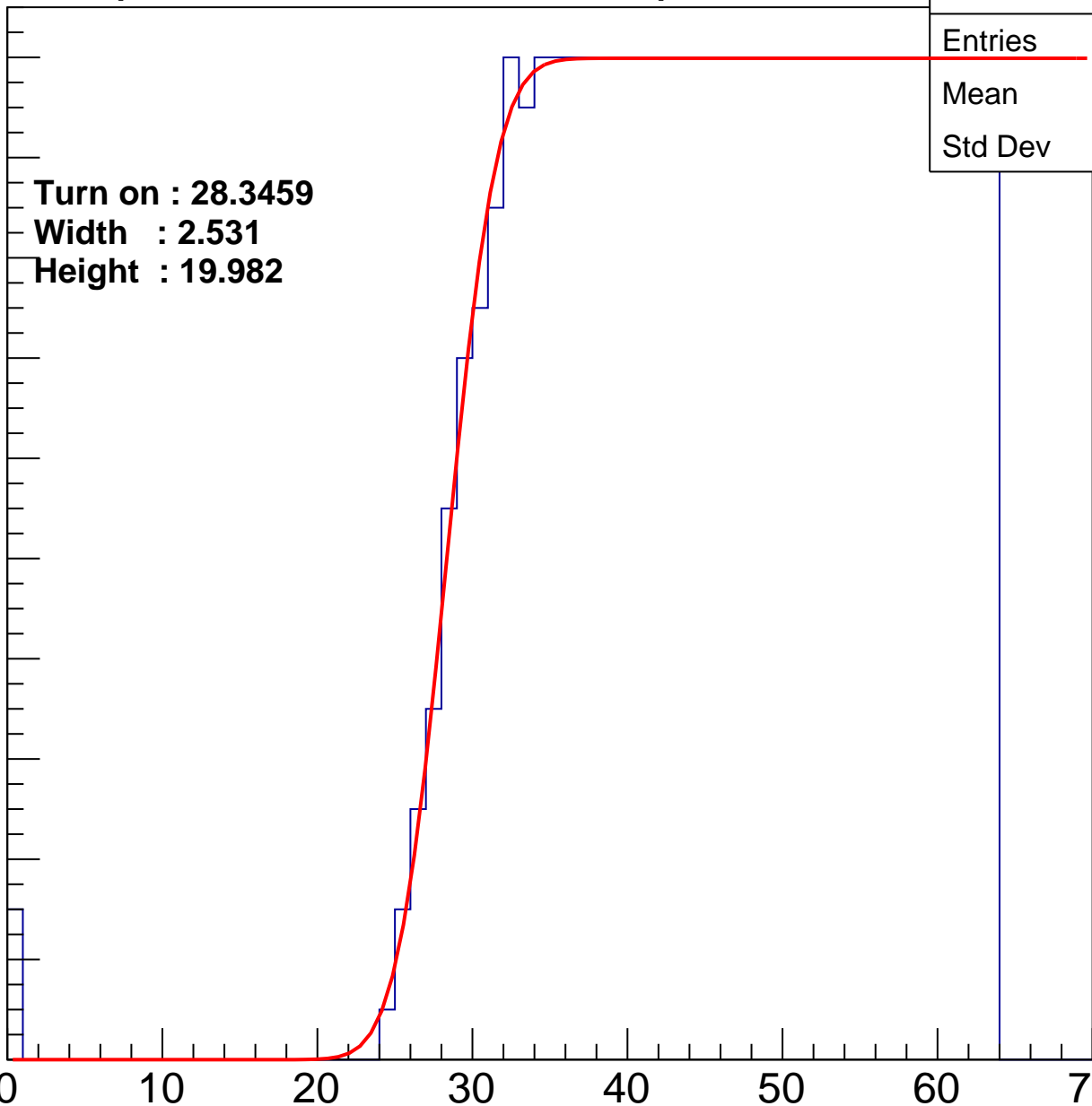
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3459
Width : 2.531
Height : 19.982

Entries	715
Mean	45.42
Std Dev	10.81

ampl



B1L001S, U24-ch36

calib_packv5_042523_0143.root, FC#2, port C2

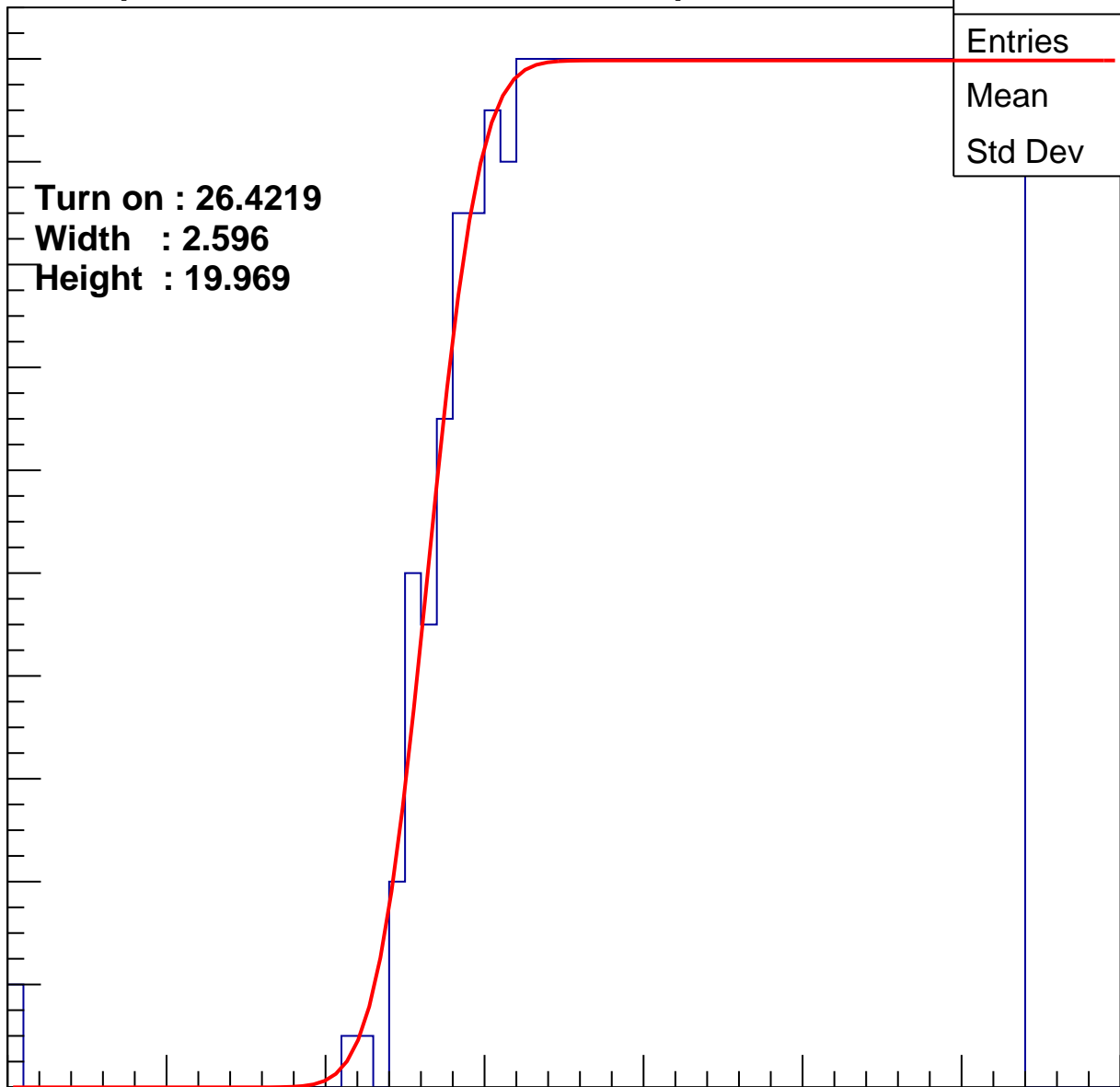
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4219
Width : 2.596
Height : 19.969

Entries	751
Mean	44.57
Std Dev	11.18

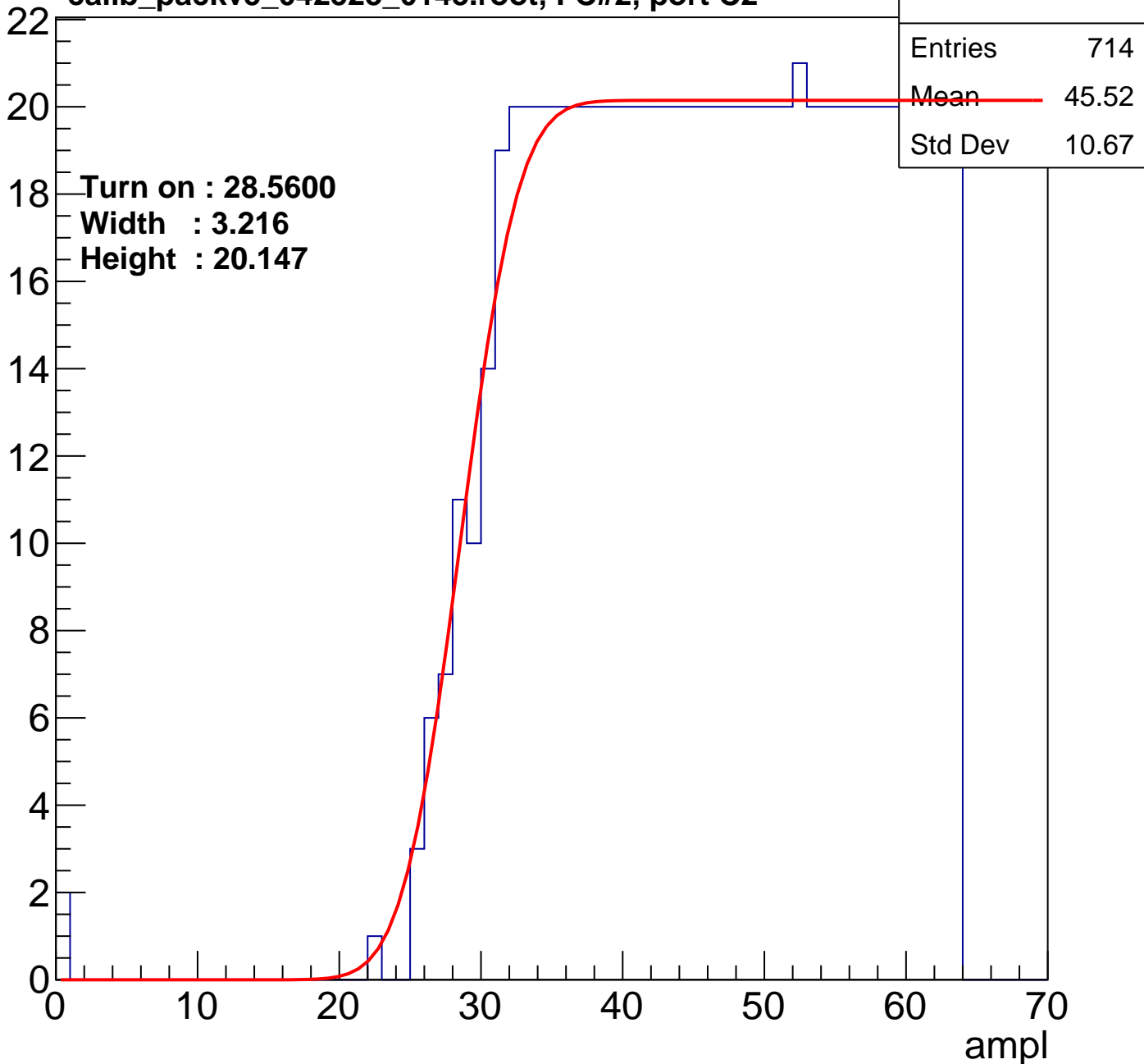
ampl



B1L001S, U24-ch37

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch38

calib_packv5_042523_0143.root, FC#2, port C2

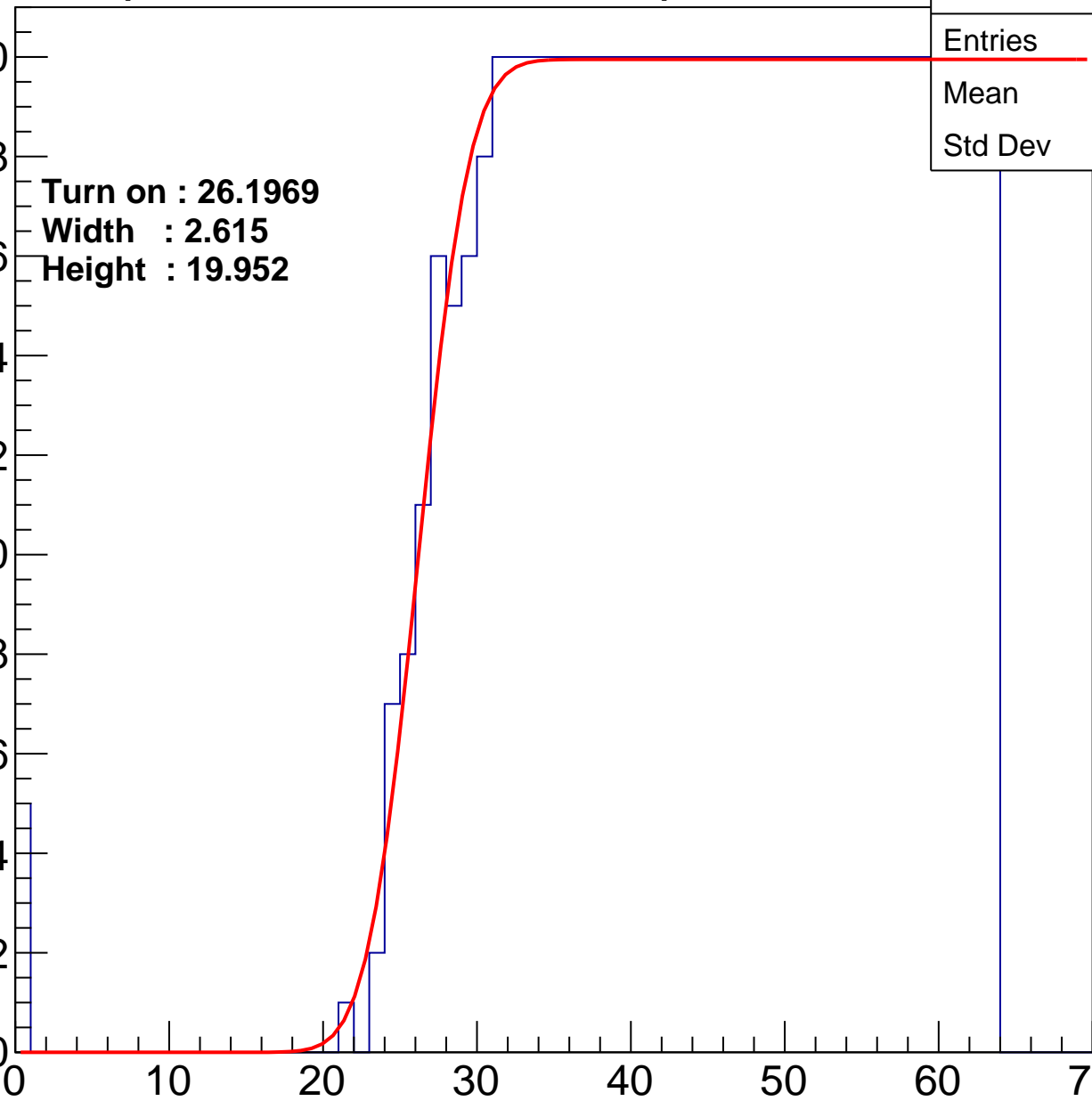
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1969
Width : 2.615
Height : 19.952

Entries	759
Mean	44.26
Std Dev	11.57

ampl



B1L001S, U24-ch39

calib_packv5_042523_0143.root, FC#2, port C2

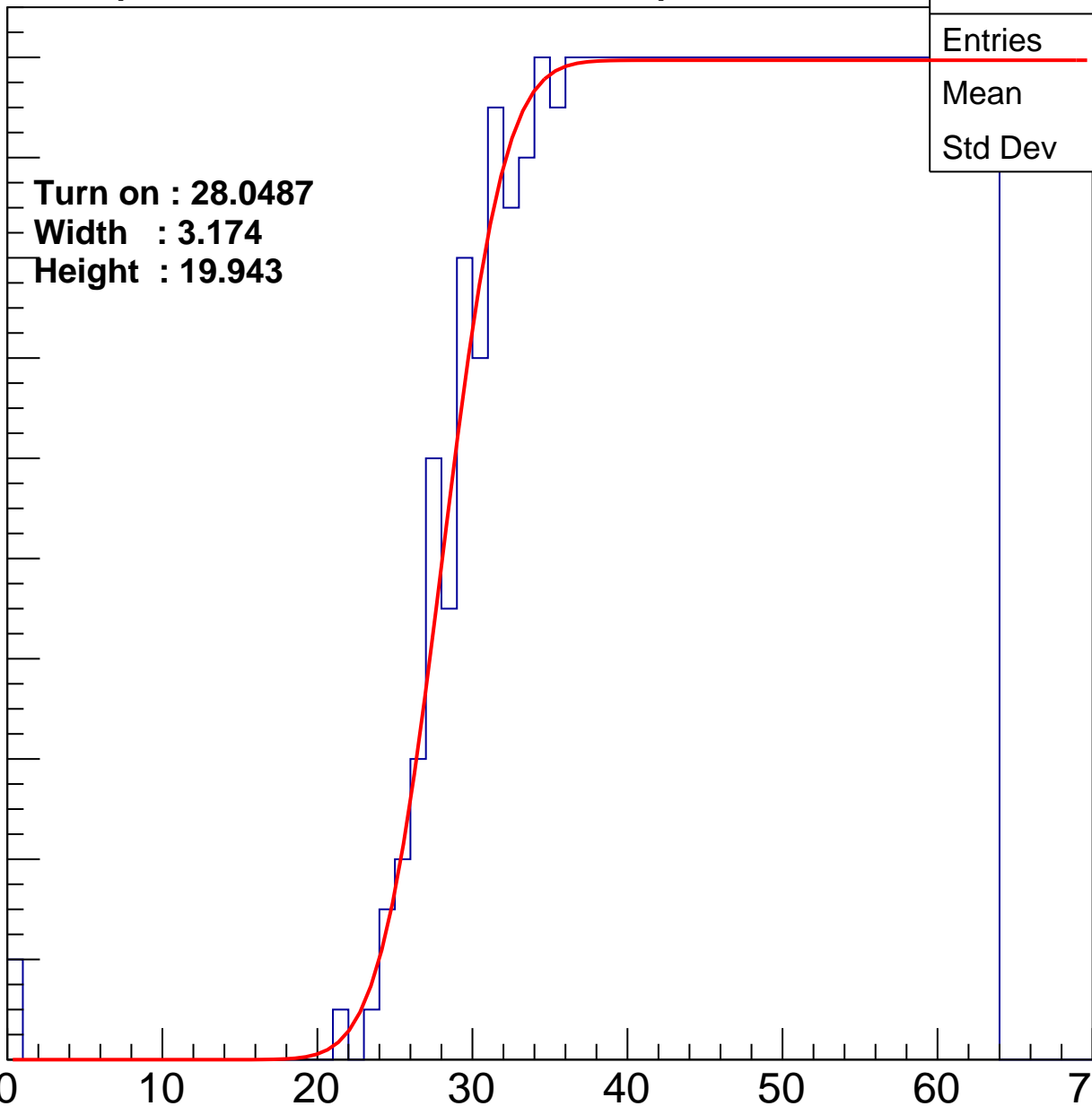
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0487
Width : 3.174
Height : 19.943

Entries	721
Mean	45.25
Std Dev	10.88

ampl



B1L001S, U24-ch40

calib_packv5_042523_0143.root, FC#2, port C2

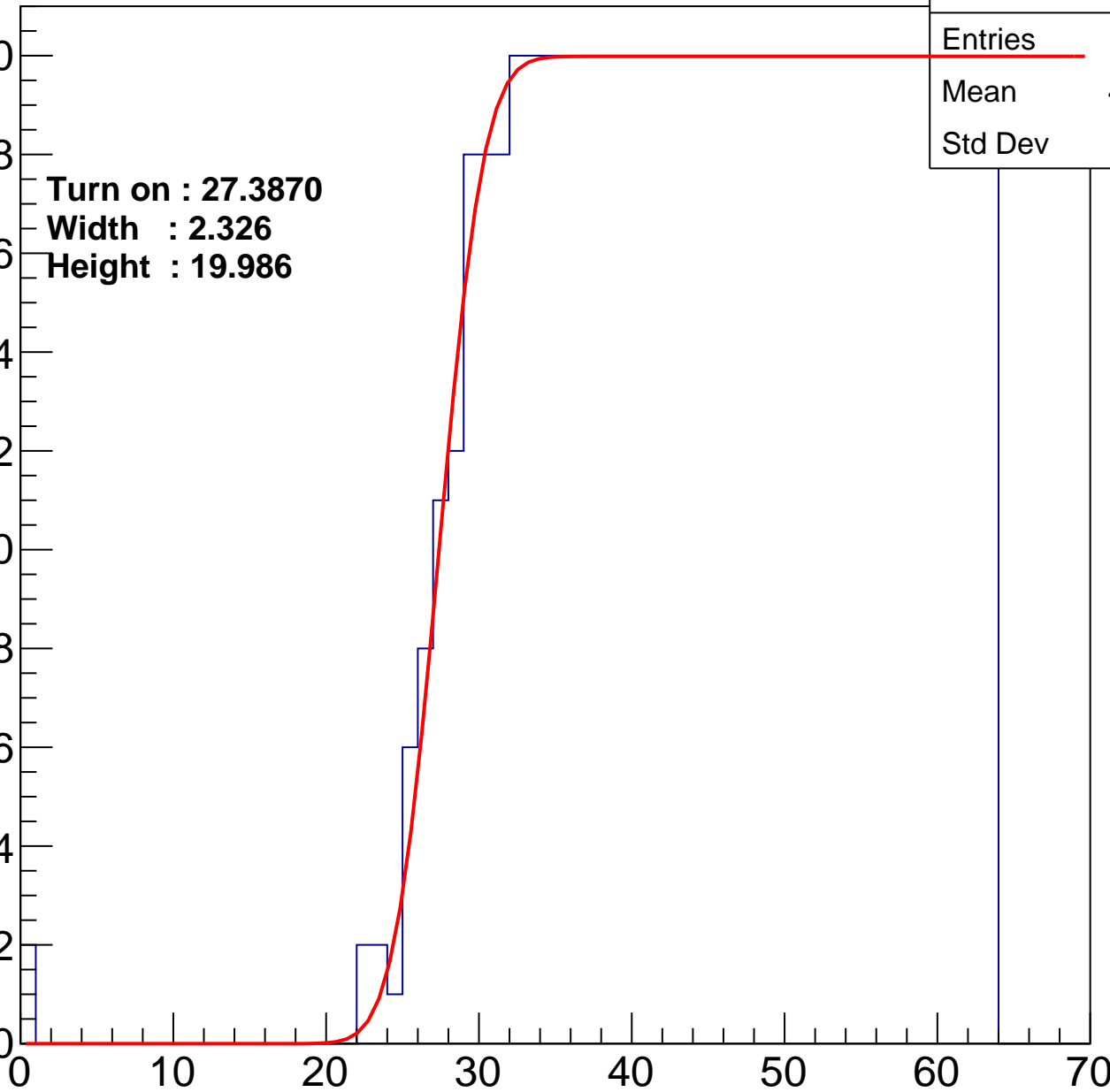
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3870
Width : 2.326
Height : 19.986

Entries	738
Mean	44.88
Std Dev	11.02

ampl



B1L001S, U24-ch41

calib_packv5_042523_0143.root, FC#2, port C2

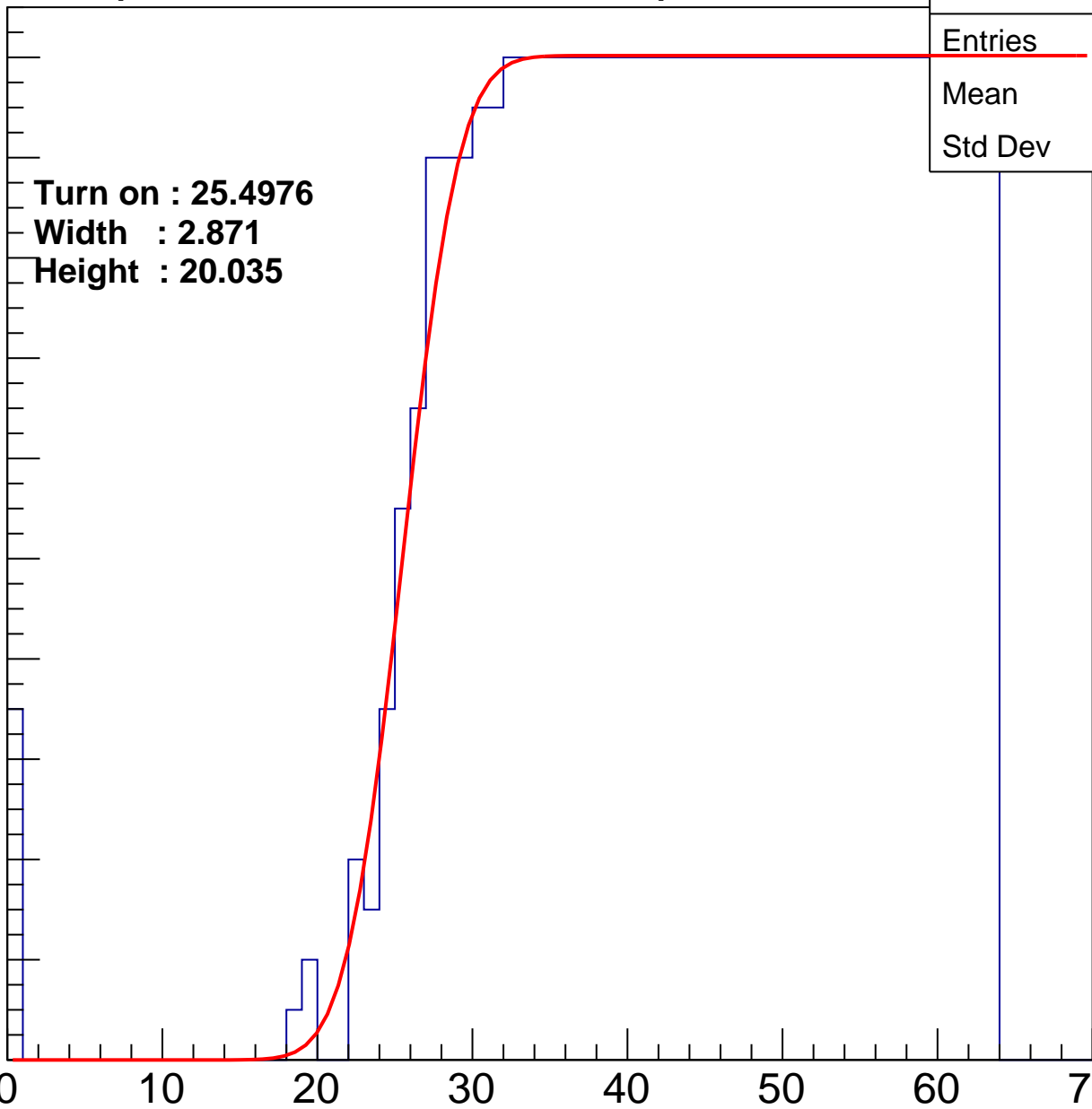
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4976
Width : 2.871
Height : 20.035

Entries	780
Mean	43.67
Std Dev	12.03

ampl



B1L001S, U24-ch42

calib_packv5_042523_0143.root, FC#2, port C2

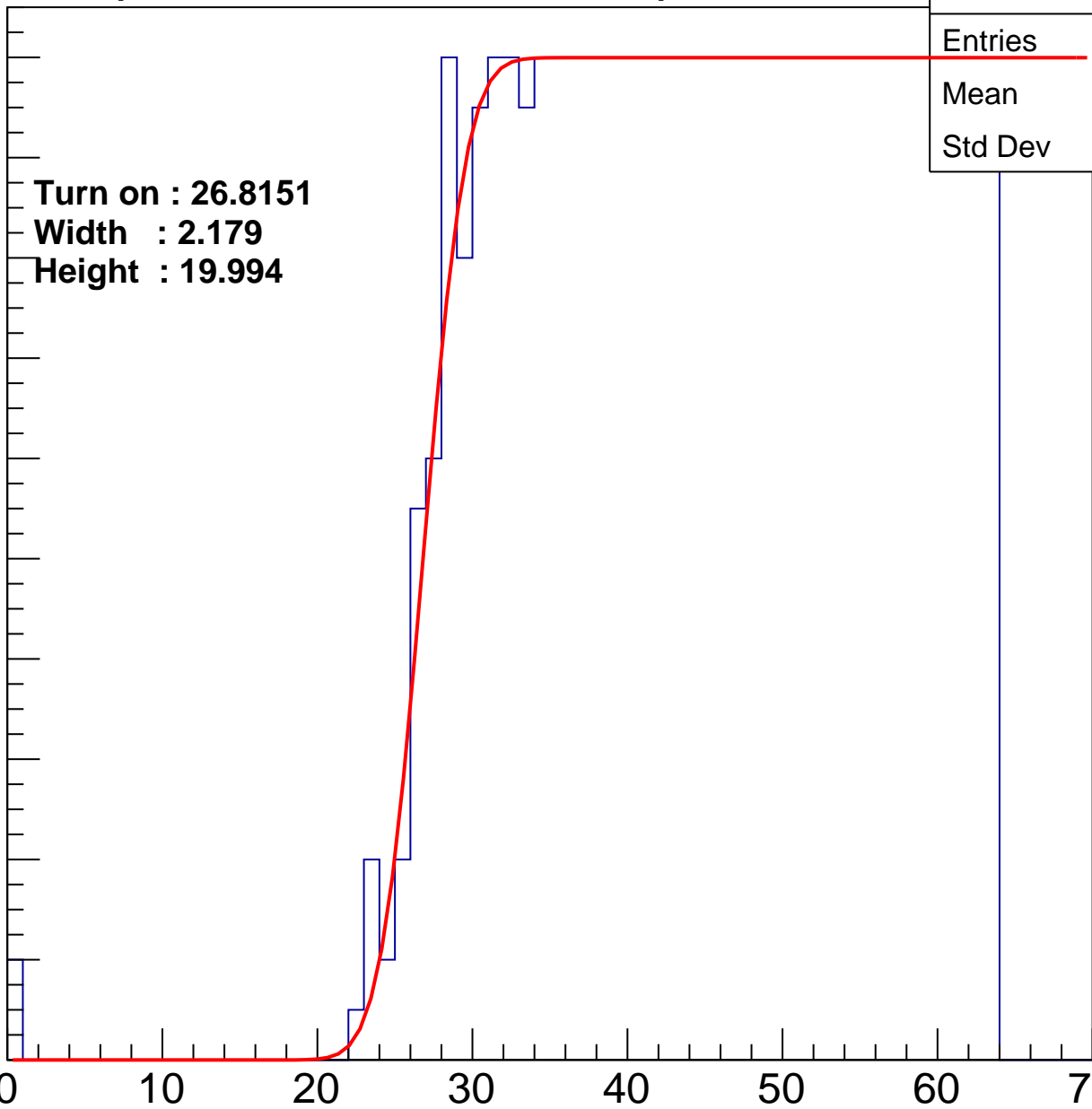
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8151
Width : 2.179
Height : 19.994

Entries	750
Mean	44.6
Std Dev	11.15

ampl



B1L001S, U24-ch43

calib_packv5_042523_0143.root, FC#2, port C2

Entry

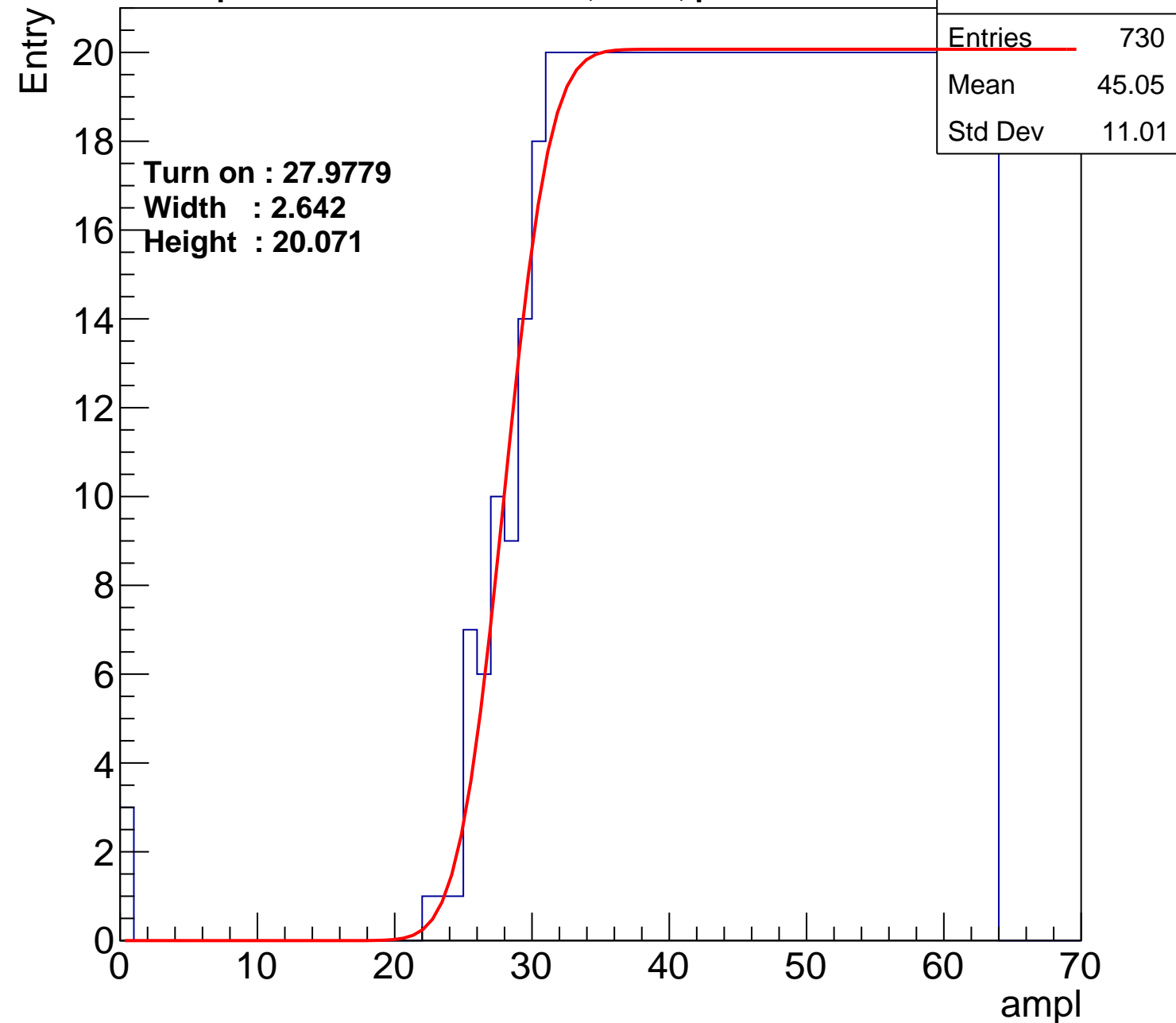
20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9779
Width : 2.642
Height : 20.071

Entries	730
Mean	45.05
Std Dev	11.01

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch44

calib_packv5_042523_0143.root, FC#2, port C2

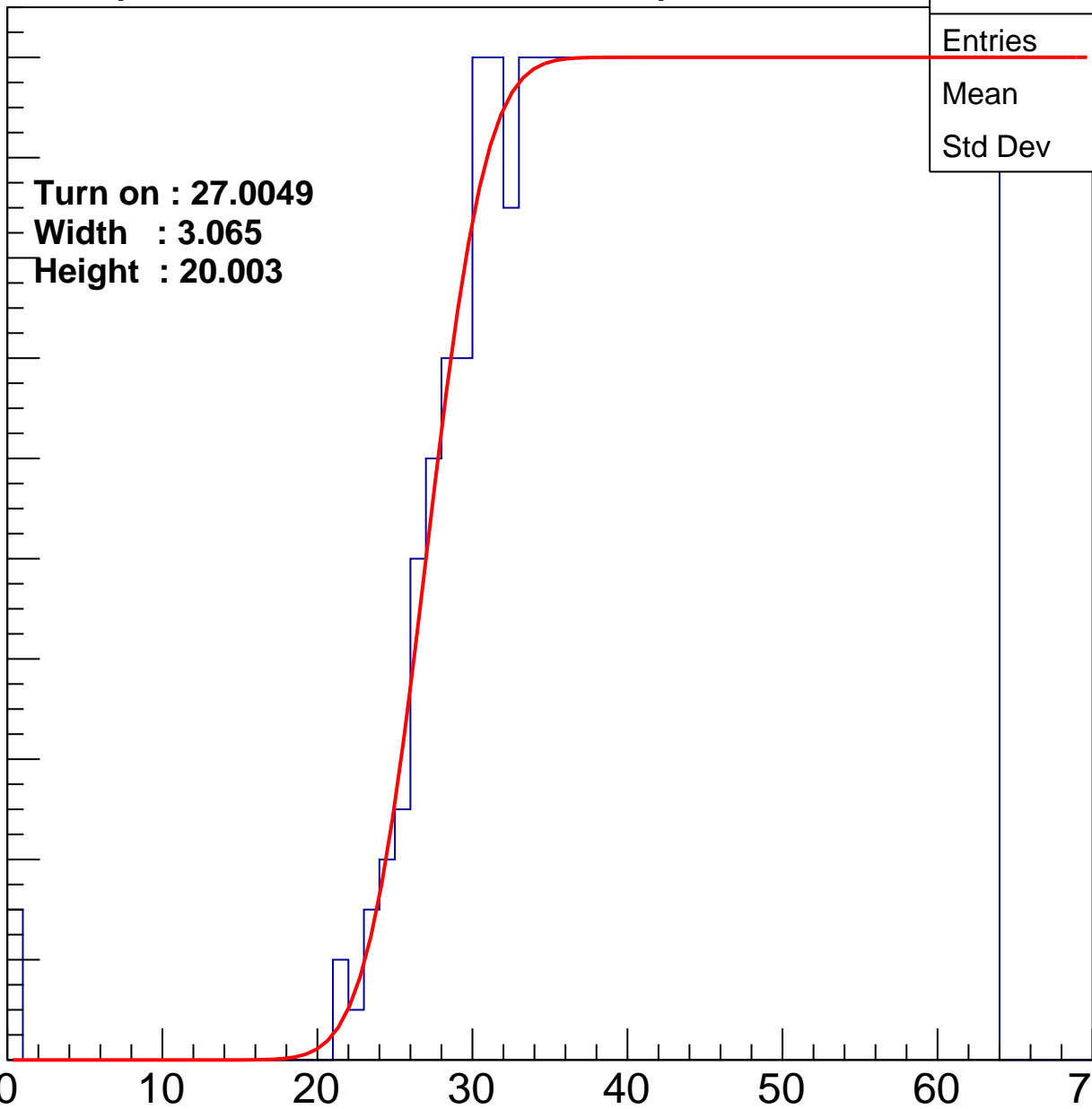
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0049
Width : 3.065
Height : 20.003

Entries	745
Mean	44.64
Std Dev	11.26

ampl



B1L001S, U24-ch45

calib_packv5_042523_0143.root, FC#2, port C2

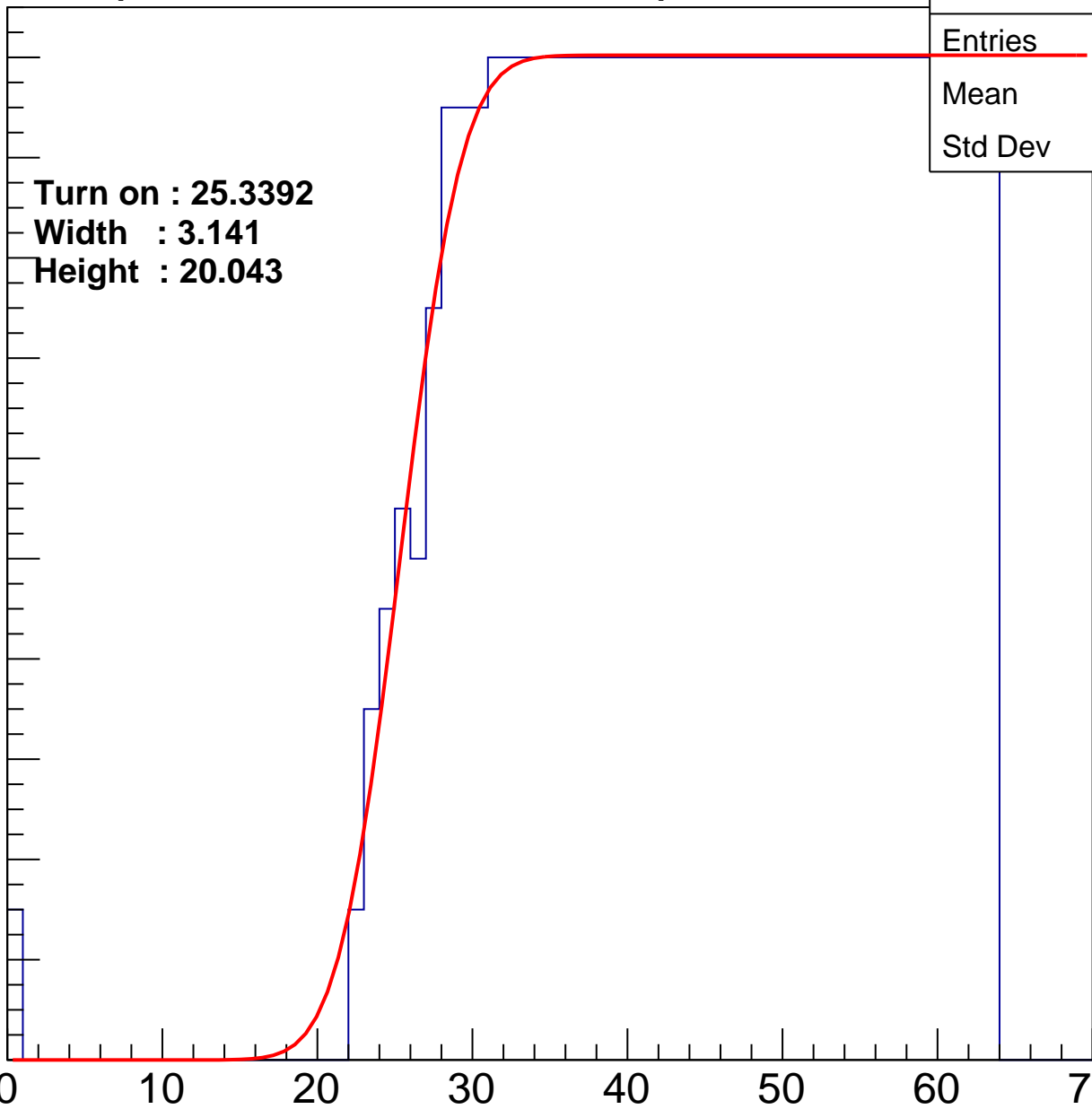
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3392
Width : 3.141
Height : 20.043

Entries	775
Mean	43.94
Std Dev	11.59

ampl



B1L001S, U24-ch46

calib_packv5_042523_0143.root, FC#2, port C2

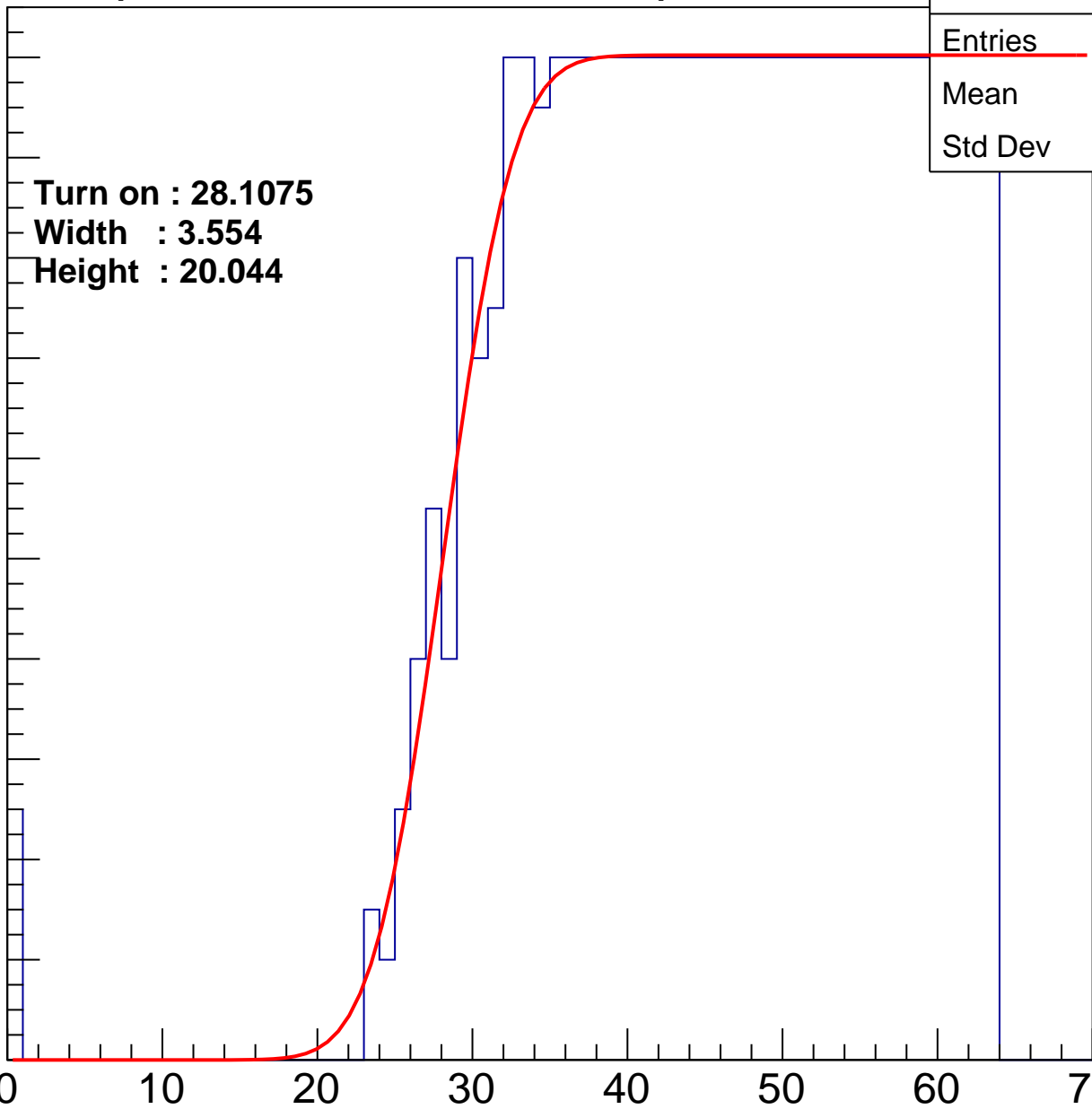
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1075
Width : 3.554
Height : 20.044

Entries	726
Mean	45.02
Std Dev	11.25

ampl



B1L001S, U24-ch47

calib_packv5_042523_0143.root, FC#2, port C2

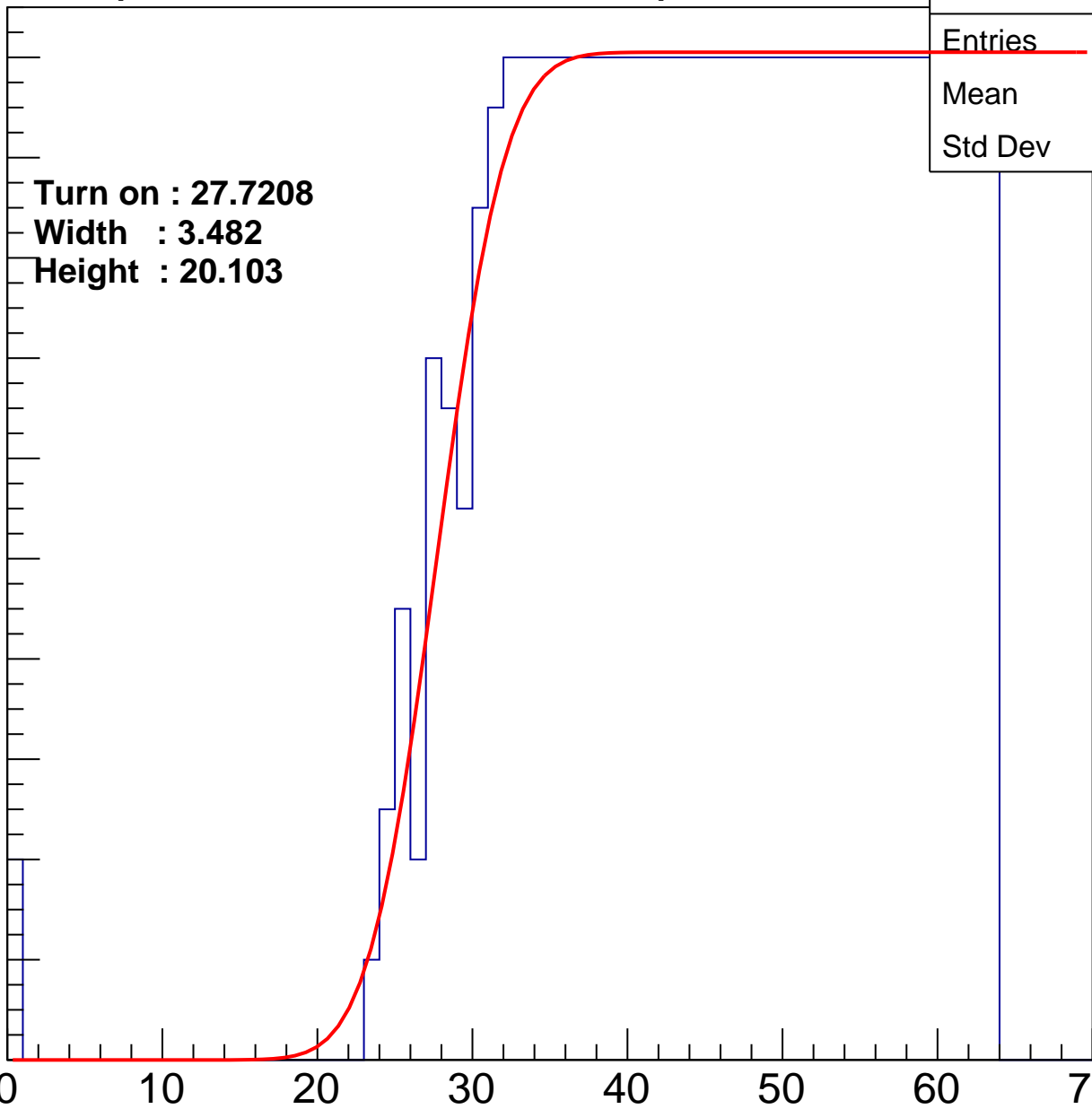
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7208
Width : 3.482
Height : 20.103

Entries	738
Mean	44.79
Std Dev	11.26

ampl



B1L001S, U24-ch48

calib_packv5_042523_0143.root, FC#2, port C2

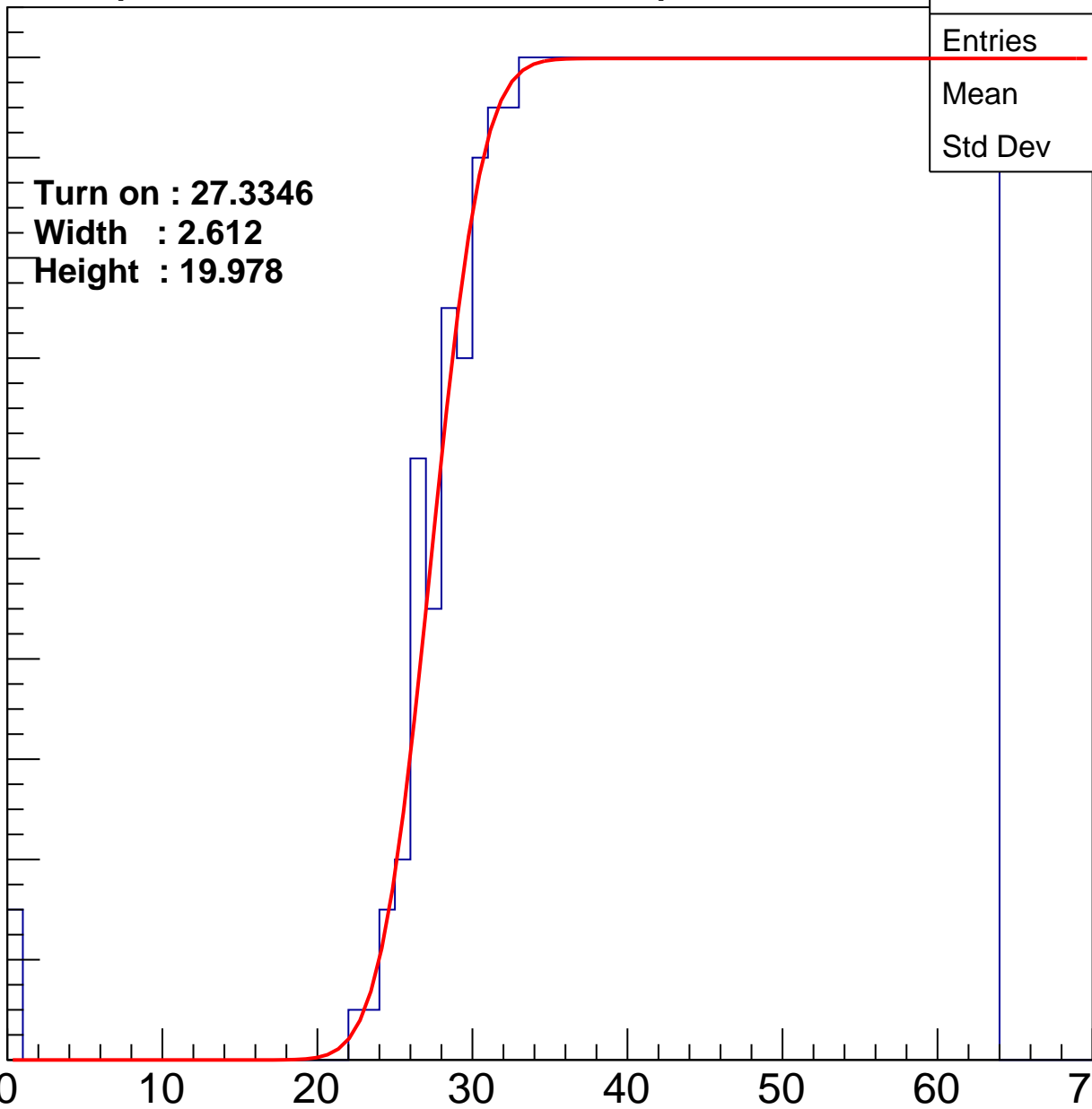
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3346
Width : 2.612
Height : 19.978

Entries	738
Mean	44.84
Std Dev	11.12

ampl



B1L001S, U24-ch49

calib_packv5_042523_0143.root, FC#2, port C2

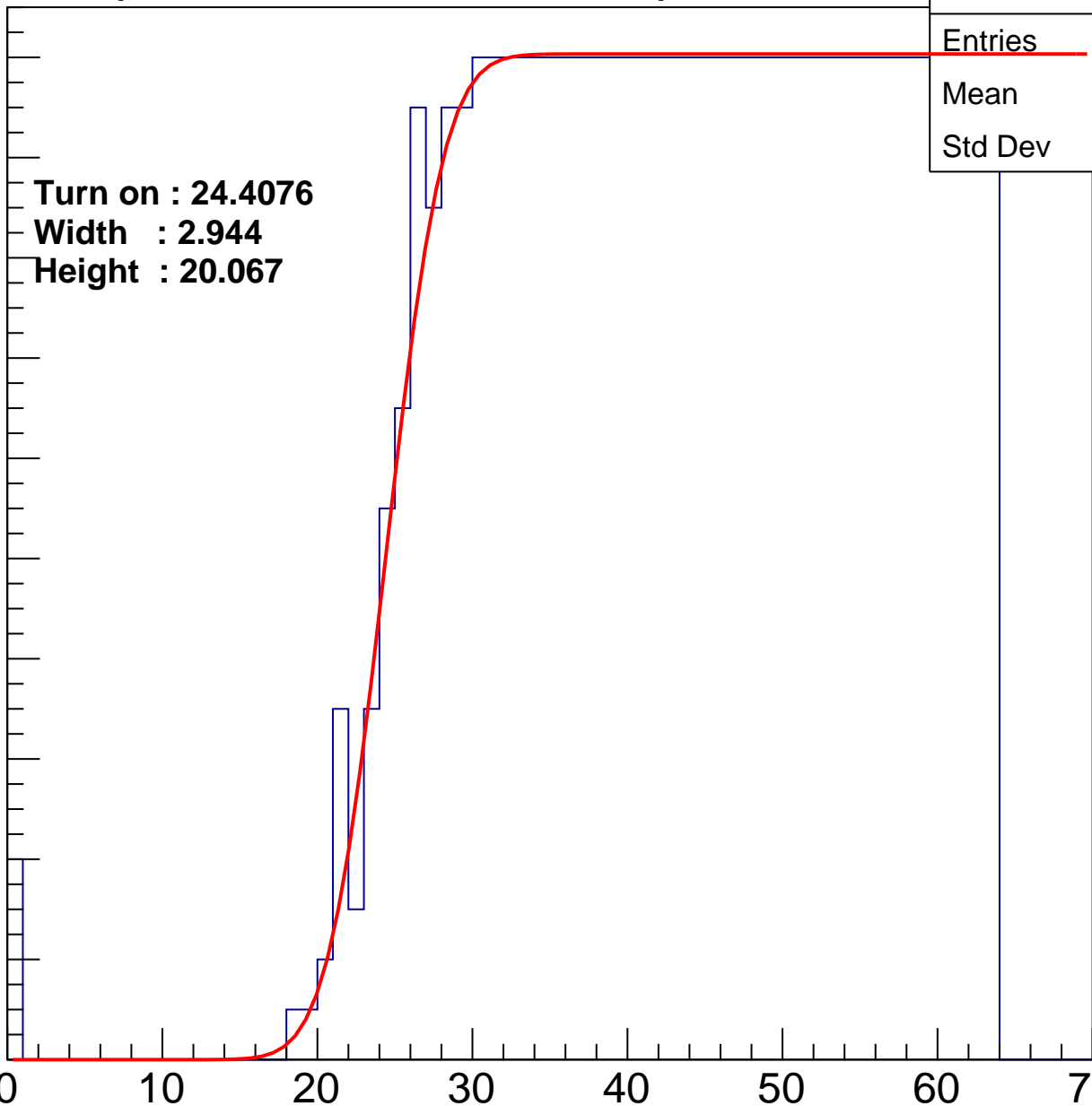
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.4076
Width : 2.944
Height : 20.067

Entries	803
Mean	43.21
Std Dev	12.07

ampl



B1L001S, U24-ch50

calib_packv5_042523_0143.root, FC#2, port C2

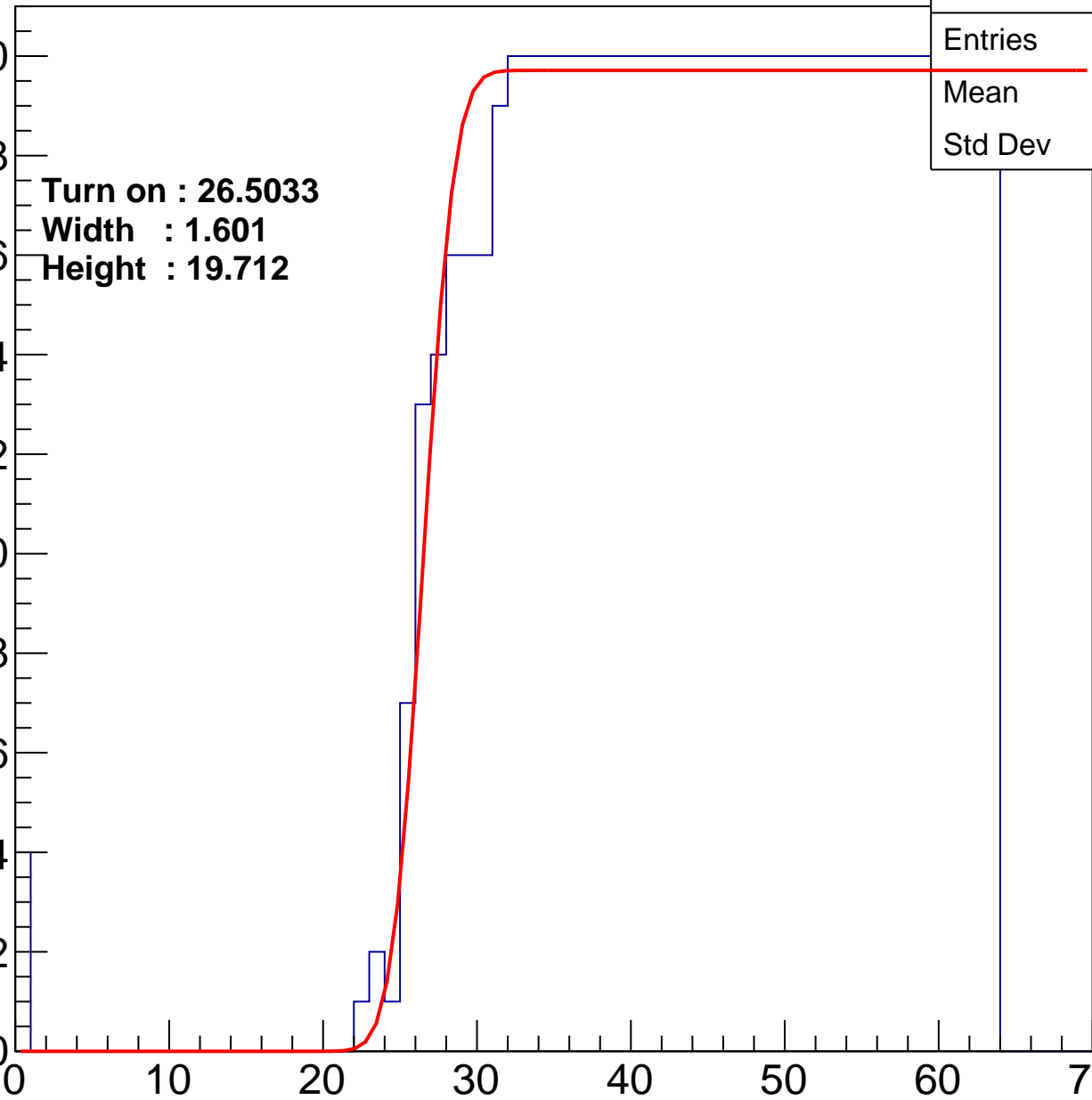
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5033
Width : 1.601
Height : 19.712

Entries	749
Mean	44.54
Std Dev	11.35

ampl



B1L001S, U24-ch51

calib_packv5_042523_0143.root, FC#2, port C2

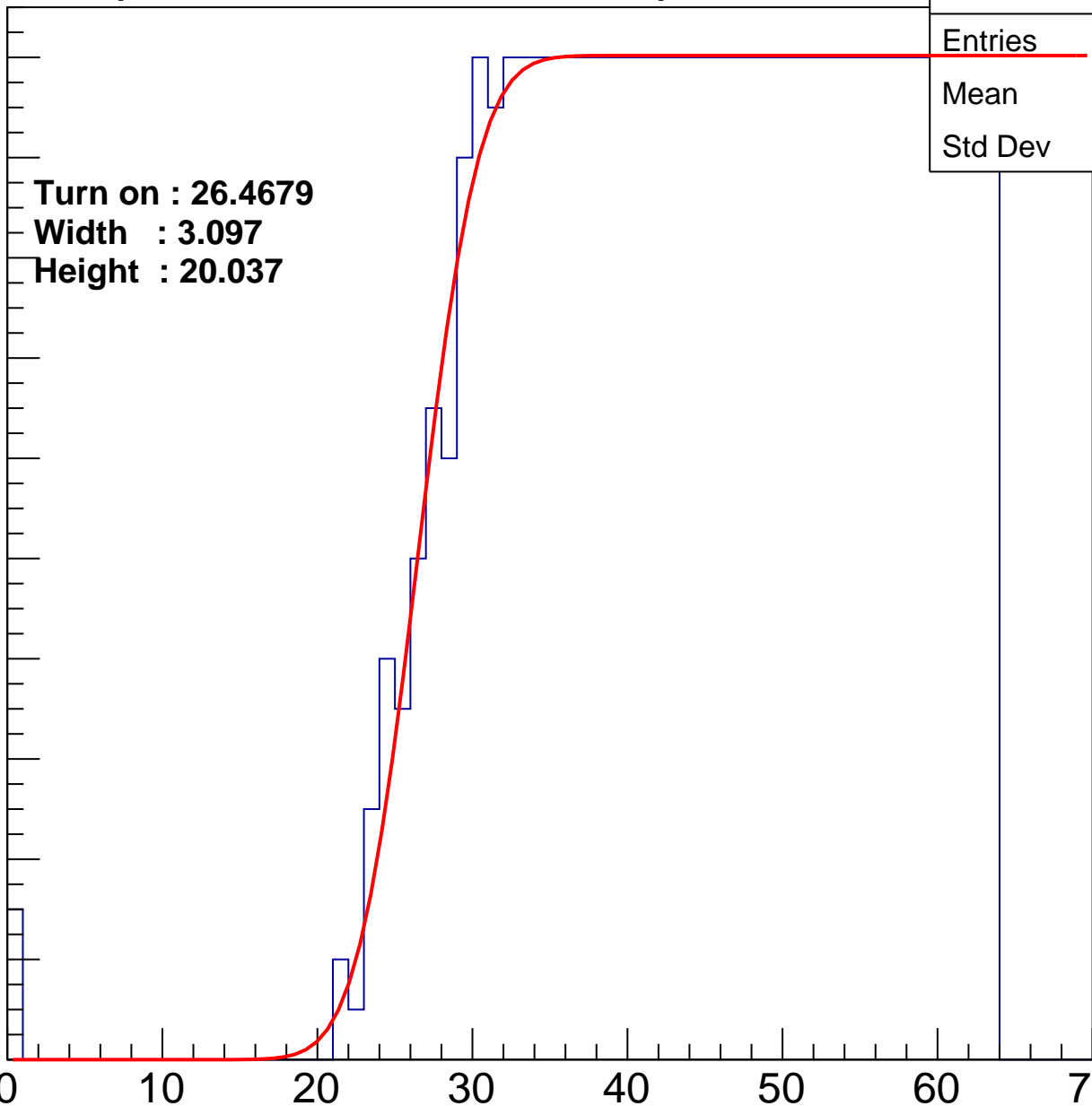
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4679
Width : 3.097
Height : 20.037

Entries	758
Mean	44.33
Std Dev	11.42

ampl



B1L001S, U24-ch52

calib_packv5_042523_0143.root, FC#2, port C2

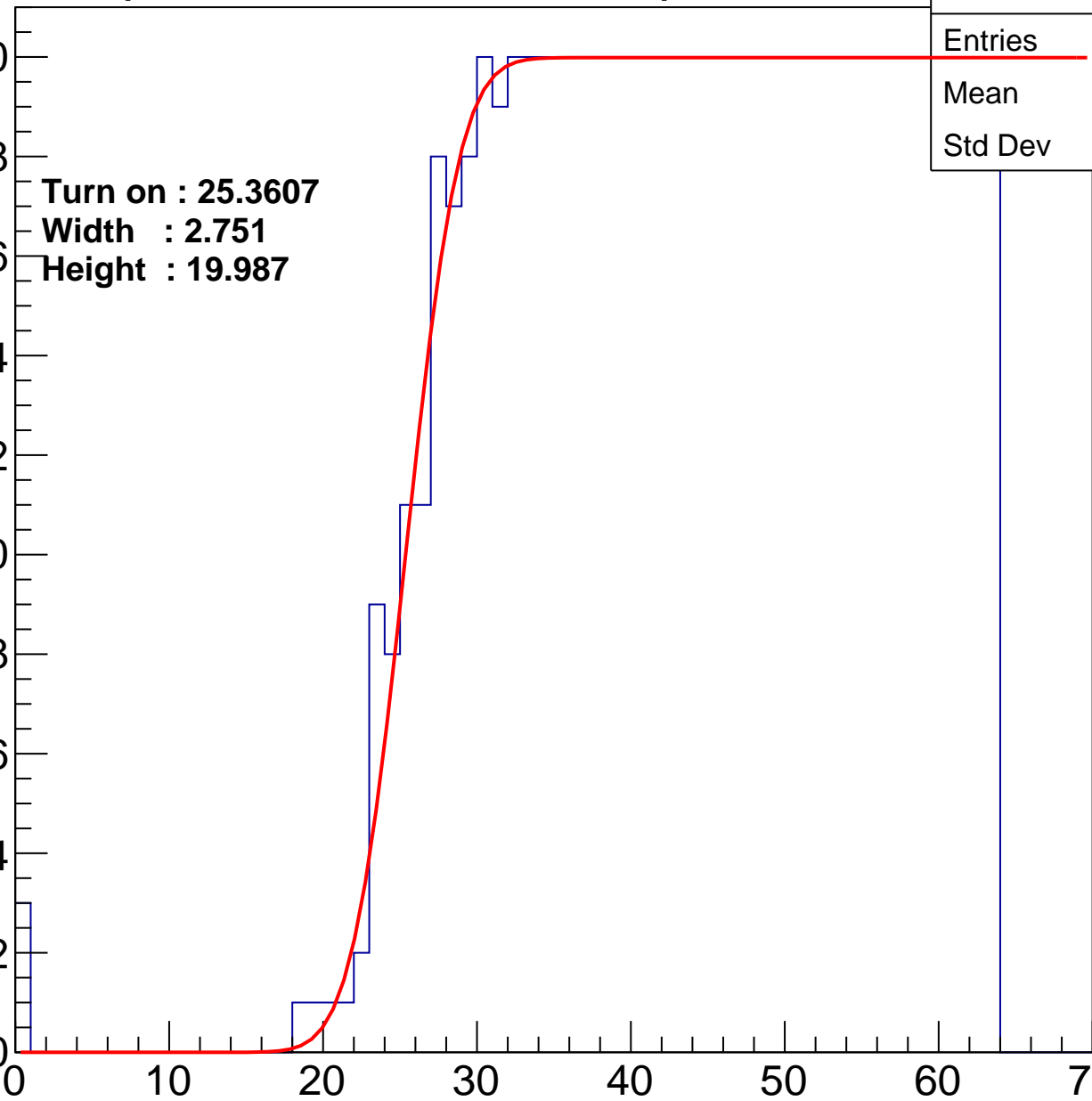
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3607
Width : 2.751
Height : 19.987

Entries	780
Mean	43.79
Std Dev	11.71

ampl



B1L001S, U24-ch53

calib_packv5_042523_0143.root, FC#2, port C2

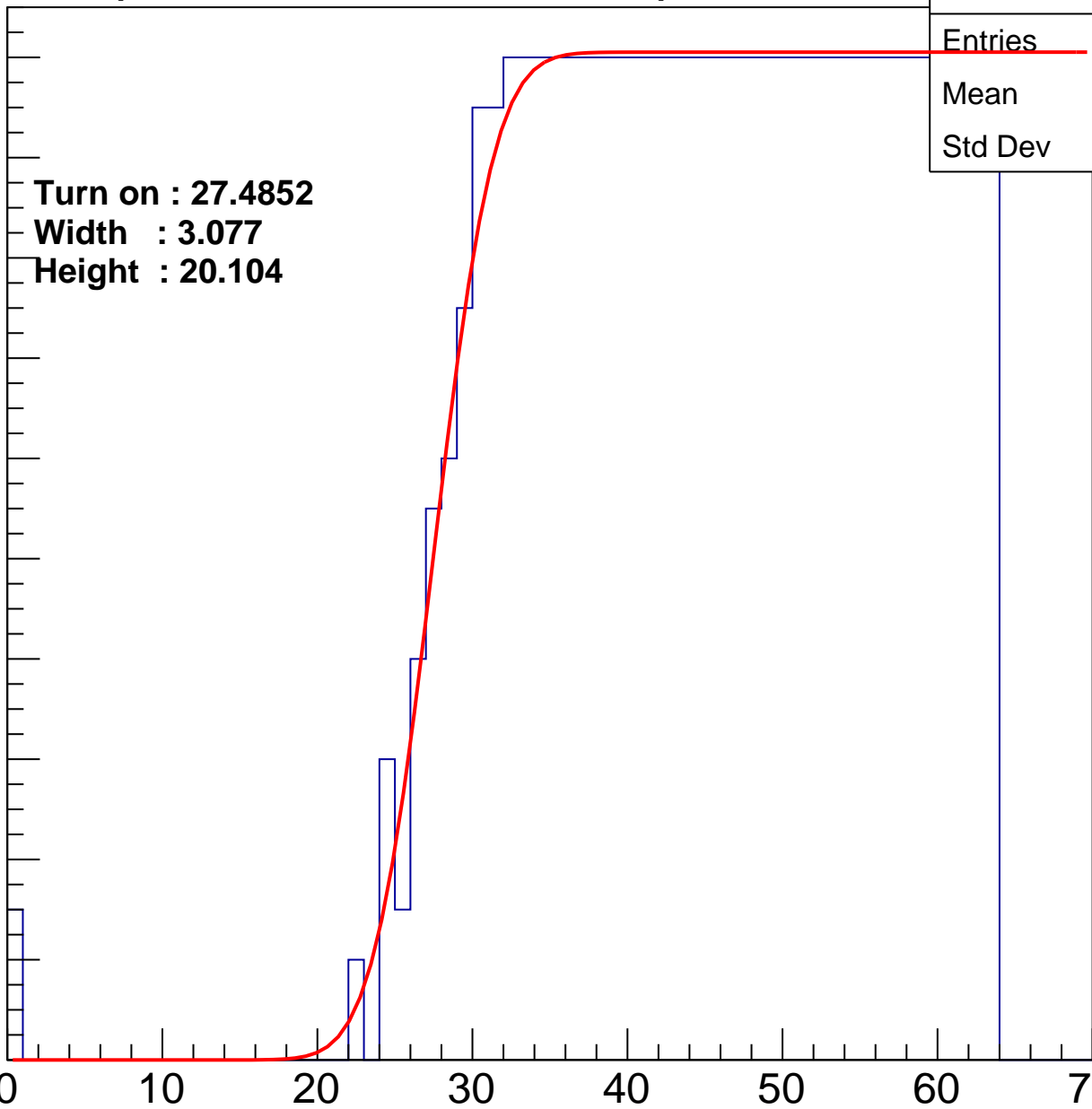
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4852
Width : 3.077
Height : 20.104

Entries	738
Mean	44.85
Std Dev	11.12

ampl



B1L001S, U24-ch54

calib_packv5_042523_0143.root, FC#2, port C2

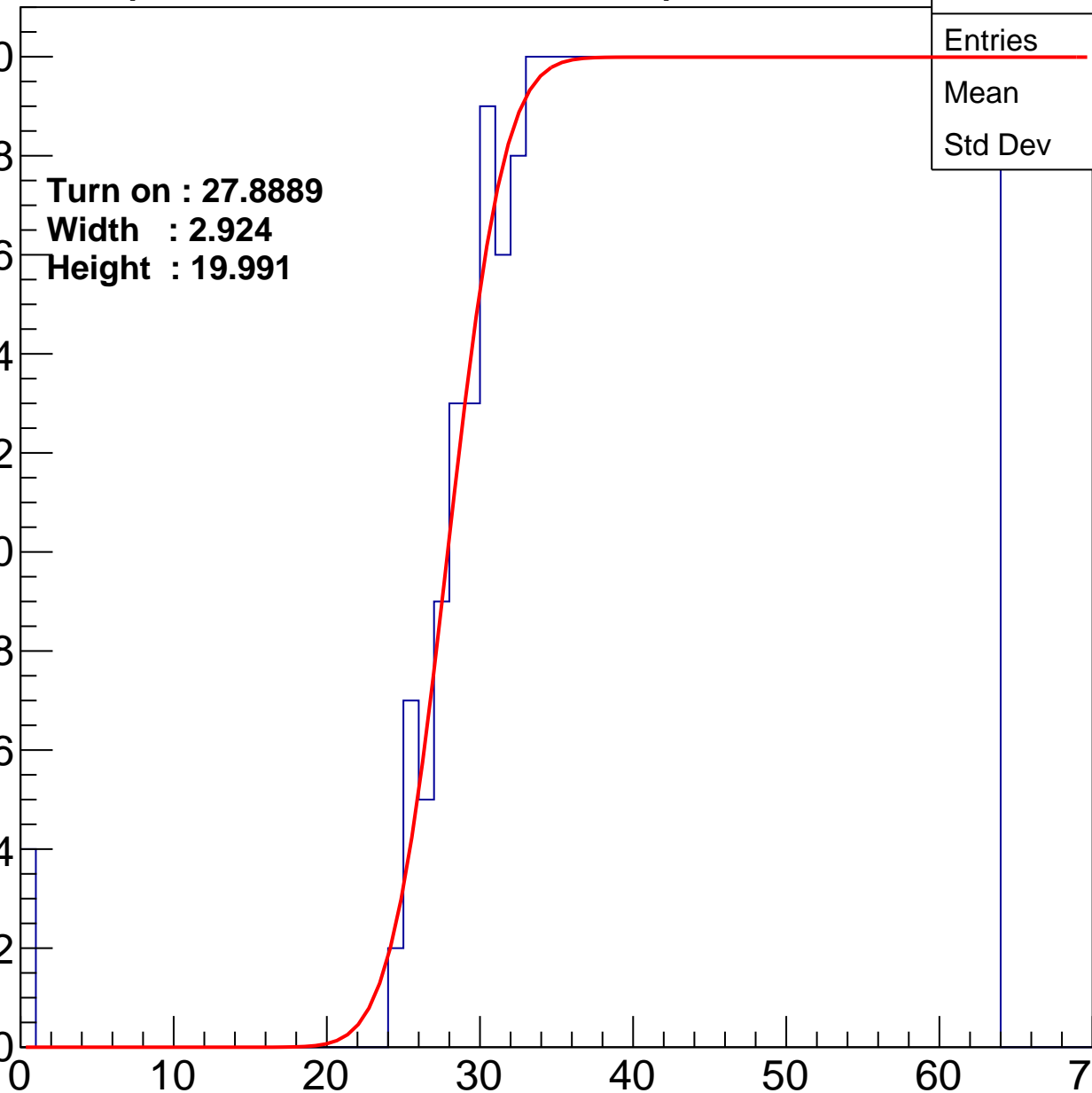
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8889
Width : 2.924
Height : 19.991

Entries	726
Mean	45.1
Std Dev	11.09

ampl



B1L001S, U24-ch55

calib_packv5_042523_0143.root, FC#2, port C2

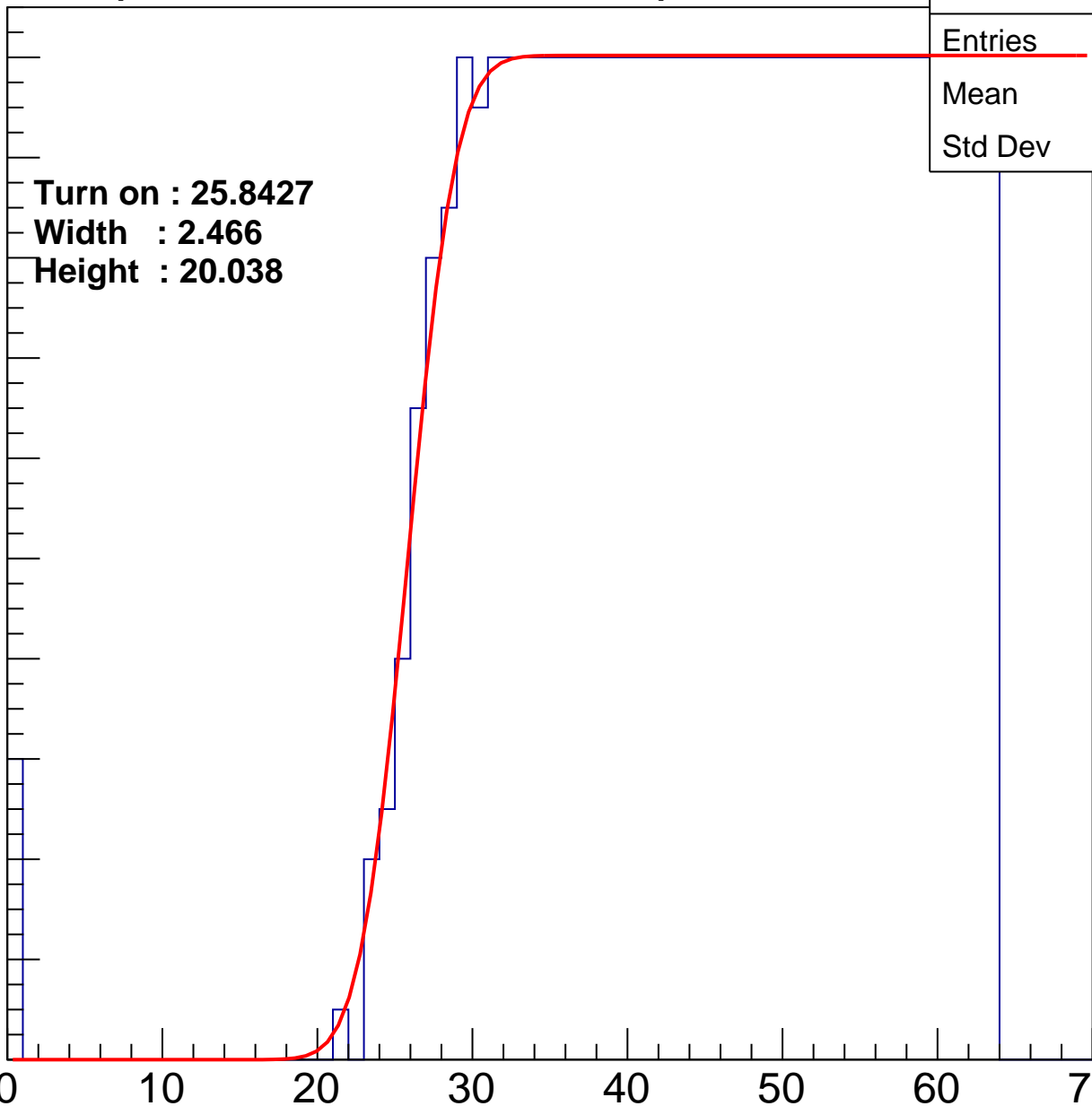
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8427
Width : 2.466
Height : 20.038

Entries	769
Mean	44.02
Std Dev	11.74

ampl



B1L001S, U24-ch56

calib_packv5_042523_0143.root, FC#2, port C2

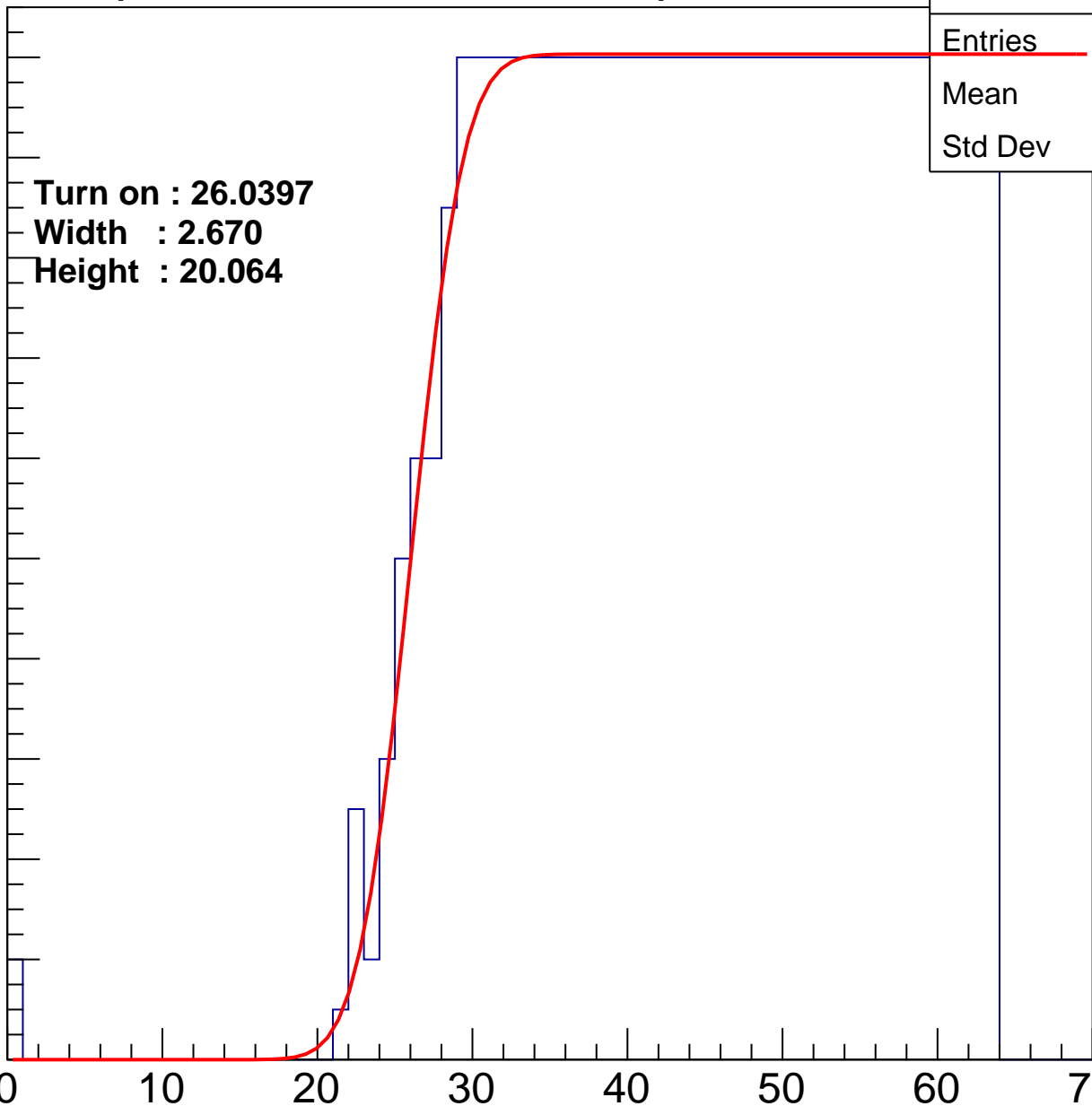
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0397
Width : 2.670
Height : 20.064

Entries	767
Mean	44.18
Std Dev	11.4

ampl



B1L001S, U24-ch57

calib_packv5_042523_0143.root, FC#2, port C2

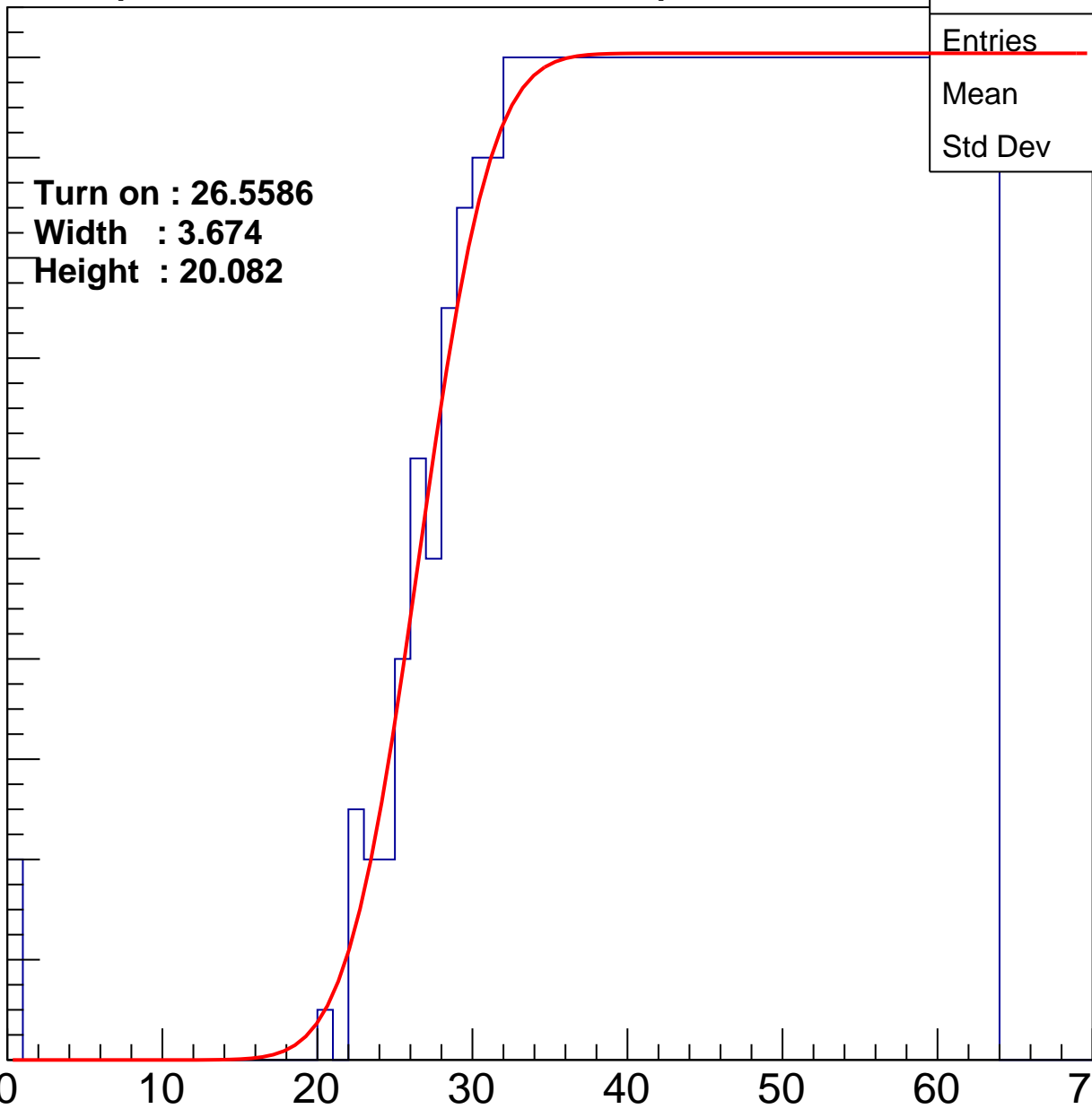
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5586
Width : 3.674
Height : 20.082

Entries	756
Mean	44.33
Std Dev	11.52

ampl



B1L001S, U24-ch58

calib_packv5_042523_0143.root, FC#2, port C2

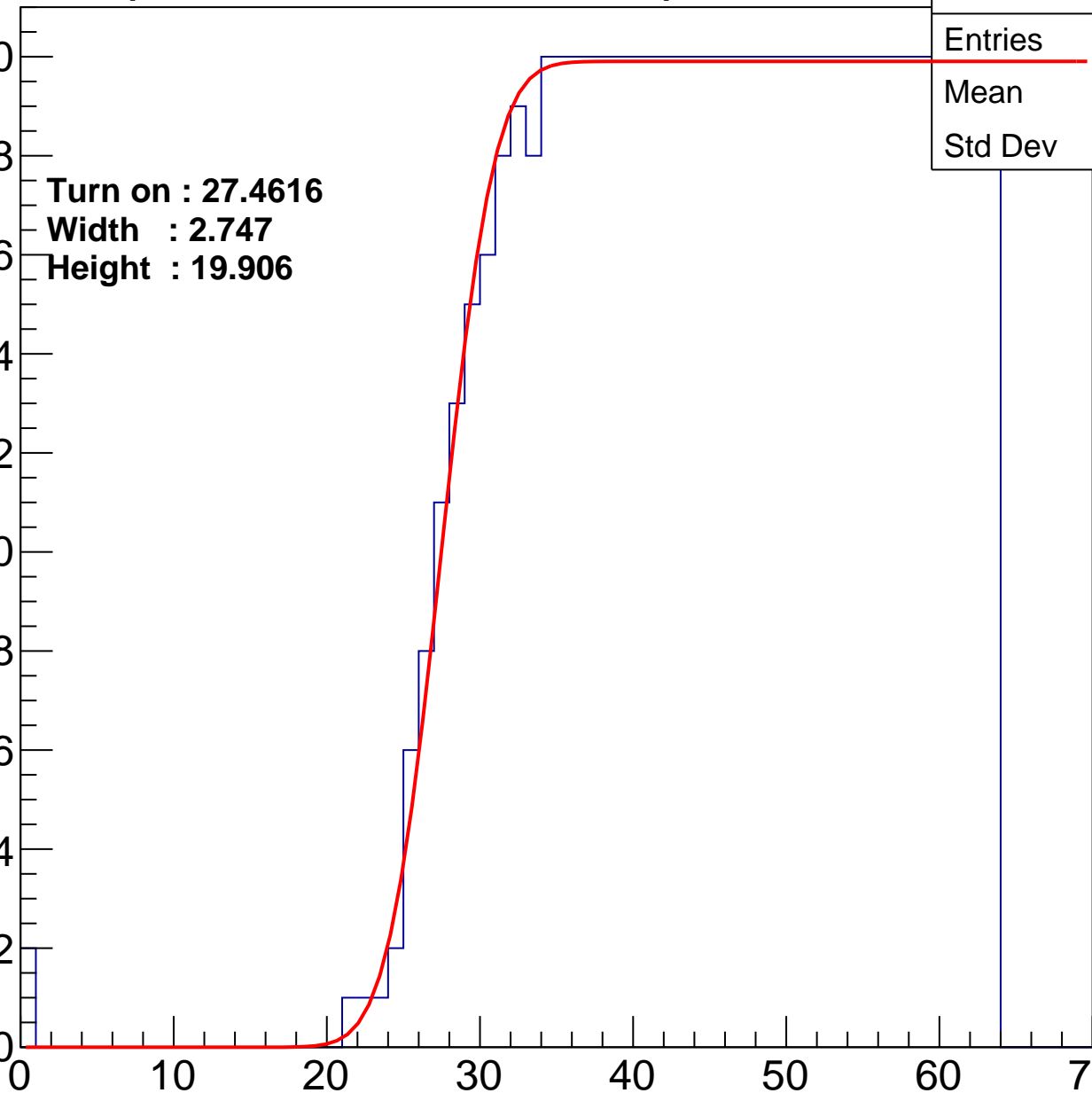
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4616
Width : 2.747
Height : 19.906

Entries	731
Mean	45.02
Std Dev	10.98

ampl



B1L001S, U24-ch59

calib_packv5_042523_0143.root, FC#2, port C2

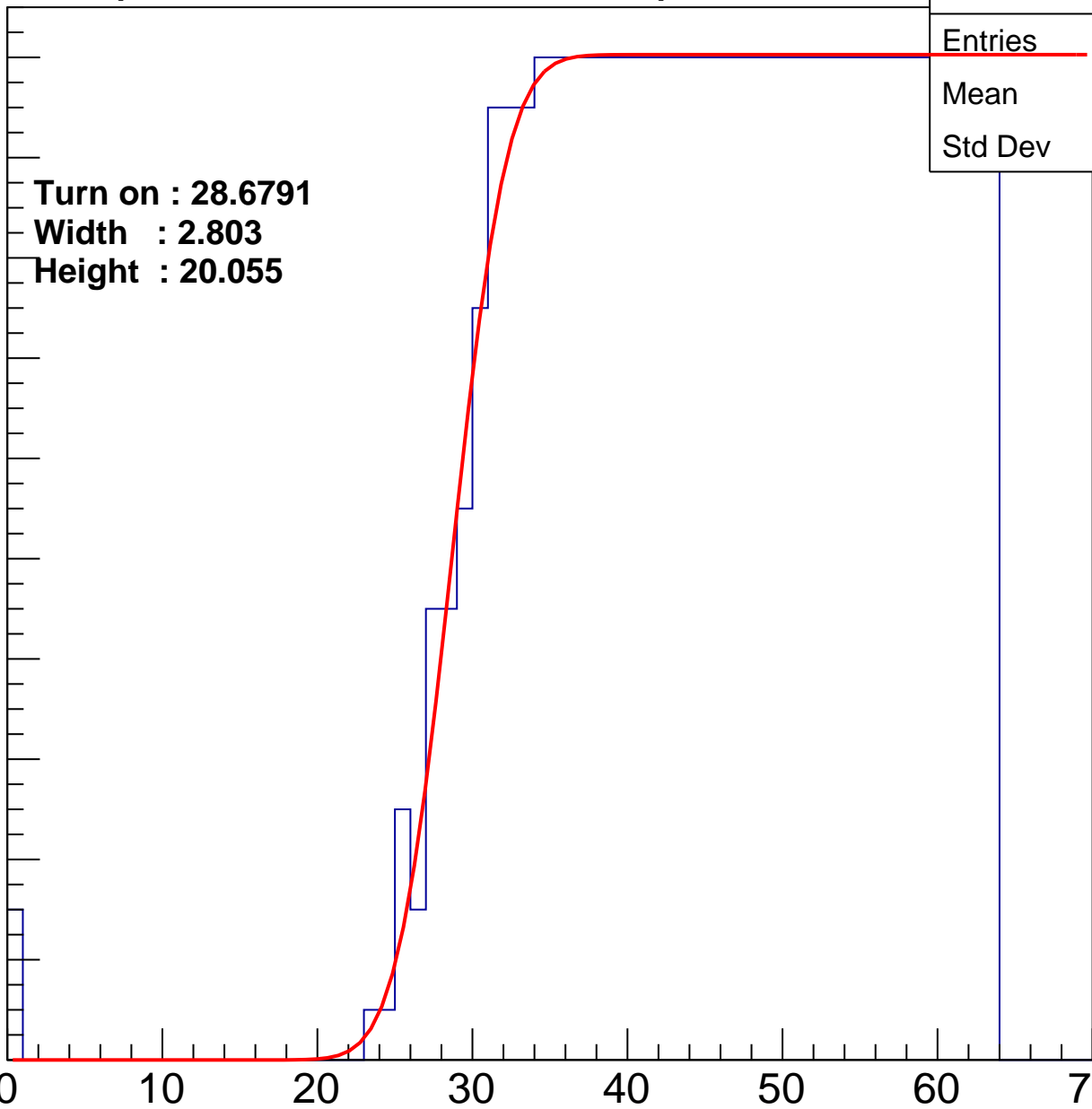
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6791
Width : 2.803
Height : 20.055

Entries	714
Mean	45.43
Std Dev	10.82

ampl



B1L001S, U24-ch60

calib_packv5_042523_0143.root, FC#2, port C2

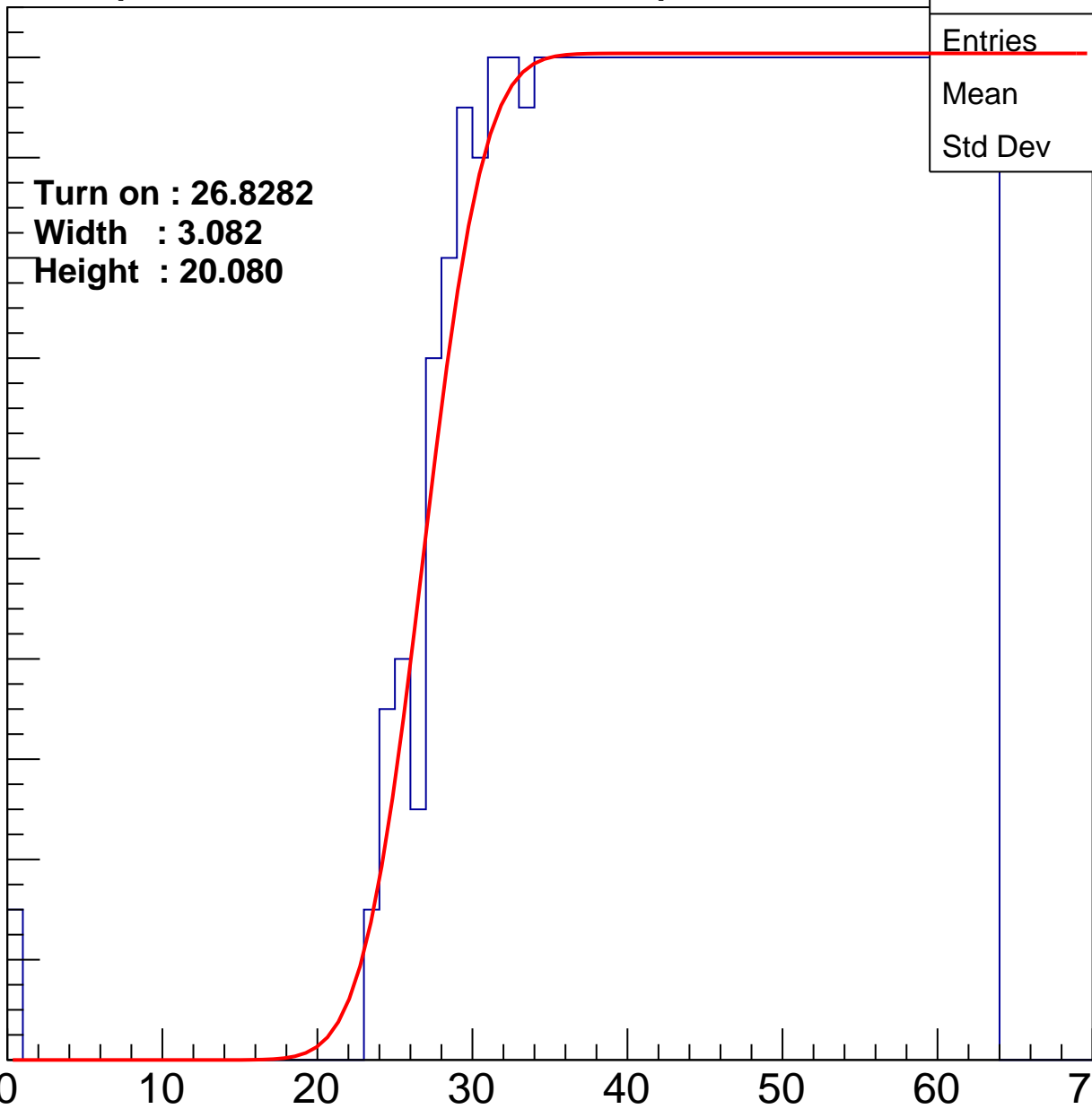
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8282
Width : 3.082
Height : 20.080

Entries	752
Mean	44.51
Std Dev	11.29

ampl



B1L001S, U24-ch61

calib_packv5_042523_0143.root, FC#2, port C2

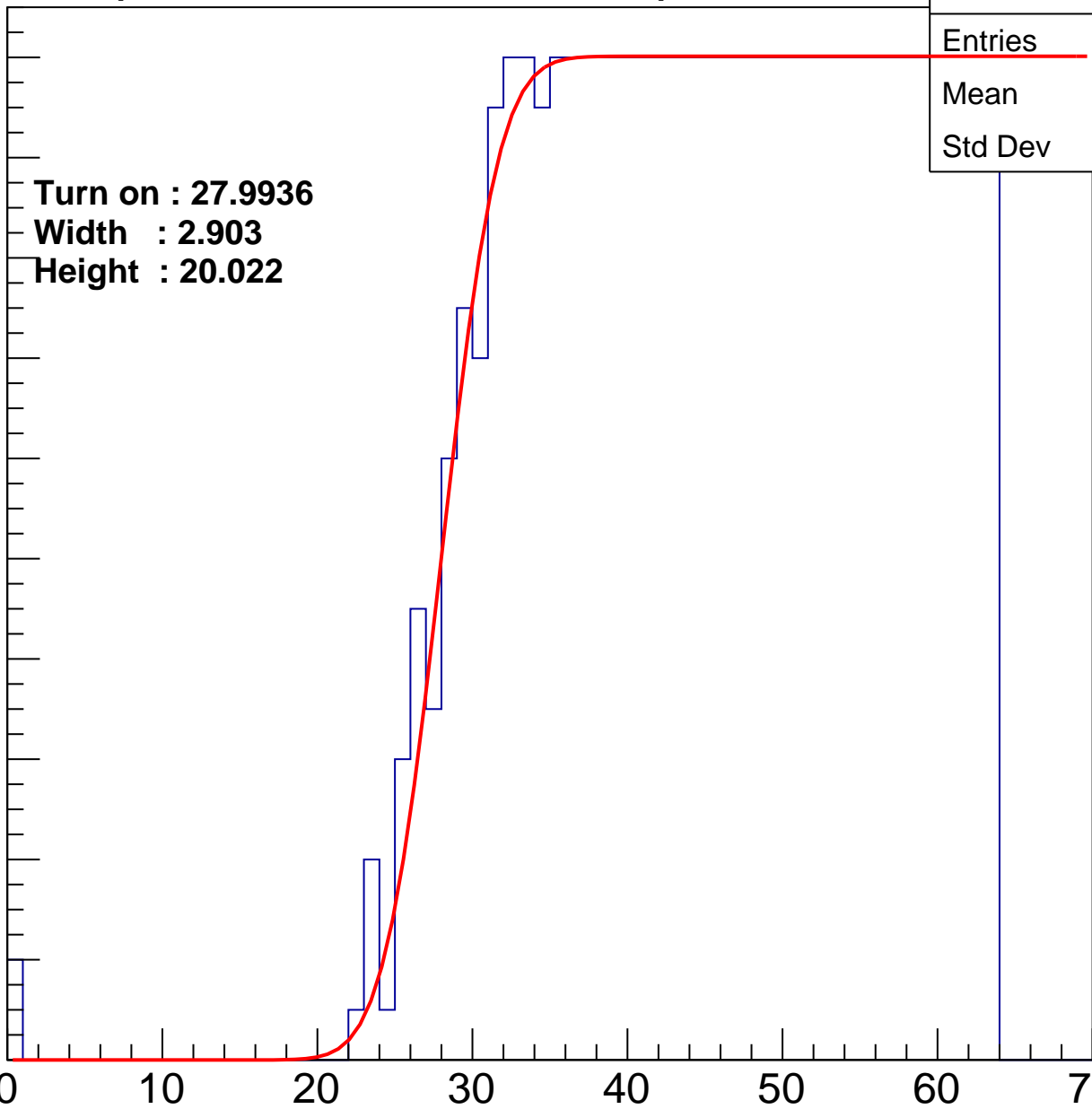
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9936
Width : 2.903
Height : 20.022

Entries	729
Mean	45.07
Std Dev	10.96

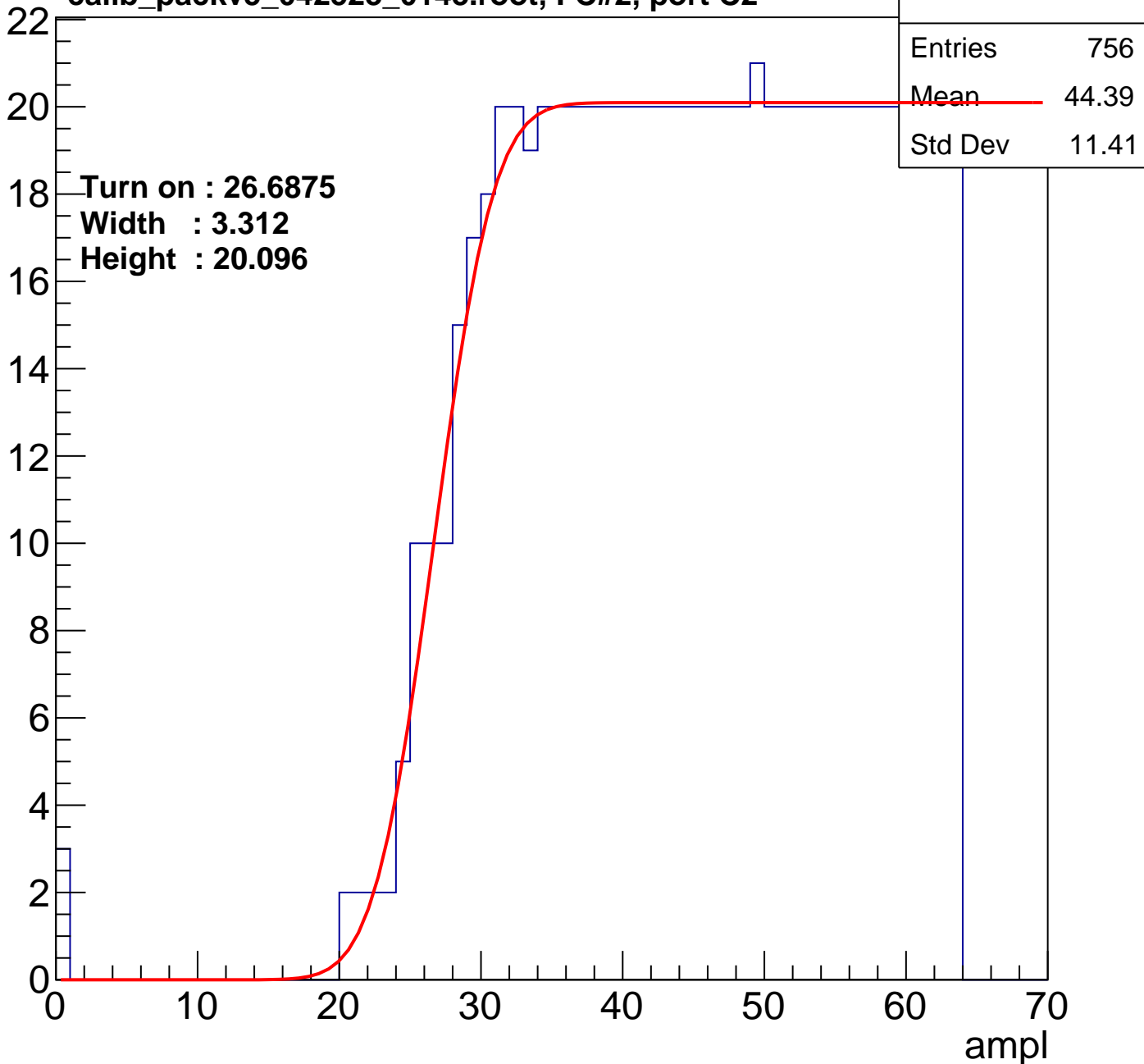
ampl



B1L001S, U24-ch62

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch63

calib_packv5_042523_0143.root, FC#2, port C2

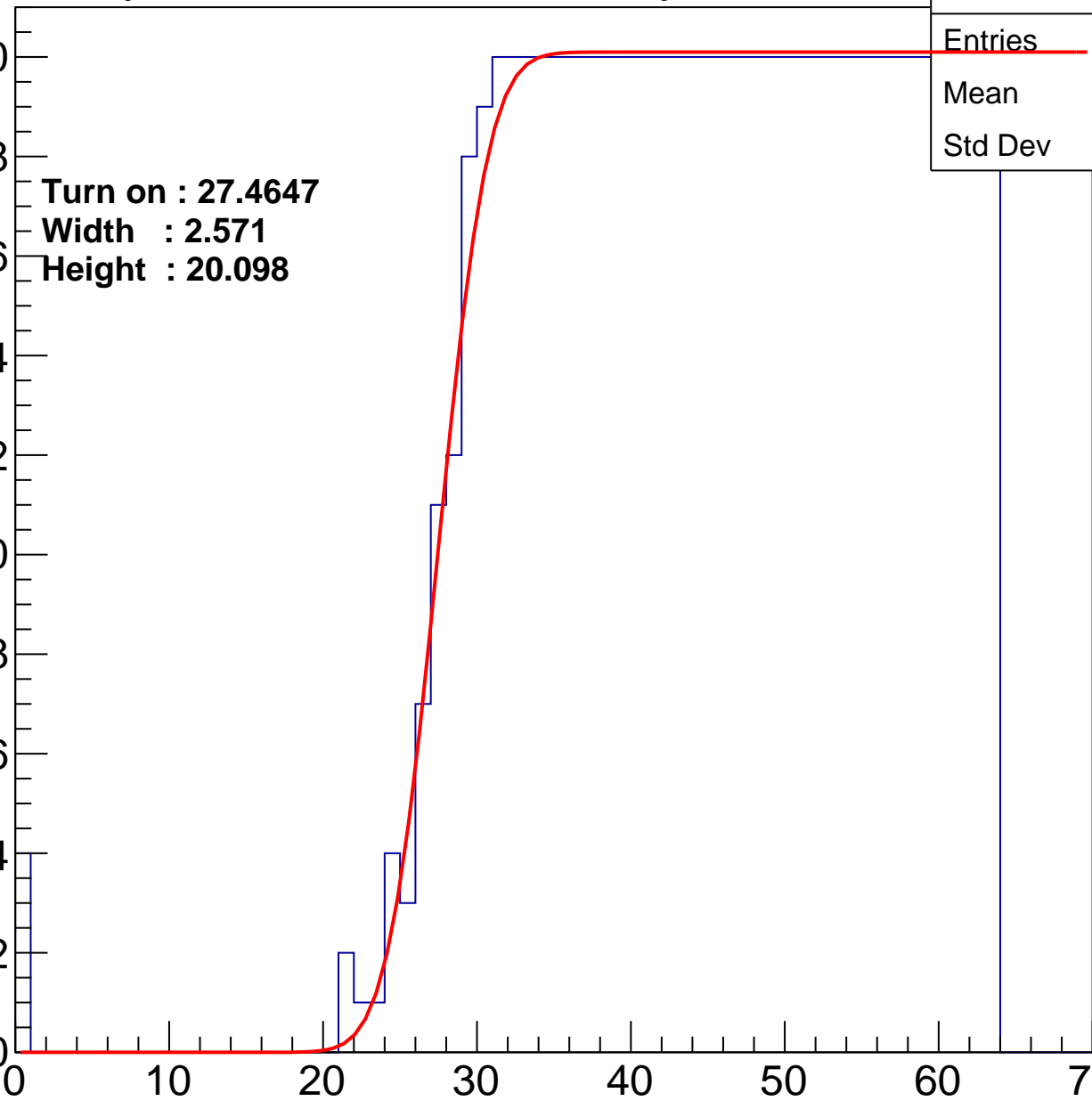
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4647
Width : 2.571
Height : 20.098

Entries	742
Mean	44.72
Std Dev	11.26

ampl



B1L001S, U24-ch64

calib_packv5_042523_0143.root, FC#2, port C2

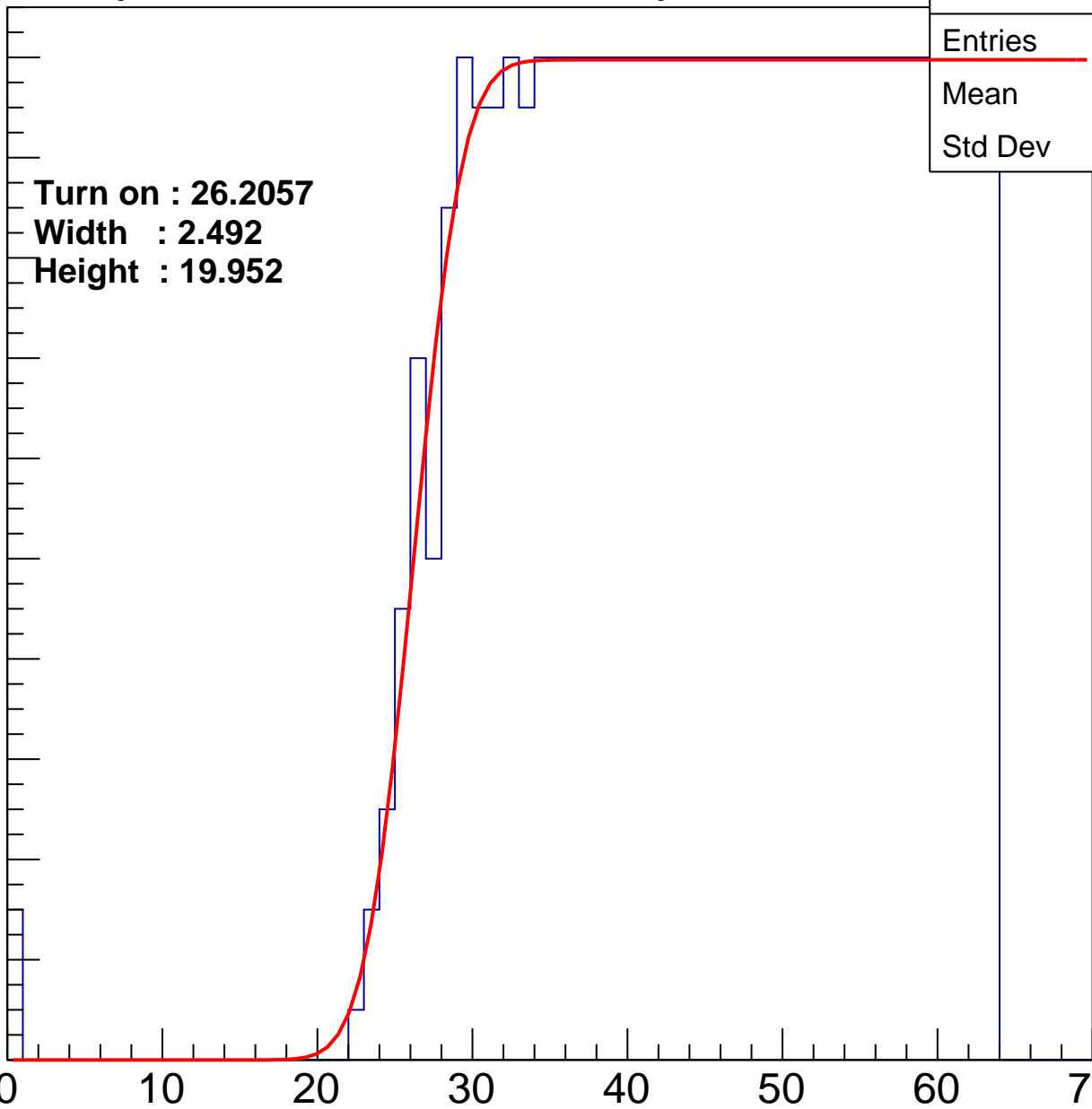
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2057
Width : 2.492
Height : 19.952

Entries	759
Mean	44.34
Std Dev	11.38

ampl



B1L001S, U24-ch65

calib_packv5_042523_0143.root, FC#2, port C2

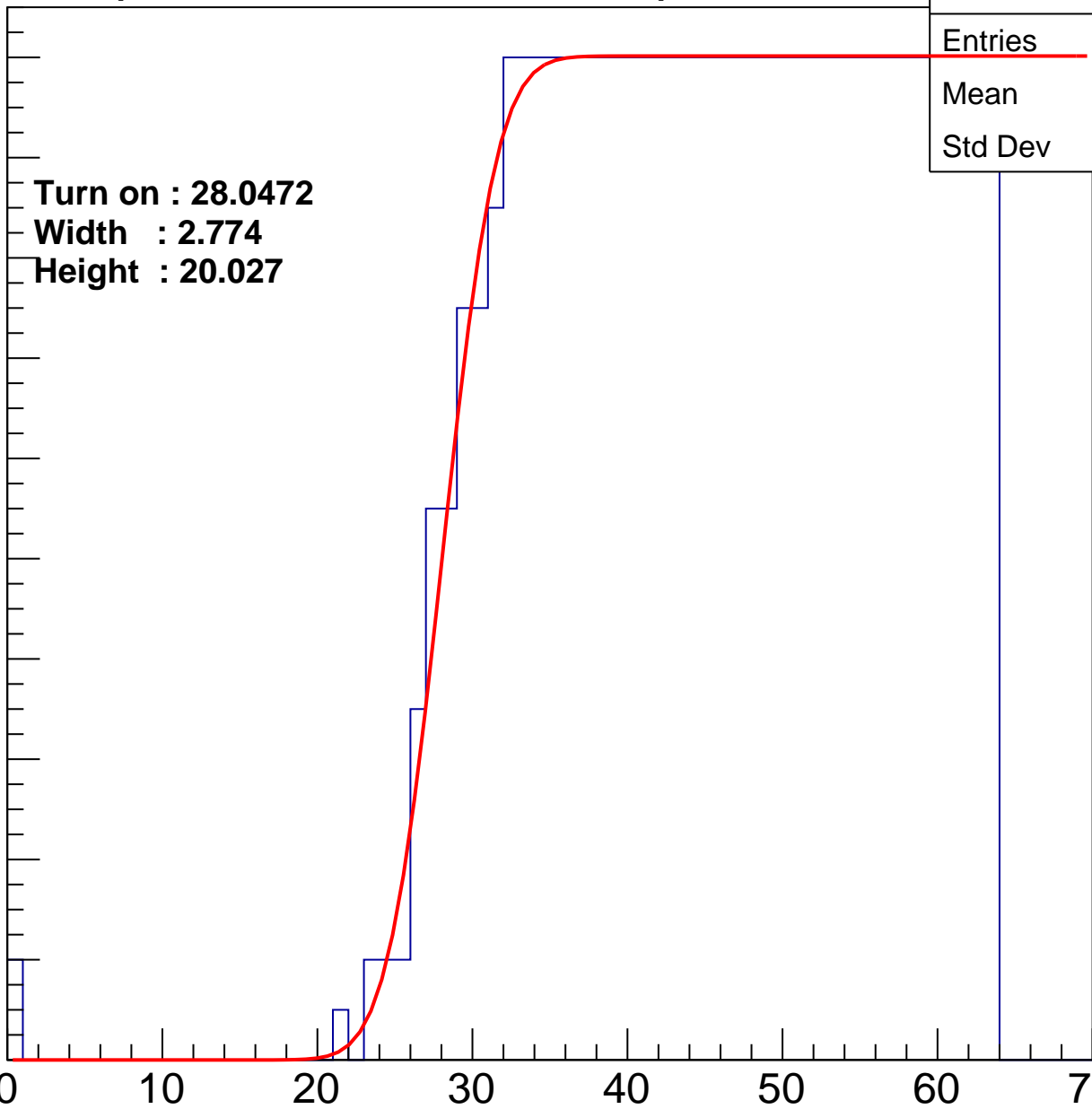
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0472
Width : 2.774
Height : 20.027

Entries	725
Mean	45.19
Std Dev	10.87

ampl



B1L001S, U24-ch66

calib_packv5_042523_0143.root, FC#2, port C2

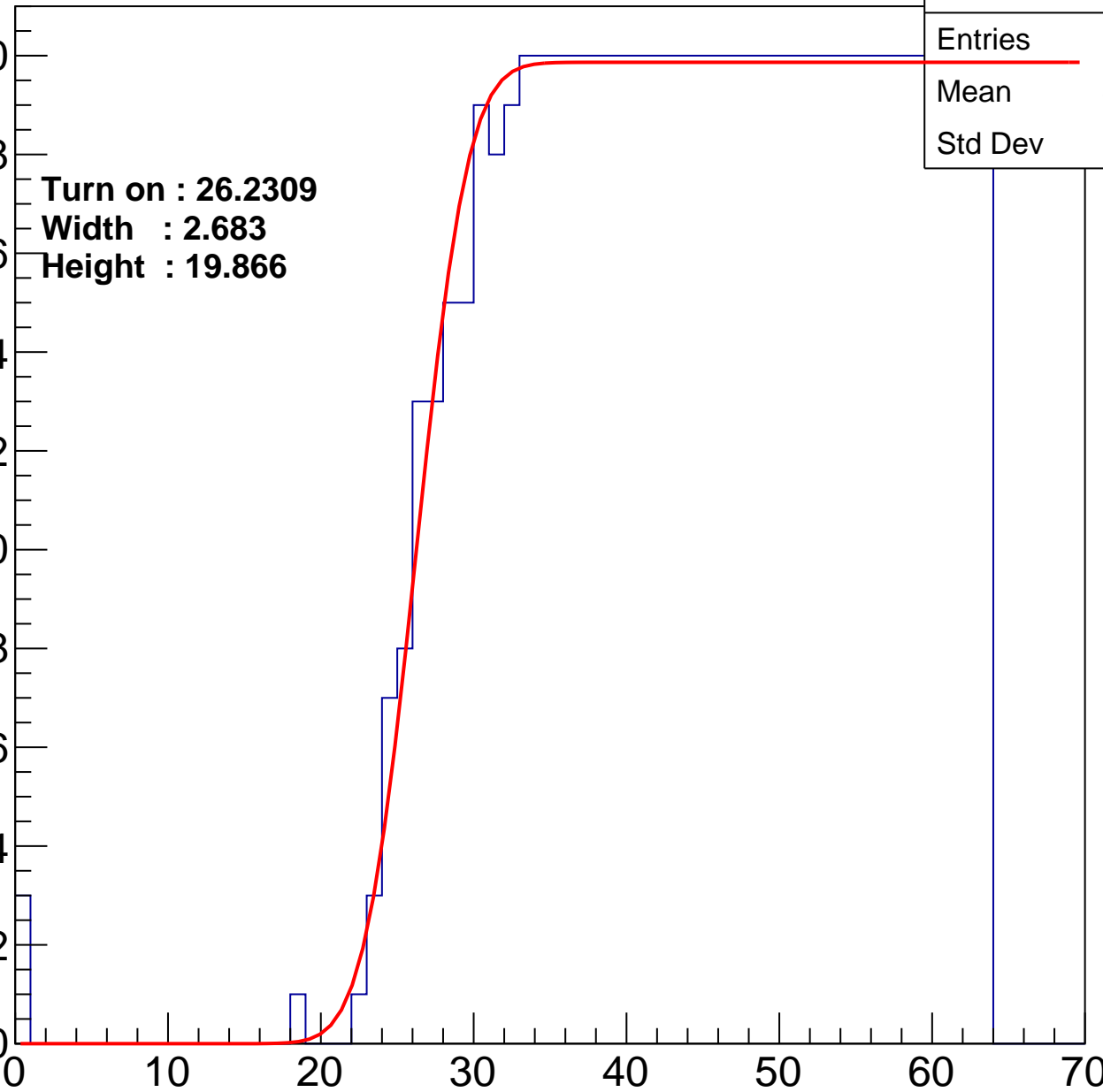
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2309
Width : 2.683
Height : 19.866

Entries	755
Mean	44.39
Std Dev	11.4

ampl



B1L001S, U24-ch67

calib_packv5_042523_0143.root, FC#2, port C2

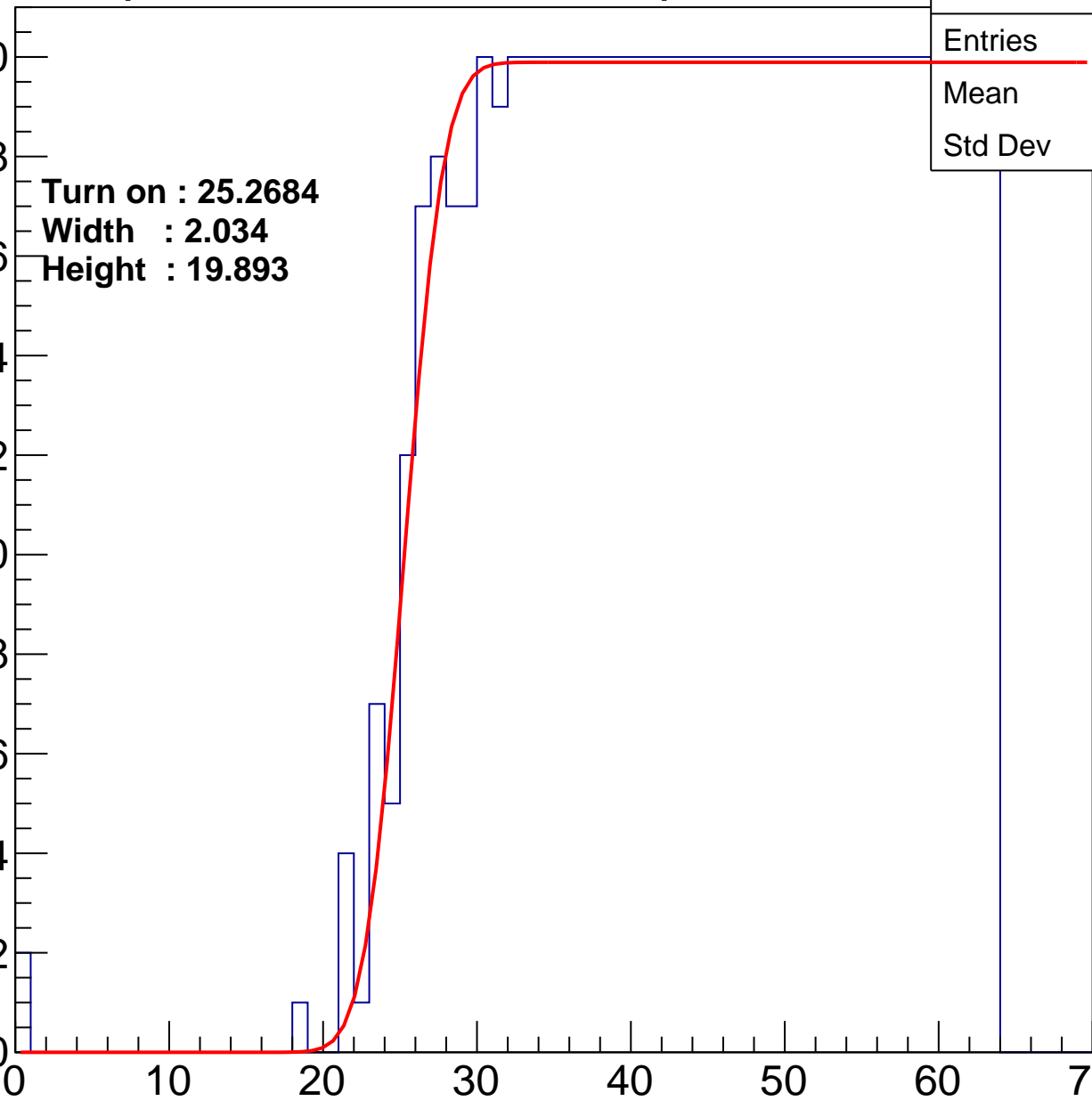
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2684
Width : 2.034
Height : 19.893

Entries	780
Mean	43.83
Std Dev	11.6

ampl



B1L001S, U24-ch68

calib_packv5_042523_0143.root, FC#2, port C2

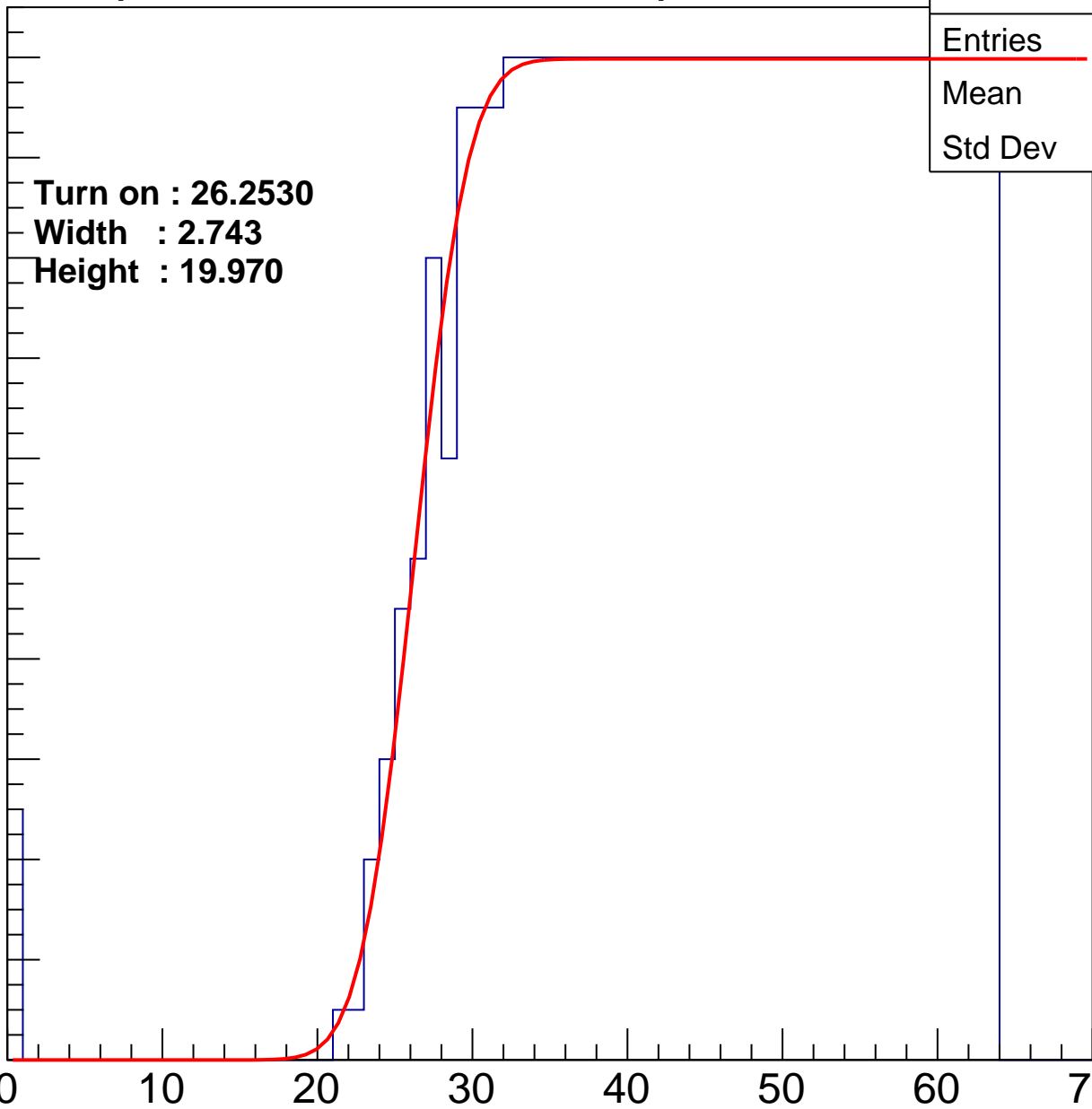
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2530
Width : 2.743
Height : 19.970

Entries	761
Mean	44.21
Std Dev	11.61

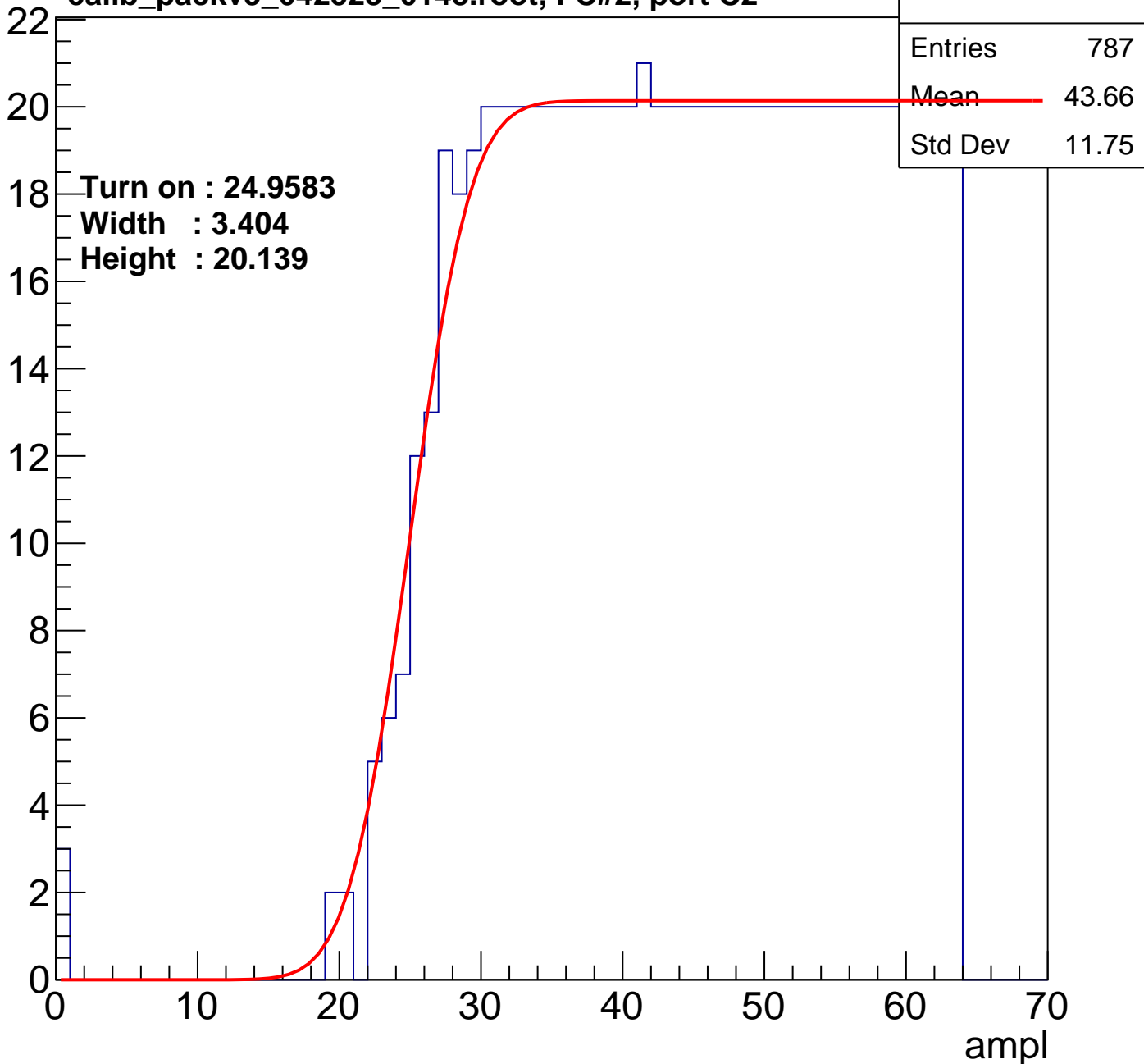
ampl



B1L001S, U24-ch69

calib_packv5_042523_0143.root, FC#2, port C2

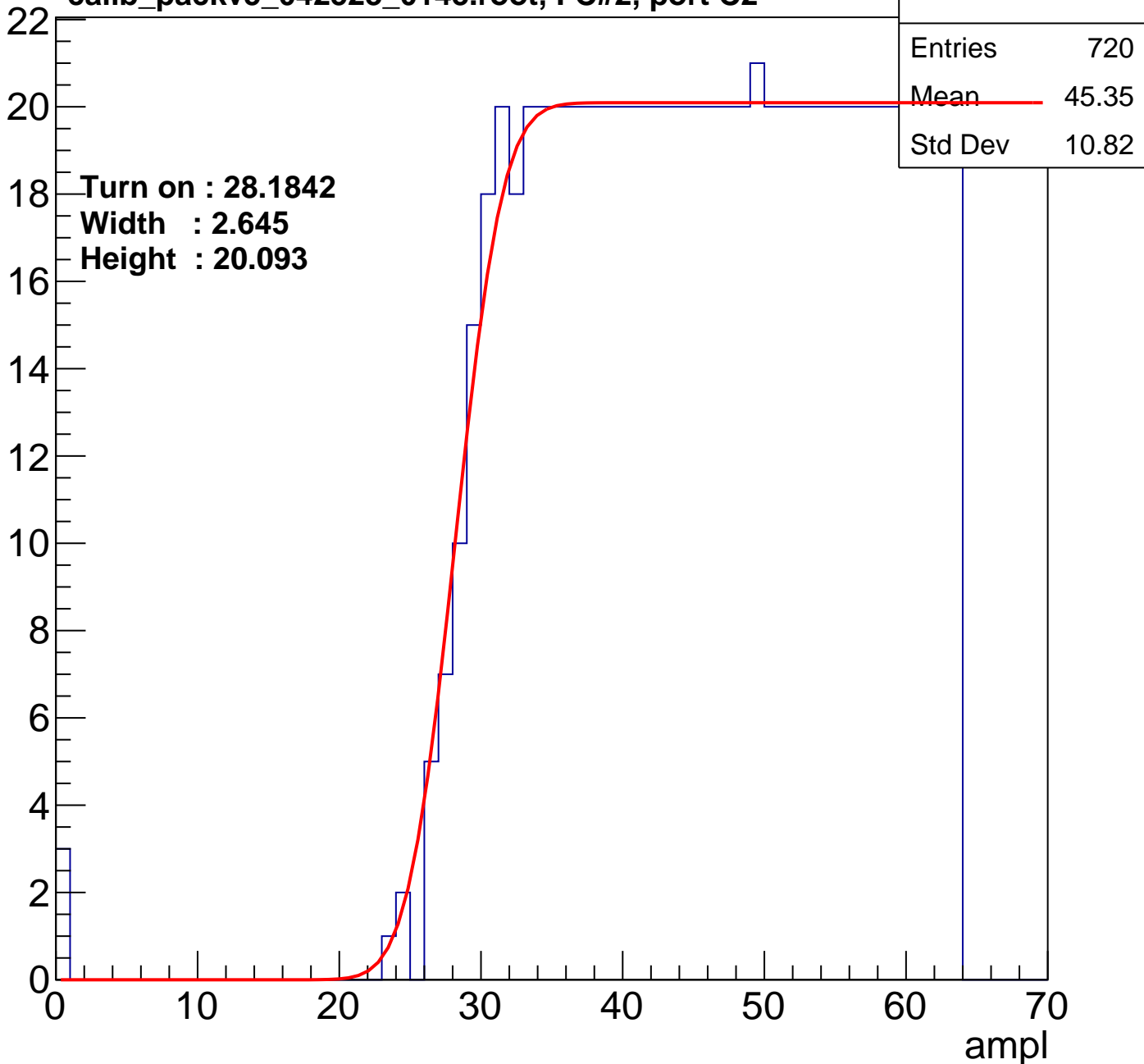
Entry



B1L001S, U24-ch70

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch71

calib_packv5_042523_0143.root, FC#2, port C2

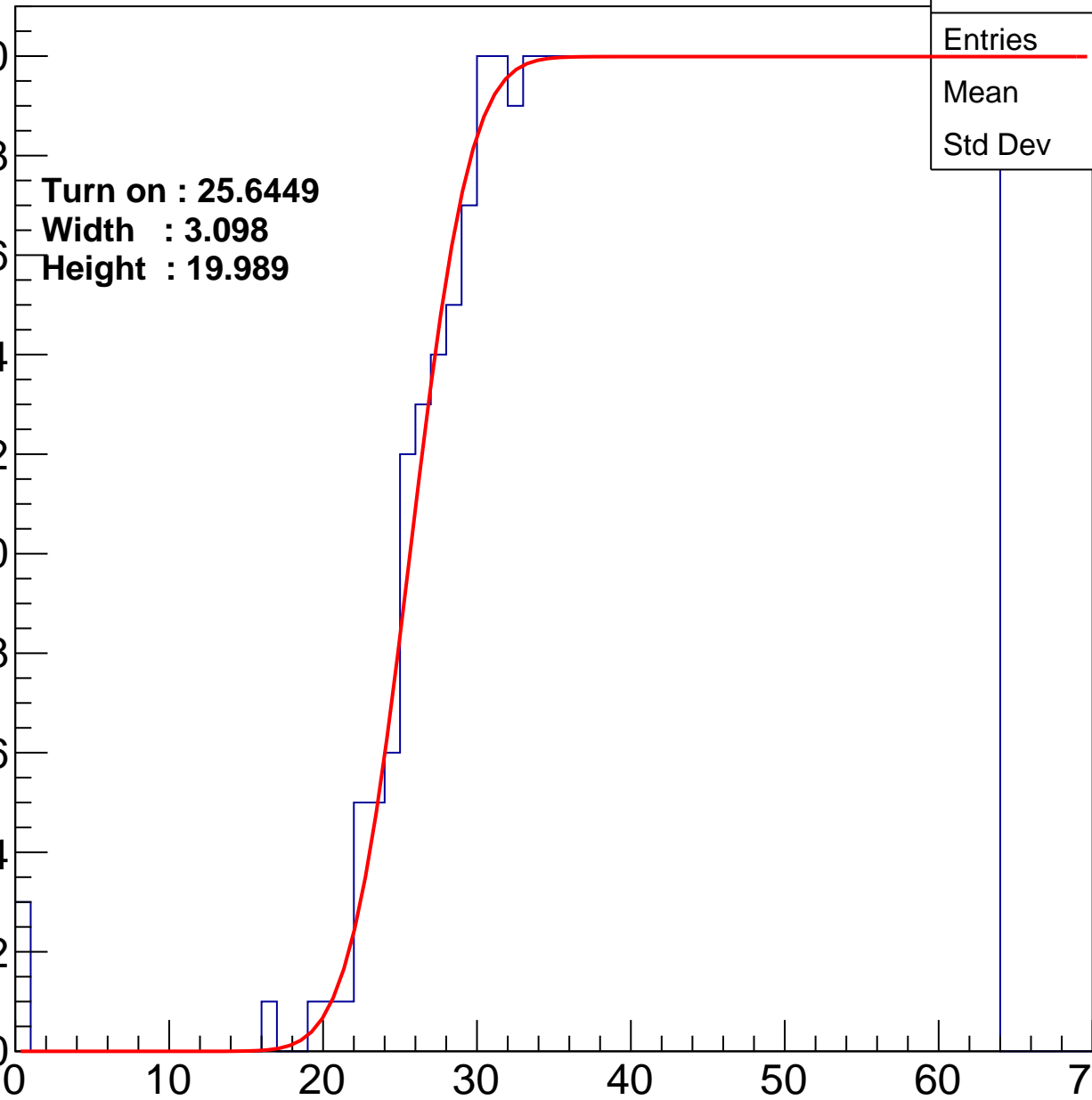
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6449
Width : 3.098
Height : 19.989

Entries	773
Mean	43.94
Std Dev	11.66

ampl



B1L001S, U24-ch72

calib_packv5_042523_0143.root, FC#2, port C2

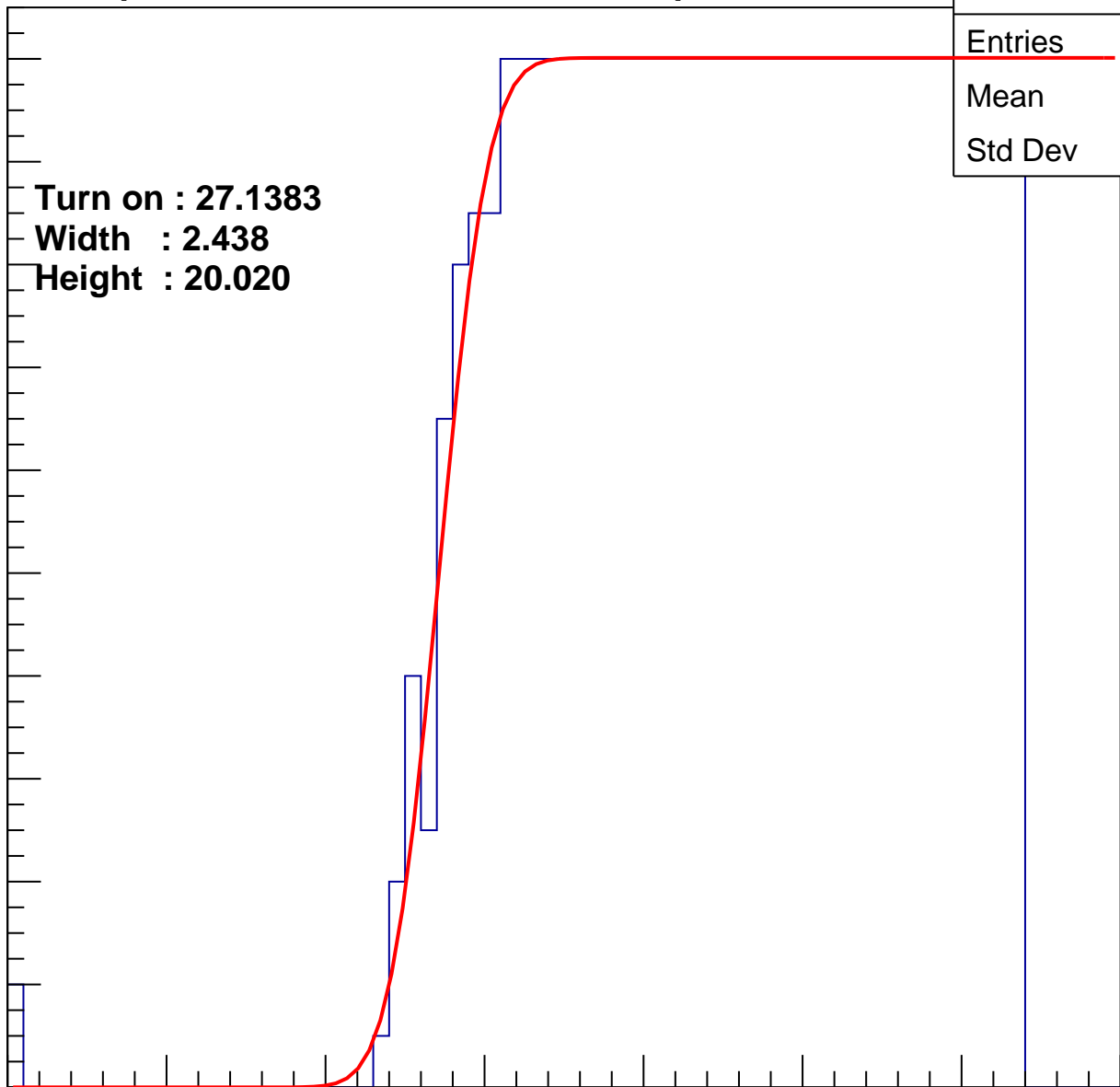
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1383
Width : 2.438
Height : 20.020

Entries	743
Mean	44.78
Std Dev	11.05

ampl



B1L001S, U24-ch73

calib_packv5_042523_0143.root, FC#2, port C2

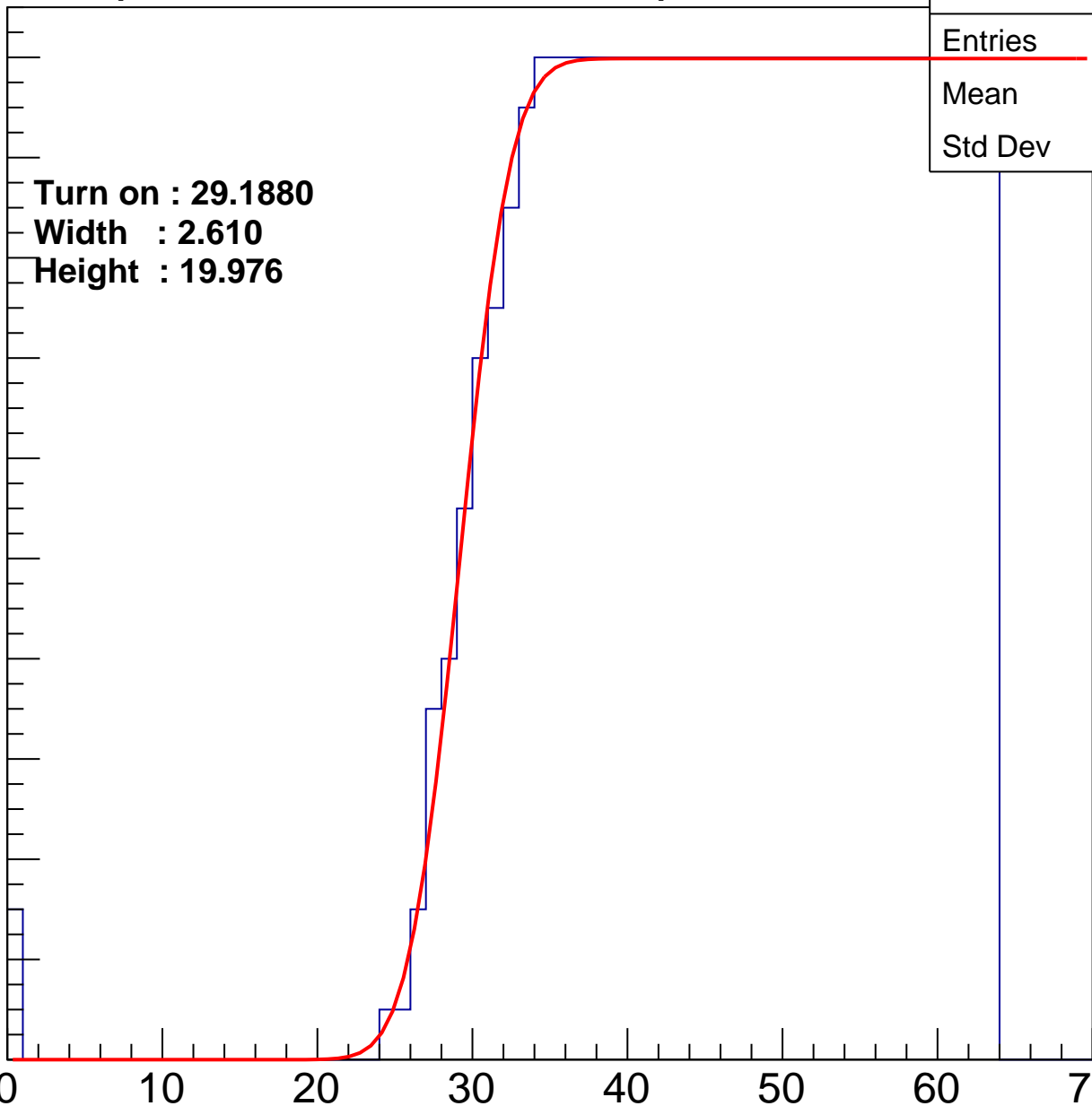
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.1880
Width : 2.610
Height : 19.976

Entries	699
Mean	45.8
Std Dev	10.63

ampl



B1L001S, U24-ch74

calib_packv5_042523_0143.root, FC#2, port C2

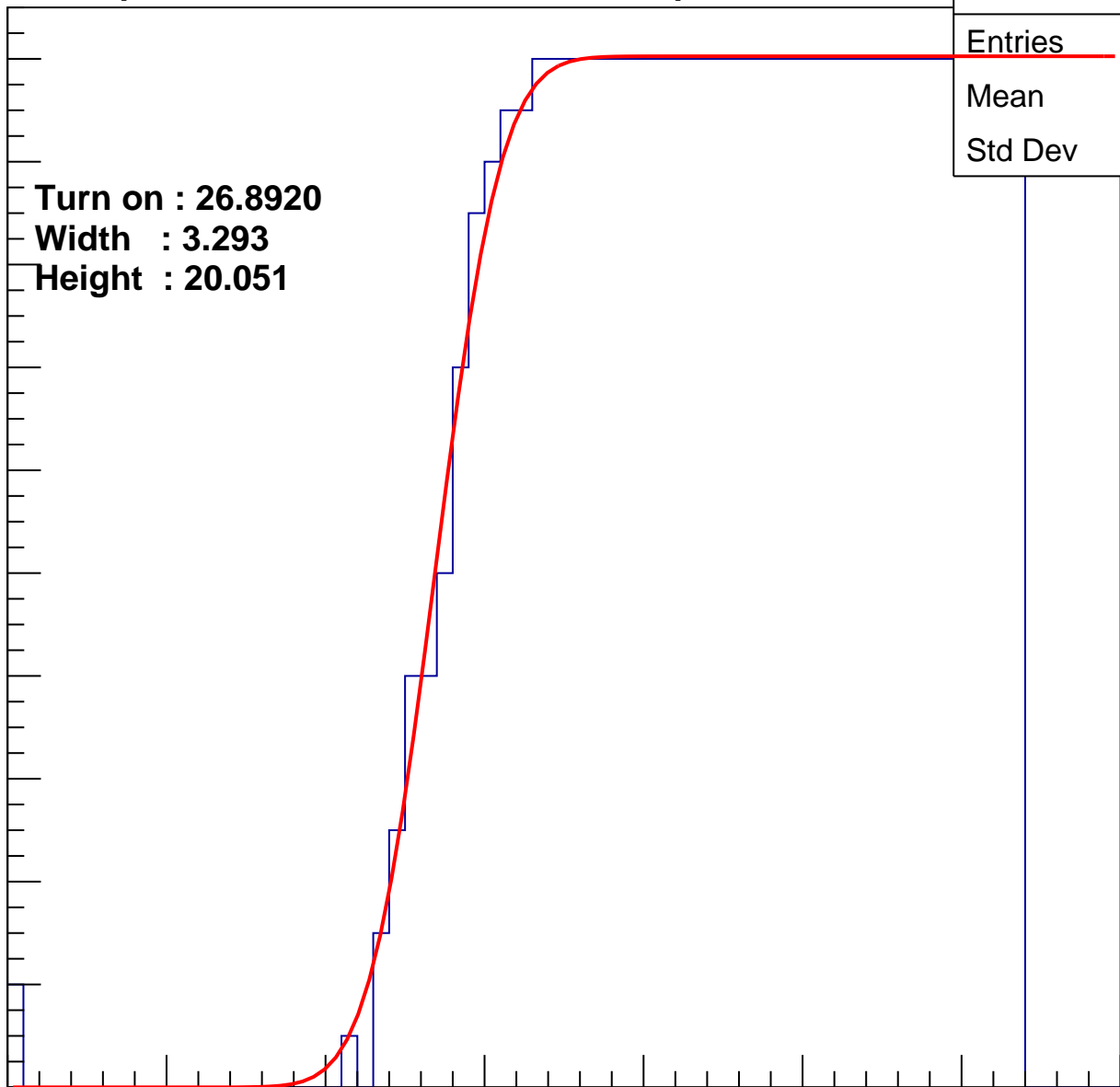
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8920
Width : 3.293
Height : 20.051

Entries	744
Mean	44.72
Std Dev	11.13

ampl



B1L001S, U24-ch75

calib_packv5_042523_0143.root, FC#2, port C2

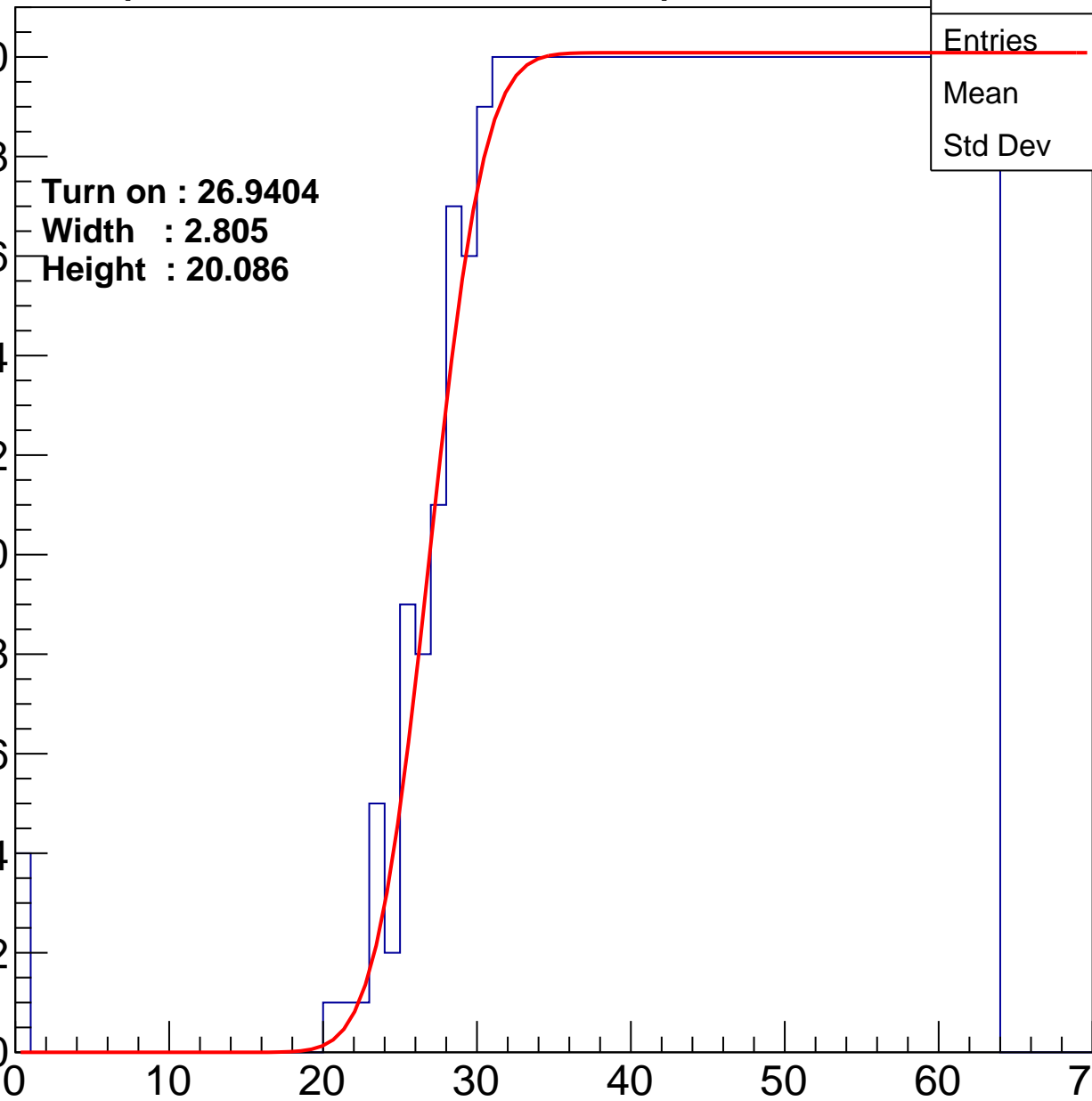
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9404
Width : 2.805
Height : 20.086

Entries	754
Mean	44.41
Std Dev	11.44

ampl



B1L001S, U24-ch76

calib_packv5_042523_0143.root, FC#2, port C2

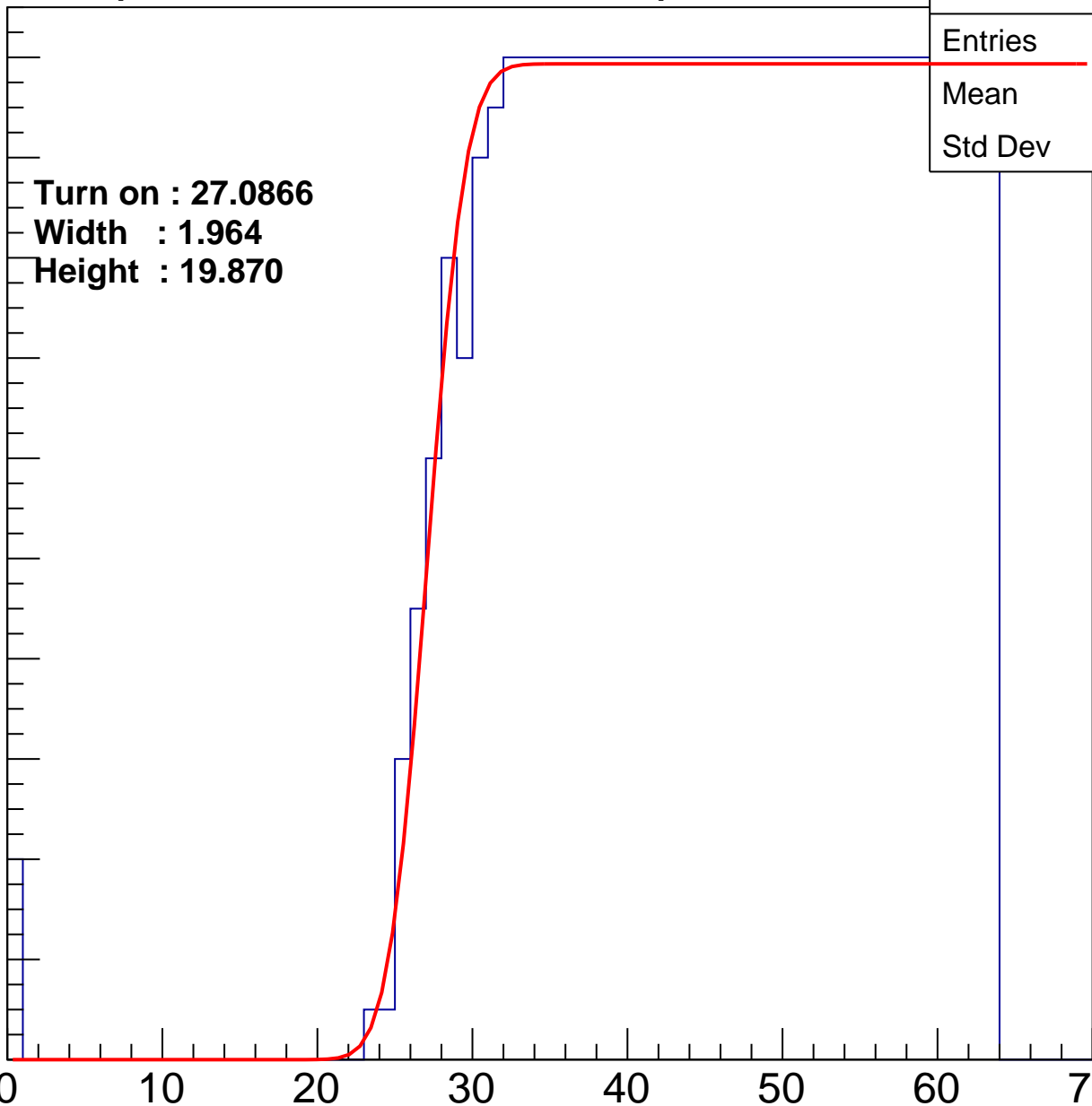
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0866
Width : 1.964
Height : 19.870

Entries	740
Mean	44.78
Std Dev	11.21

ampl



B1L001S, U24-ch77

calib_packv5_042523_0143.root, FC#2, port C2

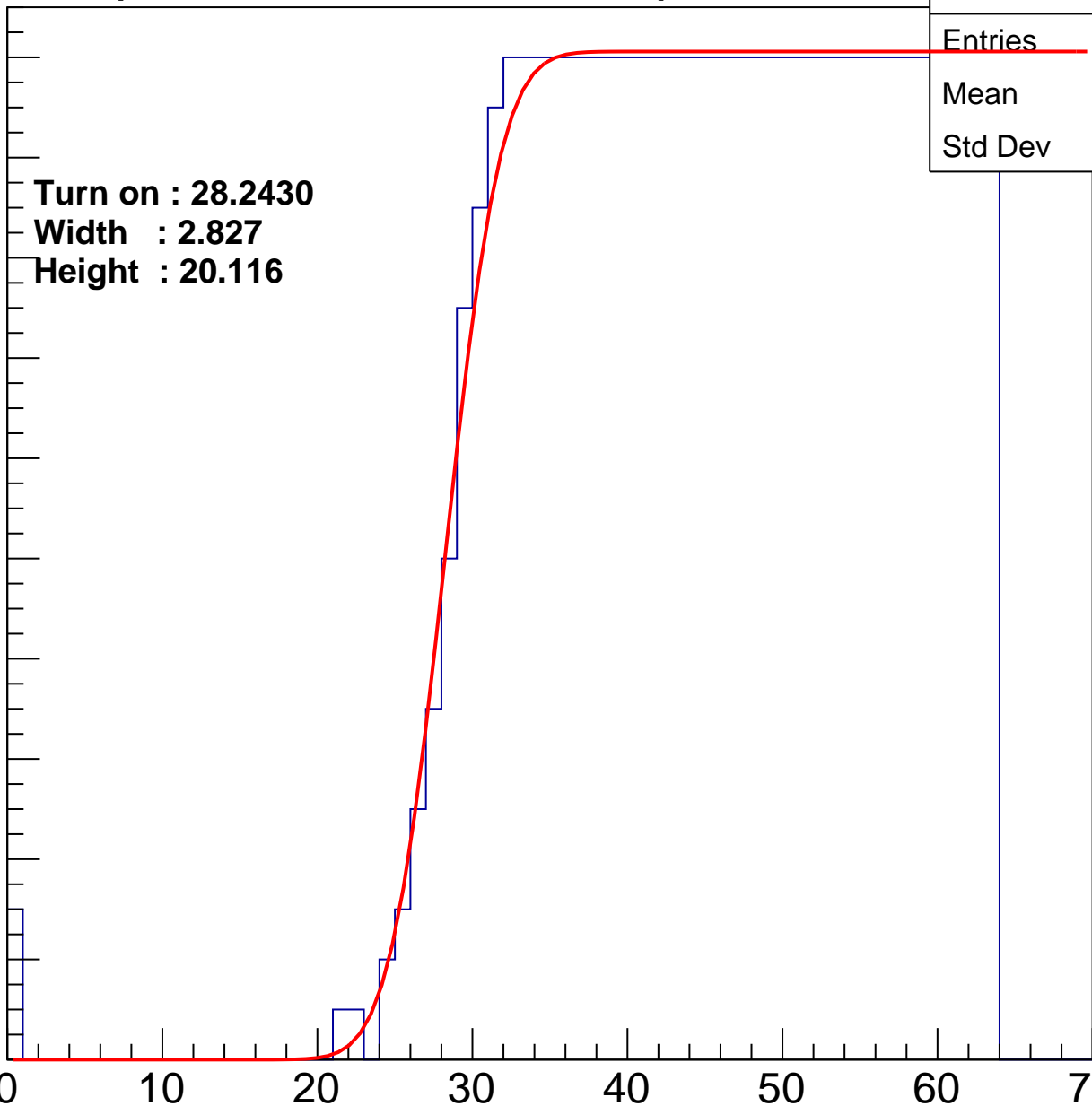
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.2430
Width : 2.827
Height : 20.116

Entries	723
Mean	45.23
Std Dev	10.92

ampl



B1L001S, U24-ch78

calib_packv5_042523_0143.root, FC#2, port C2

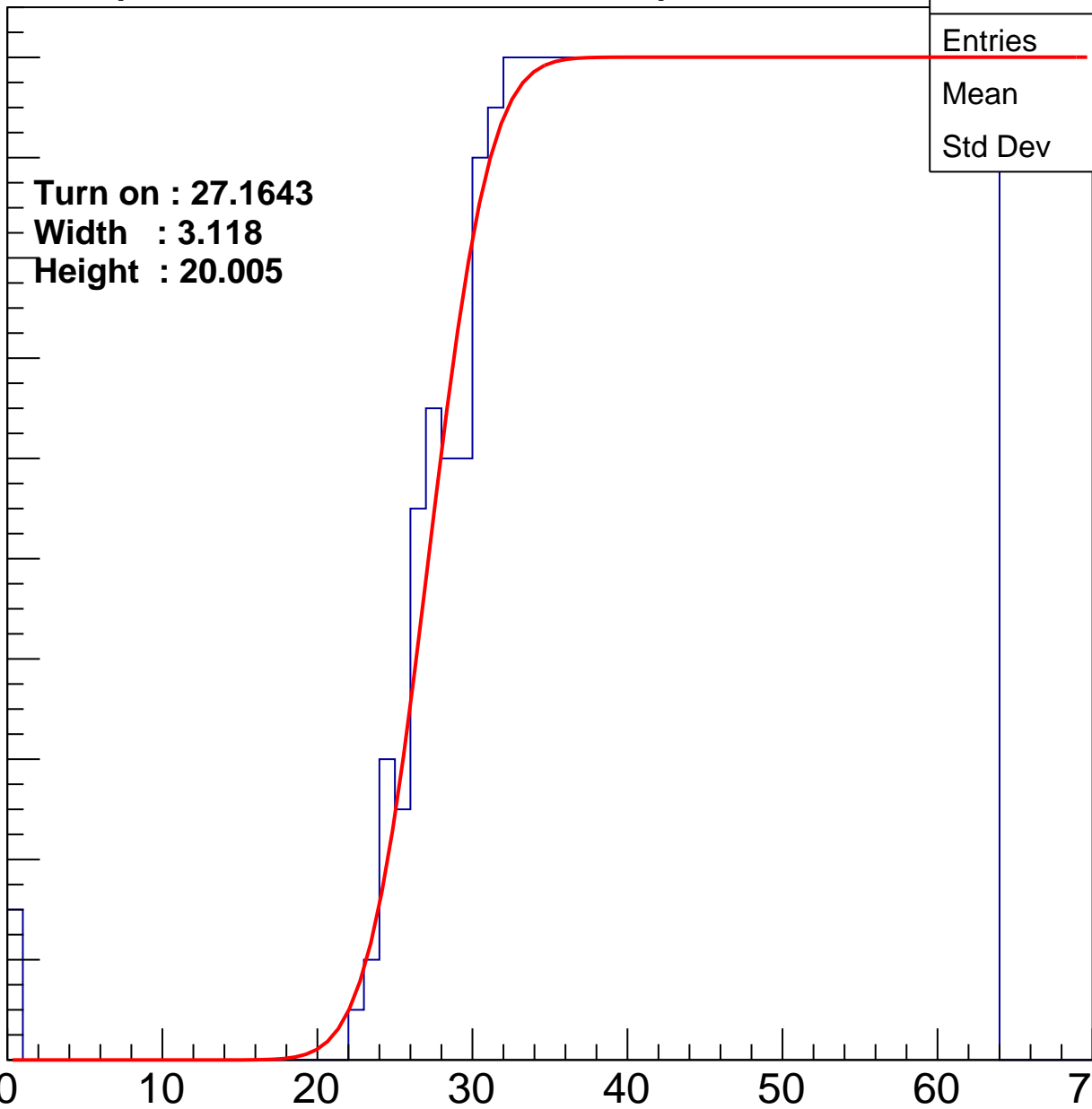
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1643
Width : 3.118
Height : 20.005

Entries	742
Mean	44.73
Std Dev	11.21

ampl



B1L001S, U24-ch79

calib_packv5_042523_0143.root, FC#2, port C2

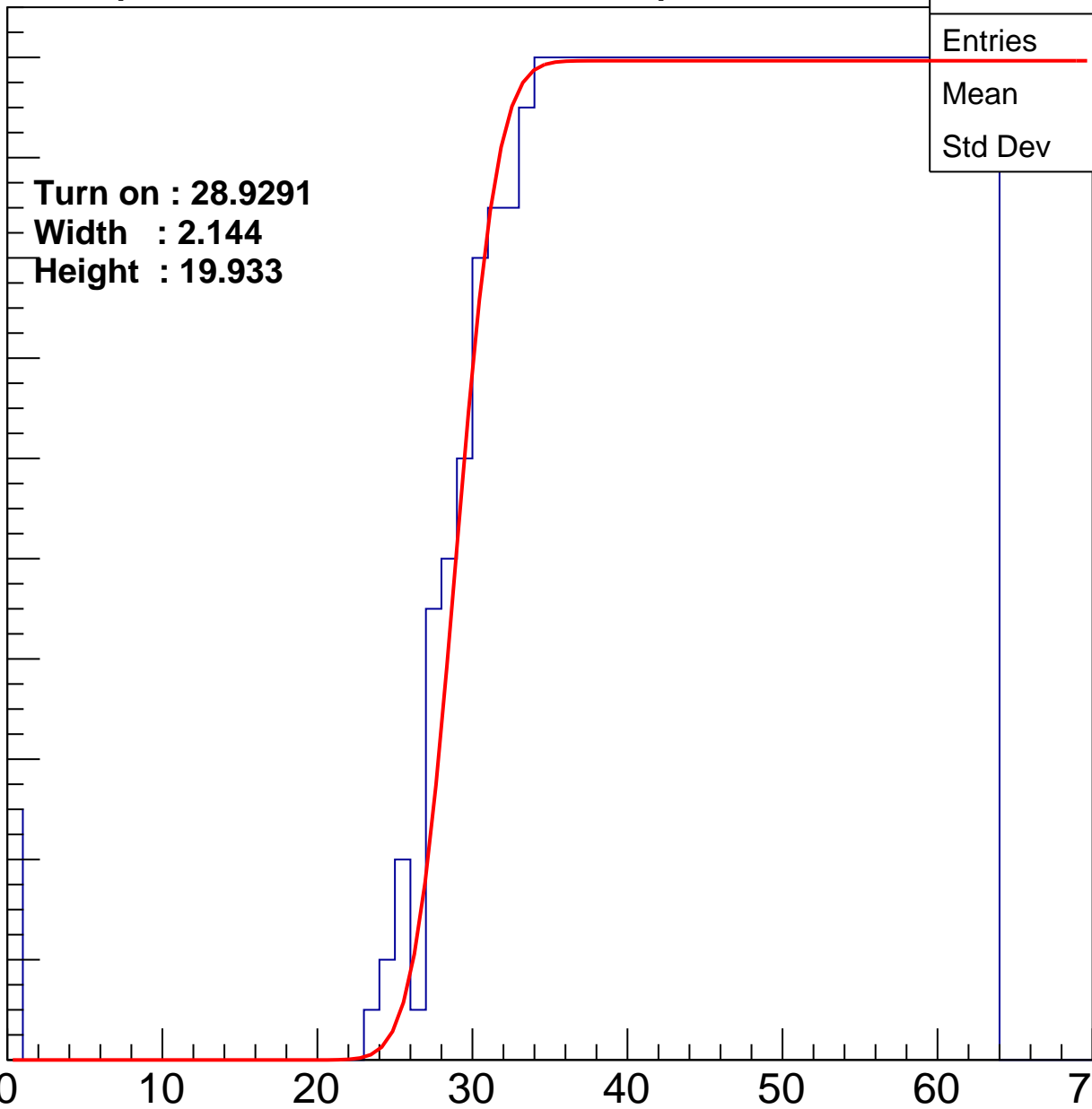
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.9291
Width : 2.144
Height : 19.933

Entries	713
Mean	45.37
Std Dev	11.05

ampl



B1L001S, U24-ch80

calib_packv5_042523_0143.root, FC#2, port C2

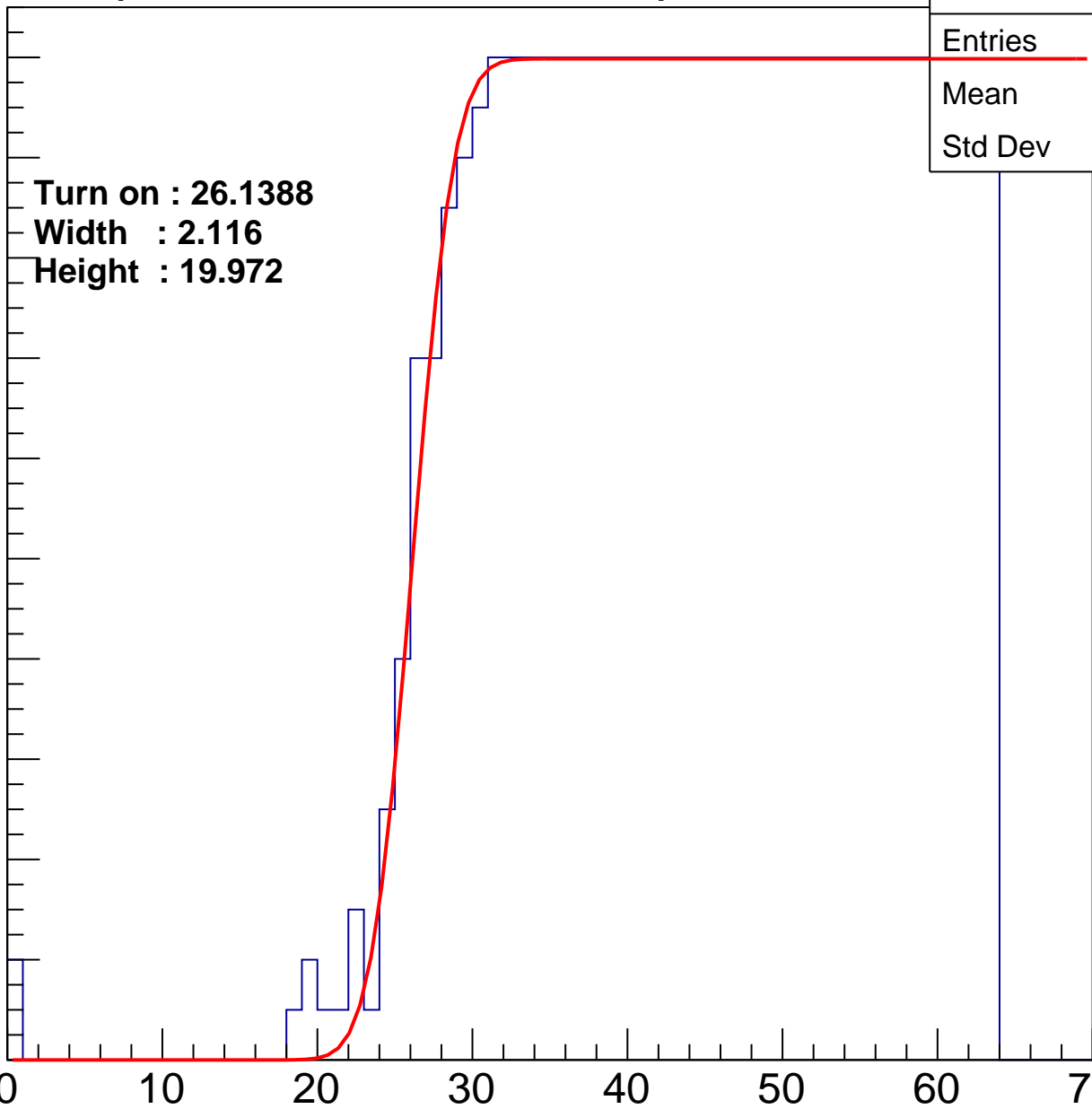
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1388
Width : 2.116
Height : 19.972

Entries	766
Mean	44.17
Std Dev	11.43

ampl



B1L001S, U24-ch81

calib_packv5_042523_0143.root, FC#2, port C2

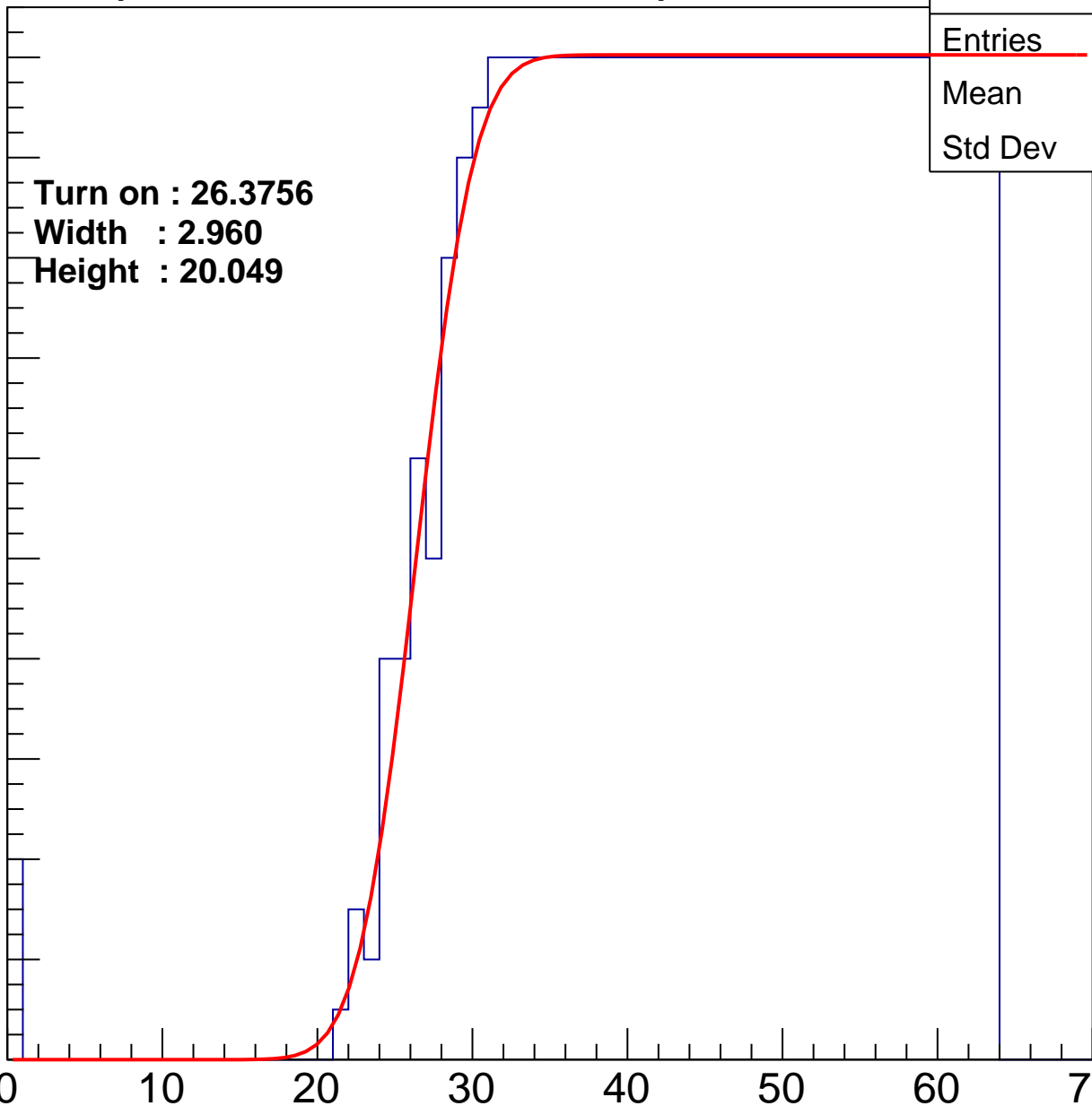
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3756
Width : 2.960
Height : 20.049

Entries	761
Mean	44.24
Std Dev	11.52

ampl



B1L001S, U24-ch82

calib_packv5_042523_0143.root, FC#2, port C2

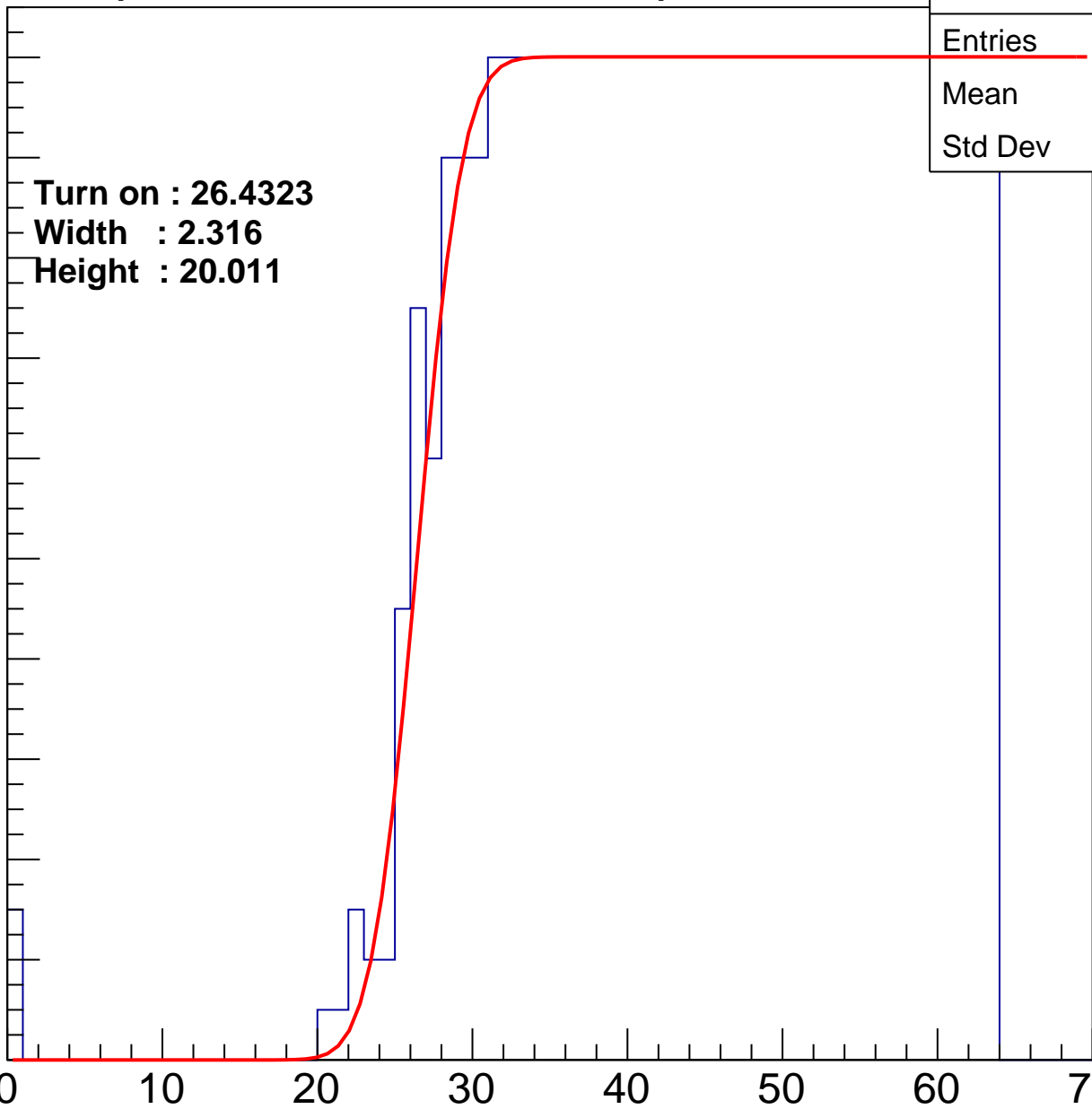
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4323
Width : 2.316
Height : 20.011

Entries	762
Mean	44.26
Std Dev	11.43

ampl



B1L001S, U24-ch83

calib_packv5_042523_0143.root, FC#2, port C2

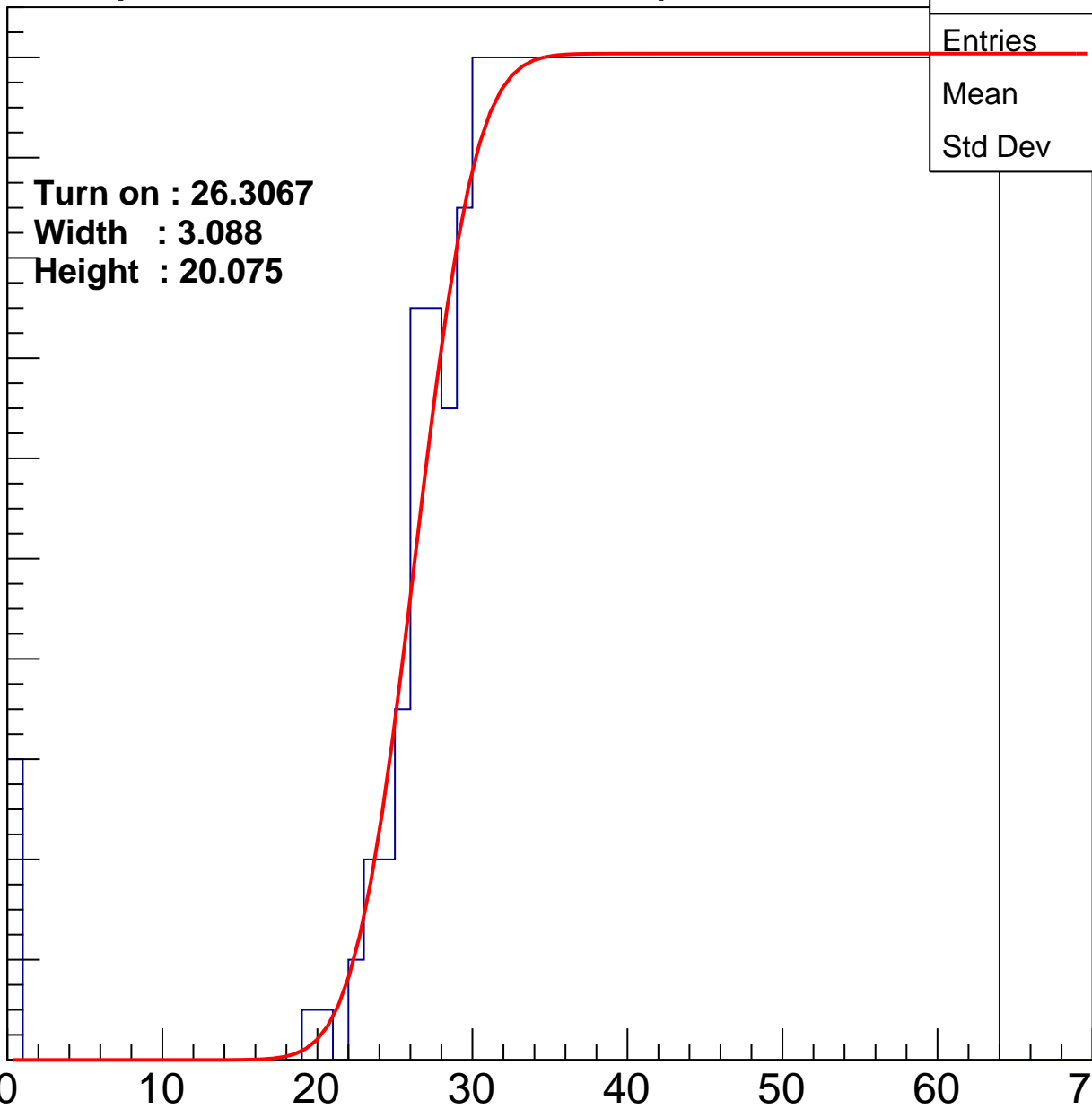
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3067
Width : 3.088
Height : 20.075

Entries	765
Mean	44.08
Std Dev	11.76

ampl



B1L001S, U24-ch84

calib_packv5_042523_0143.root, FC#2, port C2

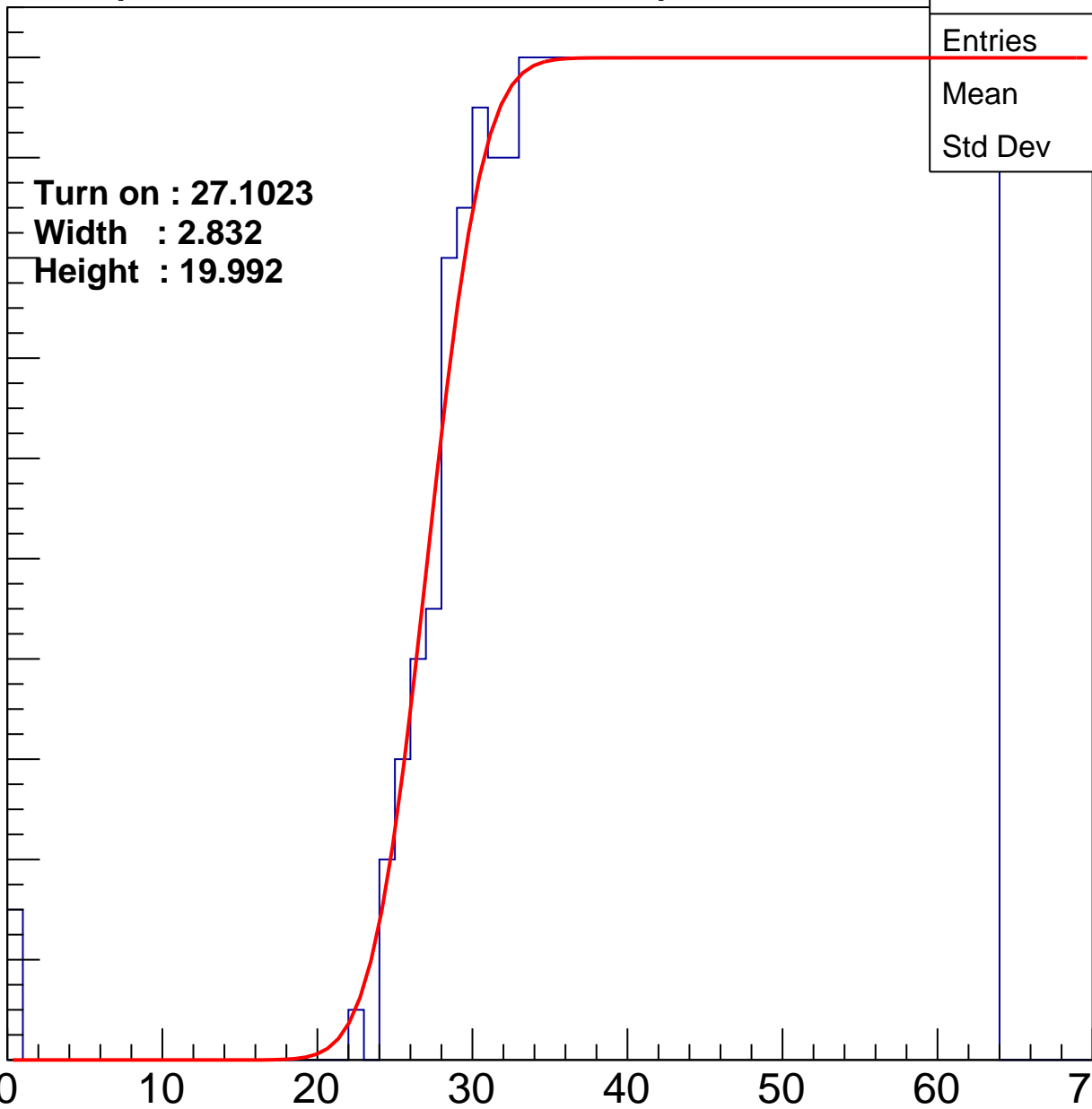
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1023
Width : 2.832
Height : 19.992

Entries	739
Mean	44.82
Std Dev	11.13

ampl



B1L001S, U24-ch85

calib_packv5_042523_0143.root, FC#2, port C2

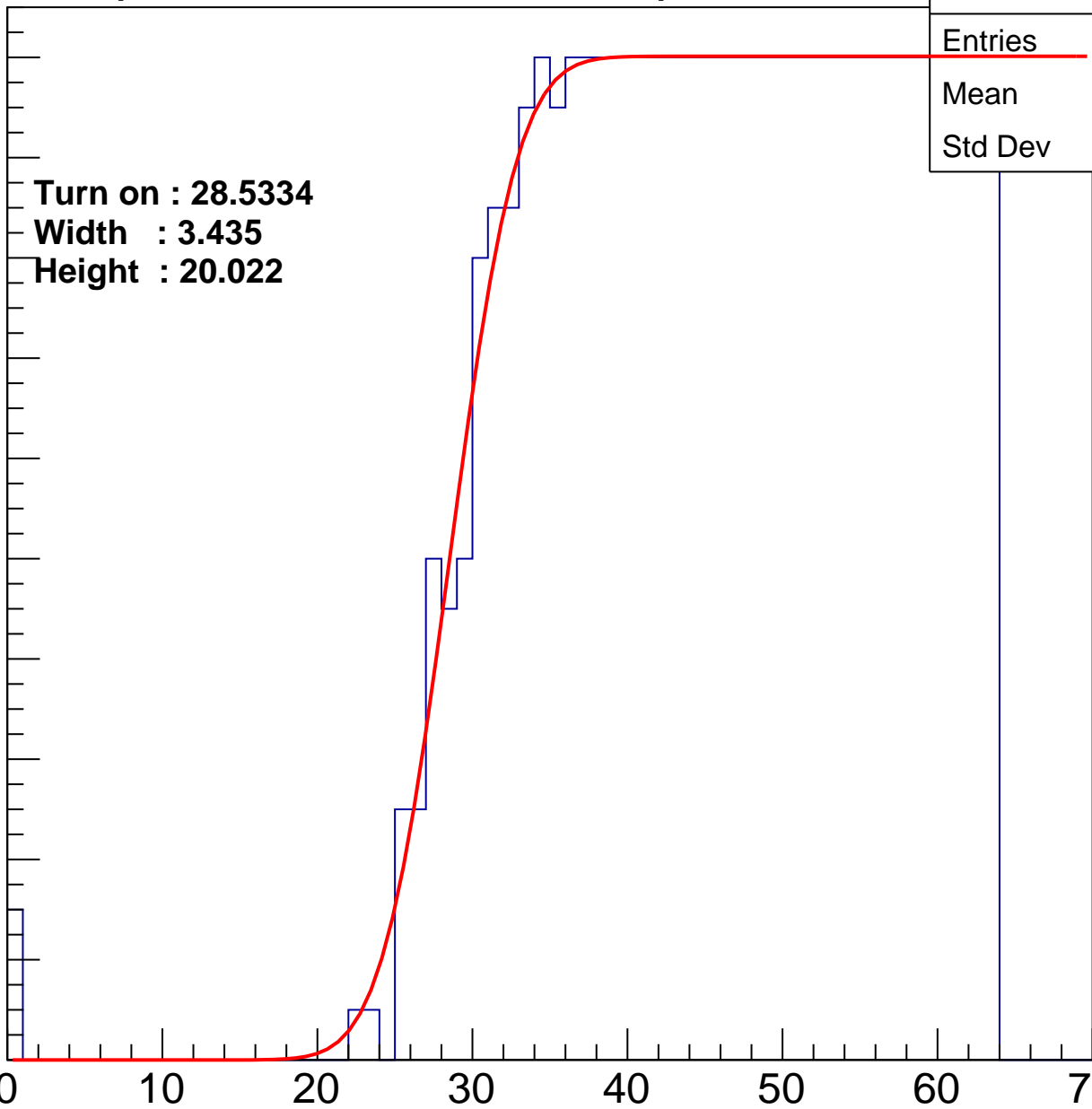
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.5334
Width : 3.435
Height : 20.022

Entries	712
Mean	45.44
Std Dev	10.85

ampl



B1L001S, U24-ch86

calib_packv5_042523_0143.root, FC#2, port C2

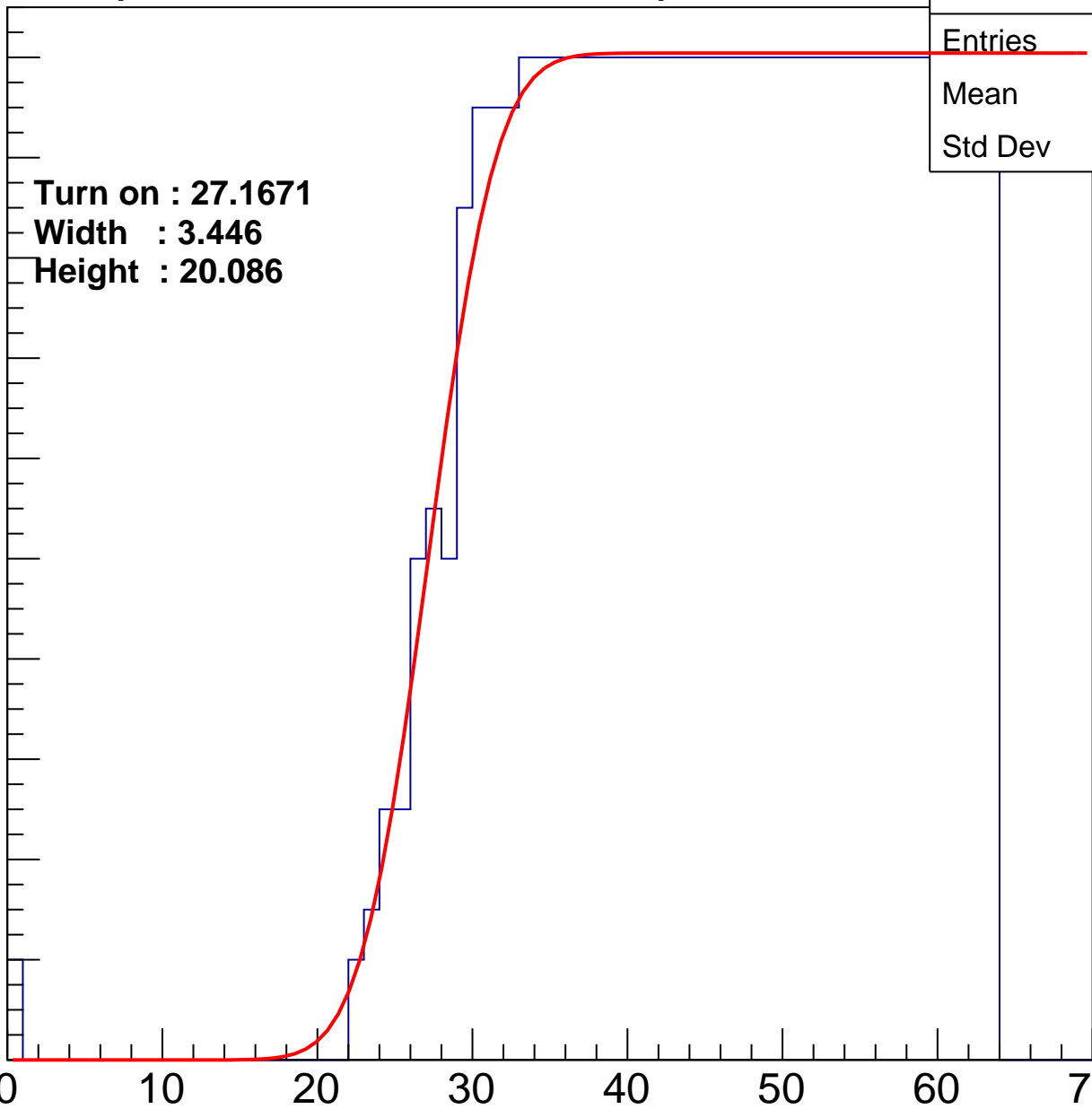
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1671
Width : 3.446
Height : 20.086

Entries	742
Mean	44.76
Std Dev	11.11

ampl



B1L001S, U24-ch87

calib_packv5_042523_0143.root, FC#2, port C2

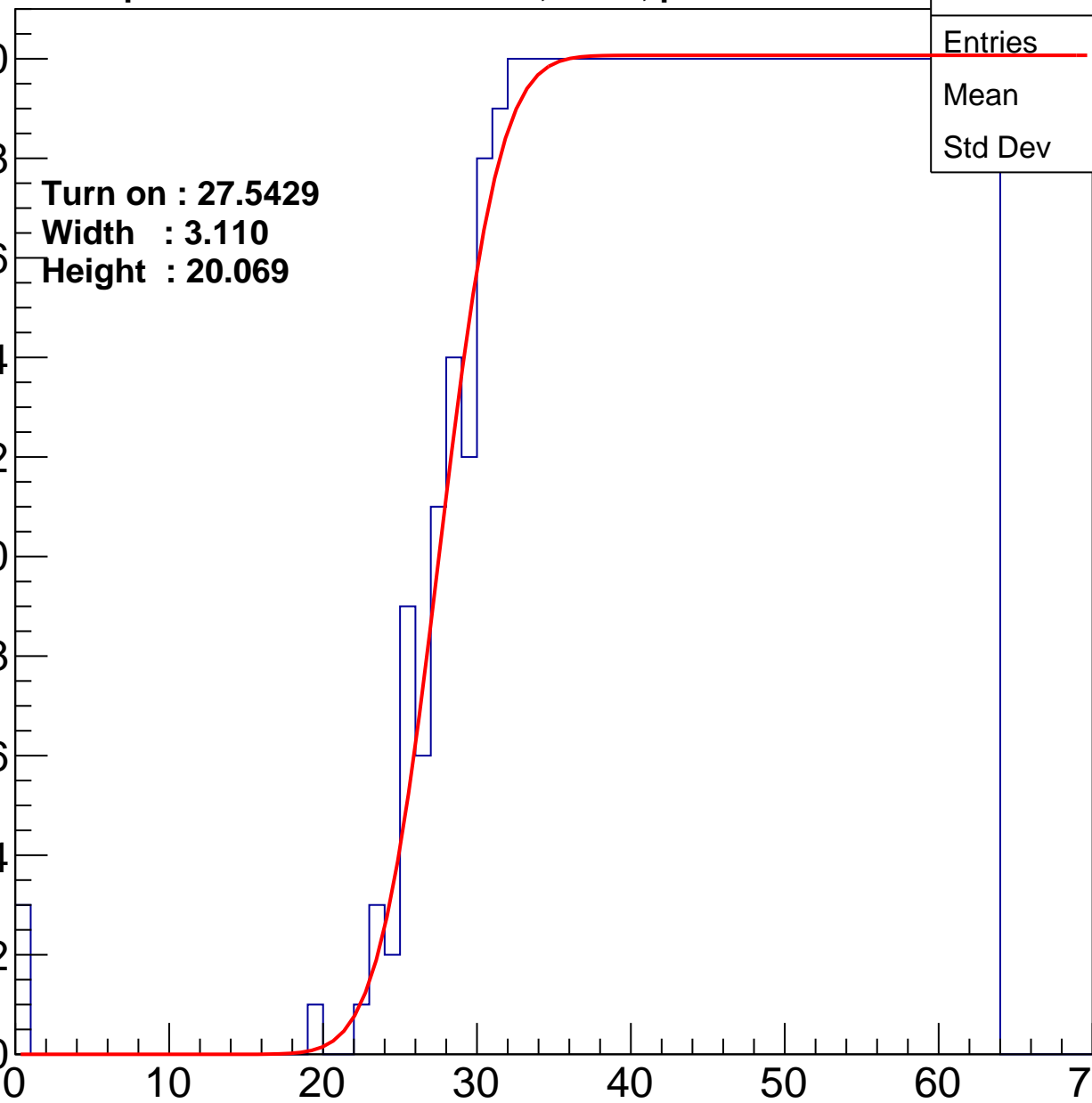
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5429
Width : 3.110
Height : 20.069

Entries	739
Mean	44.8
Std Dev	11.18

ampl



B1L001S, U24-ch88

calib_packv5_042523_0143.root, FC#2, port C2

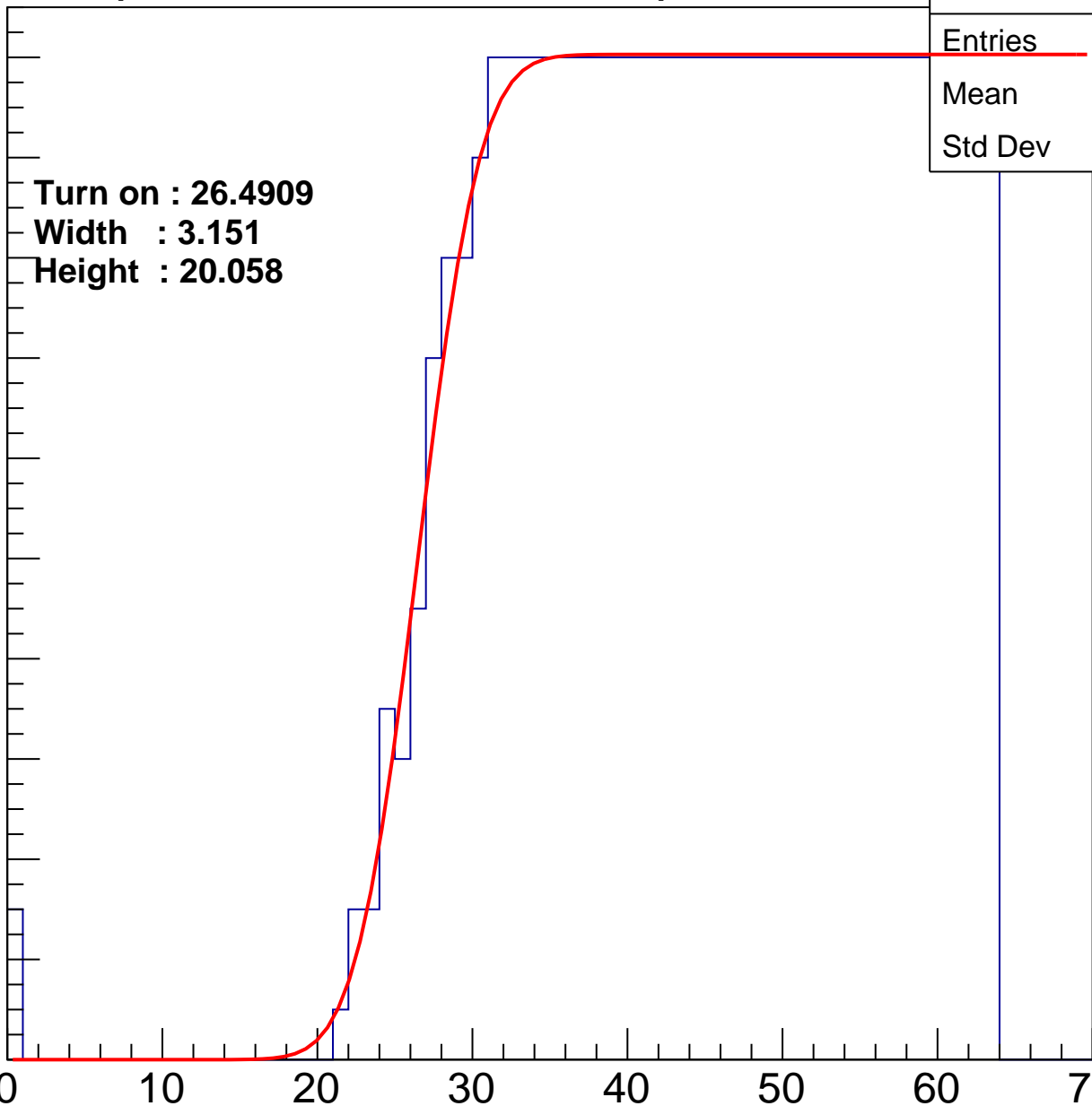
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4909
Width : 3.151
Height : 20.058

Entries	756
Mean	44.39
Std Dev	11.38

ampl



B1L001S, U24-ch89

calib_packv5_042523_0143.root, FC#2, port C2

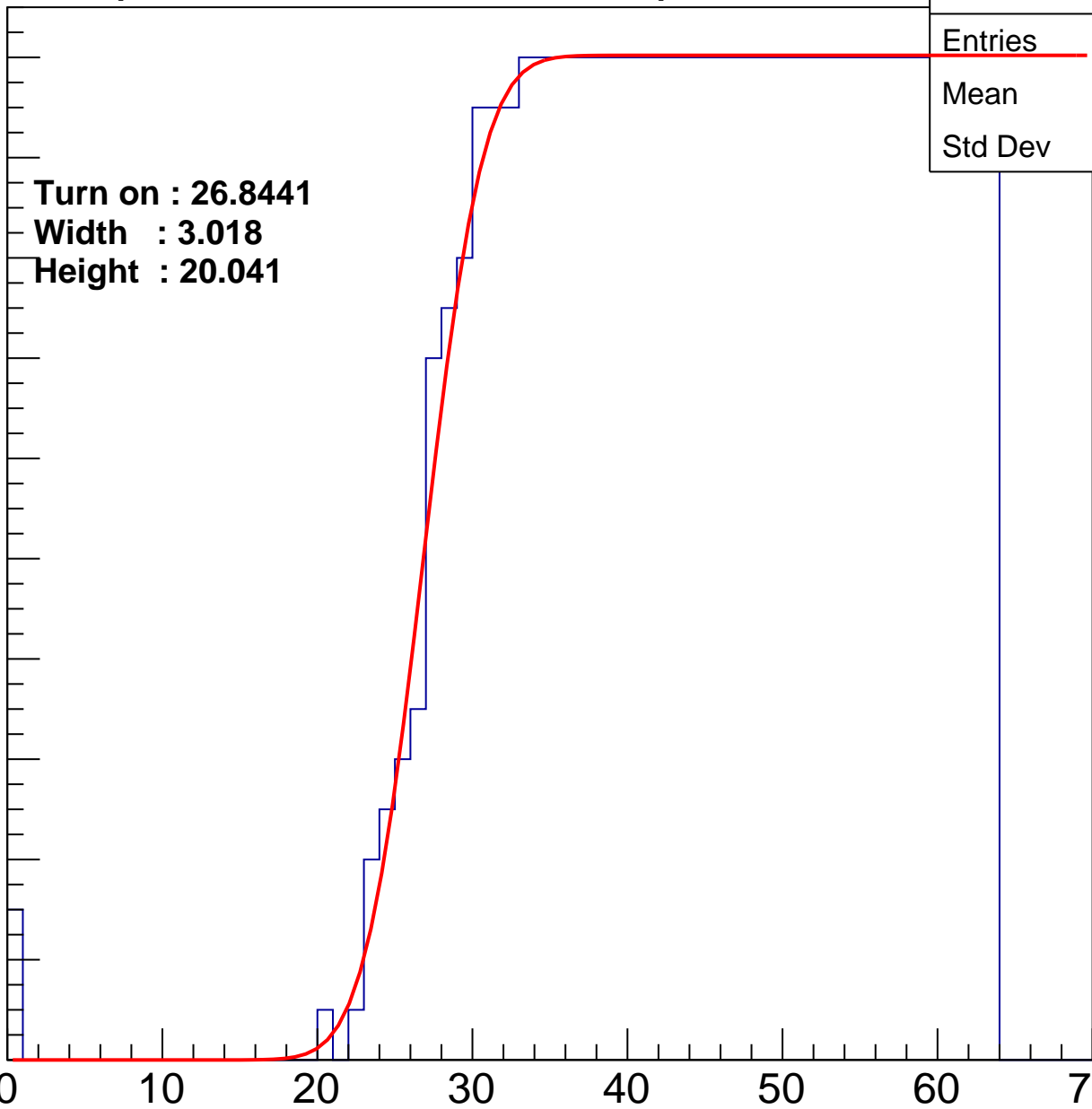
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8441
Width : 3.018
Height : 20.041

Entries	749
Mean	44.56
Std Dev	11.29

ampl



B1L001S, U24-ch90

calib_packv5_042523_0143.root, FC#2, port C2

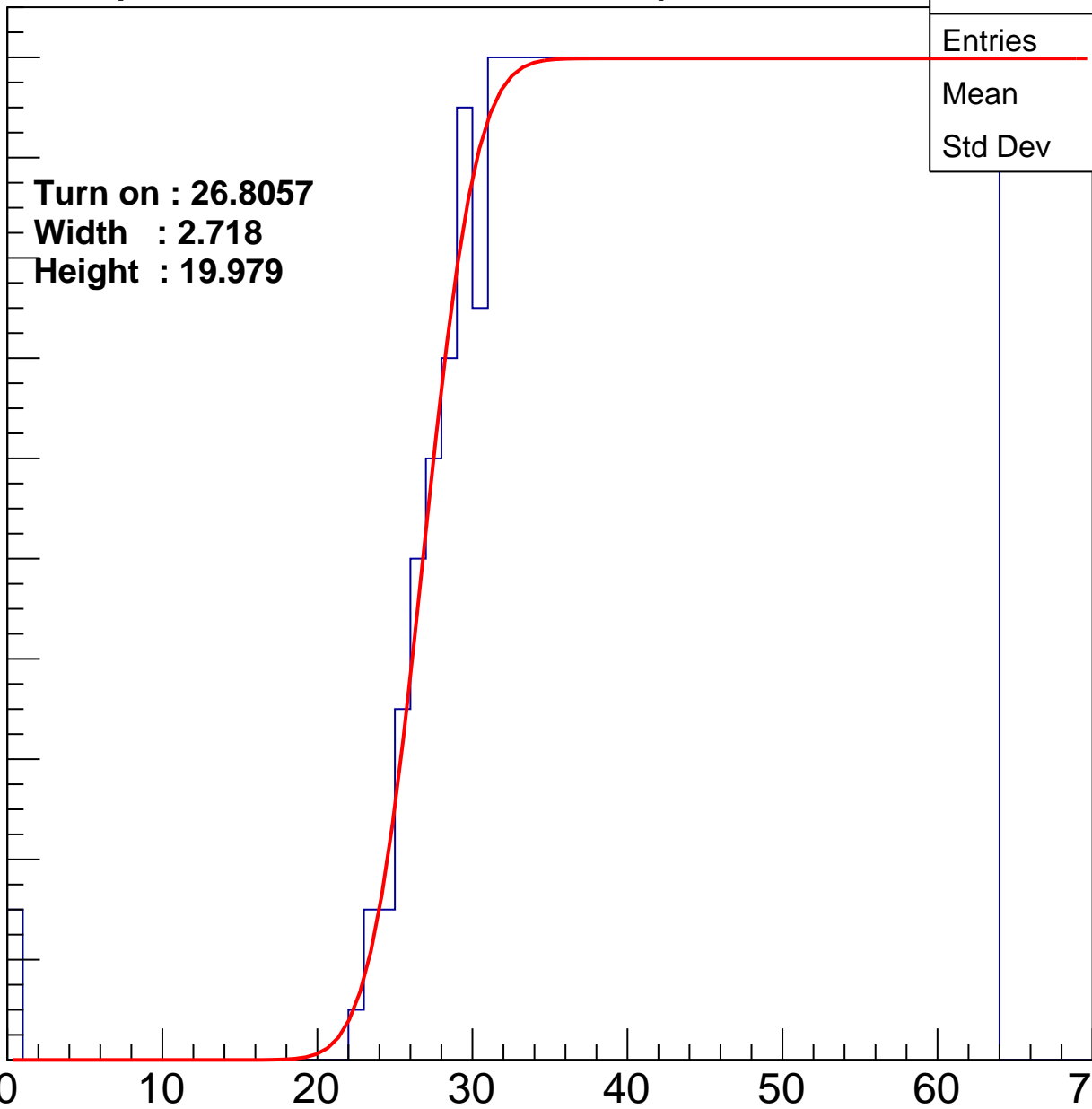
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8057
Width : 2.718
Height : 19.979

Entries	747
Mean	44.63
Std Dev	11.24

ampl



B1L001S, U24-ch91

calib_packv5_042523_0143.root, FC#2, port C2

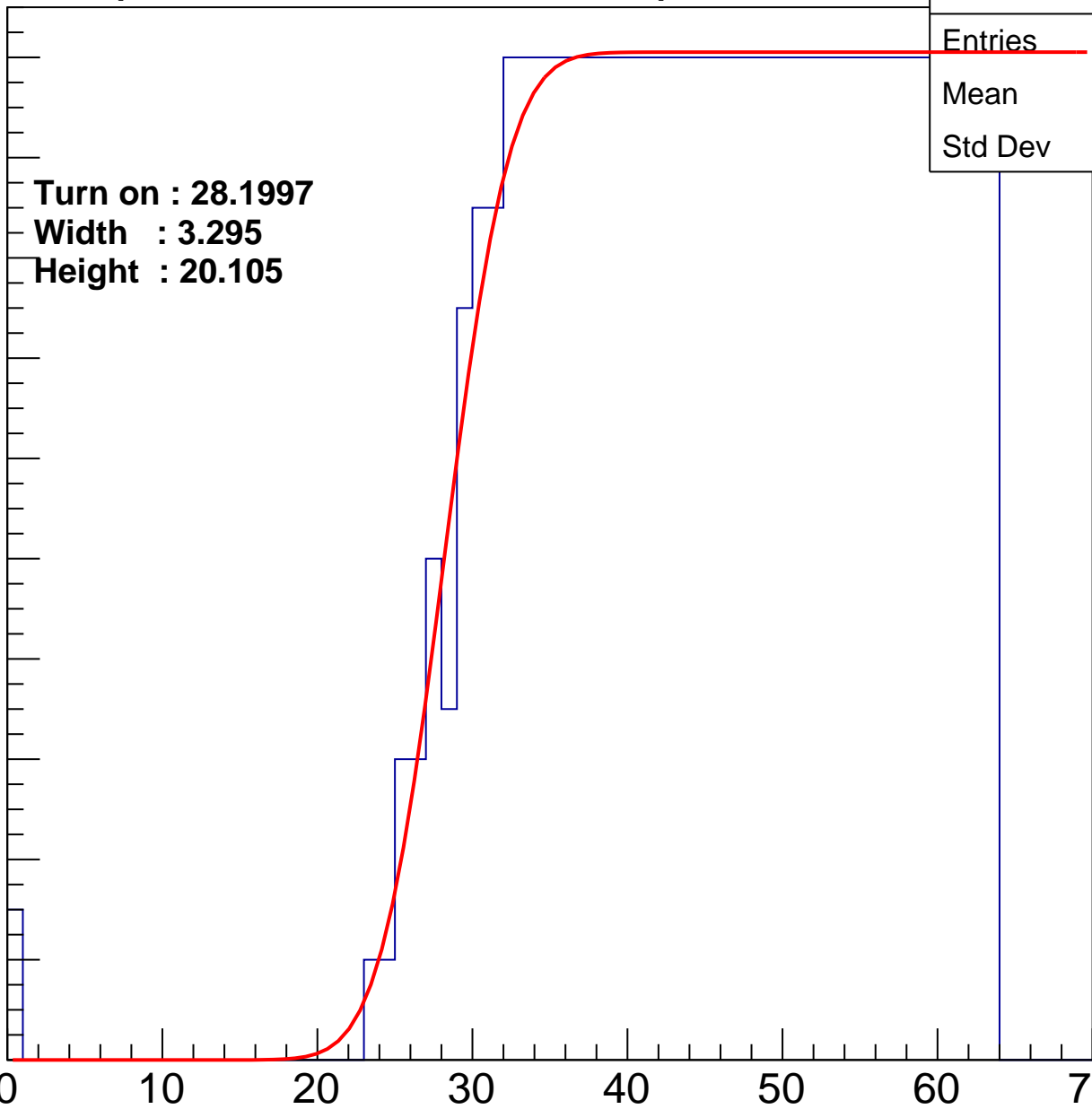
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1997
Width : 3.295
Height : 20.105

Entries	725
Mean	45.16
Std Dev	10.97

ampl



B1L001S, U24-ch92

calib_packv5_042523_0143.root, FC#2, port C2

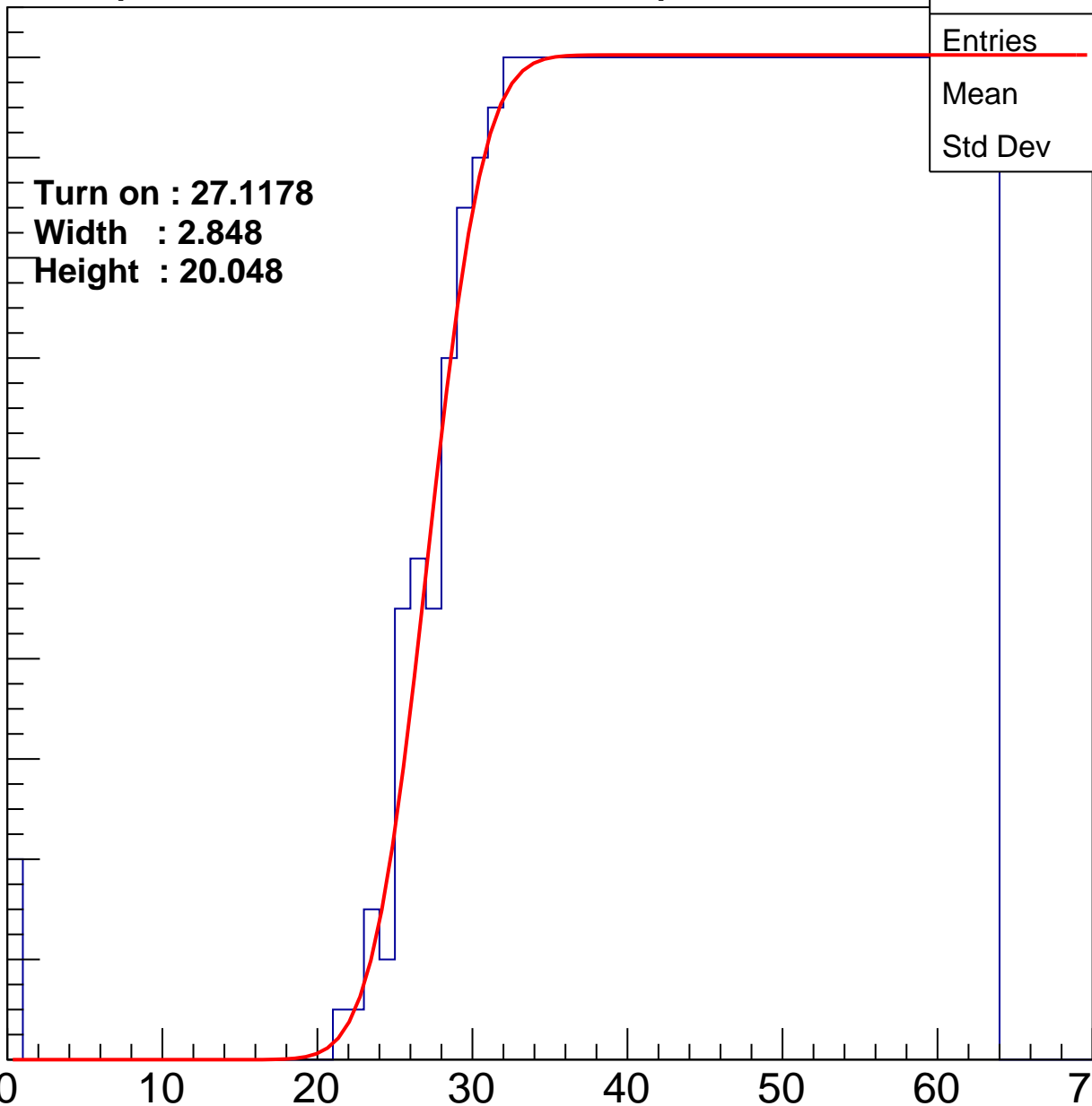
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1178
Width : 2.848
Height : 20.048

Entries	747
Mean	44.58
Std Dev	11.35

ampl



B1L001S, U24-ch93

calib_packv5_042523_0143.root, FC#2, port C2

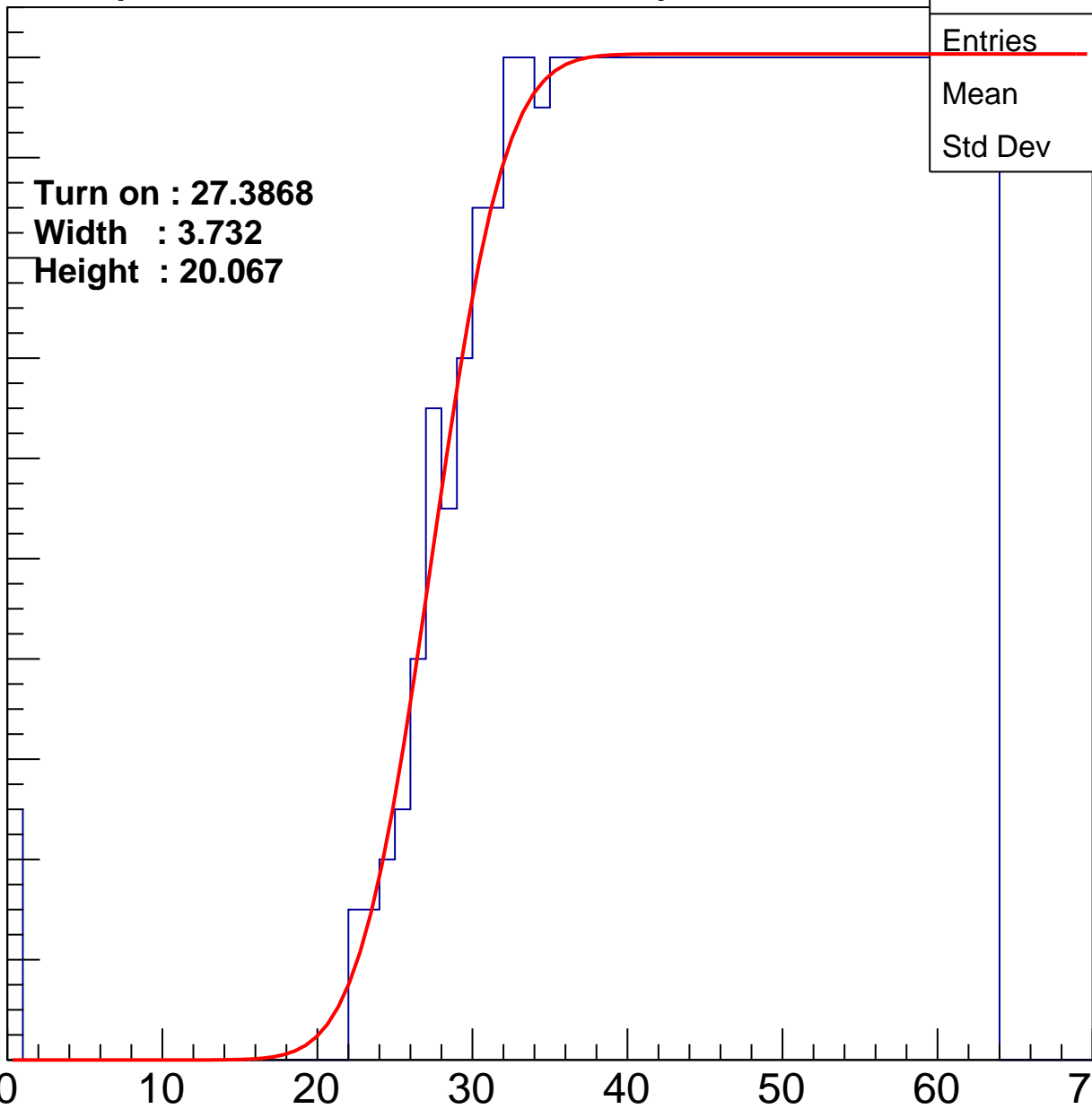
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3868
Width : 3.732
Height : 20.067

Entries	739
Mean	44.7
Std Dev	11.42

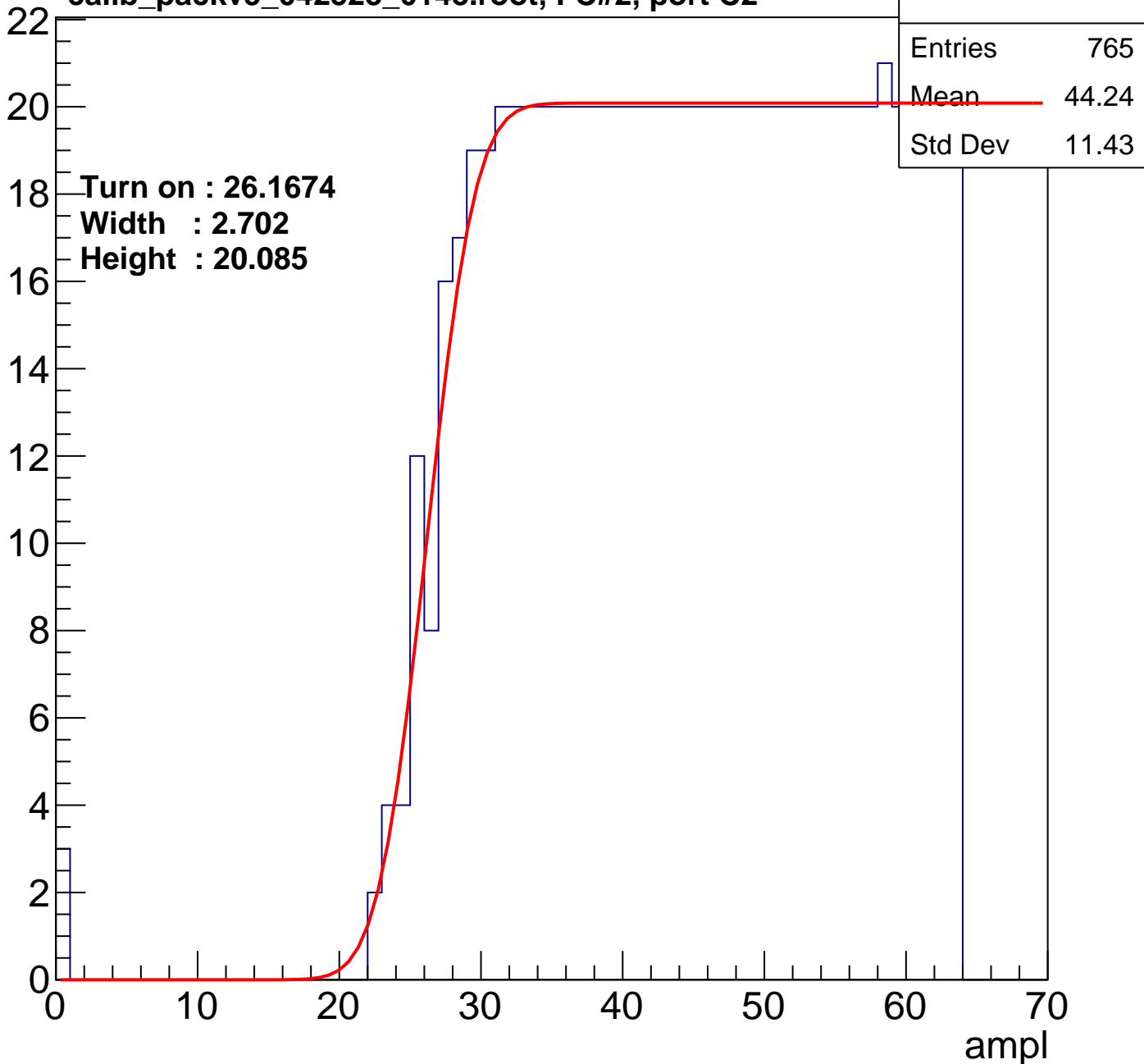
ampl



B1L001S, U24-ch94

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U24-ch95

calib_packv5_042523_0143.root, FC#2, port C2

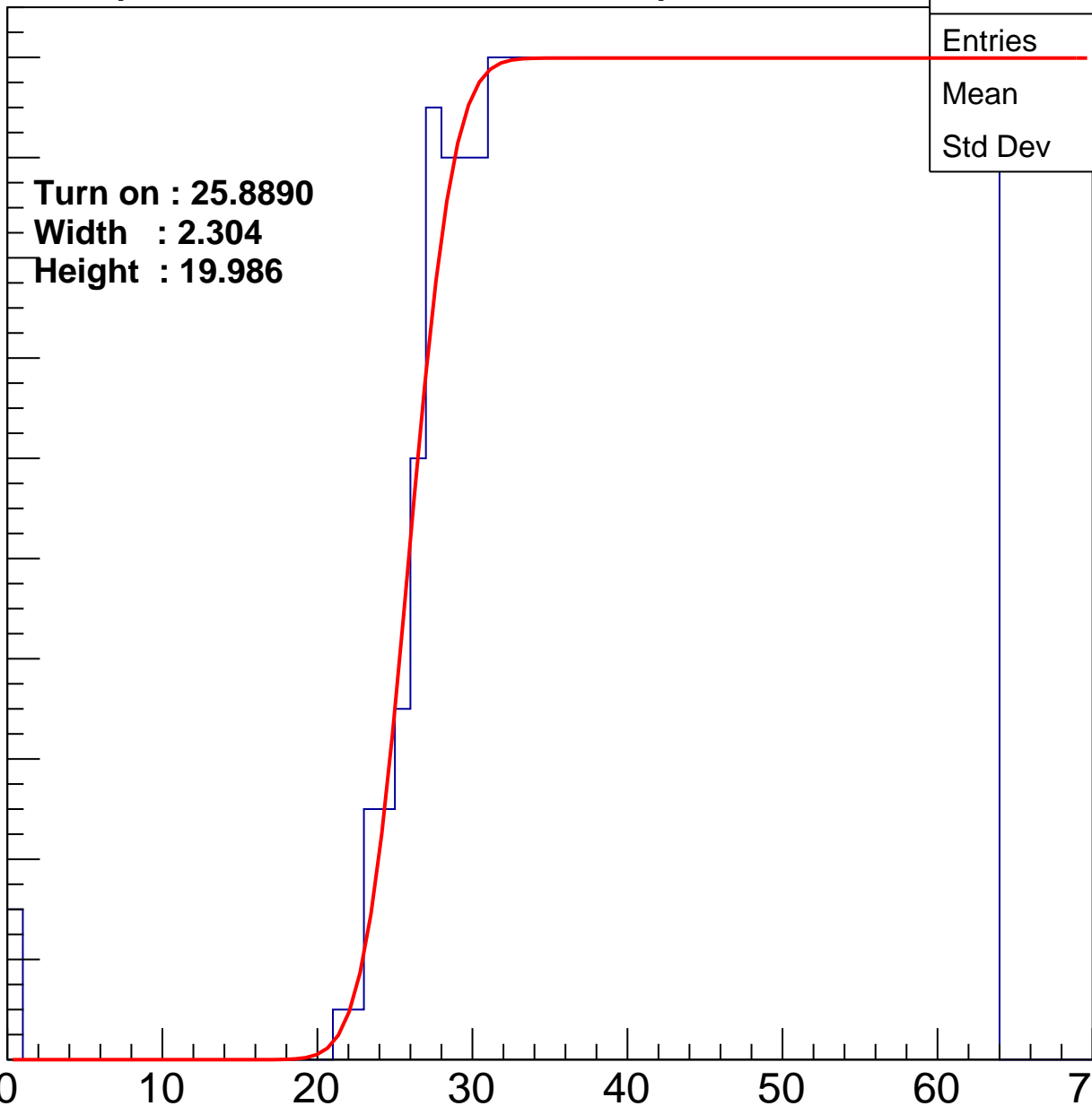
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8890
Width : 2.304
Height : 19.986

Entries	767
Mean	44.15
Std Dev	11.47

ampl



B1L001S, U24-ch96

calib_packv5_042523_0143.root, FC#2, port C2

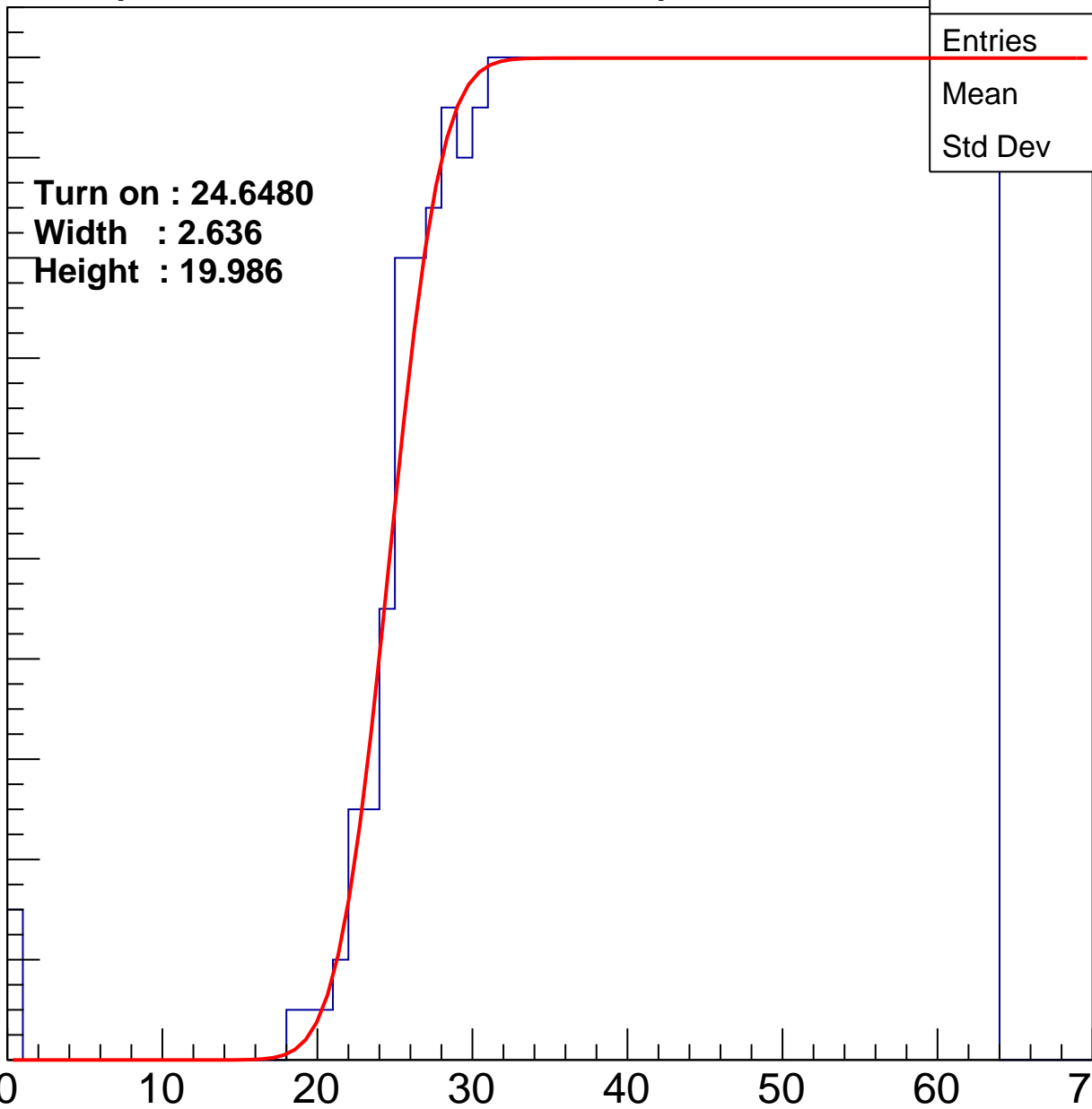
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6480
Width : 2.636
Height : 19.986

Entries	792
Mean	43.51
Std Dev	11.84

ampl



B1L001S, U24-ch97

calib_packv5_042523_0143.root, FC#2, port C2

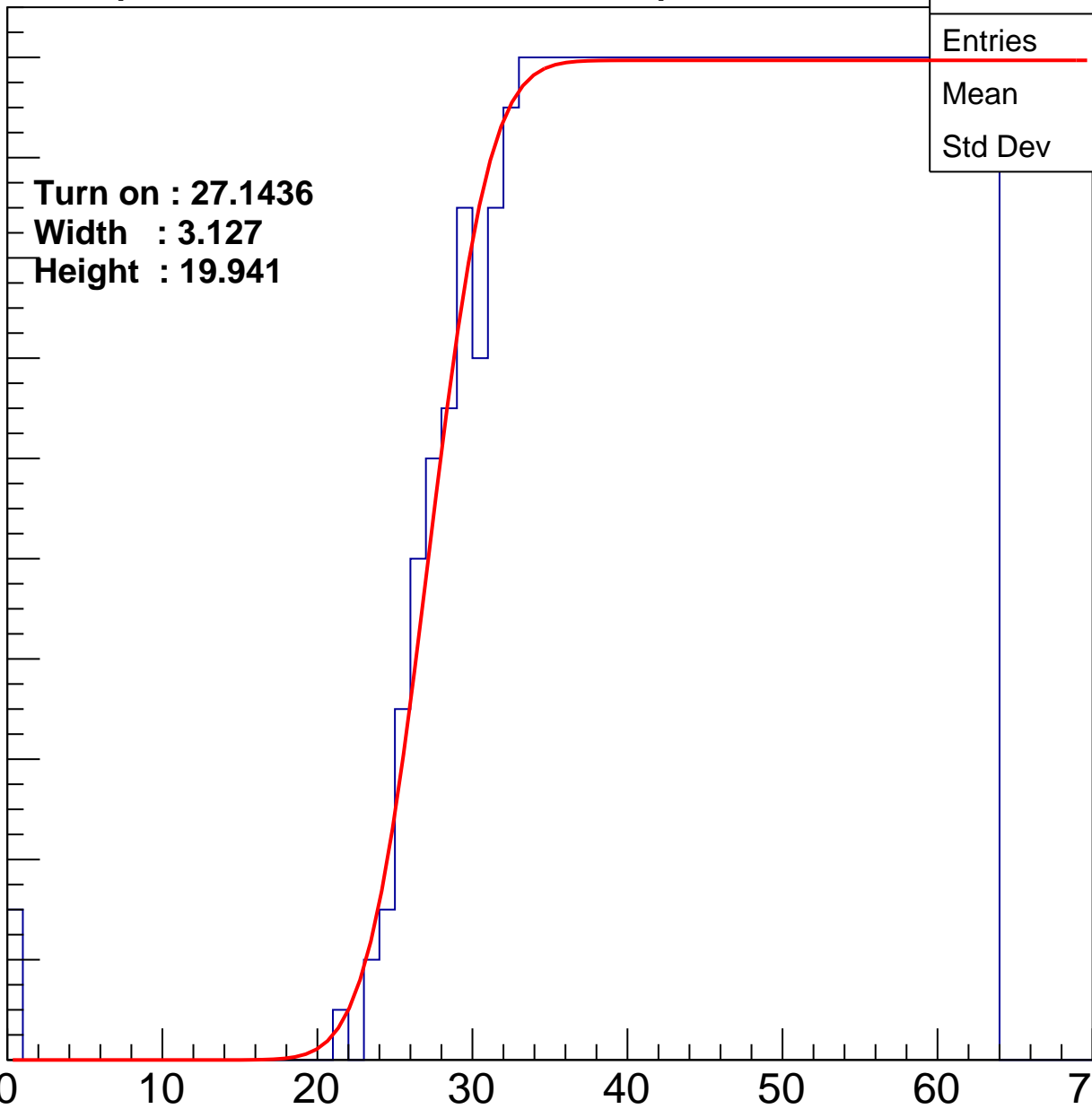
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1436
Width : 3.127
Height : 19.941

Entries	738
Mean	44.81
Std Dev	11.18

ampl



B1L001S, U24-ch98

calib_packv5_042523_0143.root, FC#2, port C2

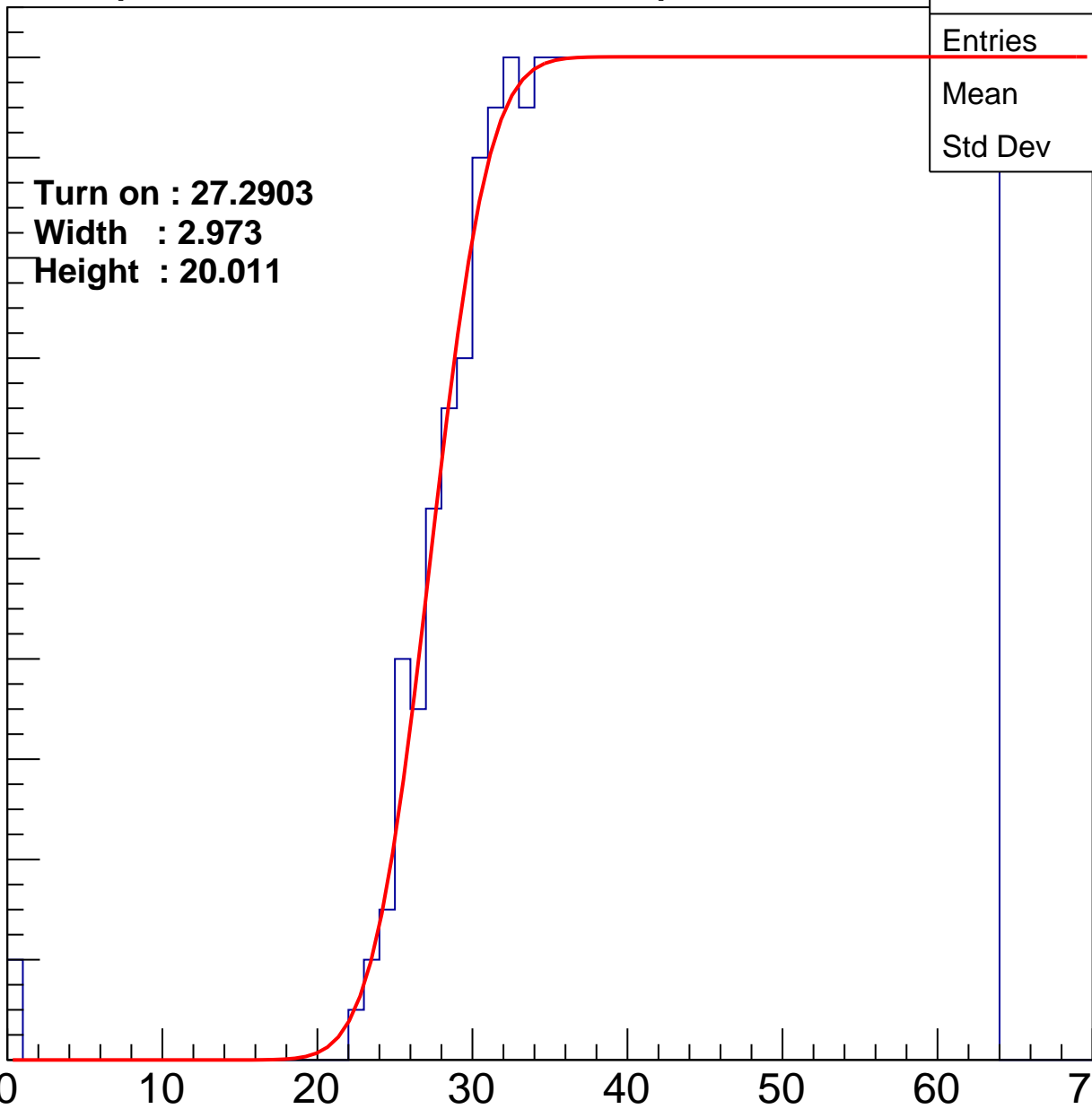
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2903
Width : 2.973
Height : 20.011

Entries	737
Mean	44.89
Std Dev	11.03

ampl



B1L001S, U24-ch99

calib_packv5_042523_0143.root, FC#2, port C2

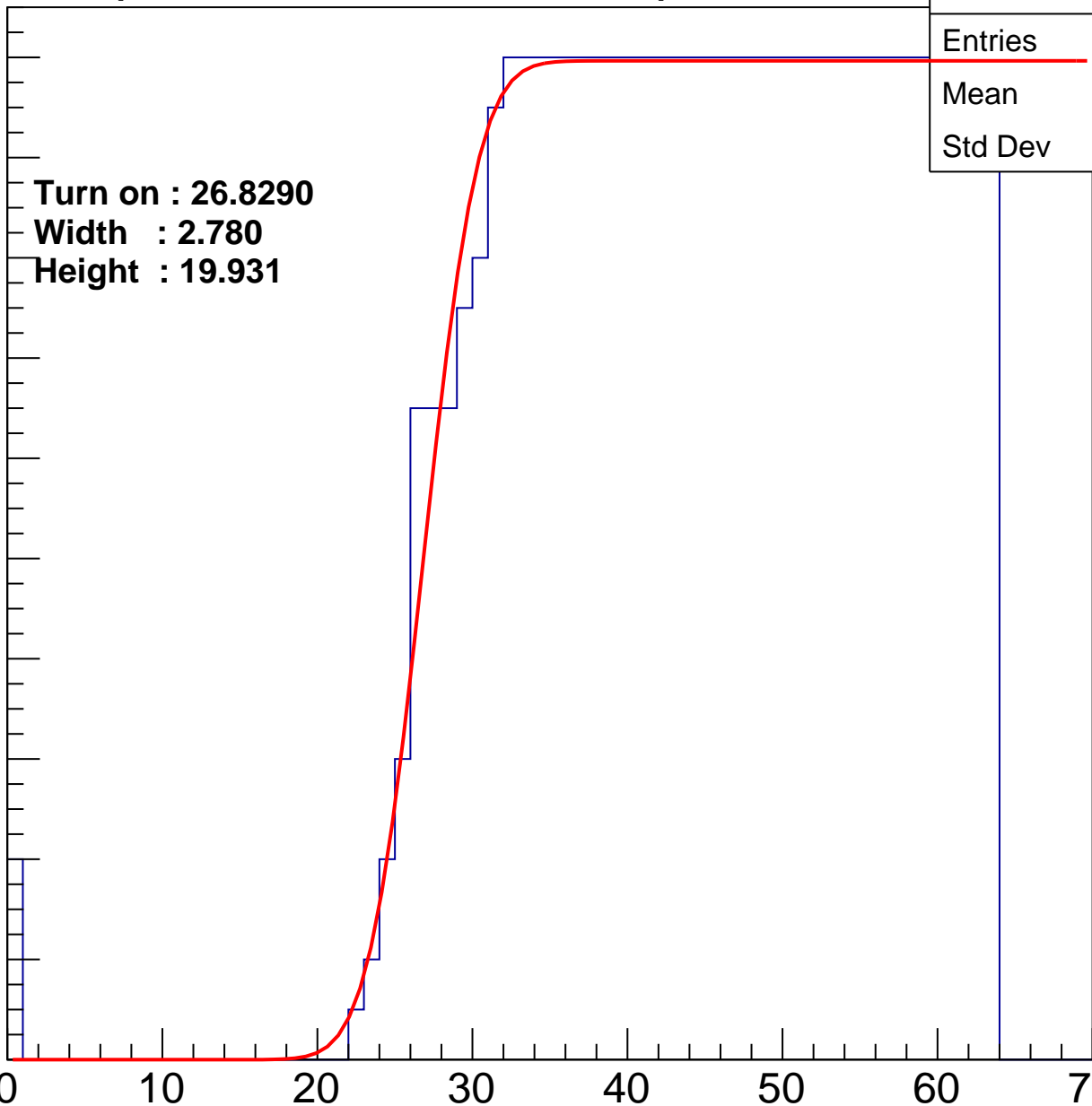
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8290
Width : 2.780
Height : 19.931

Entries	746
Mean	44.6
Std Dev	11.35

ampl



B1L001S, U24-ch100

calib_packv5_042523_0143.root, FC#2, port C2

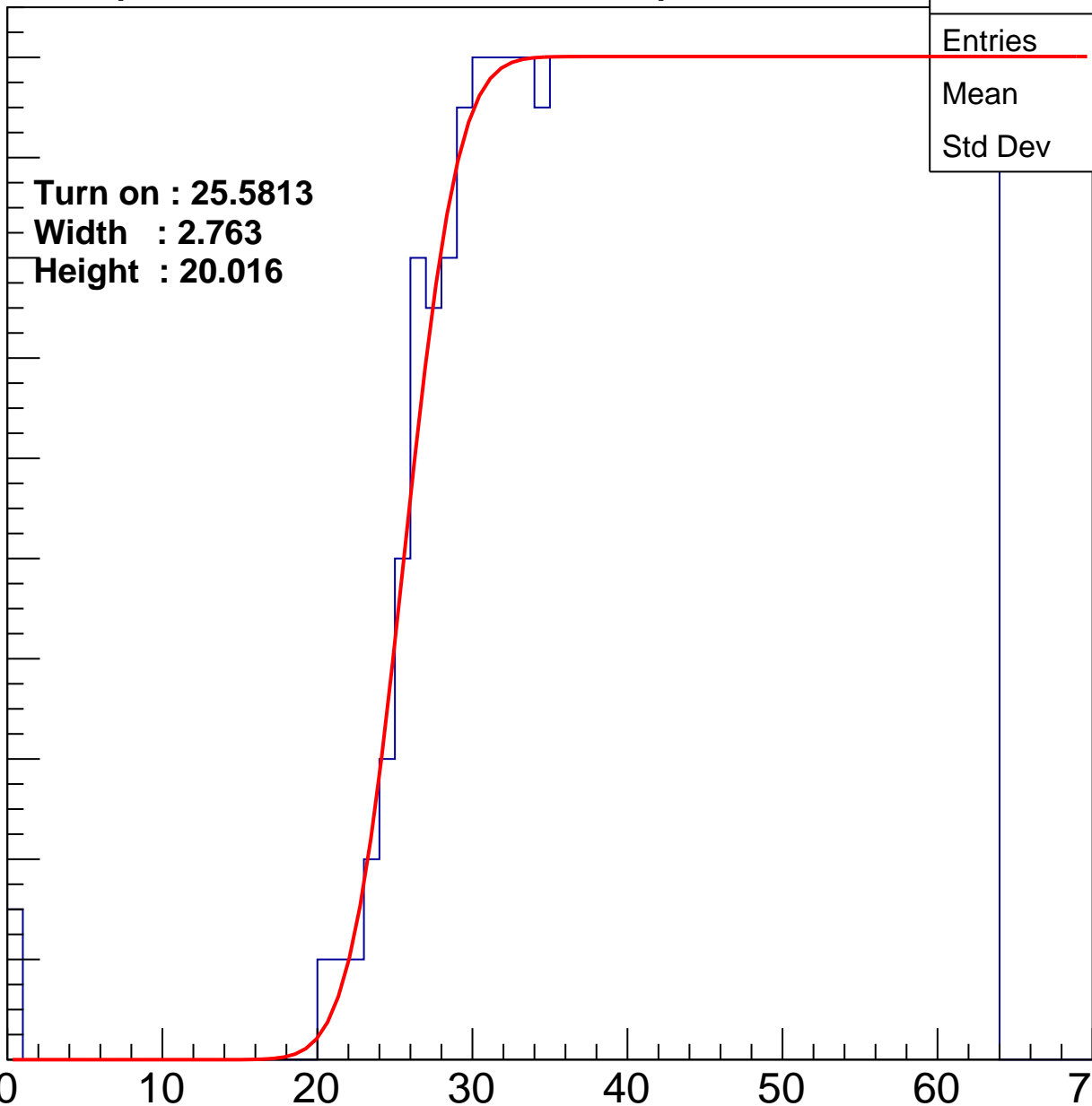
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5813
Width : 2.763
Height : 20.016

Entries	774
Mean	43.95
Std Dev	11.61

ampl



B1L001S, U24-ch101

calib_packv5_042523_0143.root, FC#2, port C2

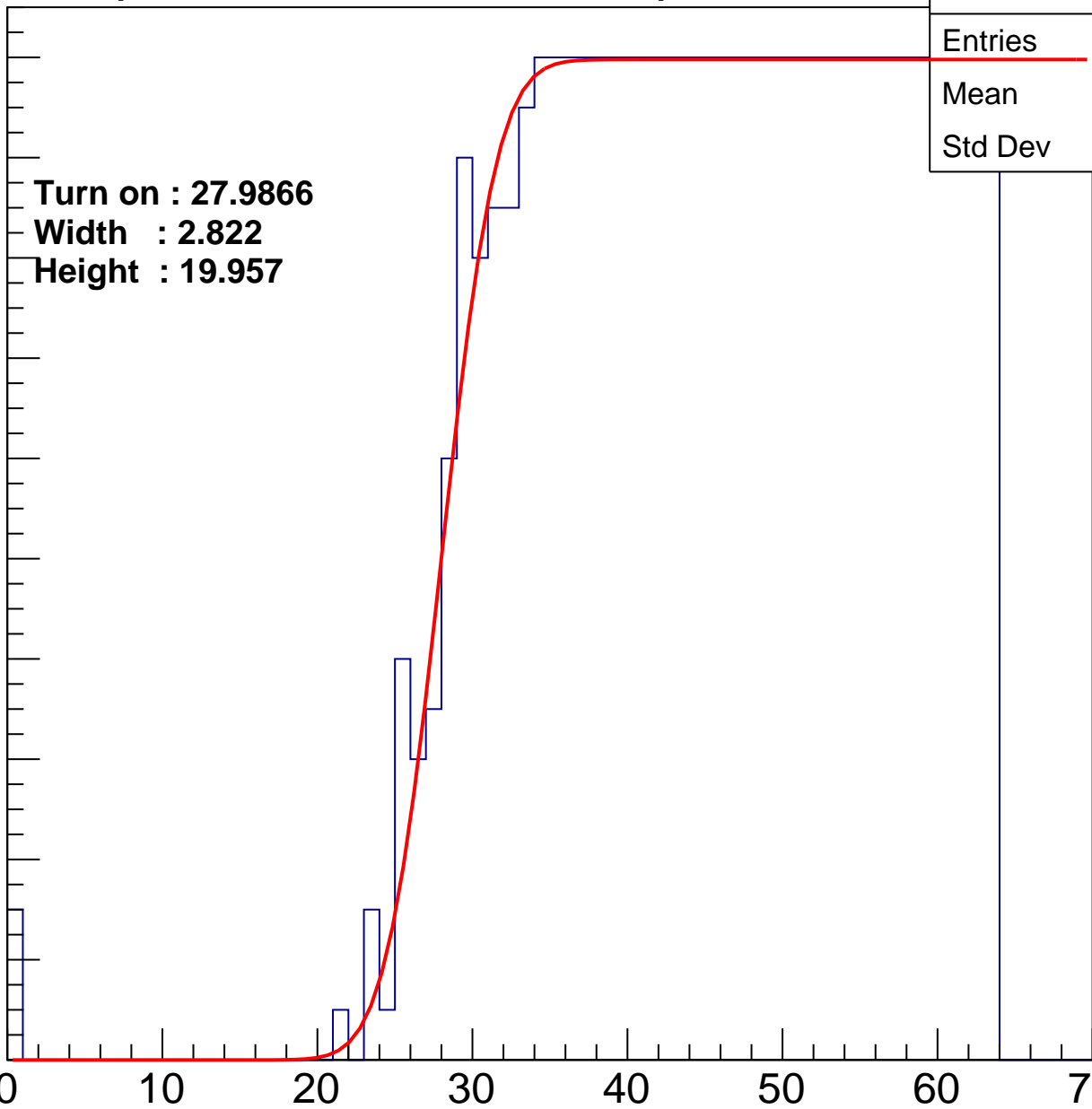
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9866
Width : 2.822
Height : 19.957

Entries	728
Mean	45.05
Std Dev	11.06

ampl



B1L001S, U24-ch102

calib_packv5_042523_0143.root, FC#2, port C2

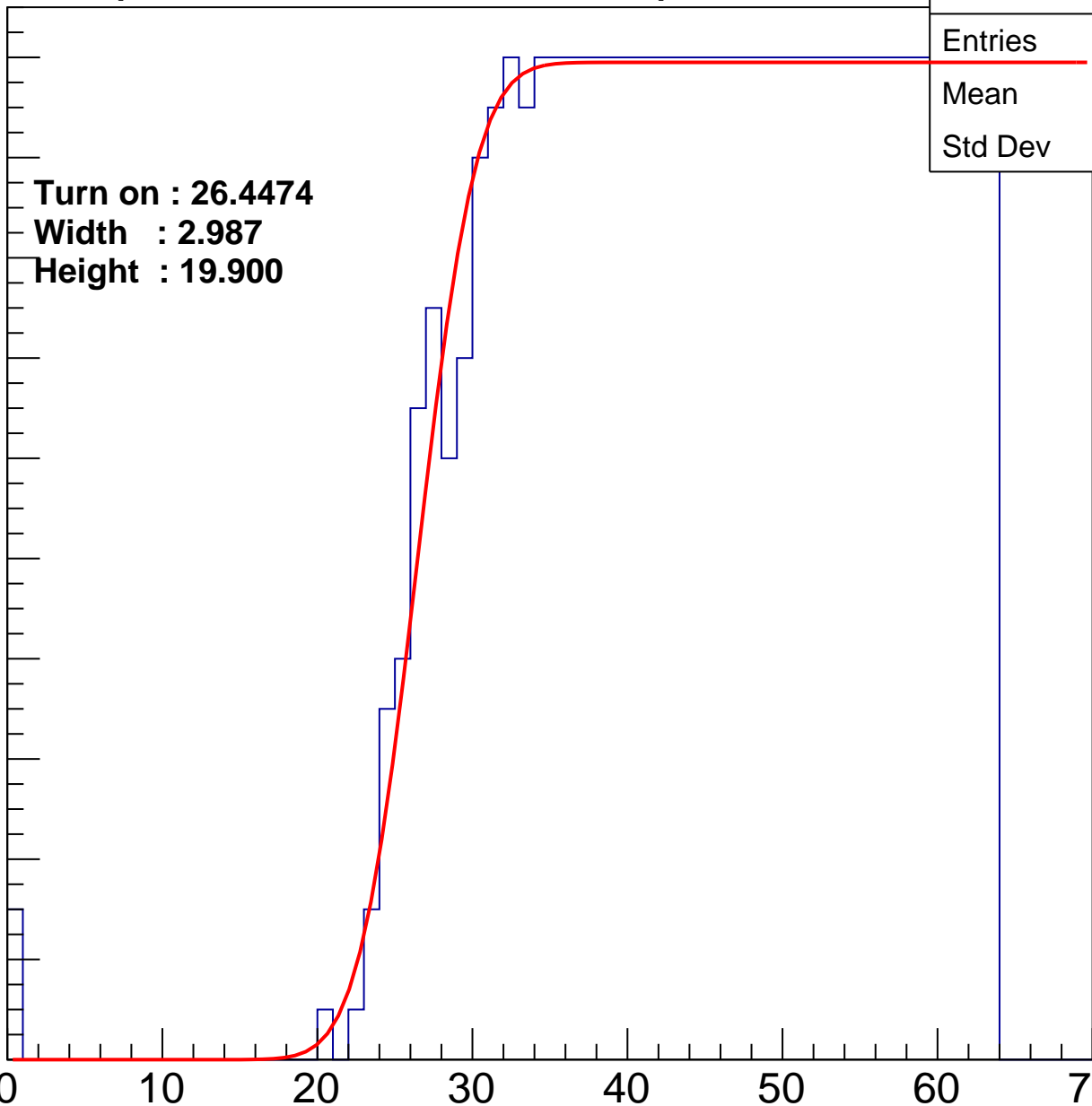
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4474
Width : 2.987
Height : 19.900

Entries	753
Mean	44.44
Std Dev	11.38

ampl



B1L001S, U24-ch103

calib_packv5_042523_0143.root, FC#2, port C2

Entry

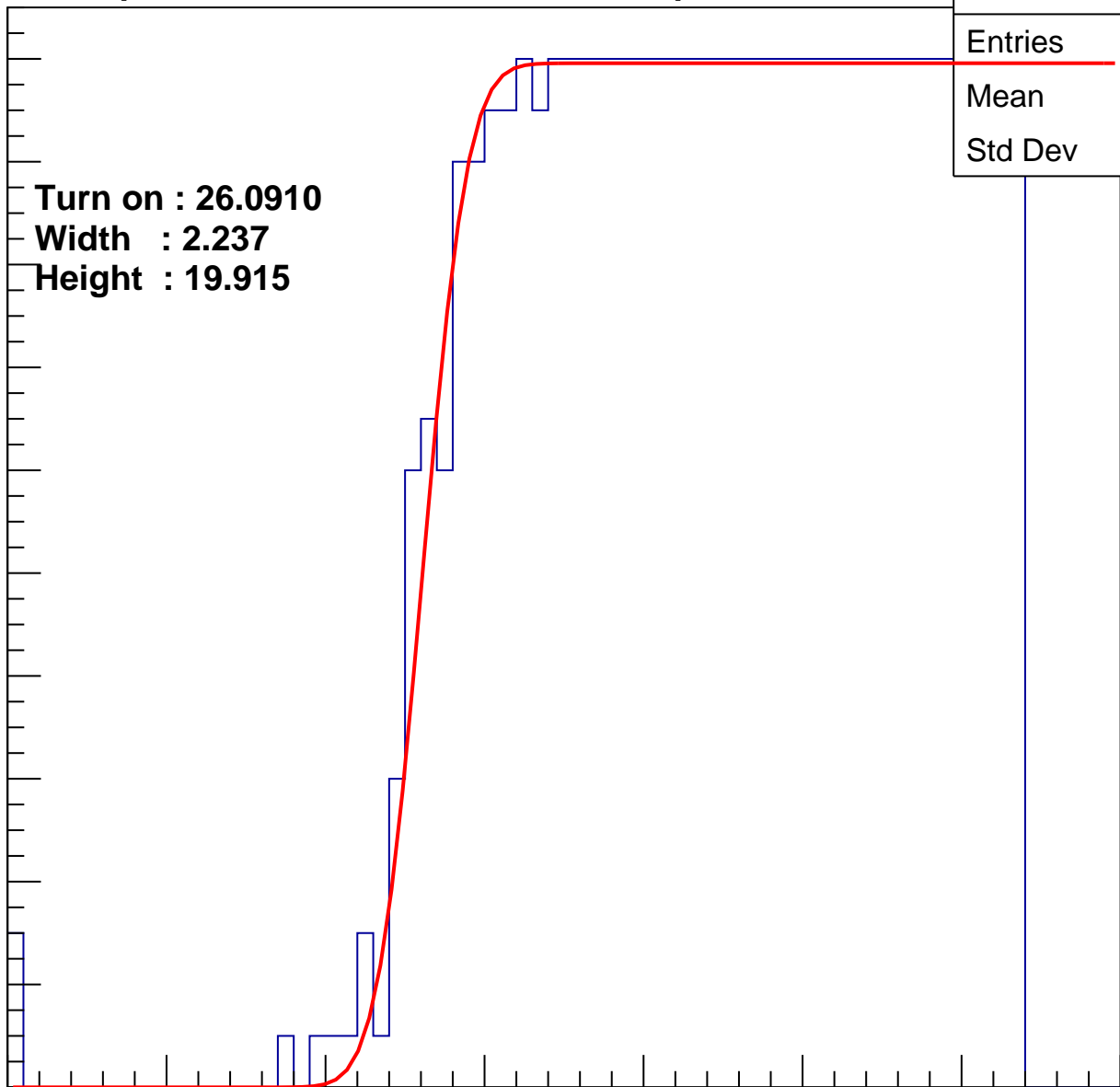
20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0910
Width : 2.237
Height : 19.915

Entries	767
Mean	44.1
Std Dev	11.56

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch104

calib_packv5_042523_0143.root, FC#2, port C2

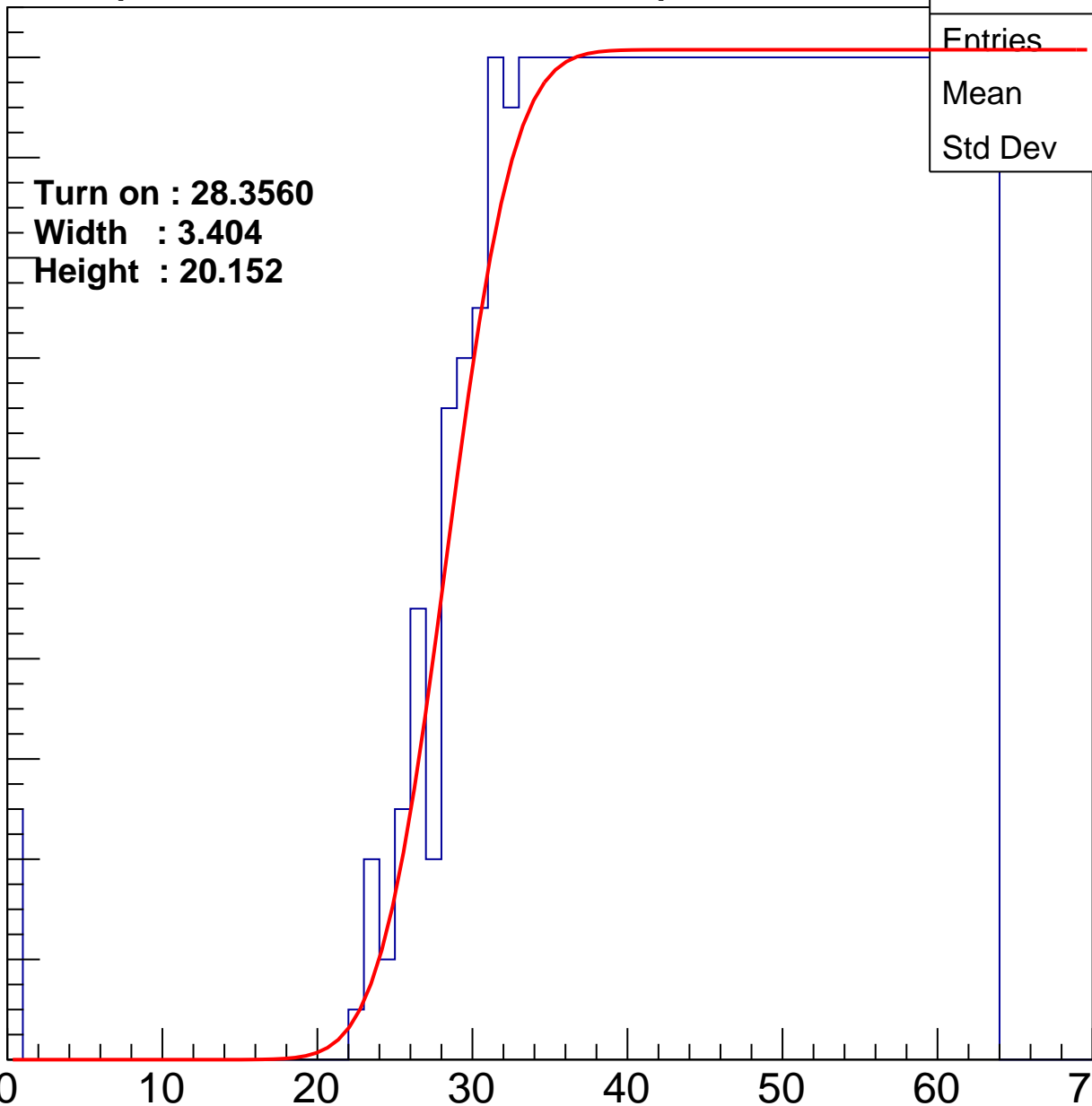
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3560
Width : 3.404
Height : 20.152

Entries	731
Mean	44.92
Std Dev	11.28

ampl



B1L001S, U24-ch105

calib_packv5_042523_0143.root, FC#2, port C2

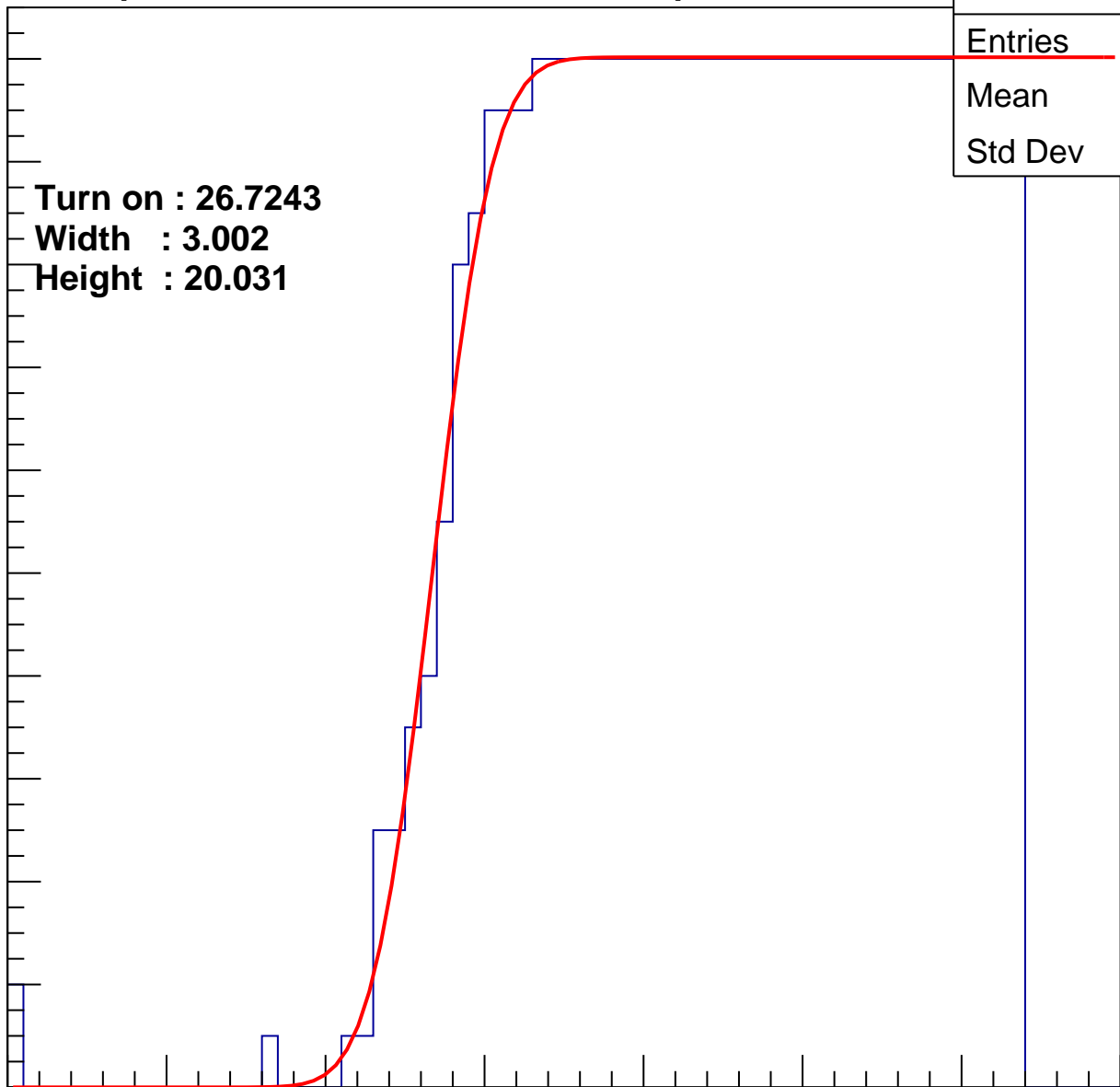
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7243
Width : 3.002
Height : 20.031

Entries	751
Mean	44.53
Std Dev	11.25

ampl



B1L001S, U24-ch106

calib_packv5_042523_0143.root, FC#2, port C2

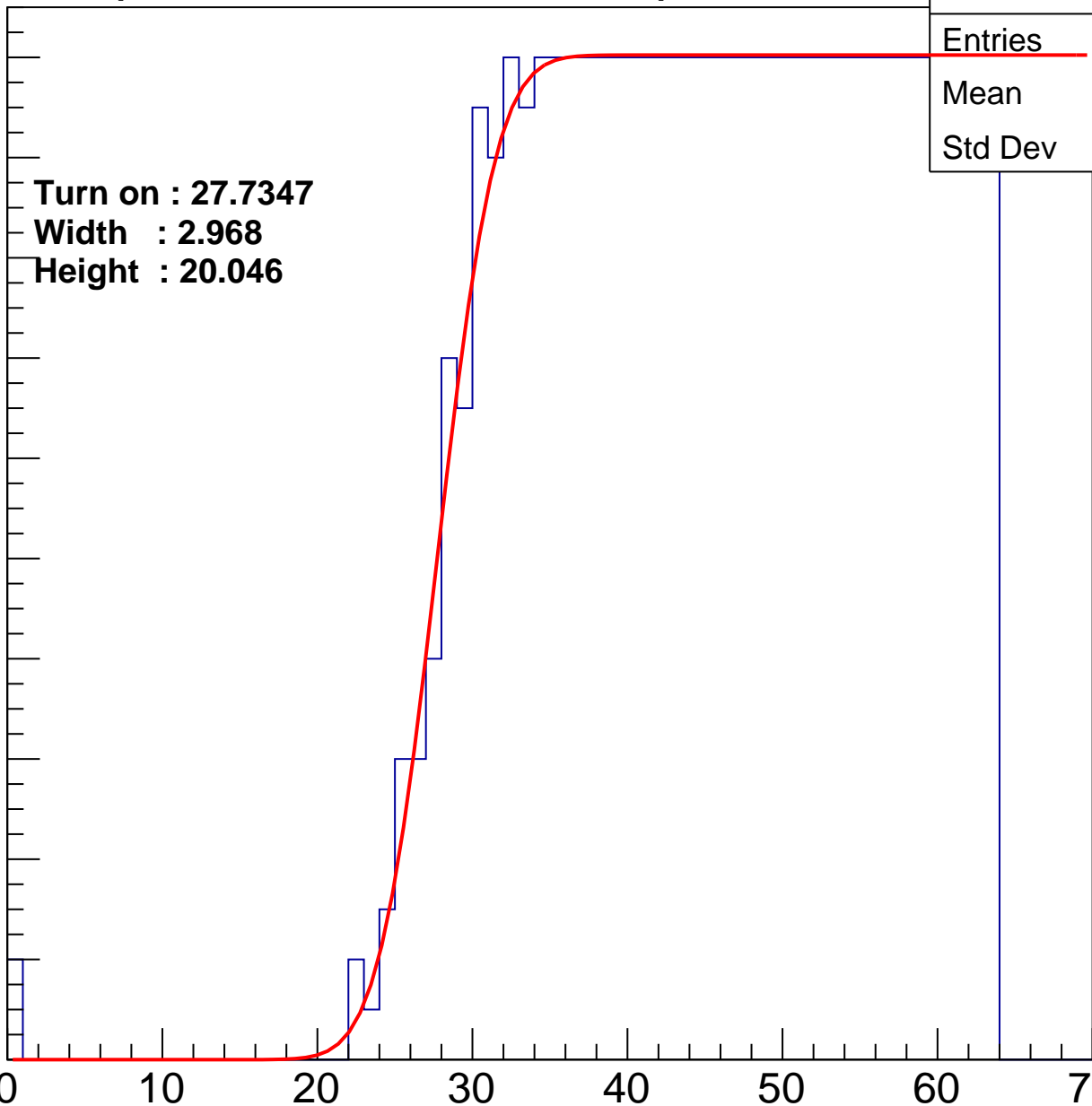
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7347
Width : 2.968
Height : 20.046

Entries	731
Mean	45.04
Std Dev	10.95

ampl



B1L001S, U24-ch107

calib_packv5_042523_0143.root, FC#2, port C2

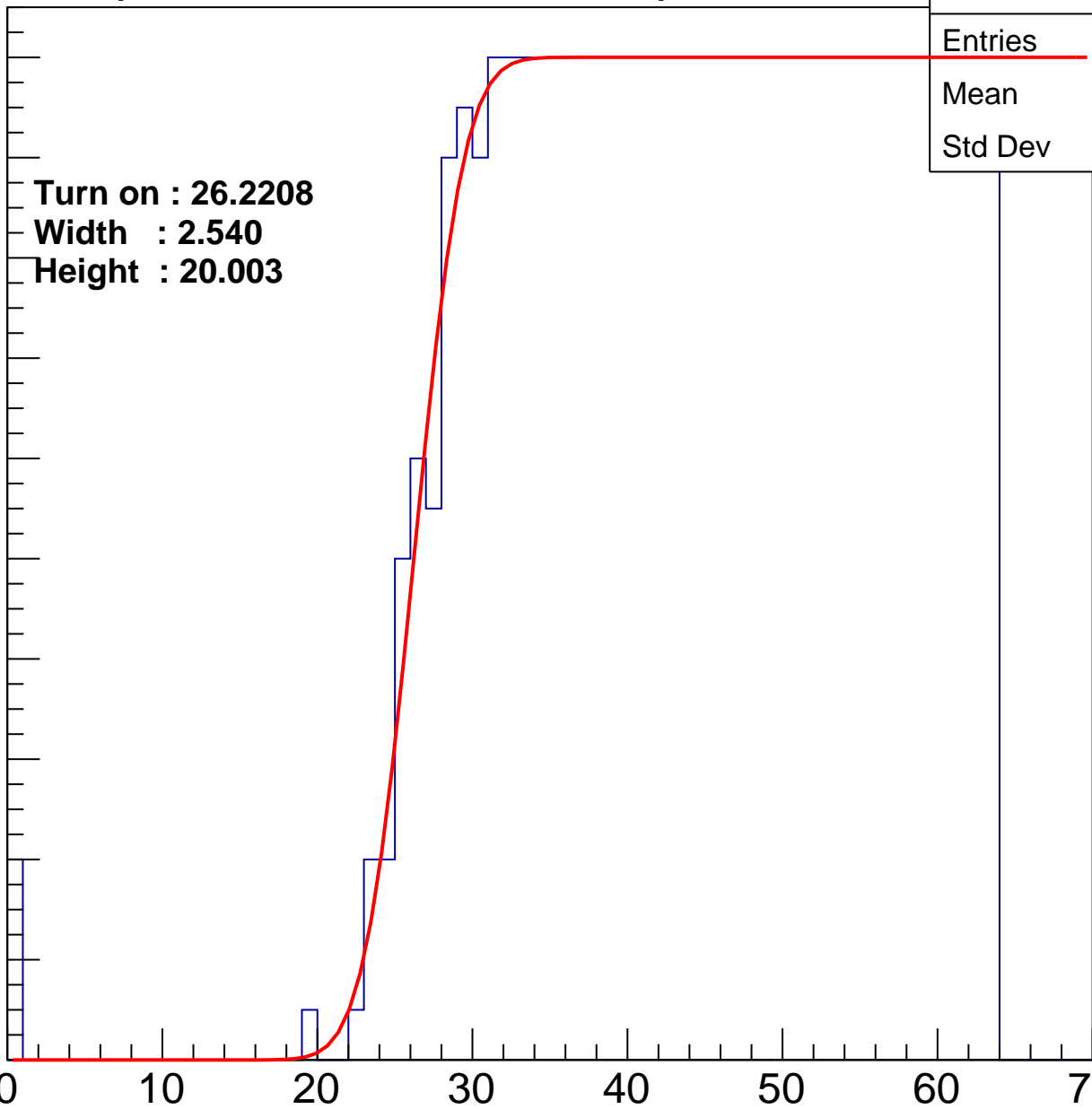
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2208
Width : 2.540
Height : 20.003

Entries	762
Mean	44.23
Std Dev	11.52

ampl



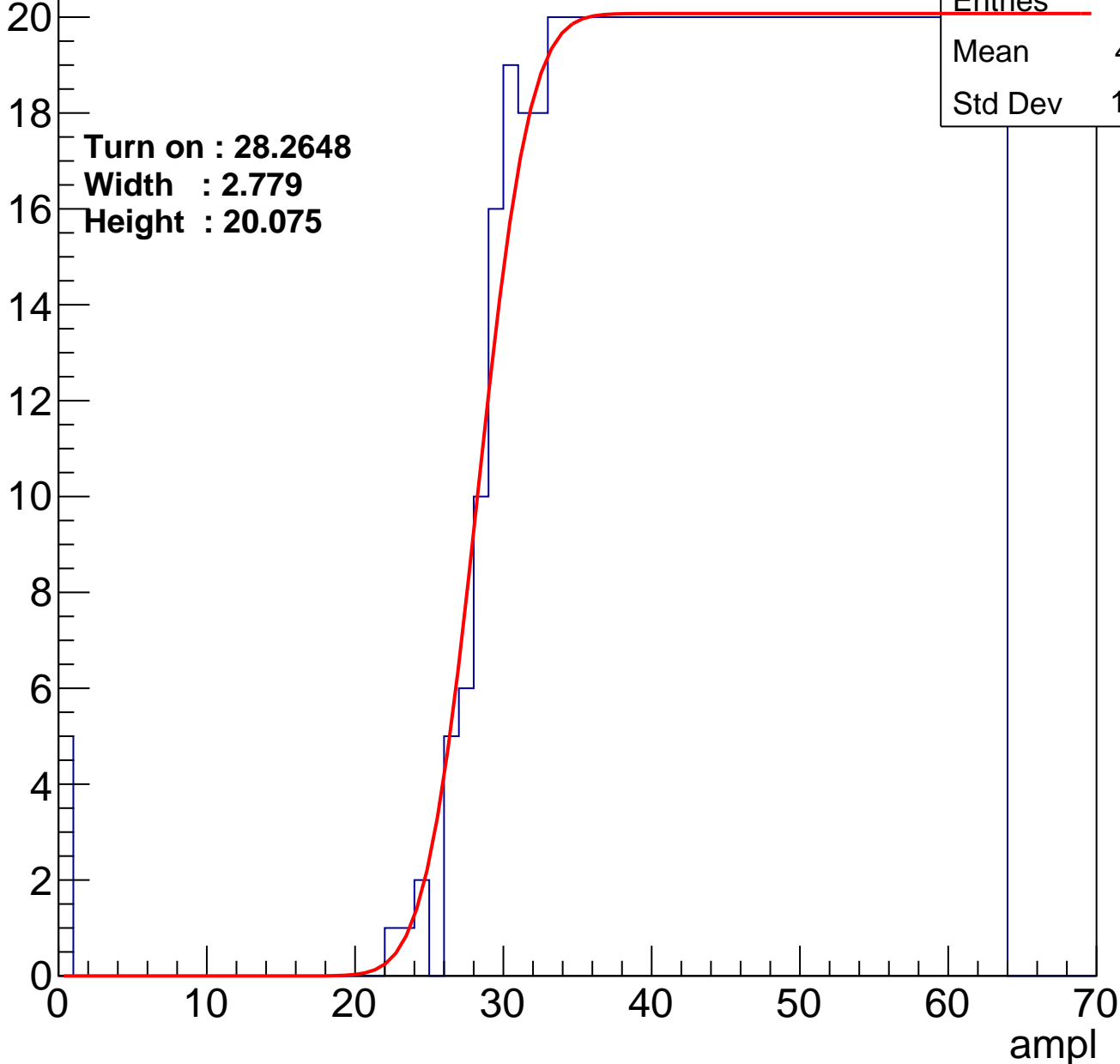
B1L001S, U24-ch108

calib_packv5_042523_0143.root, FC#2, port C2

Entries	721
Mean	45.21
Std Dev	11.09

Turn on : 28.2648
Width : 2.779
Height : 20.075

Entry



B1L001S, U24-ch109

calib_packv5_042523_0143.root, FC#2, port C2

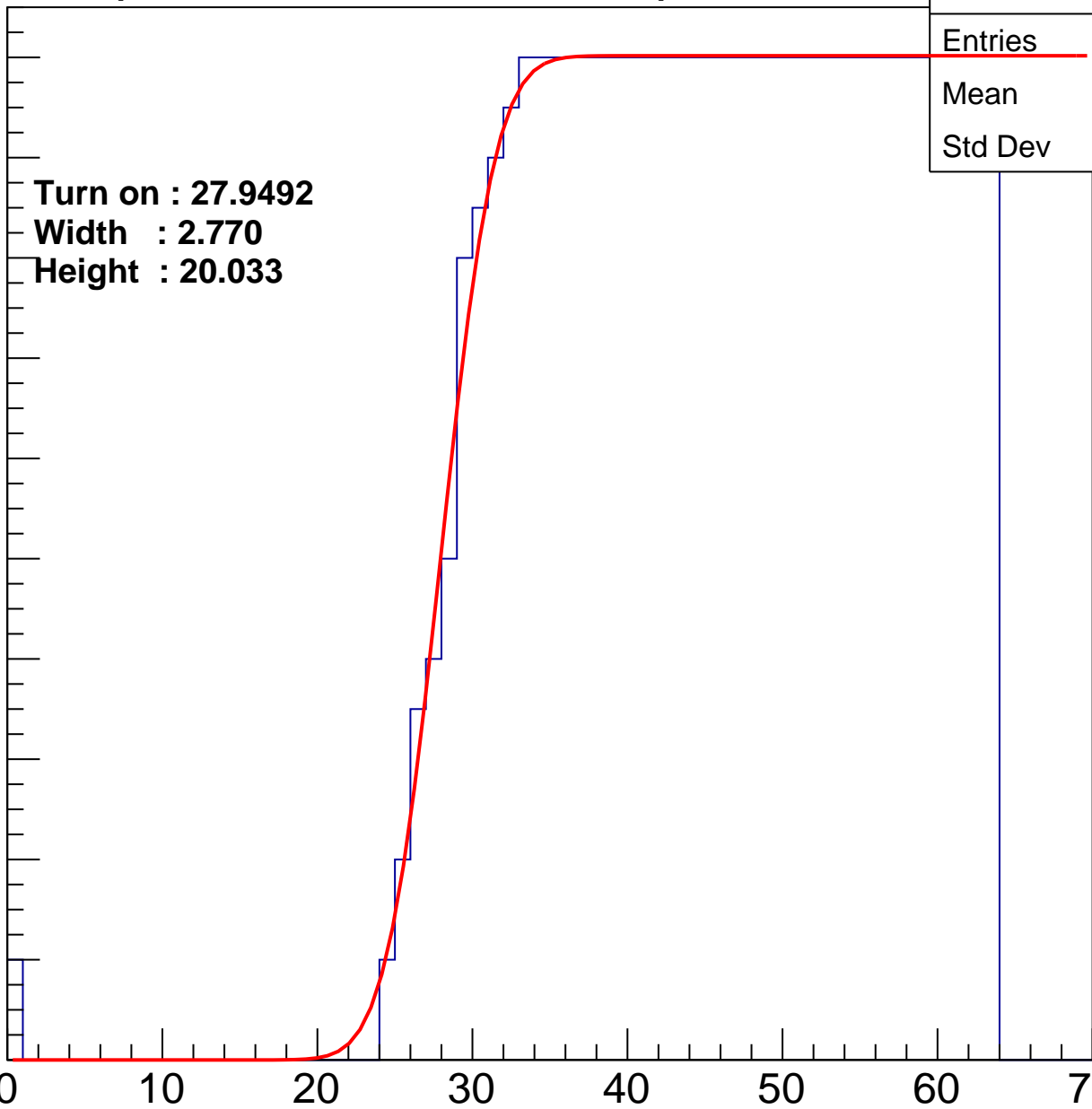
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9492
Width : 2.770
Height : 20.033

Entries	723
Mean	45.26
Std Dev	10.8

ampl



B1L001S, U24-ch110

calib_packv5_042523_0143.root, FC#2, port C2

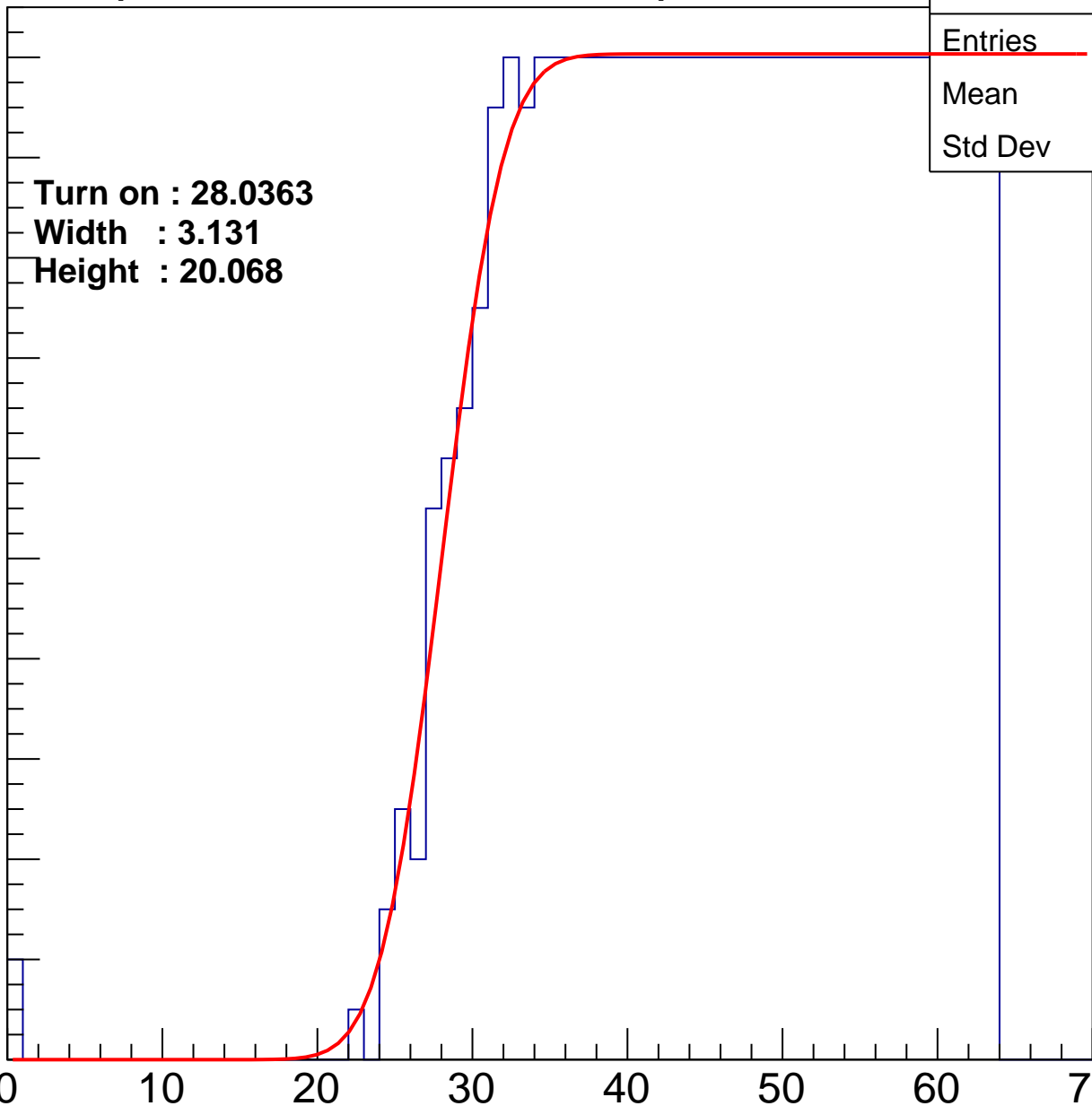
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0363
Width : 3.131
Height : 20.068

Entries	724
Mean	45.22
Std Dev	10.85

ampl



B1L001S, U24-ch111

calib_packv5_042523_0143.root, FC#2, port C2

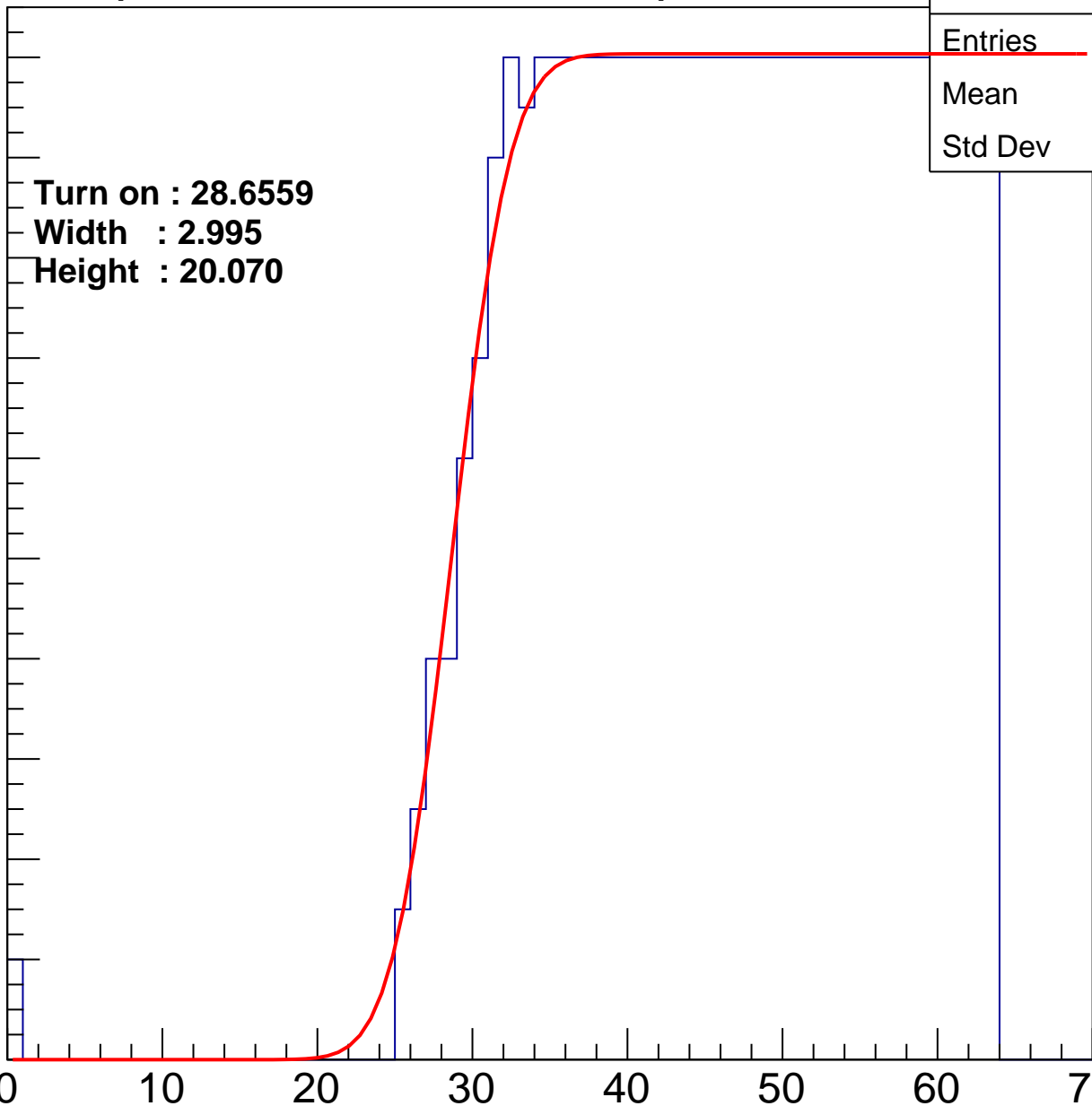
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.6559
Width : 2.995
Height : 20.070

Entries	709
Mean	45.61
Std Dev	10.61

ampl



B1L001S, U24-ch112

calib_packv5_042523_0143.root, FC#2, port C2

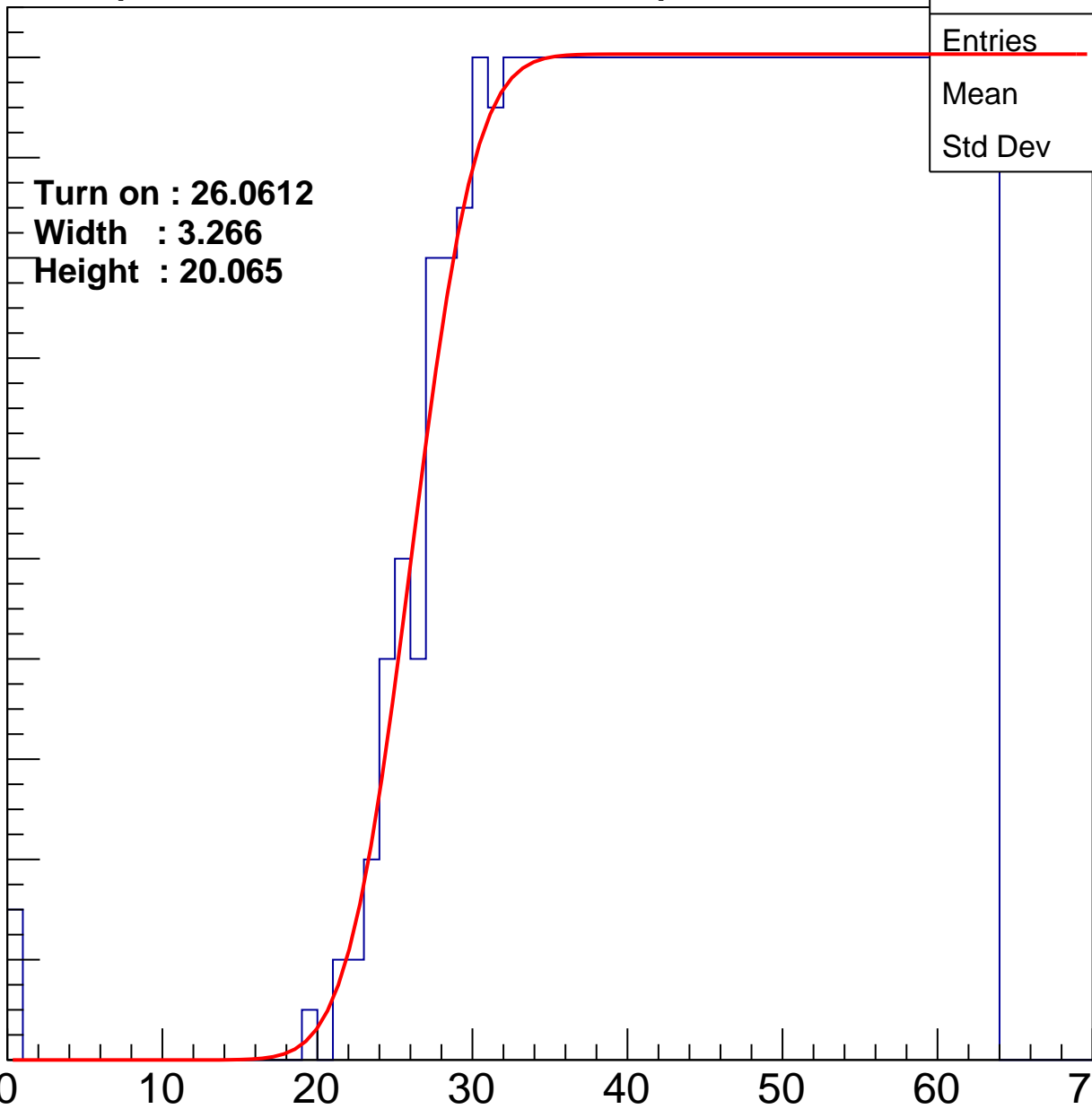
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0612
Width : 3.266
Height : 20.065

Entries	766
Mean	44.14
Std Dev	11.52

ampl



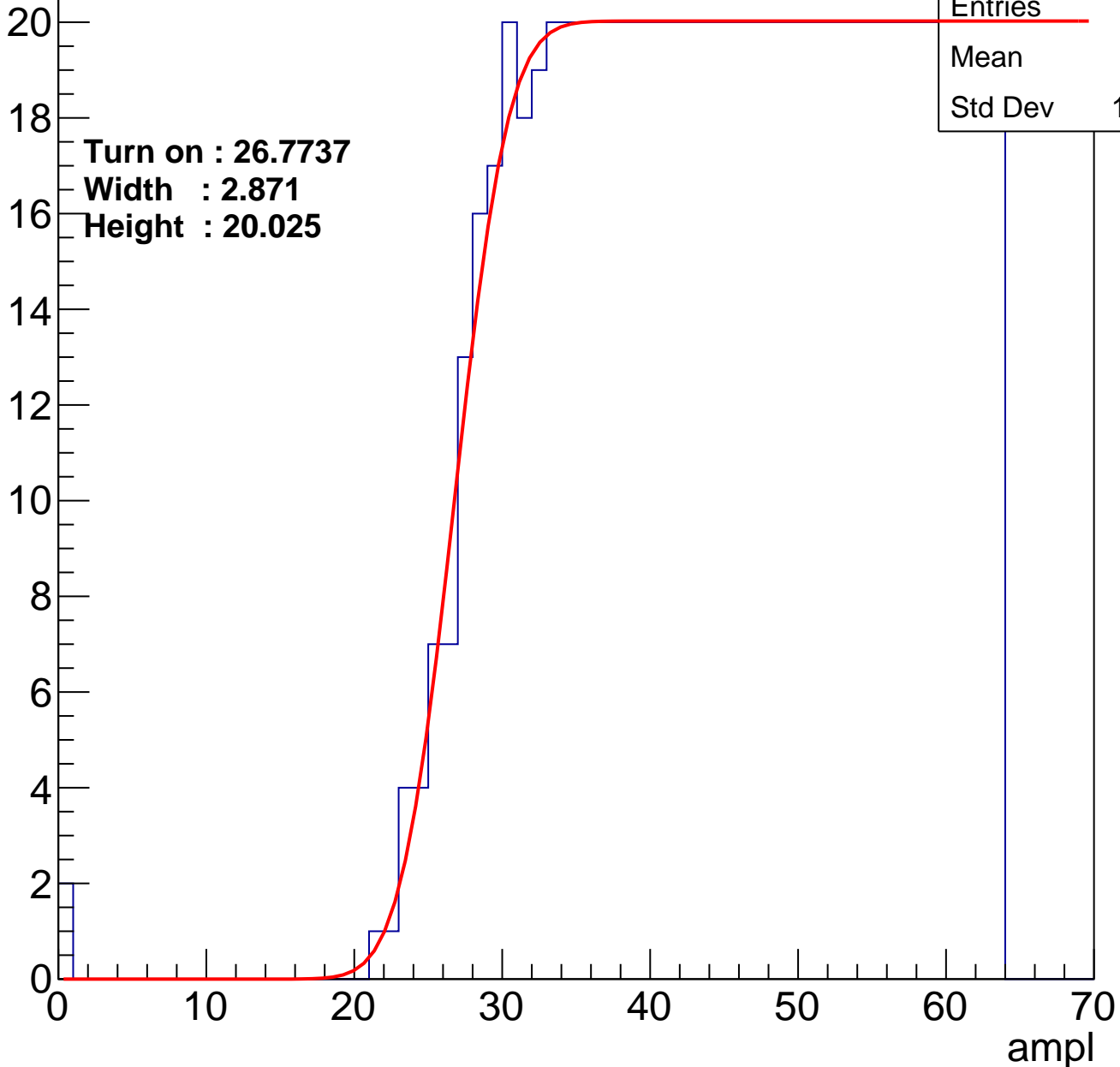
B1L001S, U24-ch113

calib_packv5_042523_0143.root, FC#2, port C2

Entries	749
Mean	44.6
Std Dev	11.18

Turn on : 26.7737
Width : 2.871
Height : 20.025

Entry



B1L001S, U24-ch114

calib_packv5_042523_0143.root, FC#2, port C2

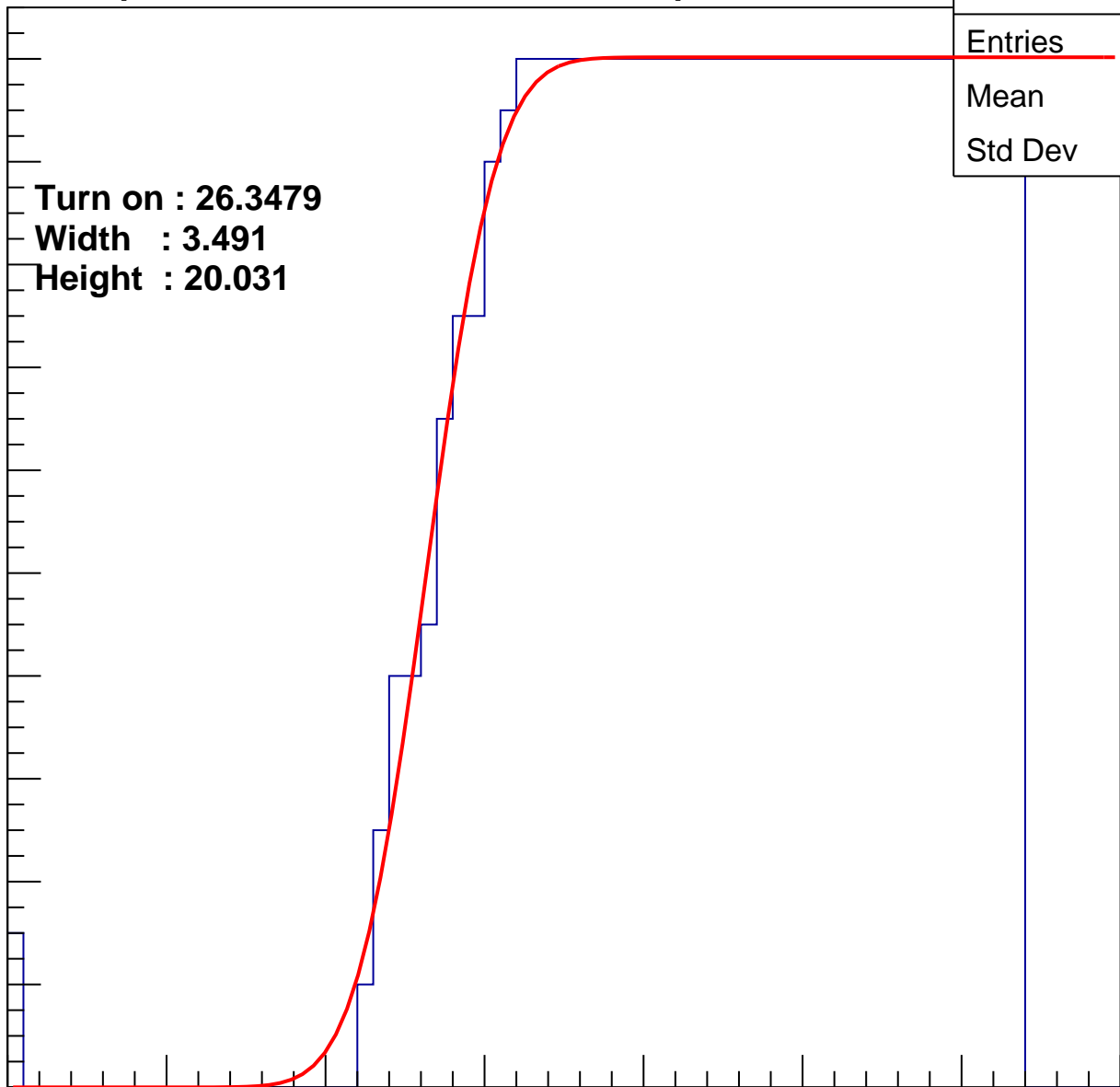
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3479
Width : 3.491
Height : 20.031

Entries	755
Mean	44.4
Std Dev	11.39

ampl



B1L001S, U24-ch115

calib_packv5_042523_0143.root, FC#2, port C2

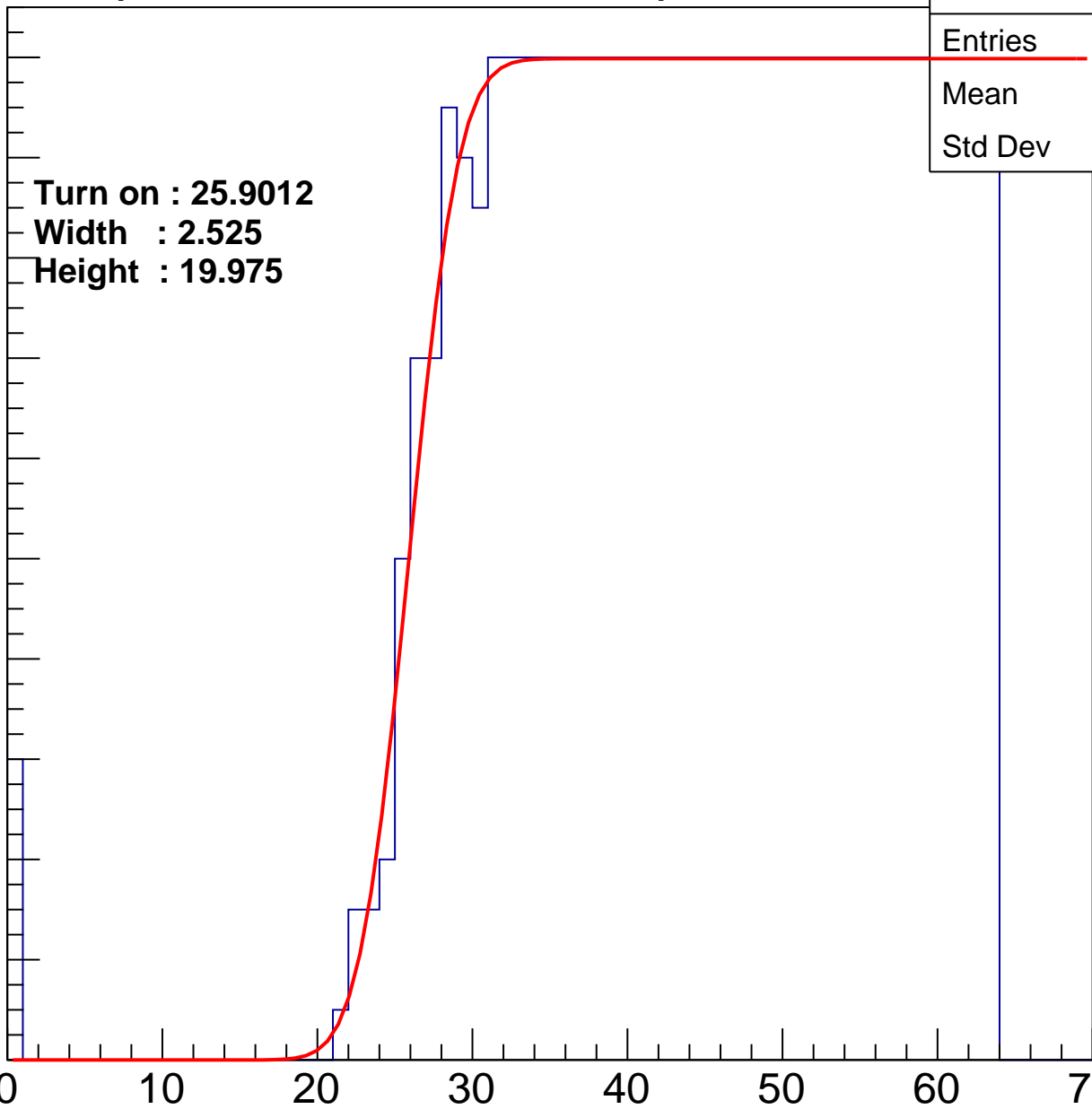
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9012
Width : 2.525
Height : 19.975

Entries	769
Mean	43.99
Std Dev	11.78

ampl



B1L001S, U24-ch116

calib_packv5_042523_0143.root, FC#2, port C2

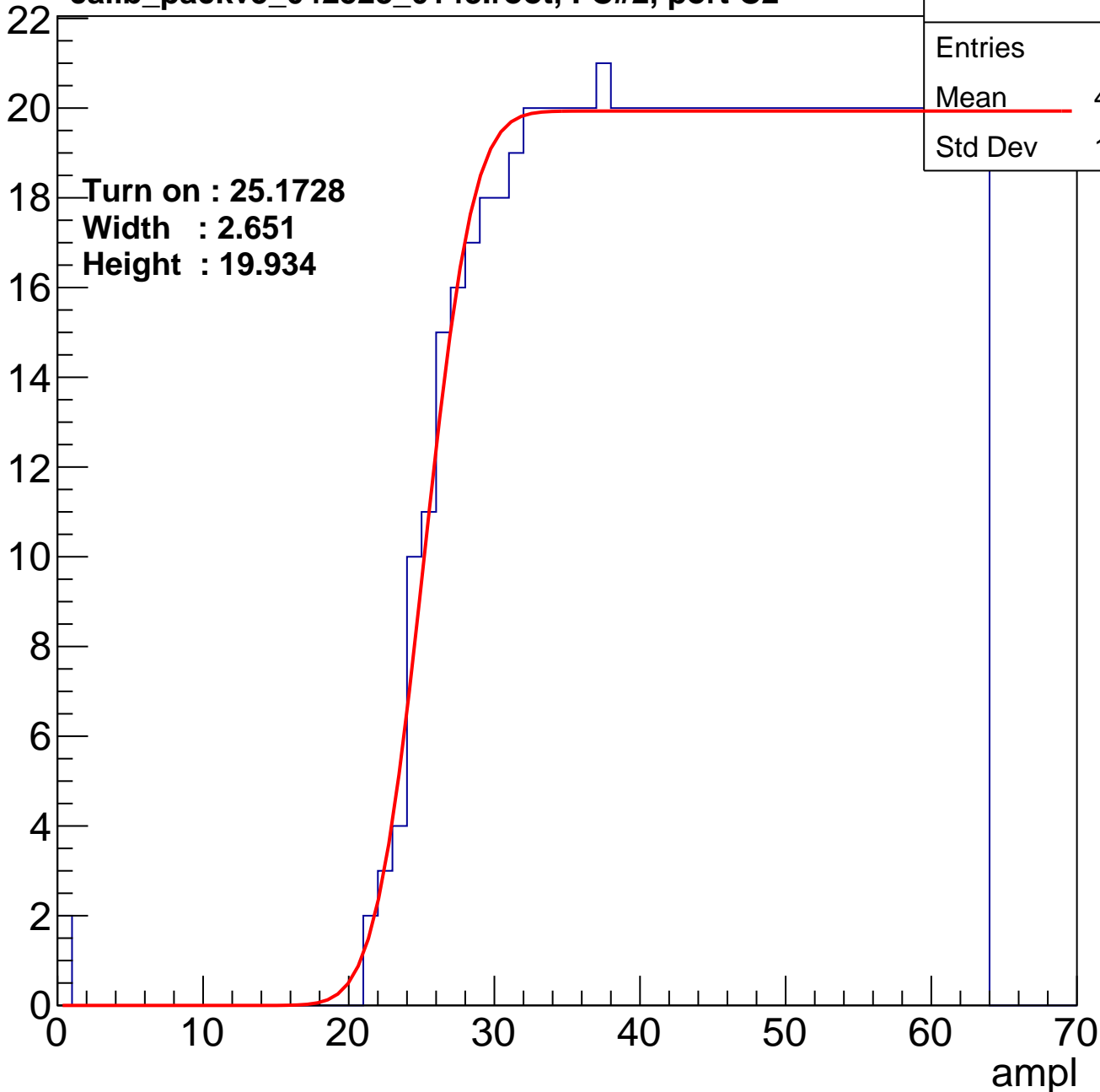
Entries	776
Mean	43.94
Std Dev	11.53

Turn on : 25.1728

Width : 2.651

Height : 19.934

Entry



B1L001S, U24-ch117

calib_packv5_042523_0143.root, FC#2, port C2

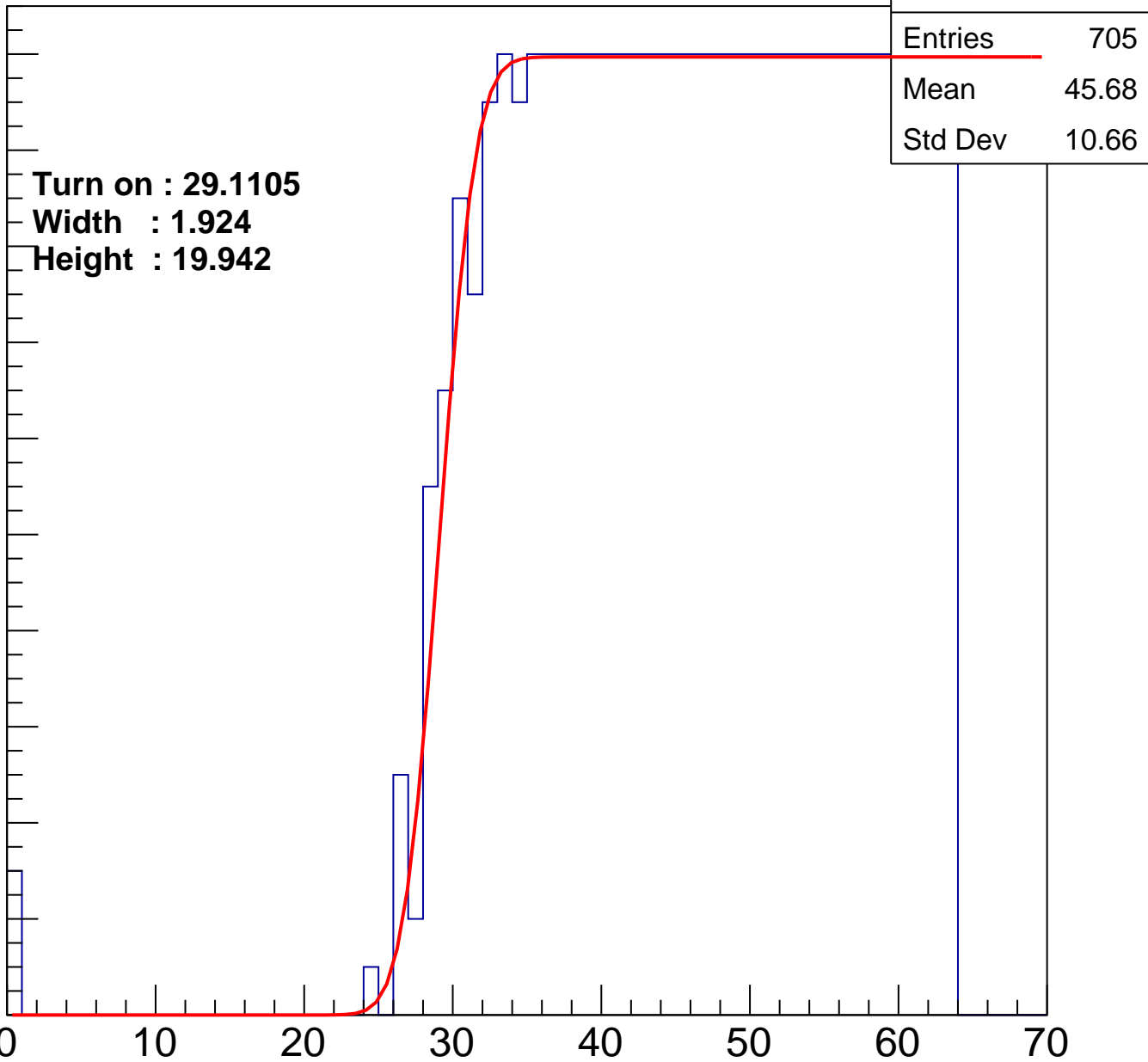
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.1105
Width : 1.924
Height : 19.942

Entries	705
Mean	45.68
Std Dev	10.66

ampl



B1L001S, U24-ch118

calib_packv5_042523_0143.root, FC#2, port C2

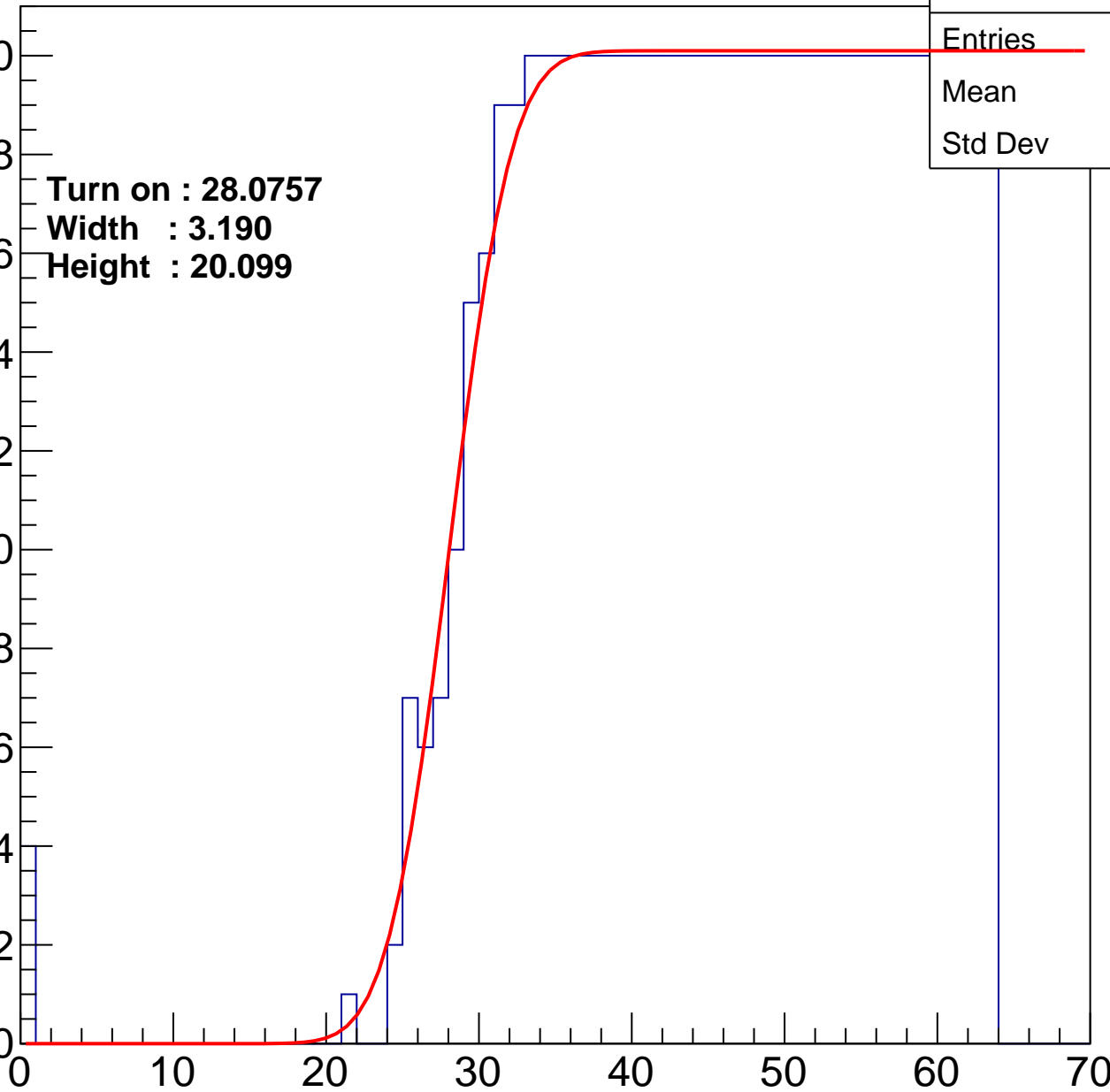
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0757
Width : 3.190
Height : 20.099

Entries	726
Mean	45.1
Std Dev	11.09

ampl



B1L001S, U24-ch119

calib_packv5_042523_0143.root, FC#2, port C2

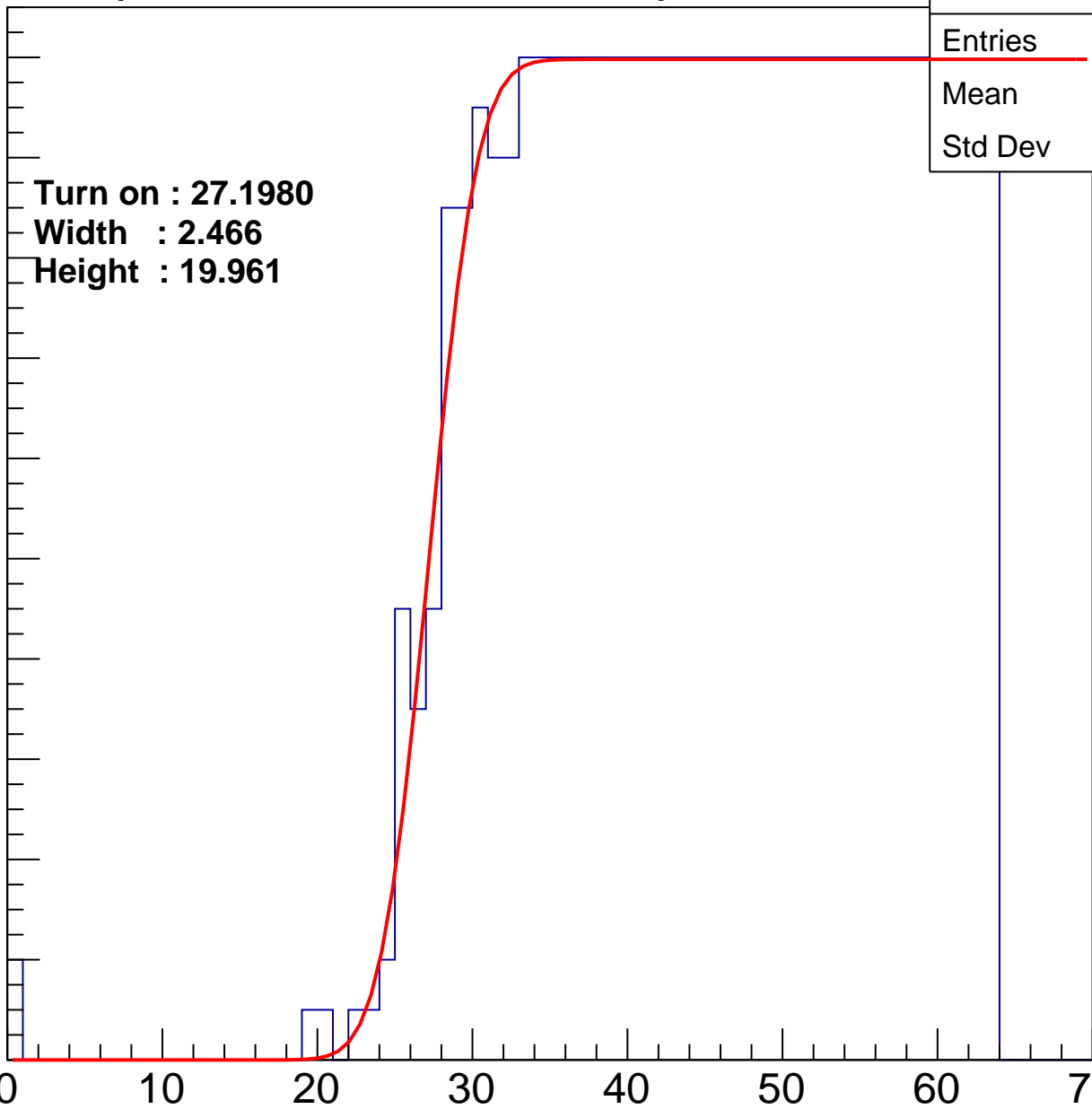
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1980
Width : 2.466
Height : 19.961

Entries	742
Mean	44.76
Std Dev	11.11

ampl



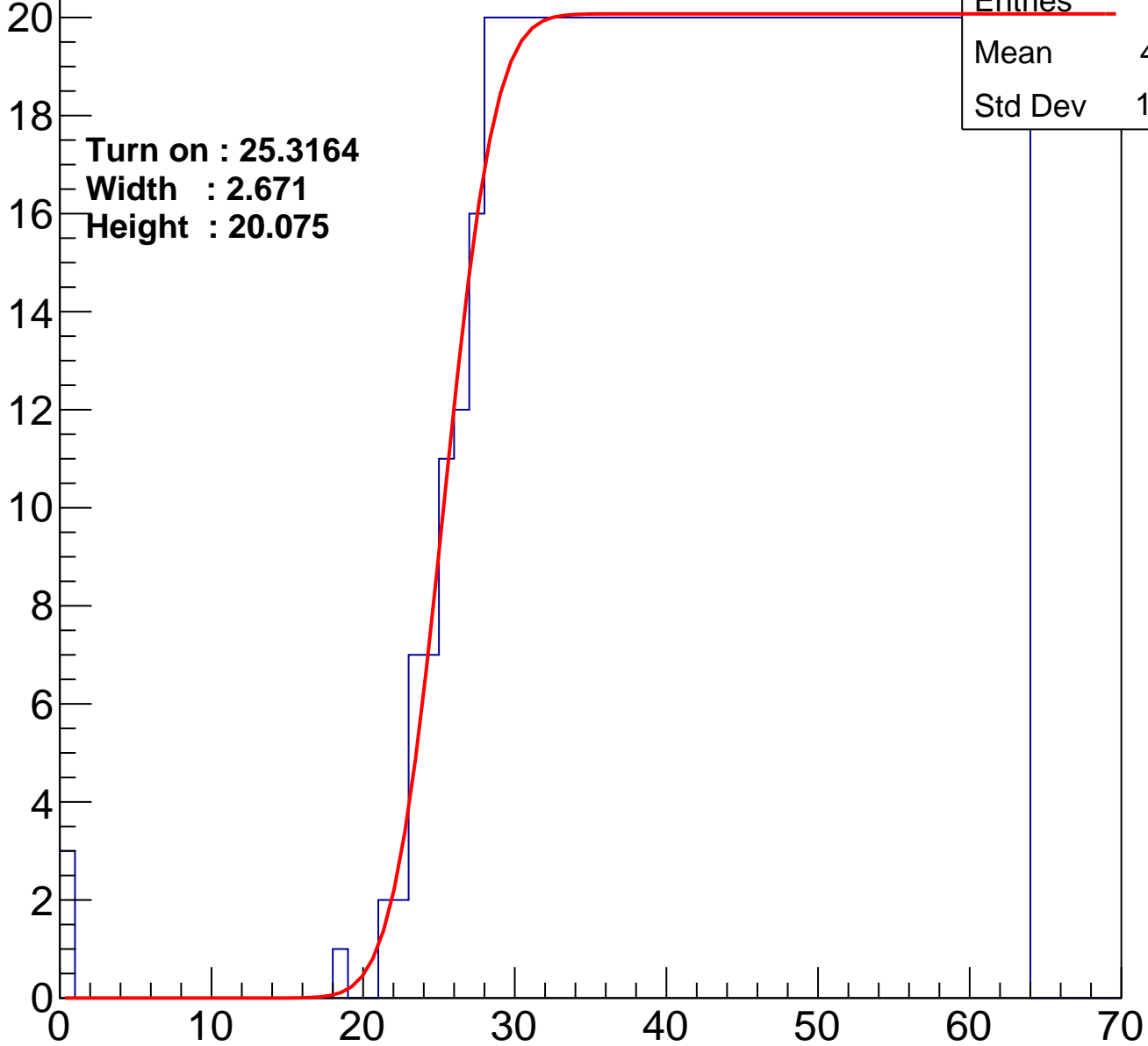
B1L001S, U24-ch120

calib_packv5_042523_0143.root, FC#2, port C2

Entries	781
Mean	43.81
Std Dev	11.66

Turn on : 25.3164
Width : 2.671
Height : 20.075

Entry



B1L001S, U24-ch121

calib_packv5_042523_0143.root, FC#2, port C2

Entry

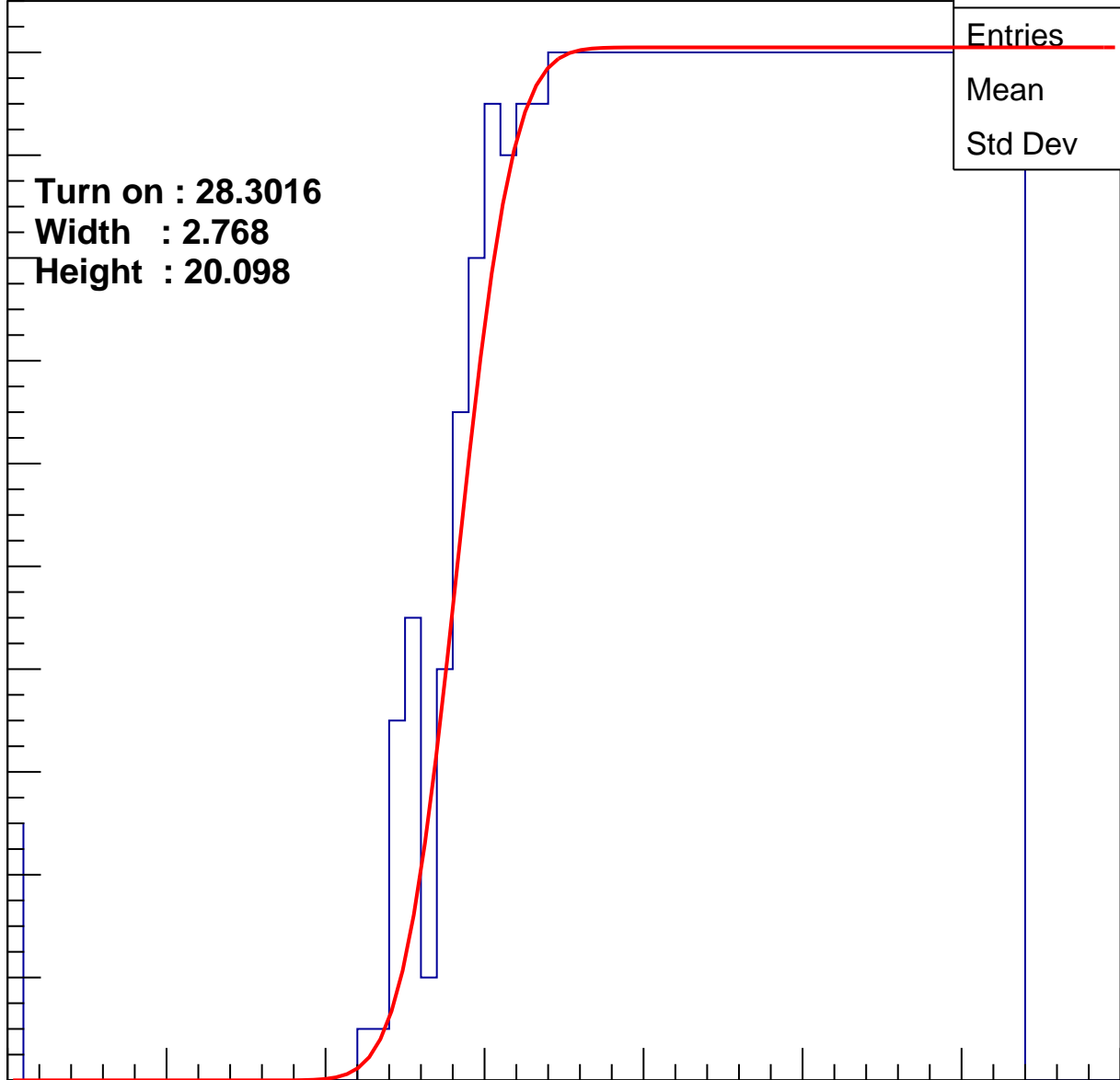
20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3016
Width : 2.768
Height : 20.098

Entries	737
Mean	44.77
Std Dev	11.35

ampl

0 10 20 30 40 50 60 70



B1L001S, U24-ch122

calib_packv5_042523_0143.root, FC#2, port C2

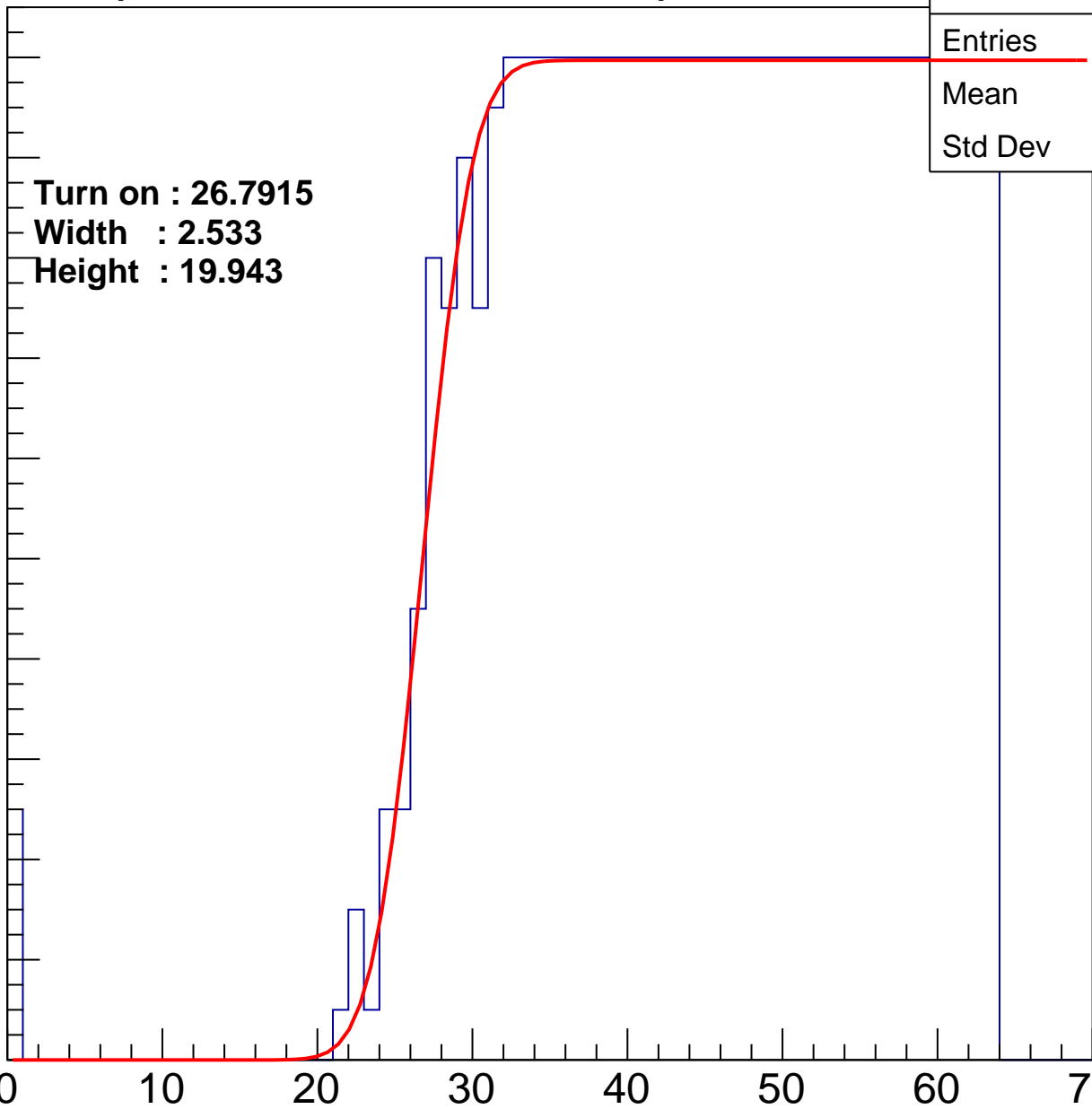
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7915
Width : 2.533
Height : 19.943

Entries	752
Mean	44.42
Std Dev	11.52

ampl



B1L001S, U24-ch123

calib_packv5_042523_0143.root, FC#2, port C2

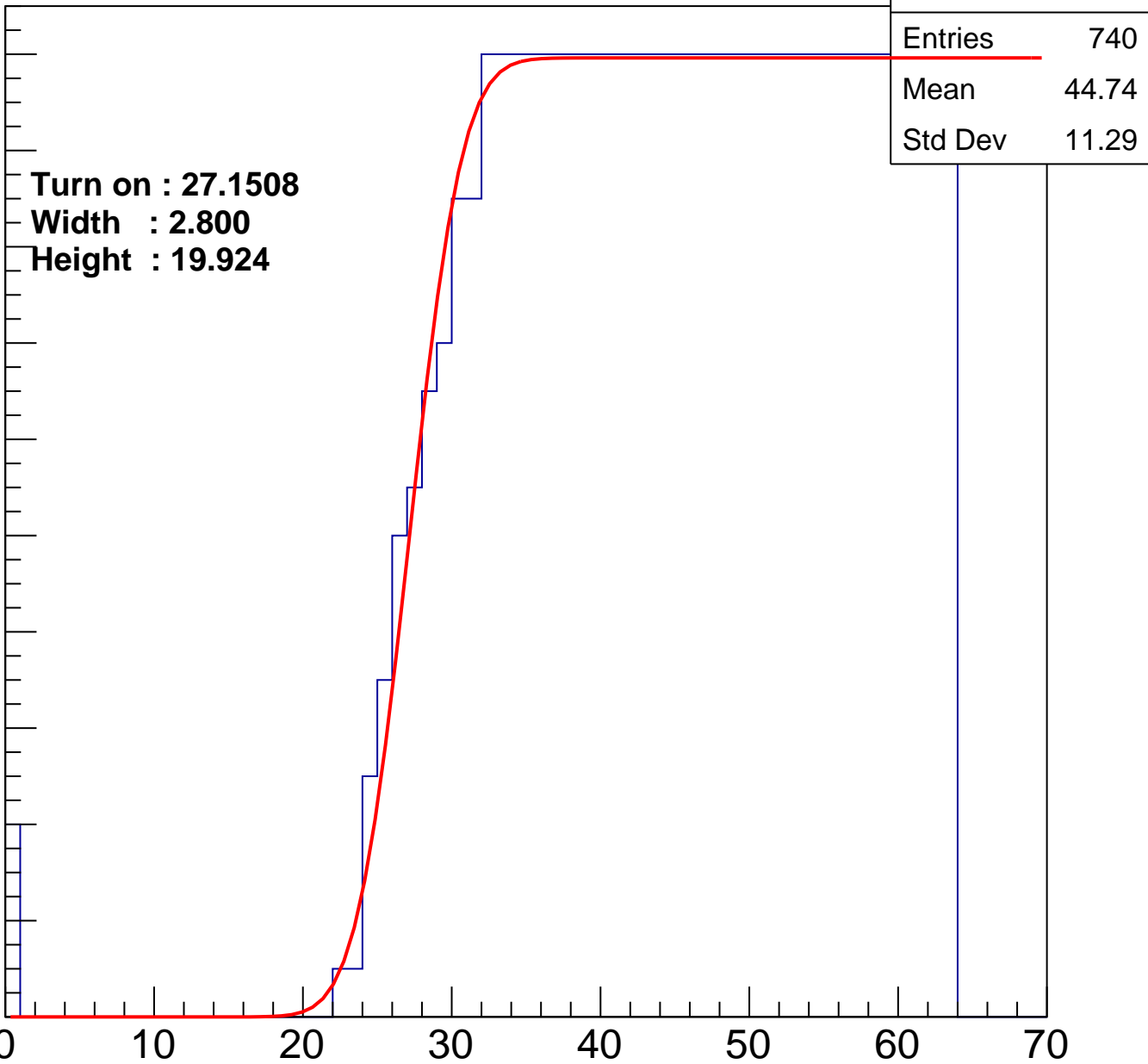
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1508
Width : 2.800
Height : 19.924

Entries	740
Mean	44.74
Std Dev	11.29

ampl



B1L001S, U24-ch124

calib_packv5_042523_0143.root, FC#2, port C2

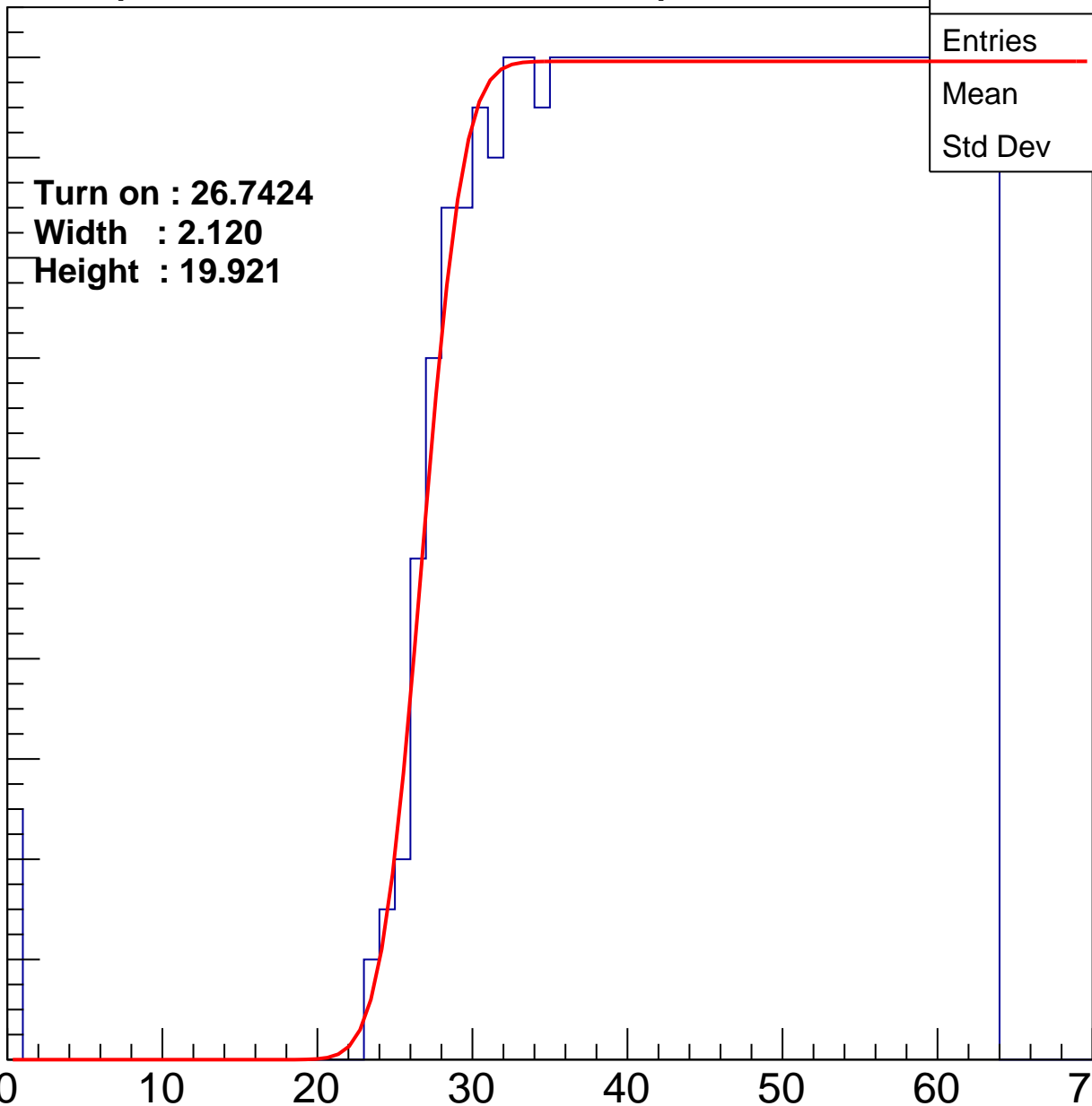
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7424
Width : 2.120
Height : 19.921

Entries	748
Mean	44.54
Std Dev	11.42

ampl



B1L001S, U24-ch125

calib_packv5_042523_0143.root, FC#2, port C2

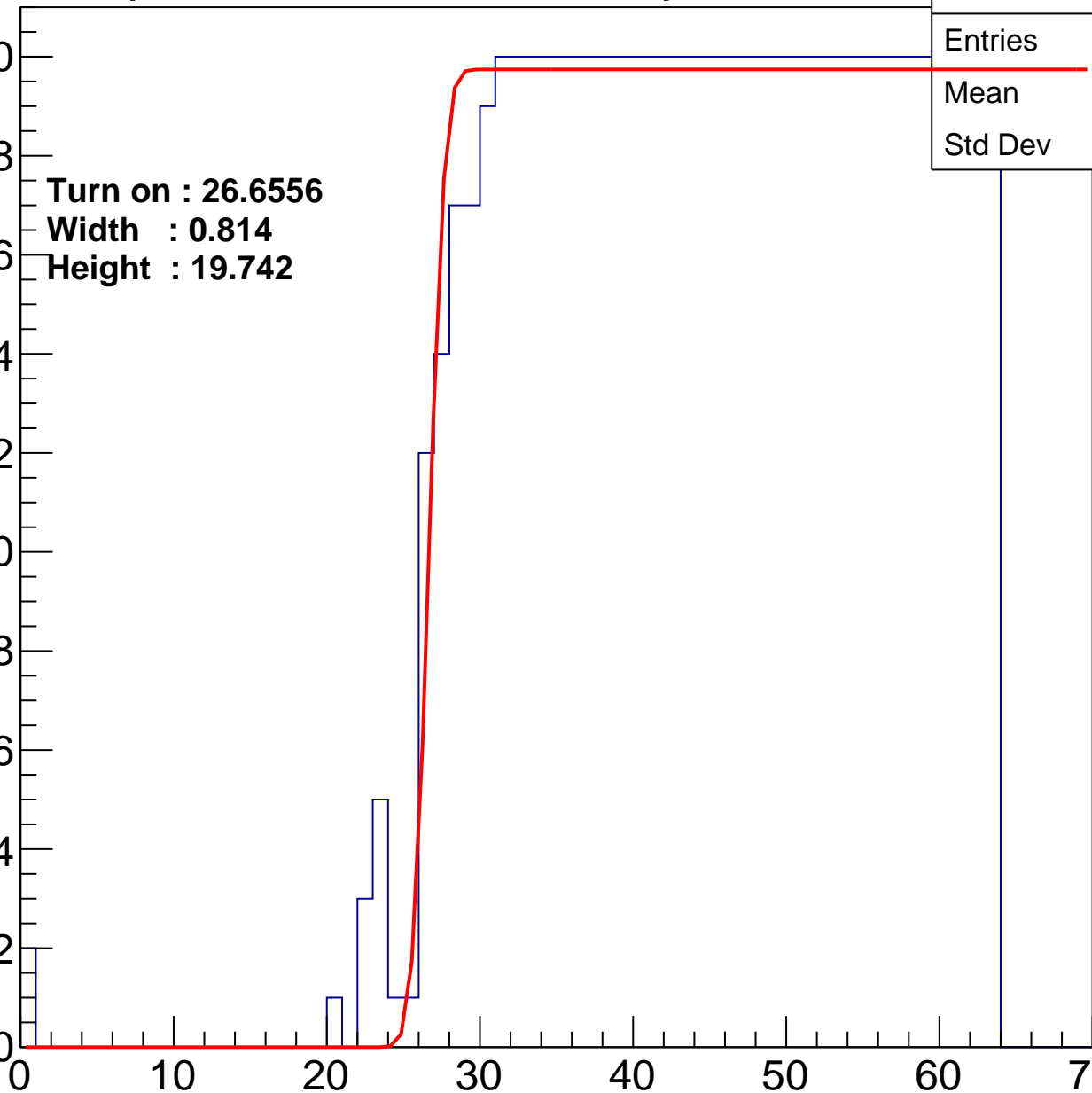
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6556
Width : 0.814
Height : 19.742

Entries	752
Mean	44.55
Std Dev	11.2

ampl



B1L001S, U24-ch126

calib_packv5_042523_0143.root, FC#2, port C2

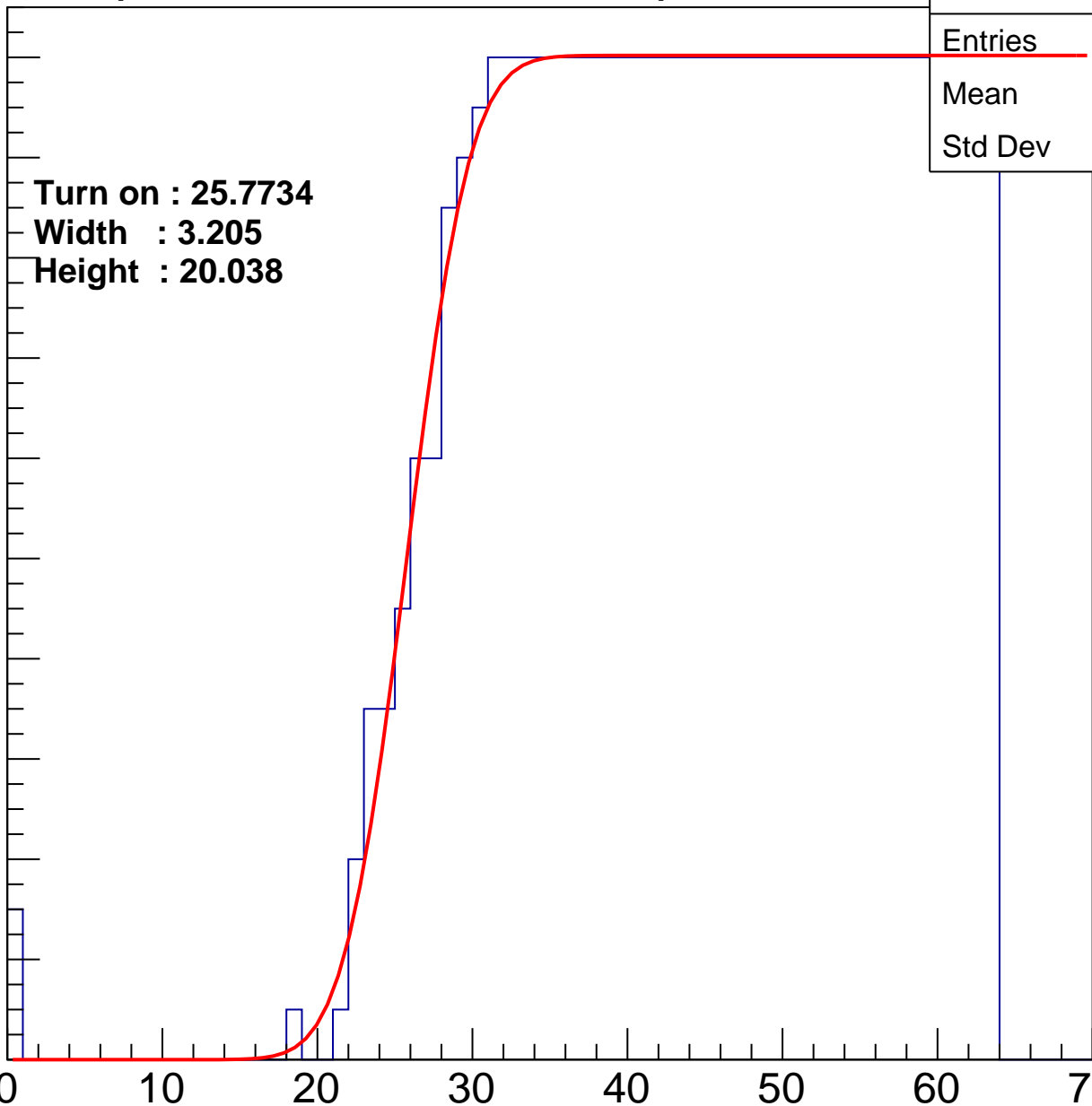
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7734
Width : 3.205
Height : 20.038

Entries	770
Mean	44.03
Std Dev	11.59

ampl



B1L001S, U24-ch127

calib_packv5_042523_0143.root, FC#2, port C2

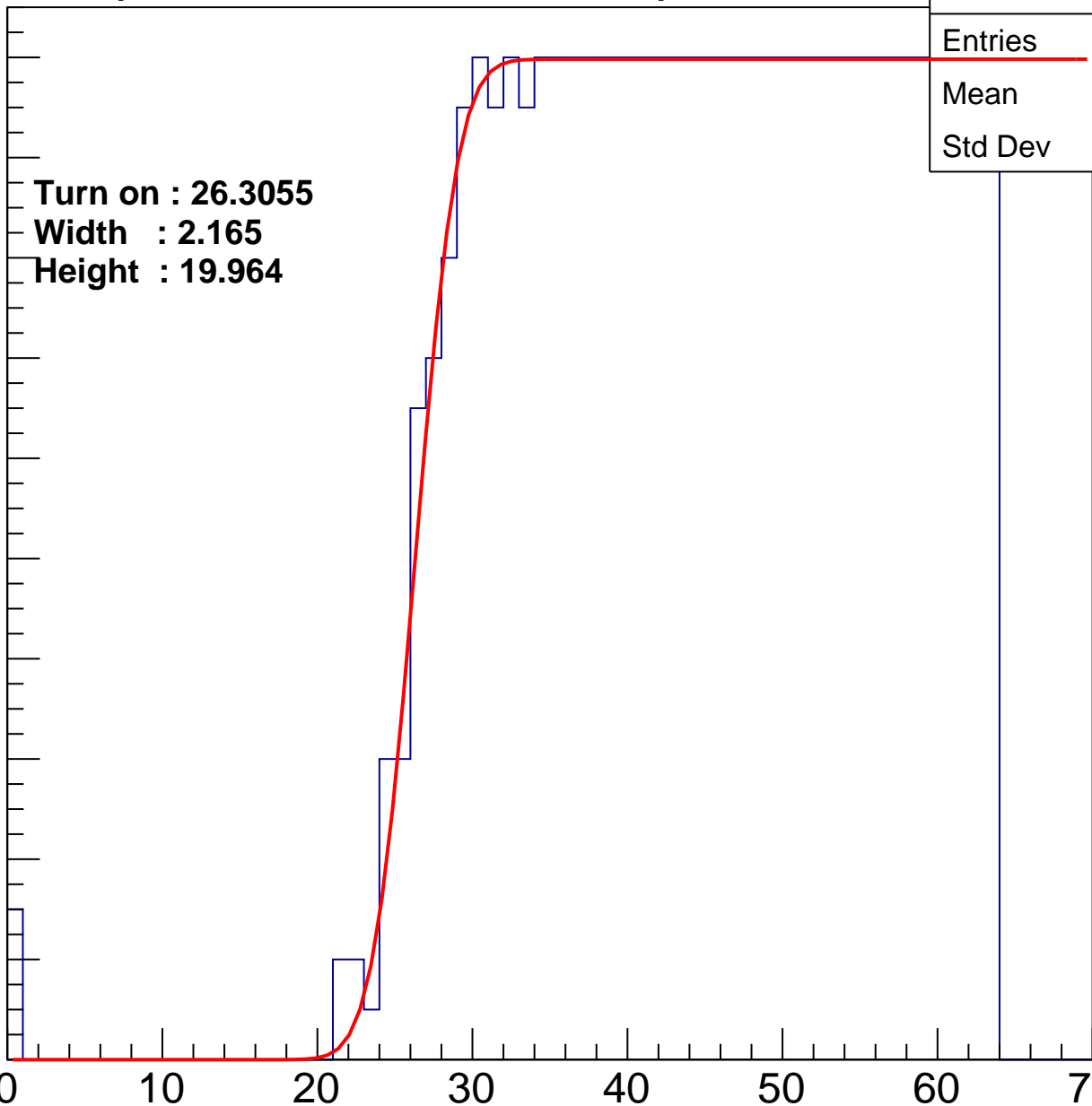
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3055
Width : 2.165
Height : 19.964

Entries	760
Mean	44.31
Std Dev	11.4

ampl



B1L001S, U24-ch127

calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3055
Width : 2.165
Height : 19.964

Entries	760
Mean	44.31
Std Dev	11.4

ampl

