

B1L100S, U6-ch0

calib_packv5_042523_0143.root, FC#4, port A2

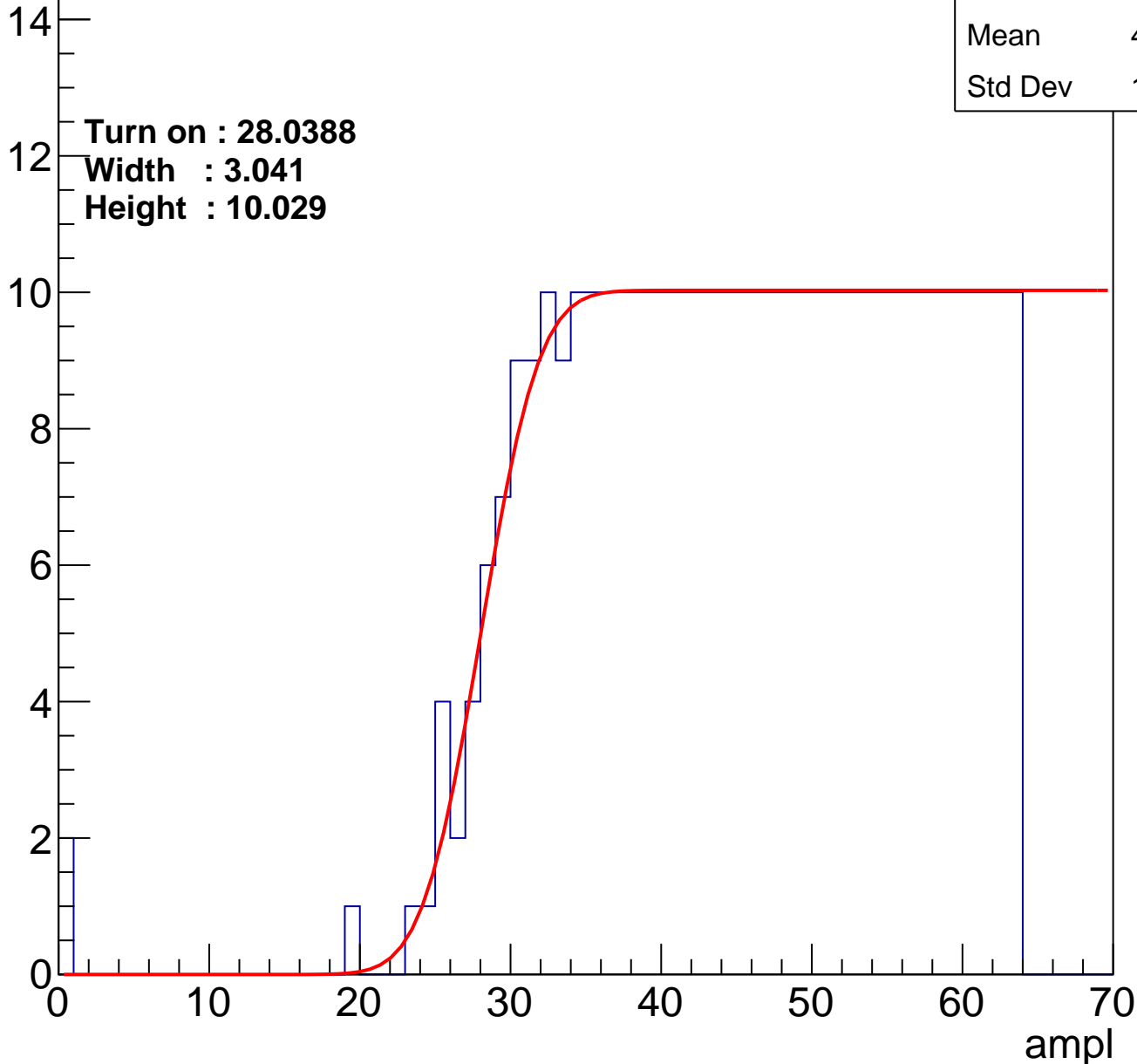
Entries	365
Mean	44.97
Std Dev	11.19

Turn on : 28.0388

Width : 3.041

Height : 10.029

Entry



B1L100S, U6-ch1

calib_packv5_042523_0143.root, FC#4, port A2

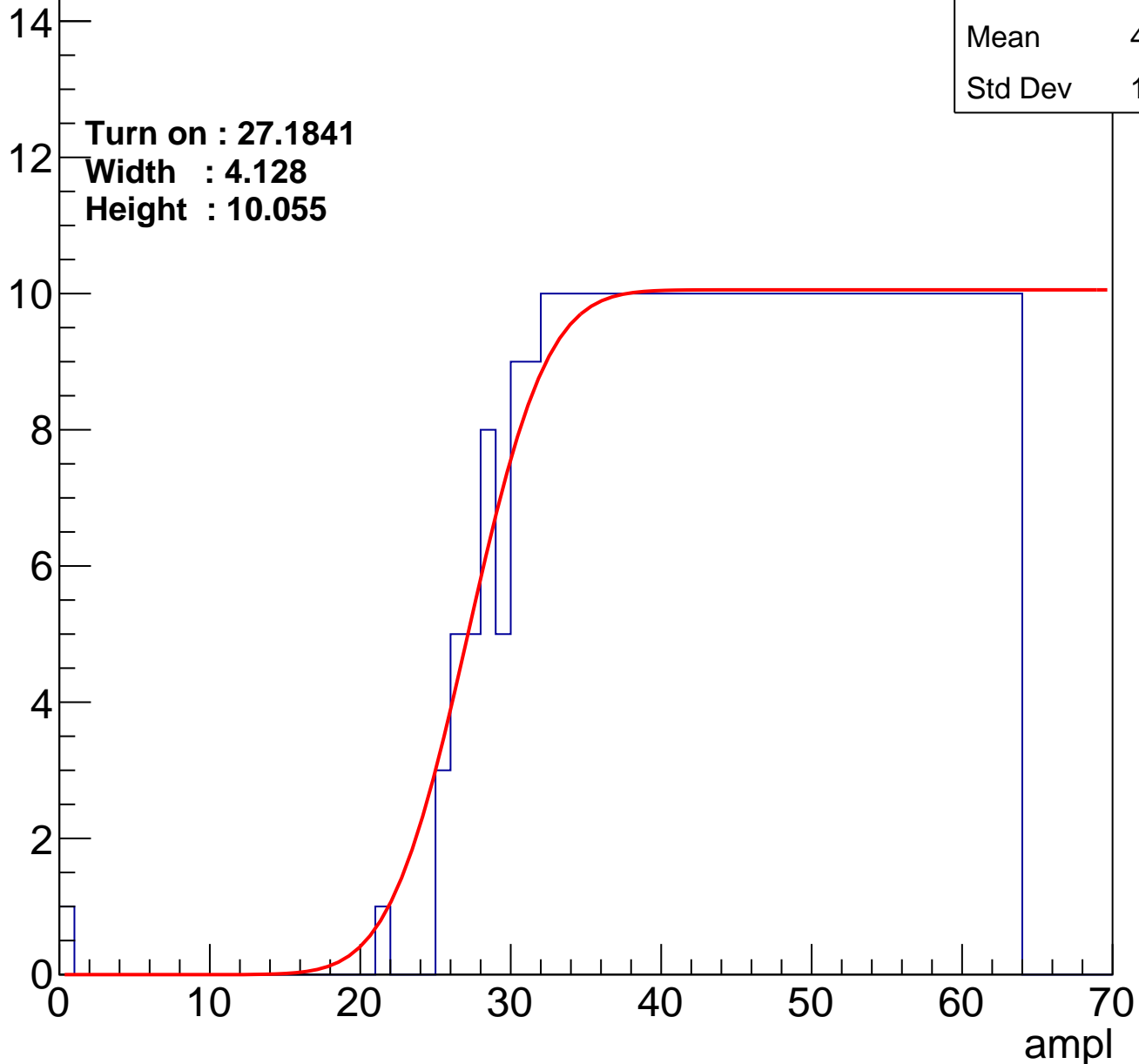
Entries	366
Mean	45.02
Std Dev	10.95

Turn on : 27.1841

Width : 4.128

Height : 10.055

Entry



B1L100S, U6-ch2

calib_packv5_042523_0143.root, FC#4, port A2

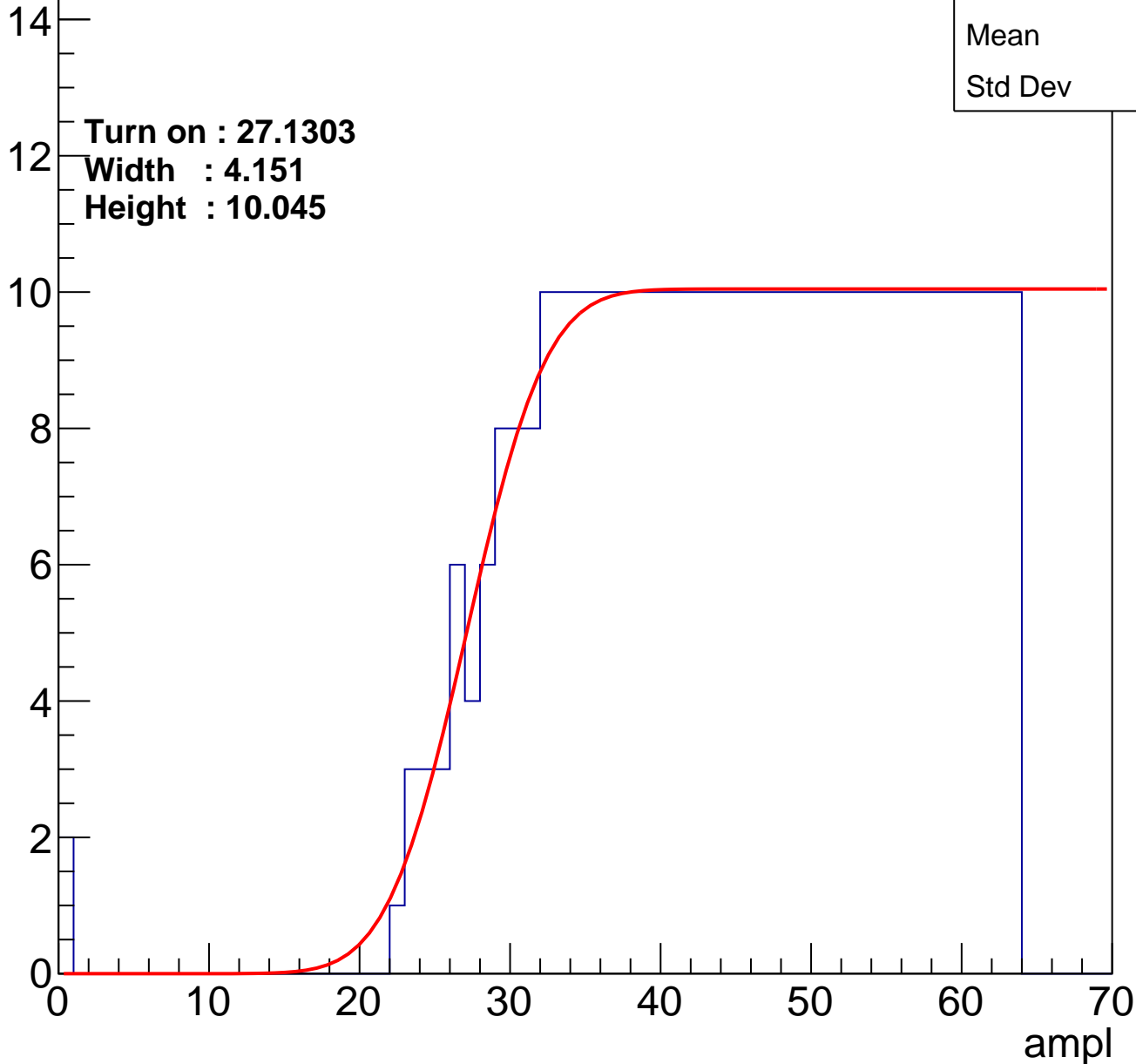
Entries	372
Mean	44.6
Std Dev	11.4

Turn on : 27.1303

Width : 4.151

Height : 10.045

Entry



B1L100S, U6-ch3

calib_packv5_042523_0143.root, FC#4, port A2

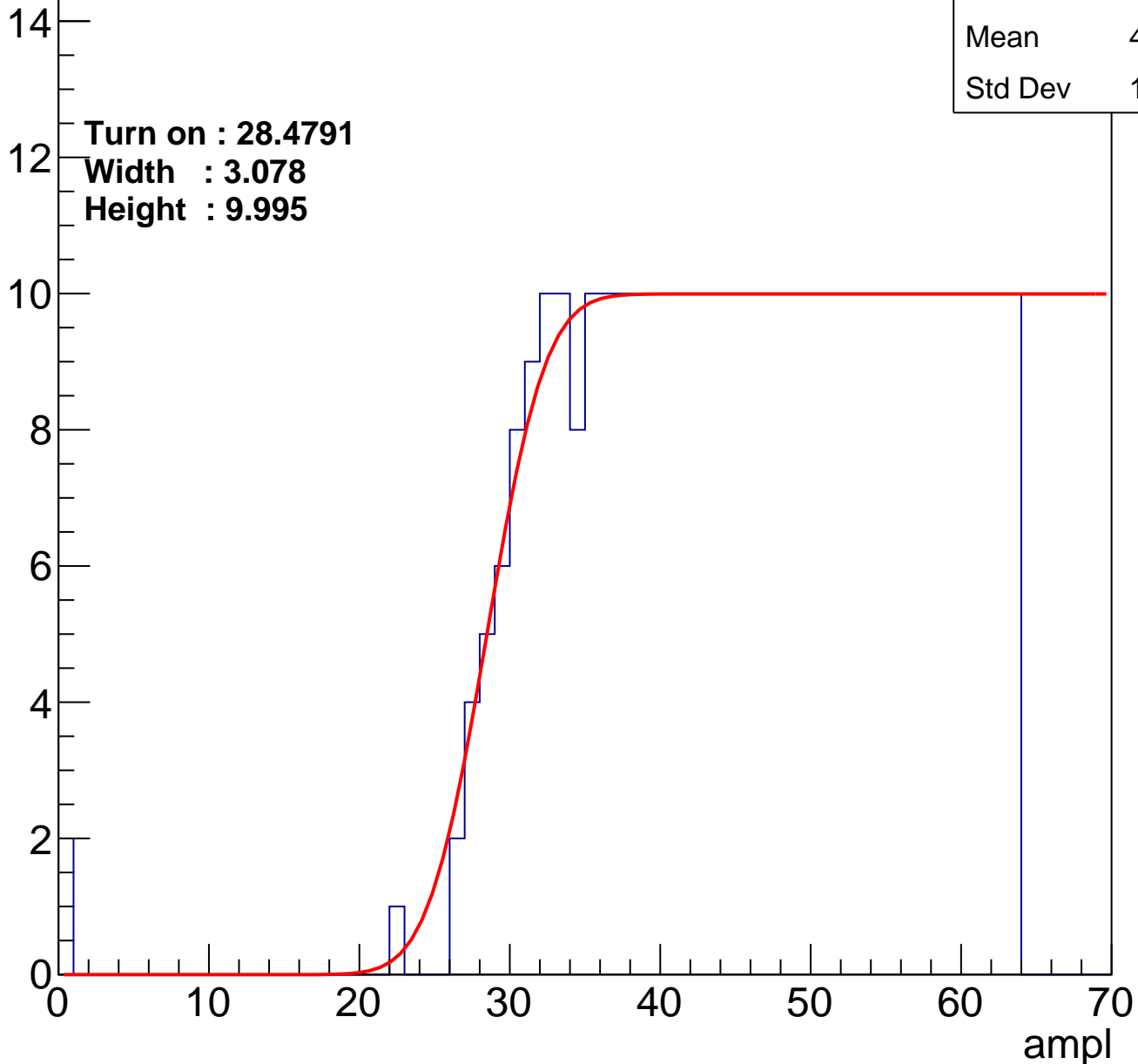
Entries	355
Mean	45.48
Std Dev	10.89

Turn on : 28.4791

Width : 3.078

Height : 9.995

Entry



B1L100S, U6-ch4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.67
Std Dev	11.35

Turn on : 27.5618

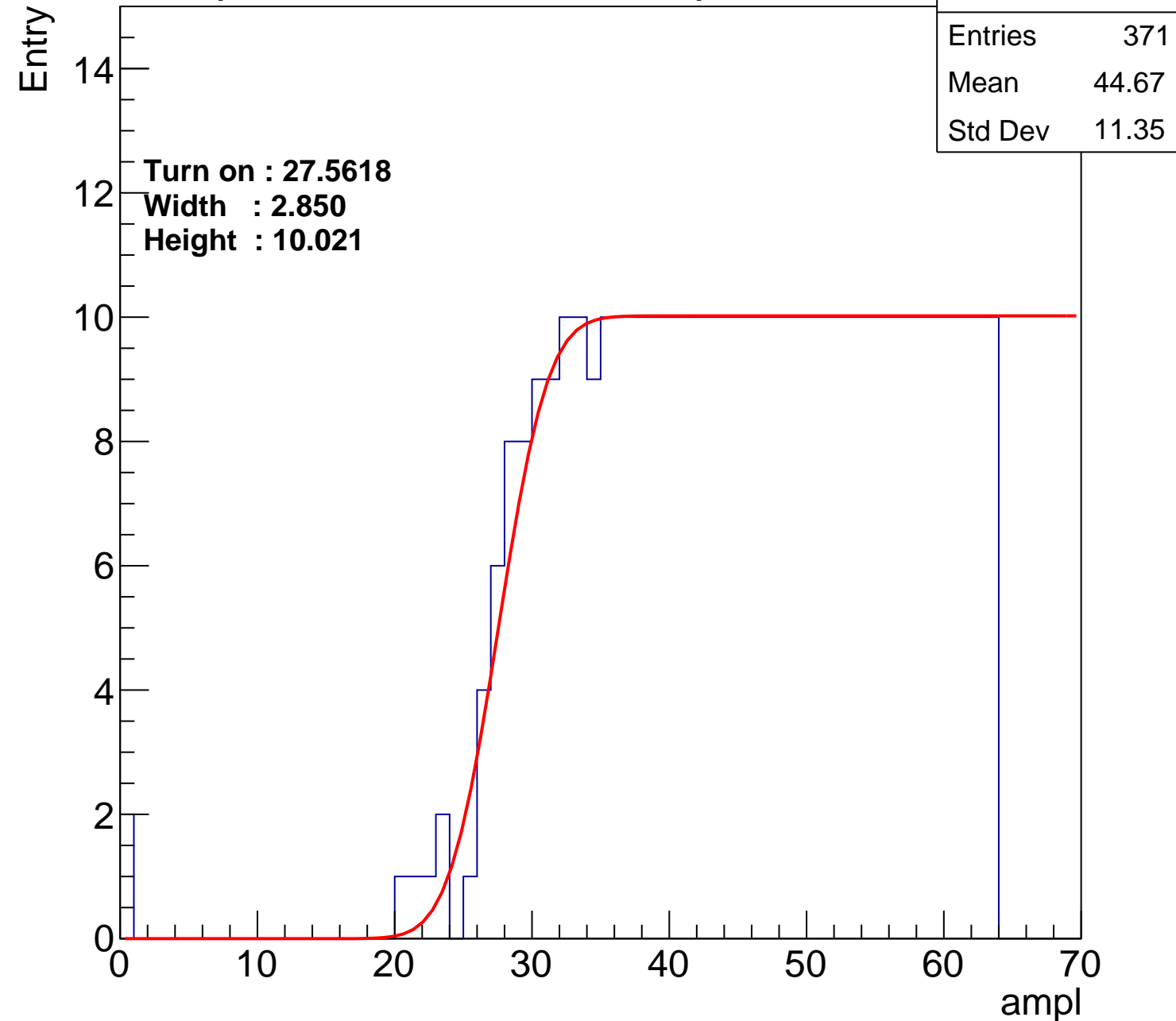
Width : 2.850

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch5

calib_packv5_042523_0143.root, FC#4, port A2

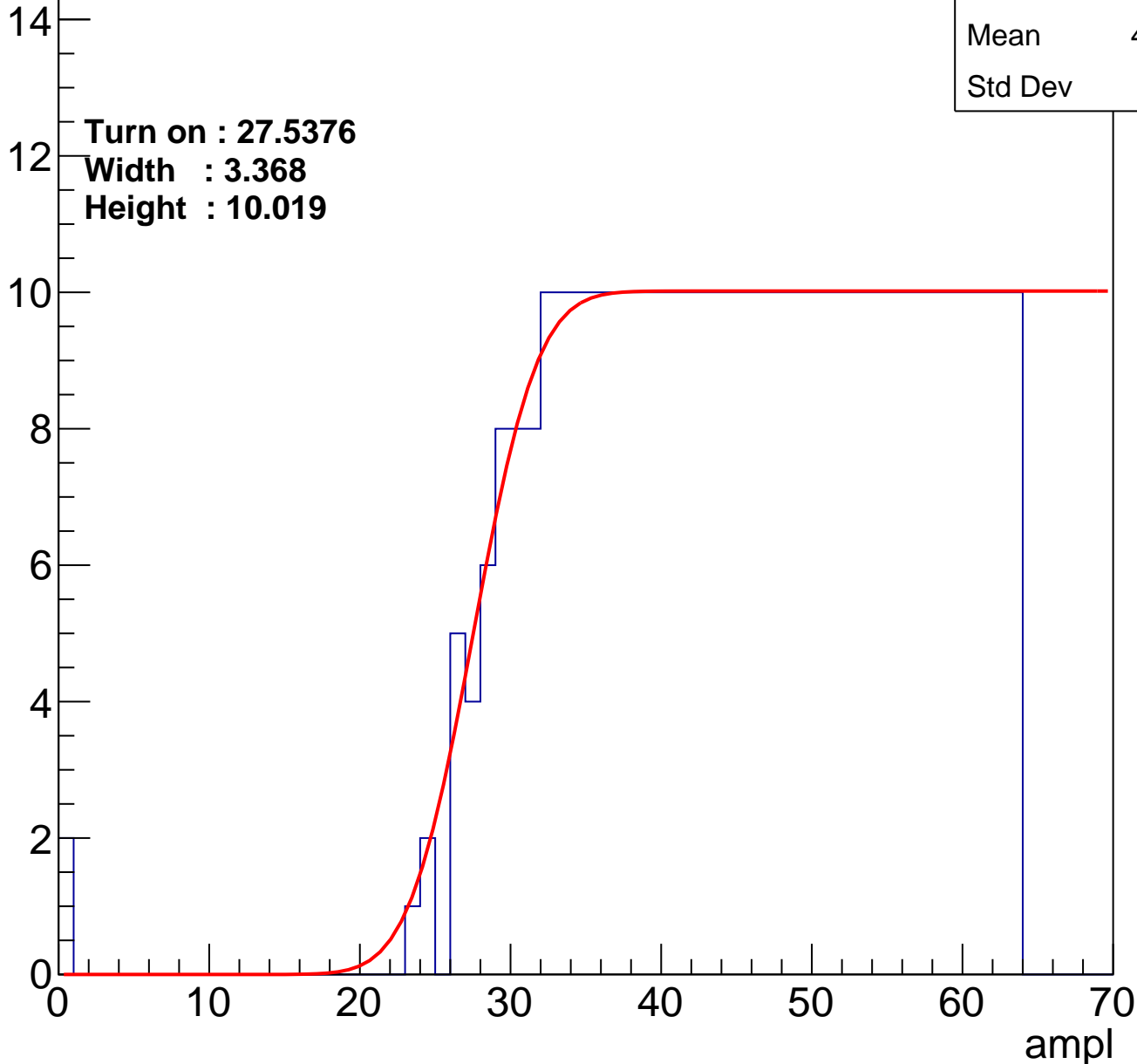
Entries	364
Mean	45.05
Std Dev	11.11

Turn on : 27.5376

Width : 3.368

Height : 10.019

Entry



B1L100S, U6-ch6

calib_packv5_042523_0143.root, FC#4, port A2

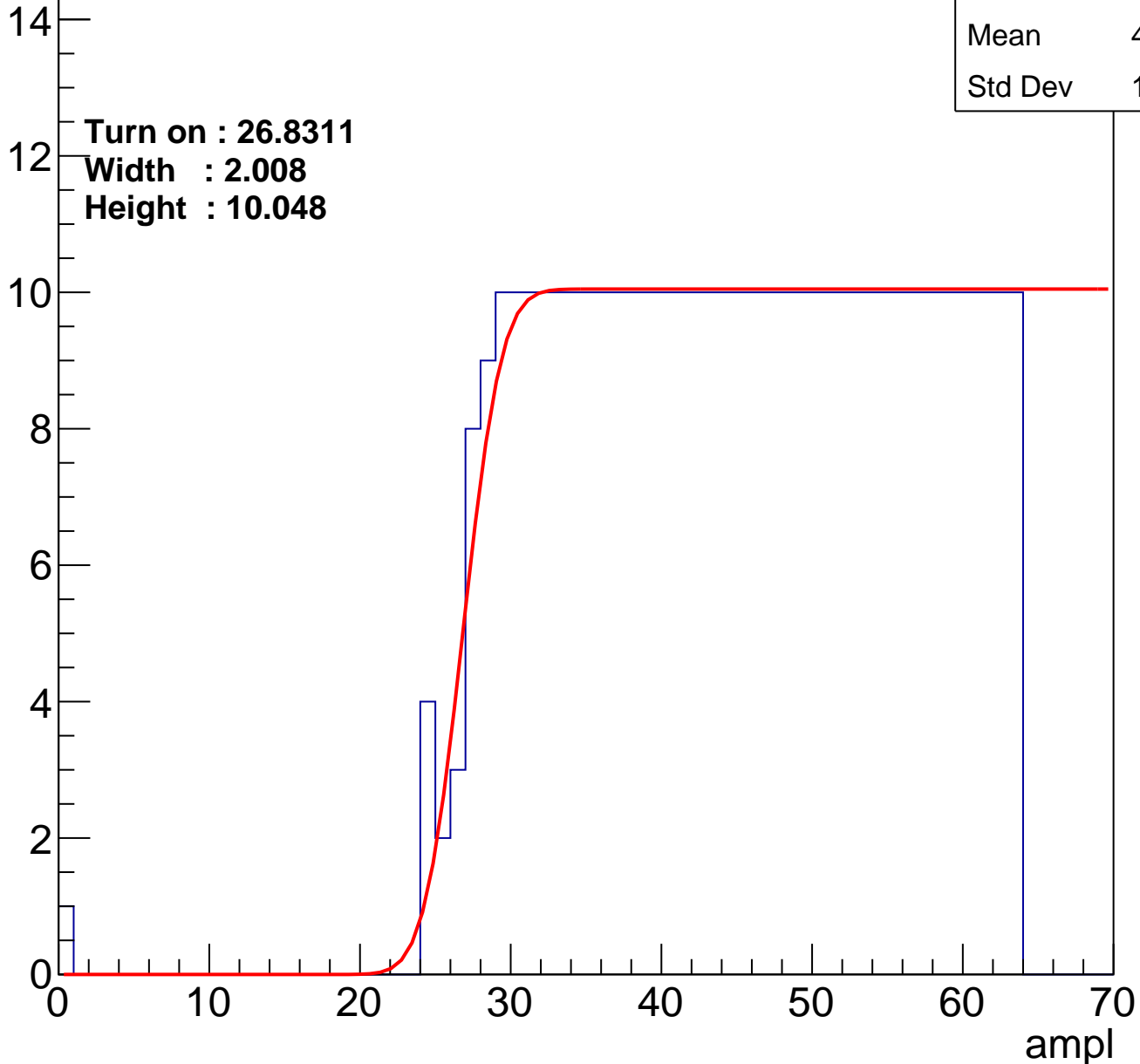
Entries	377
Mean	44.54
Std Dev	11.15

Turn on : 26.8311

Width : 2.008

Height : 10.048

Entry



B1L100S, U6-ch7

calib_packv5_042523_0143.root, FC#4, port A2

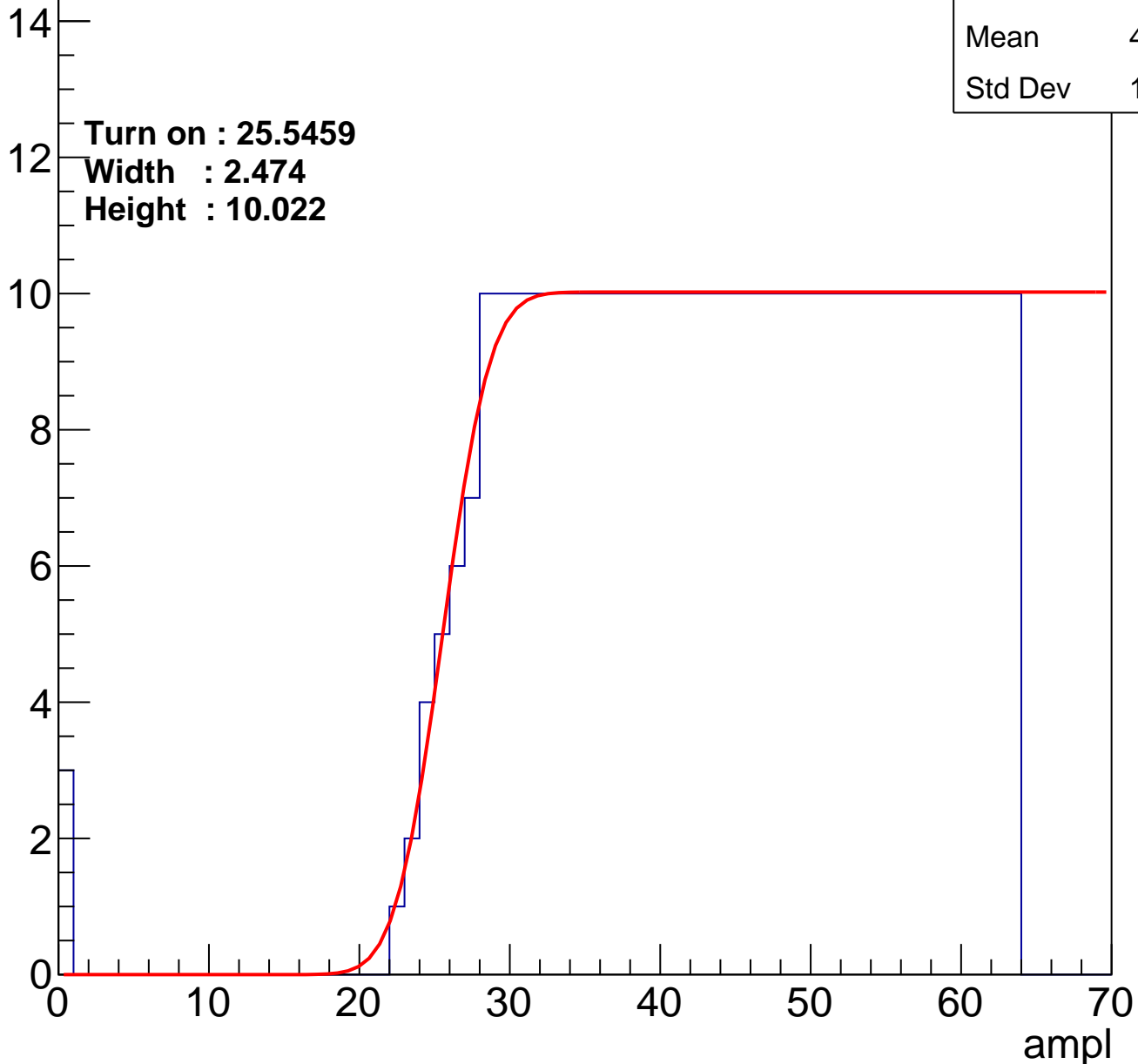
Entries	388
Mean	43.85
Std Dev	11.82

Turn on : 25.5459

Width : 2.474

Height : 10.022

Entry



B1L100S, U6-ch8

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.33
Std Dev	11.5

Turn on : 25.6584

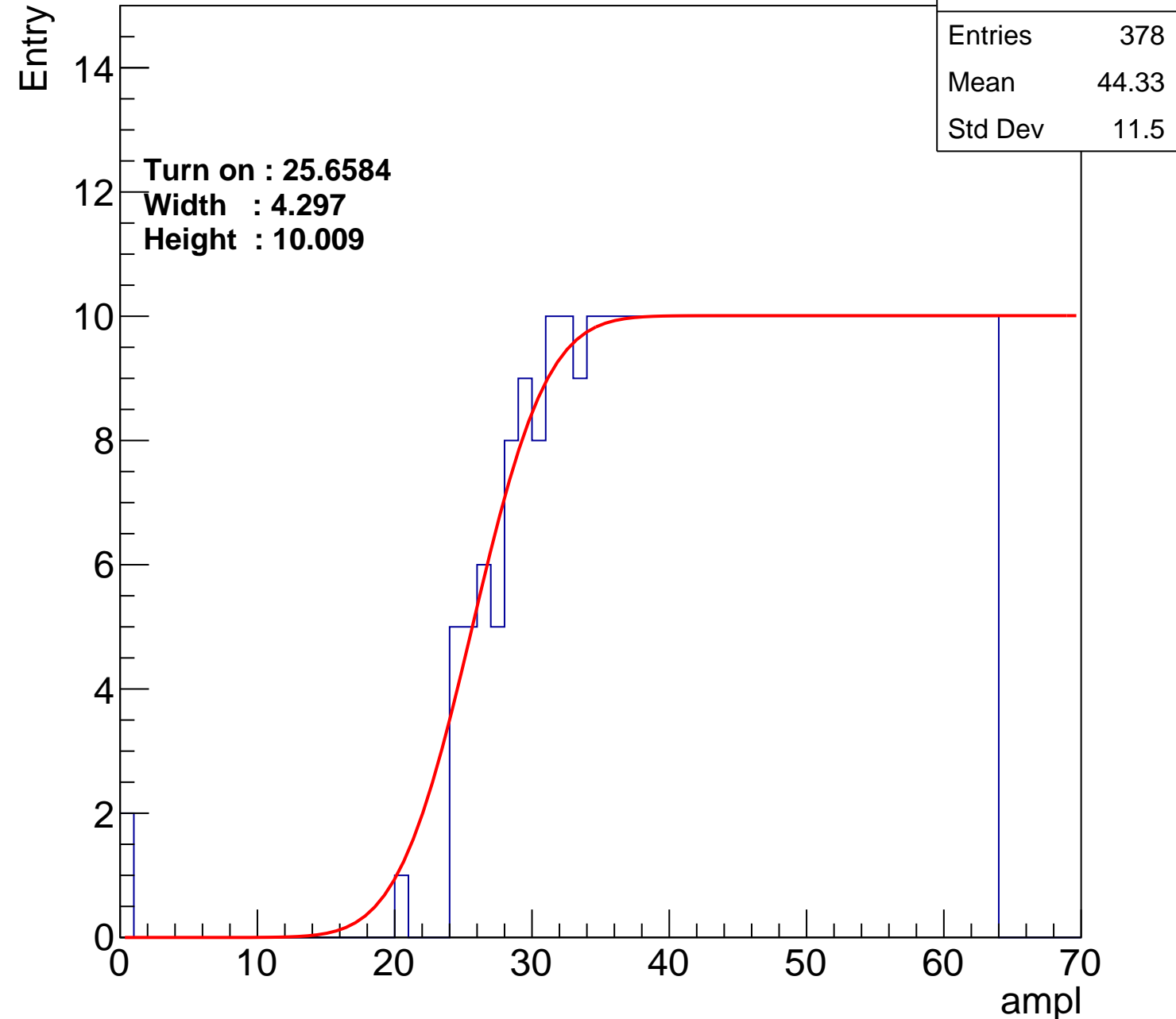
Width : 4.297

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch9

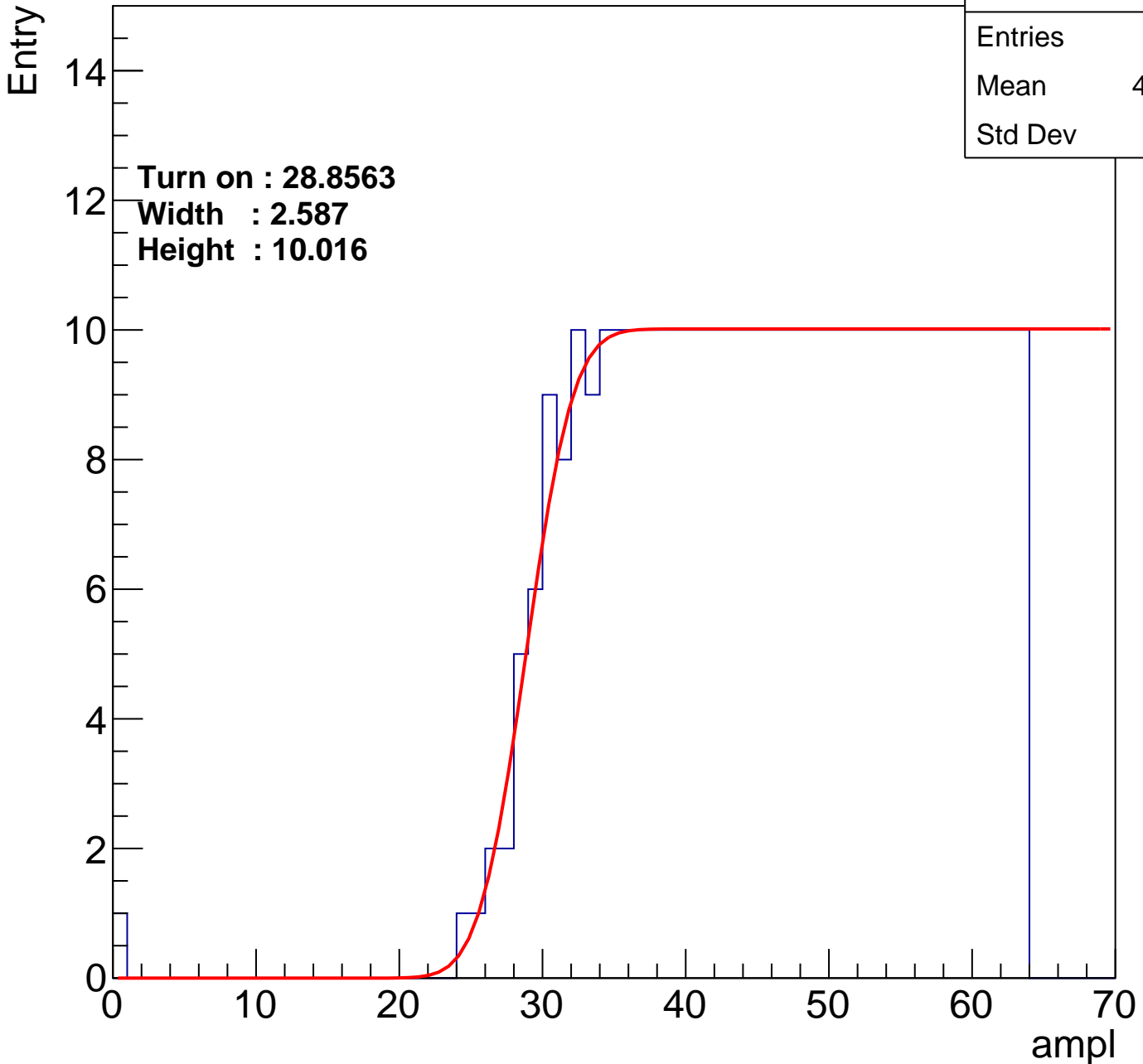
calib_packv5_042523_0143.root, FC#4, port A2

Entries	354
Mean	45.63
Std Dev	10.6

Turn on : 28.8563

Width : 2.587

Height : 10.016



B1L100S, U6-ch10

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.84
Std Dev	11.08

Turn on : 27.5346

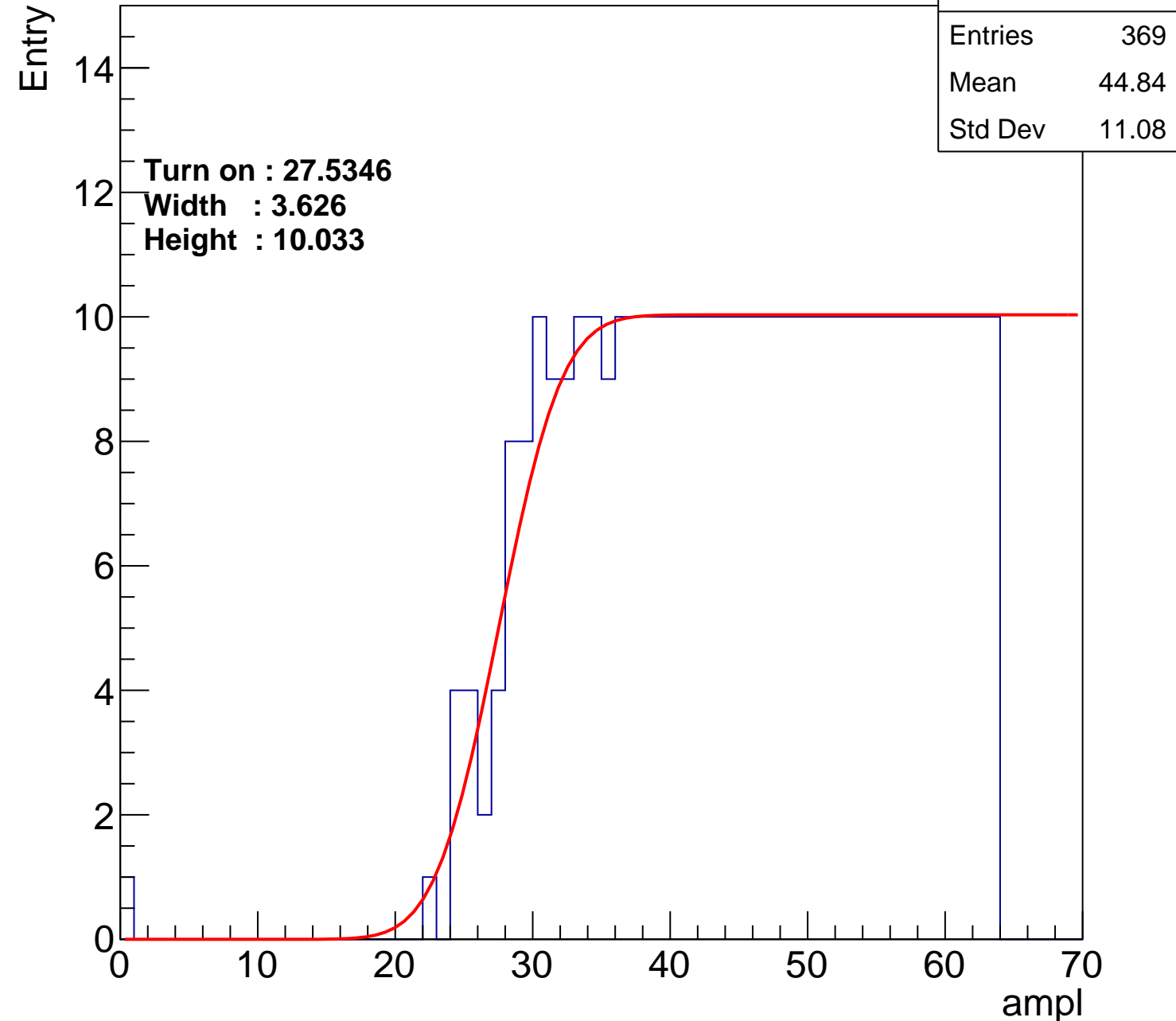
Width : 3.626

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch11

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.46
Std Dev	11.27

Turn on : 26.7122

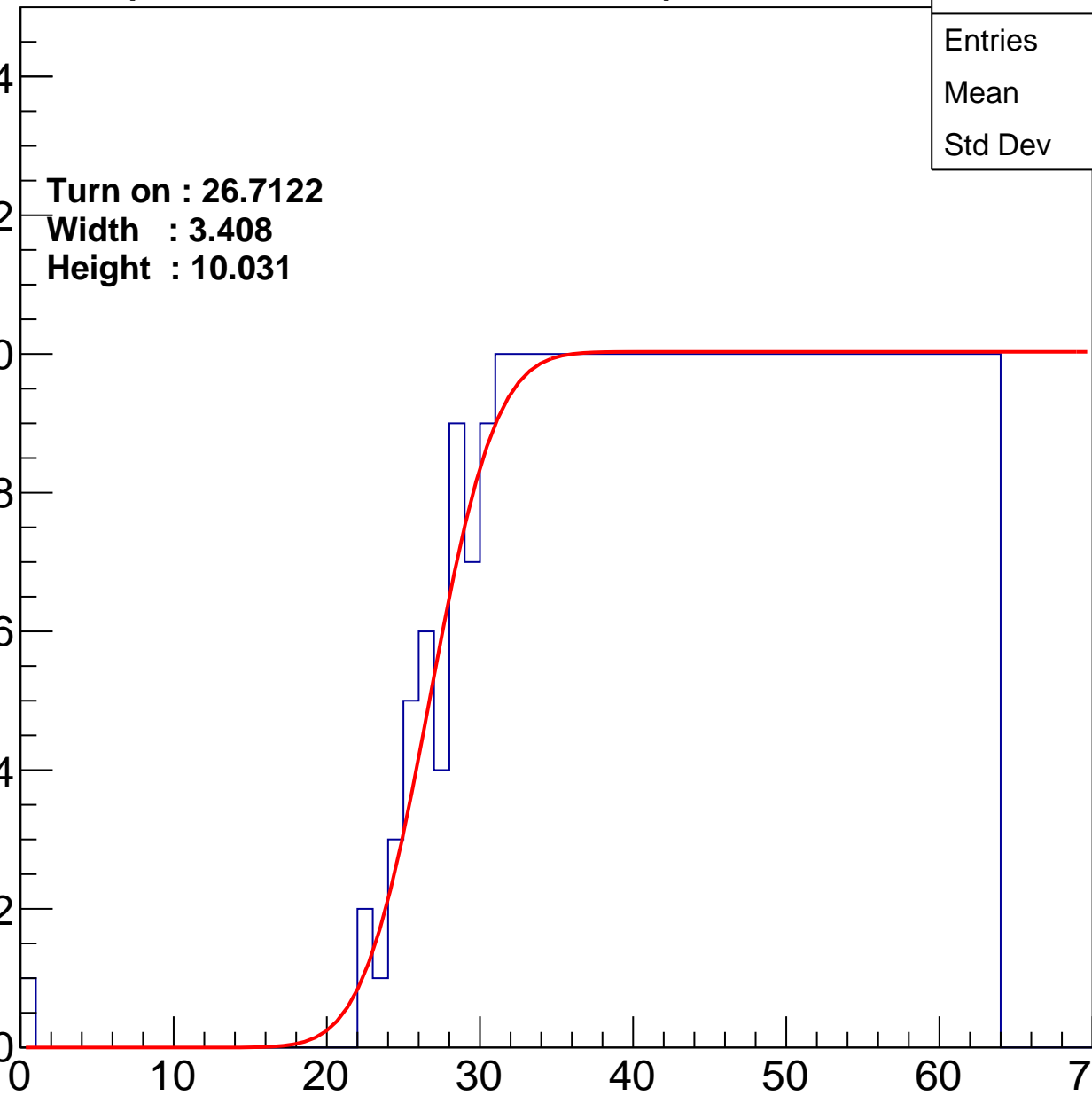
Width : 3.408

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch12

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.13
Std Dev	11.62

Turn on : 26.3235

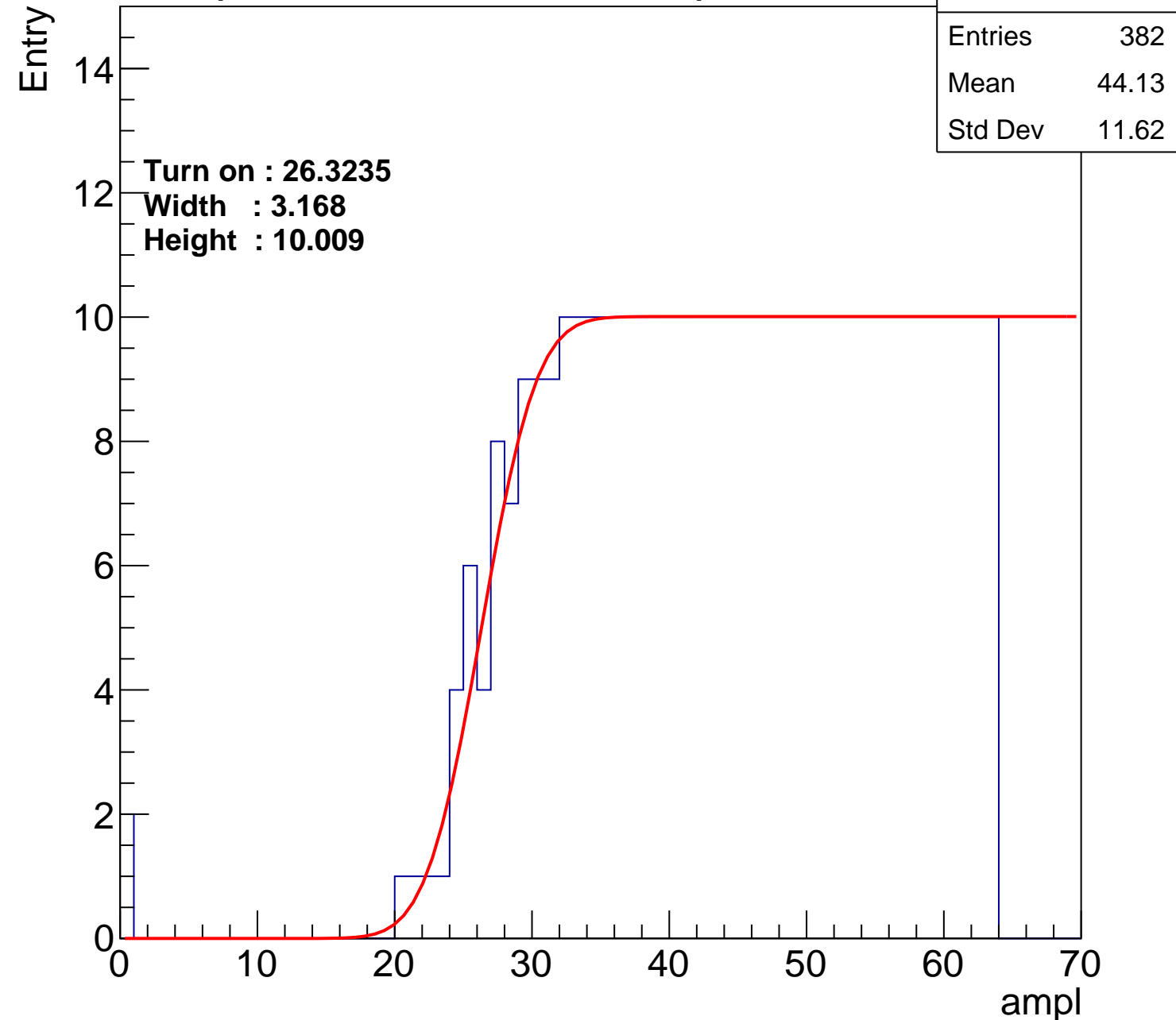
Width : 3.168

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch13

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.38
Std Dev	11.66

Turn on : 26.8686

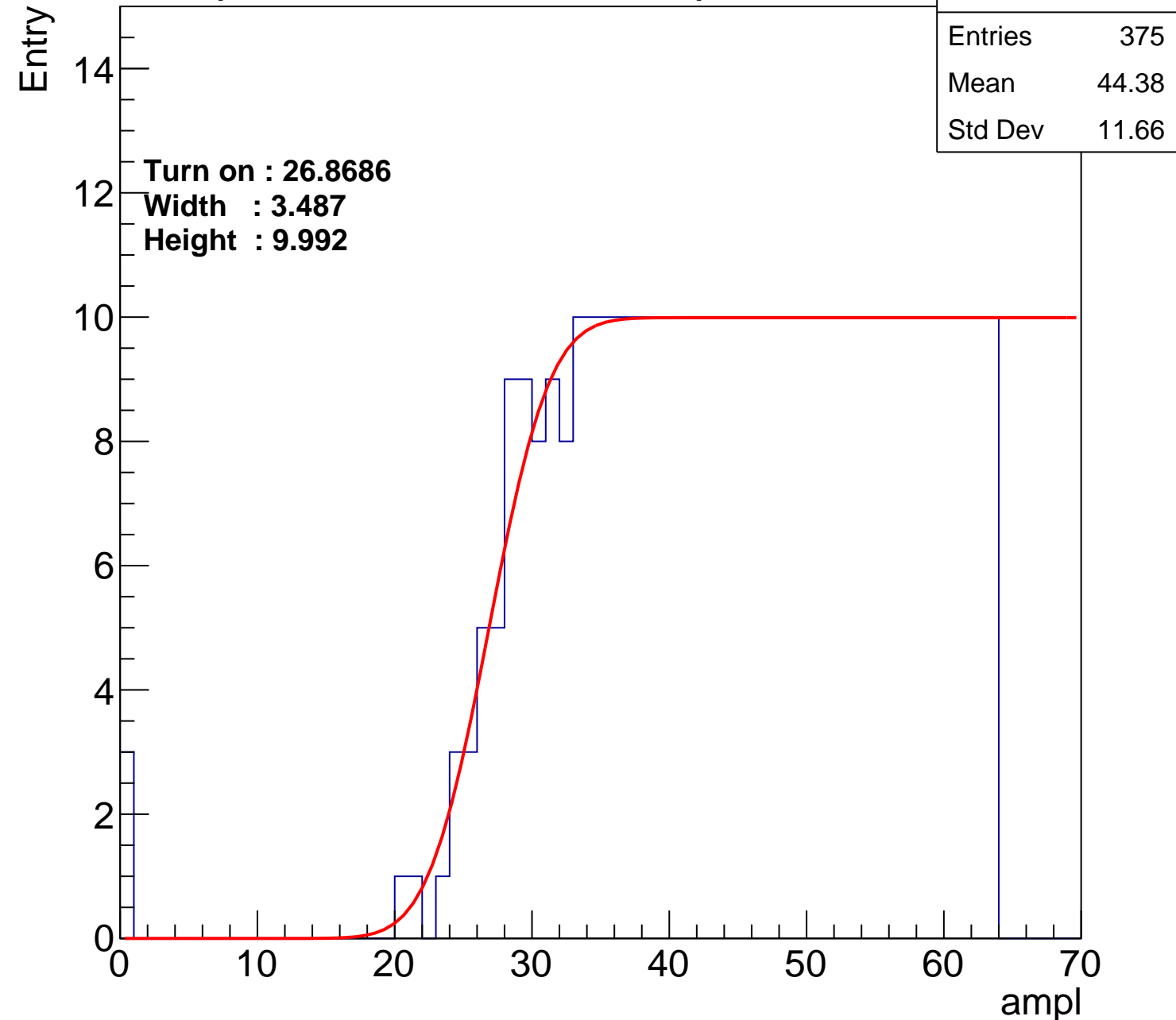
Width : 3.487

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch14

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.77
Std Dev	11.24

Turn on : 27.5767

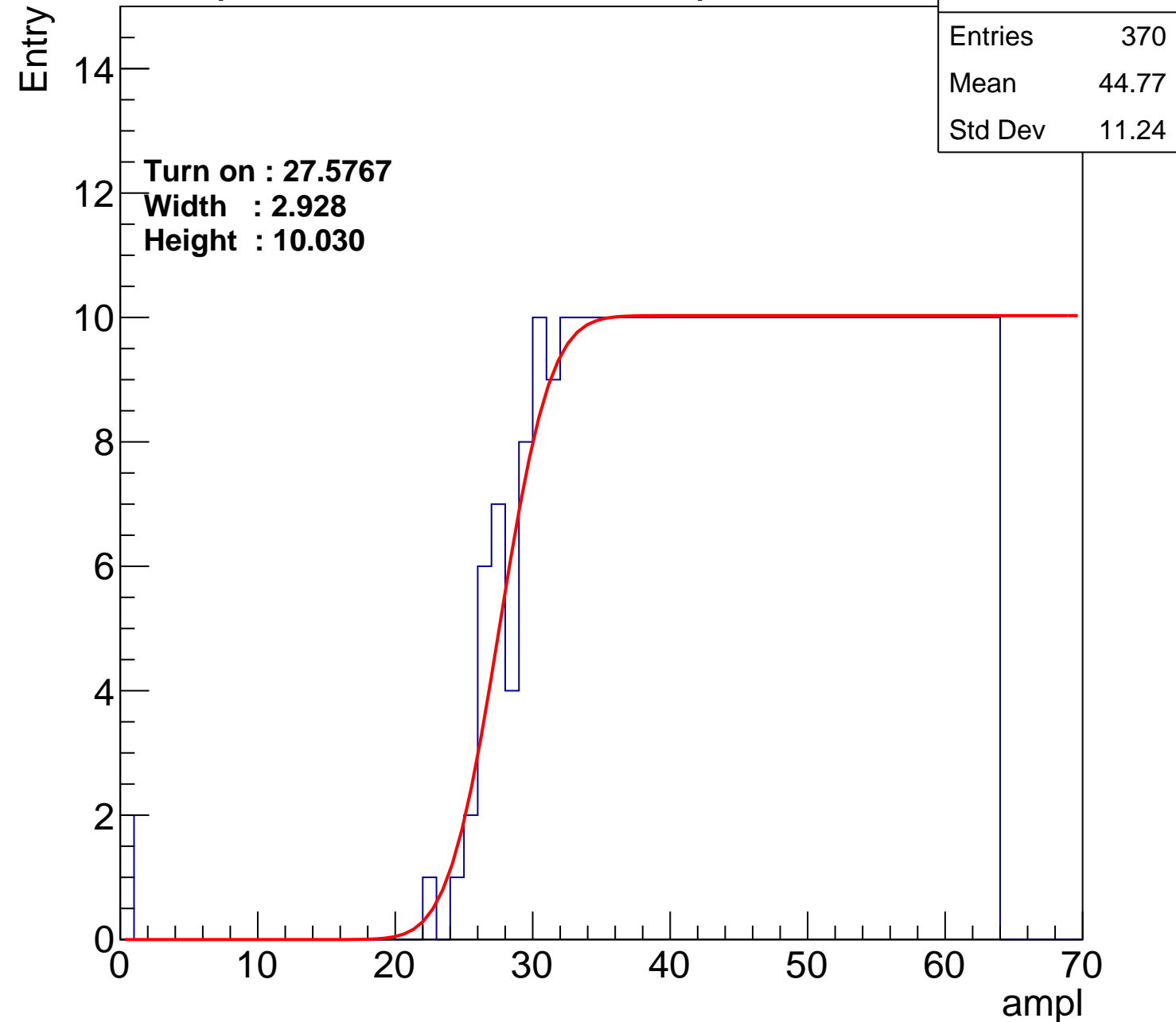
Width : 2.928

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch15

calib_packv5_042523_0143.root, FC#4, port A2

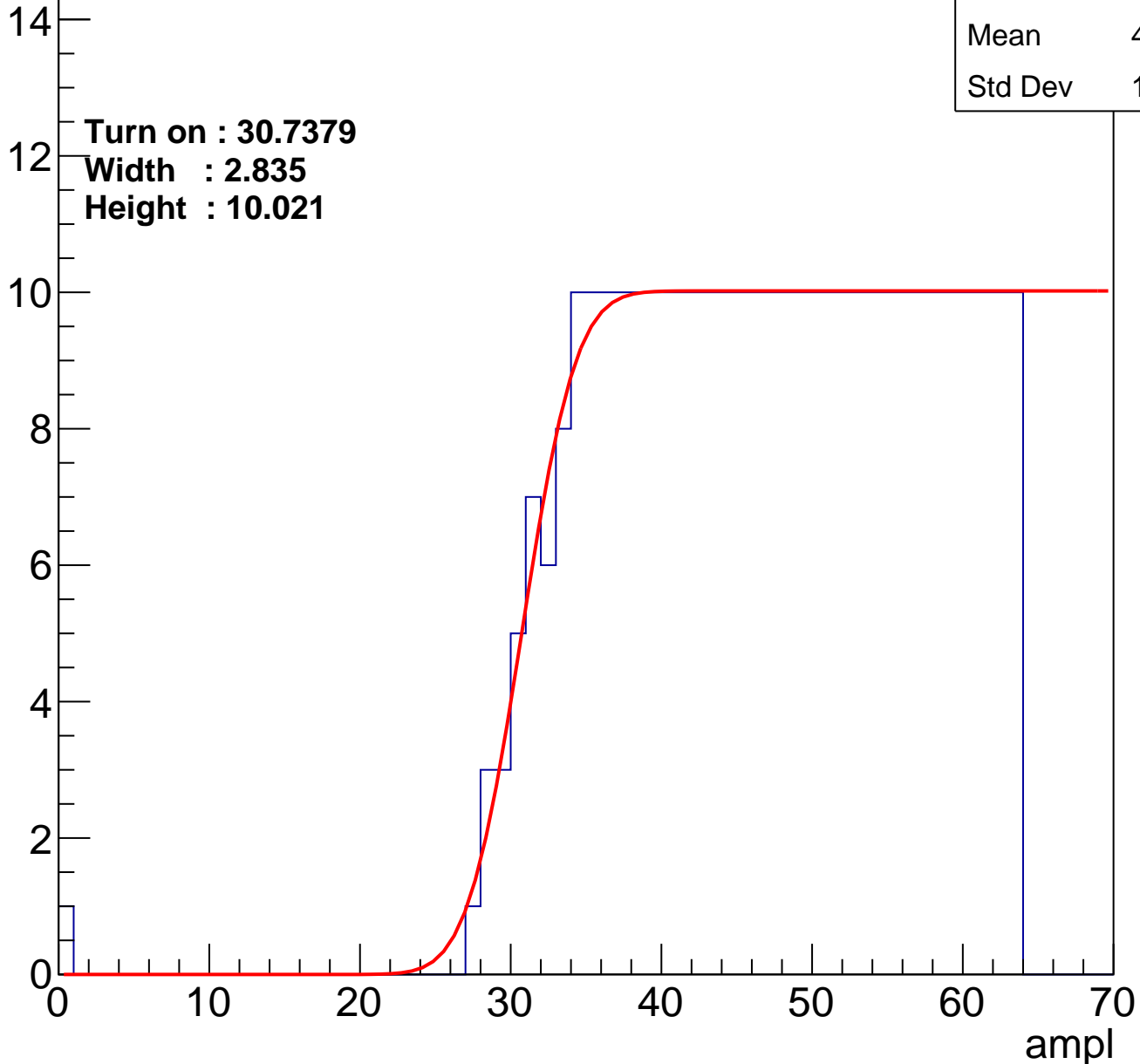
Entries	334
Mean	46.62
Std Dev	10.08

Turn on : 30.7379

Width : 2.835

Height : 10.021

Entry



B1L100S, U6-ch16

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.2657

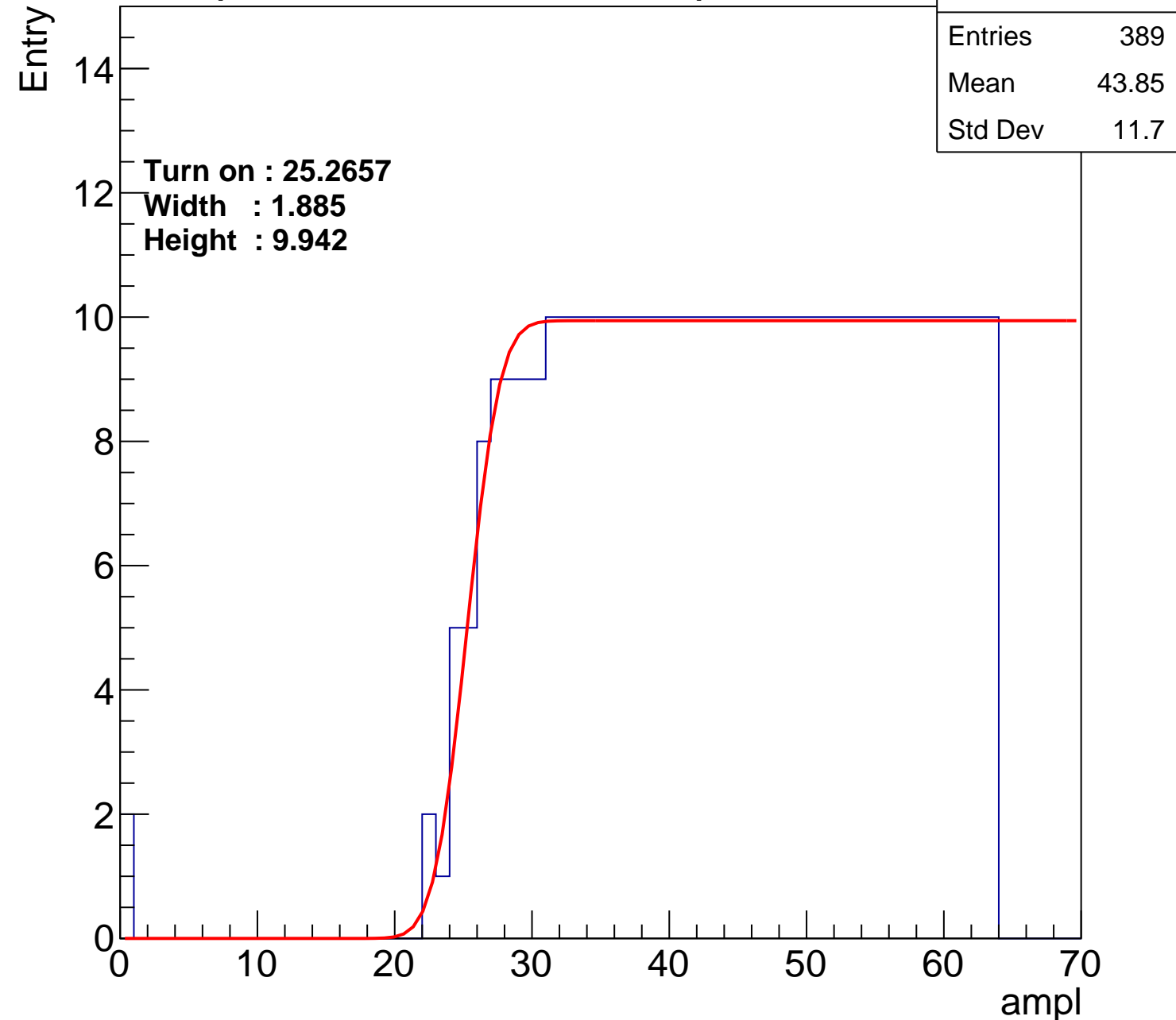
Width : 1.885

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch17

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.4699

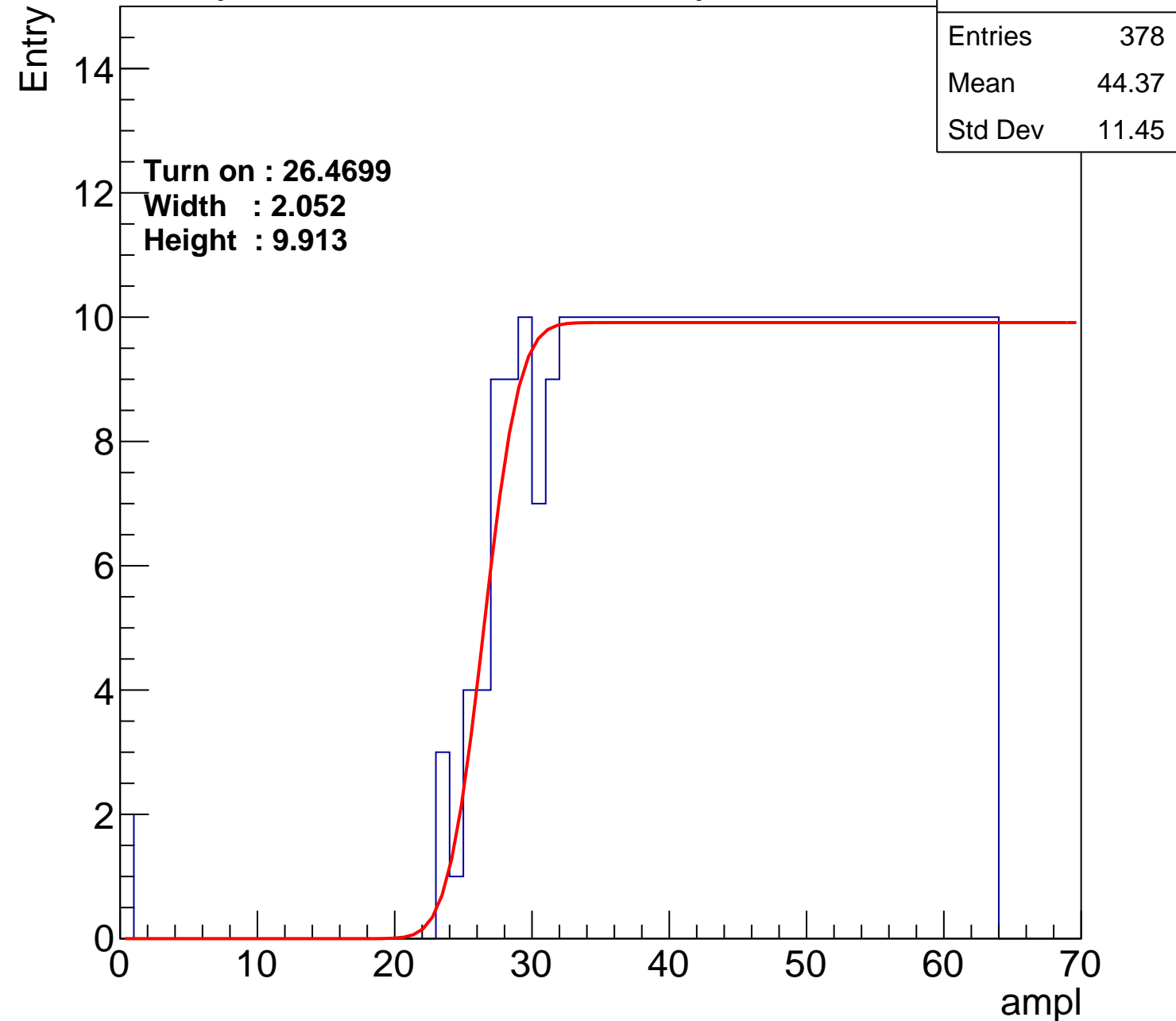
Width : 2.052

Height : 9.913

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch18

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.67
Std Dev	12.07

Turn on : 25.3551

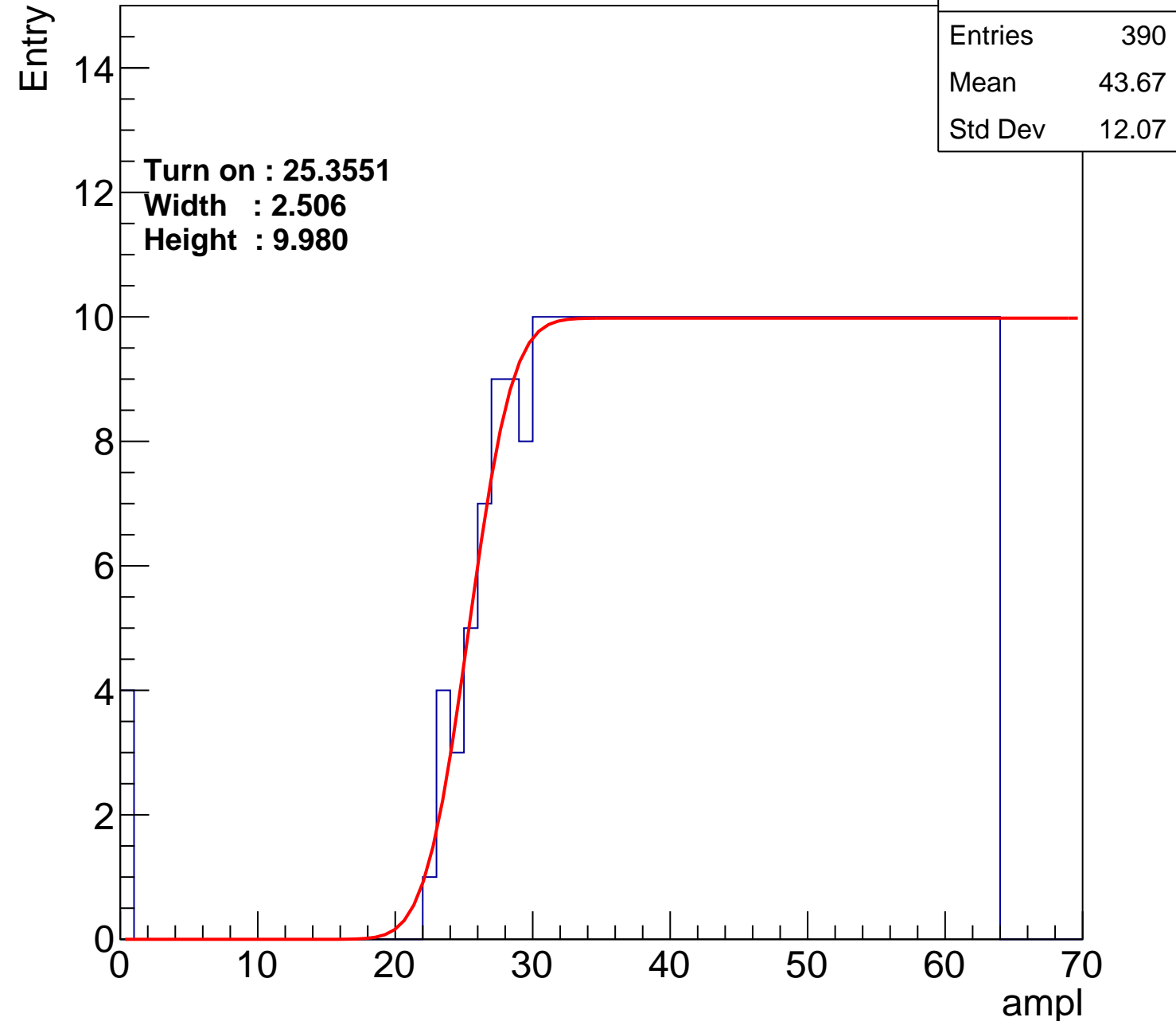
Width : 2.506

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch19

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.71
Std Dev	11.1

Turn on : 26.3521

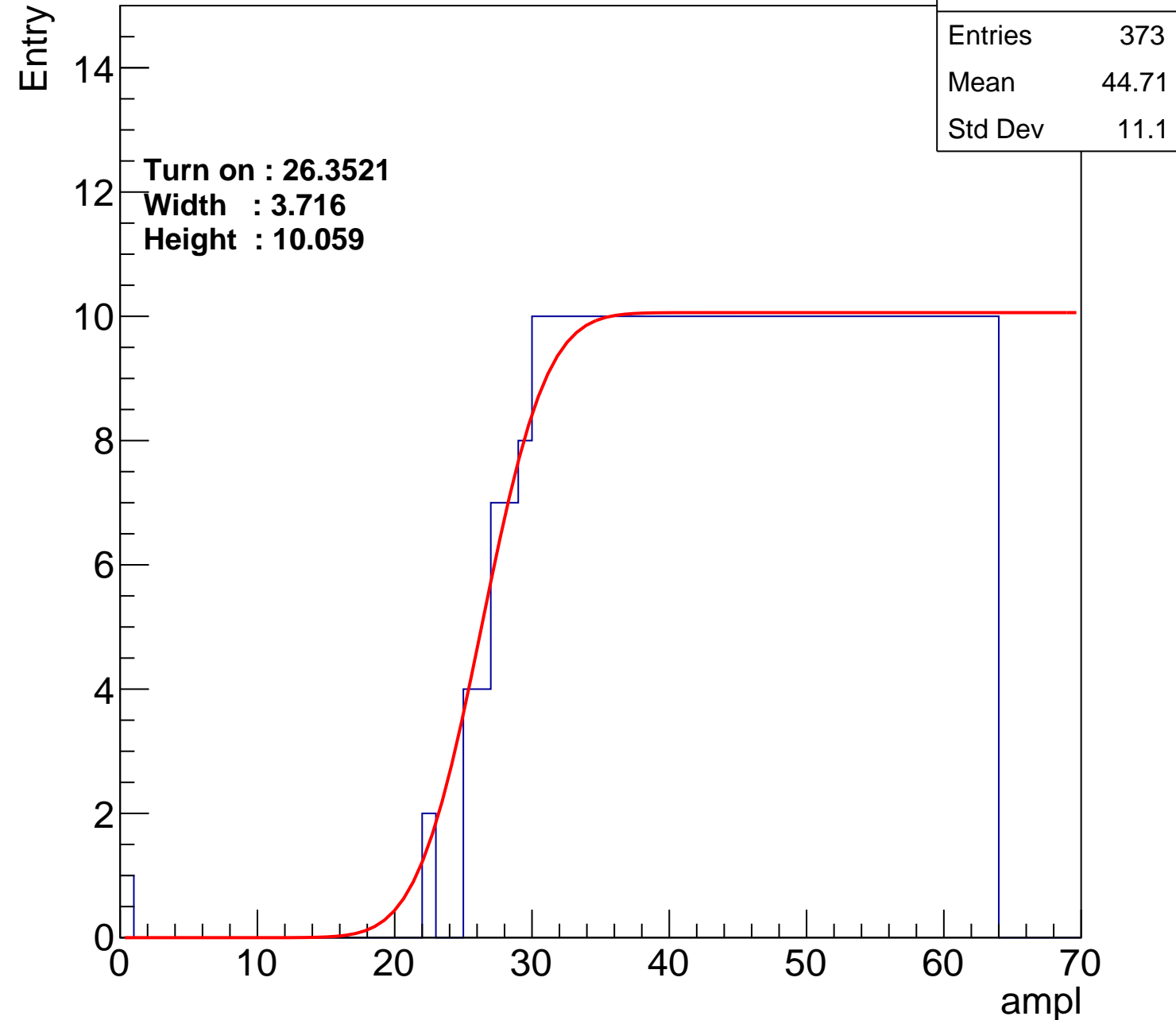
Width : 3.716

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch20

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 26.1446

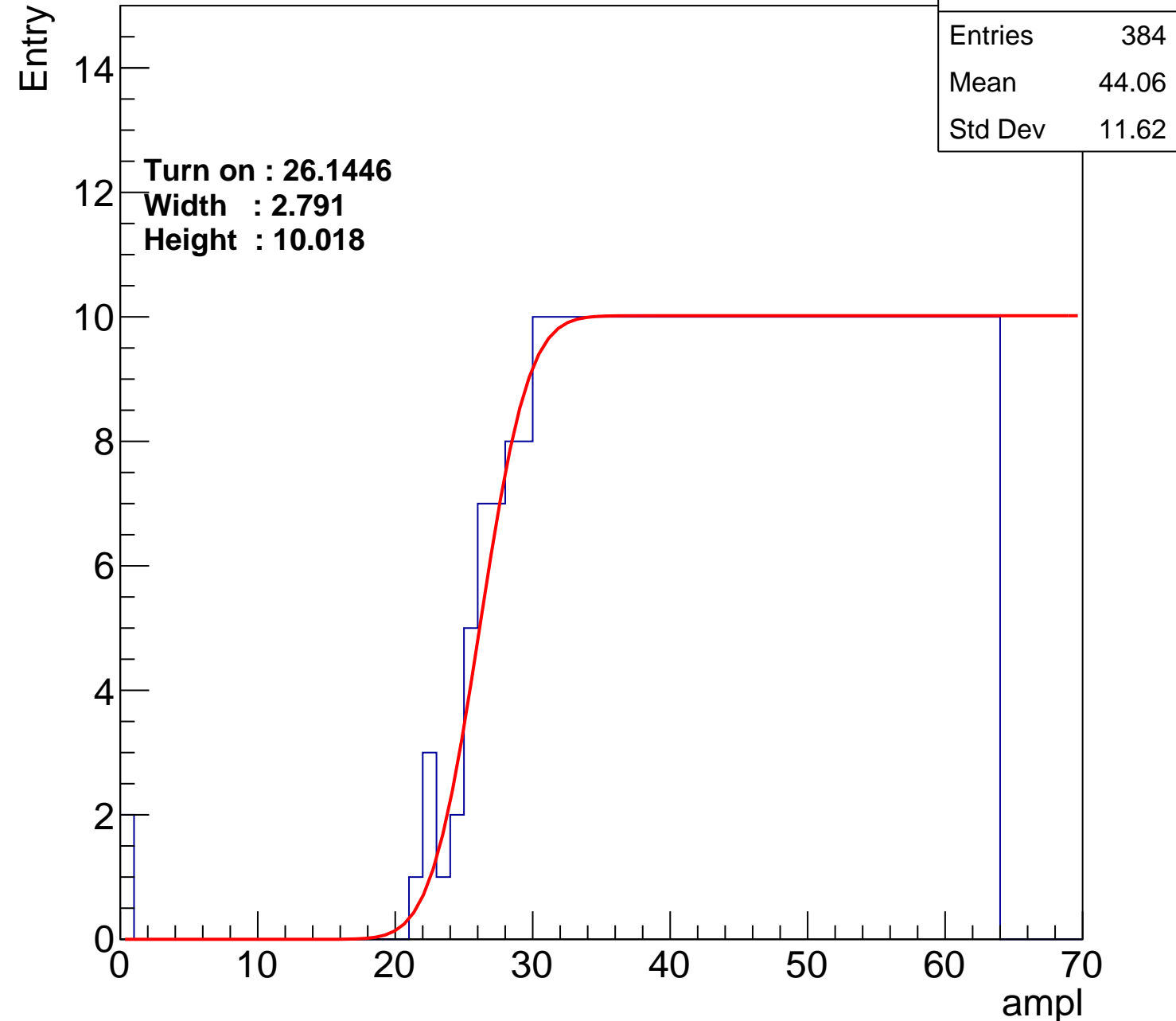
Width : 2.791

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch21

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.79
Std Dev	12.04

Turn on : 25.7774

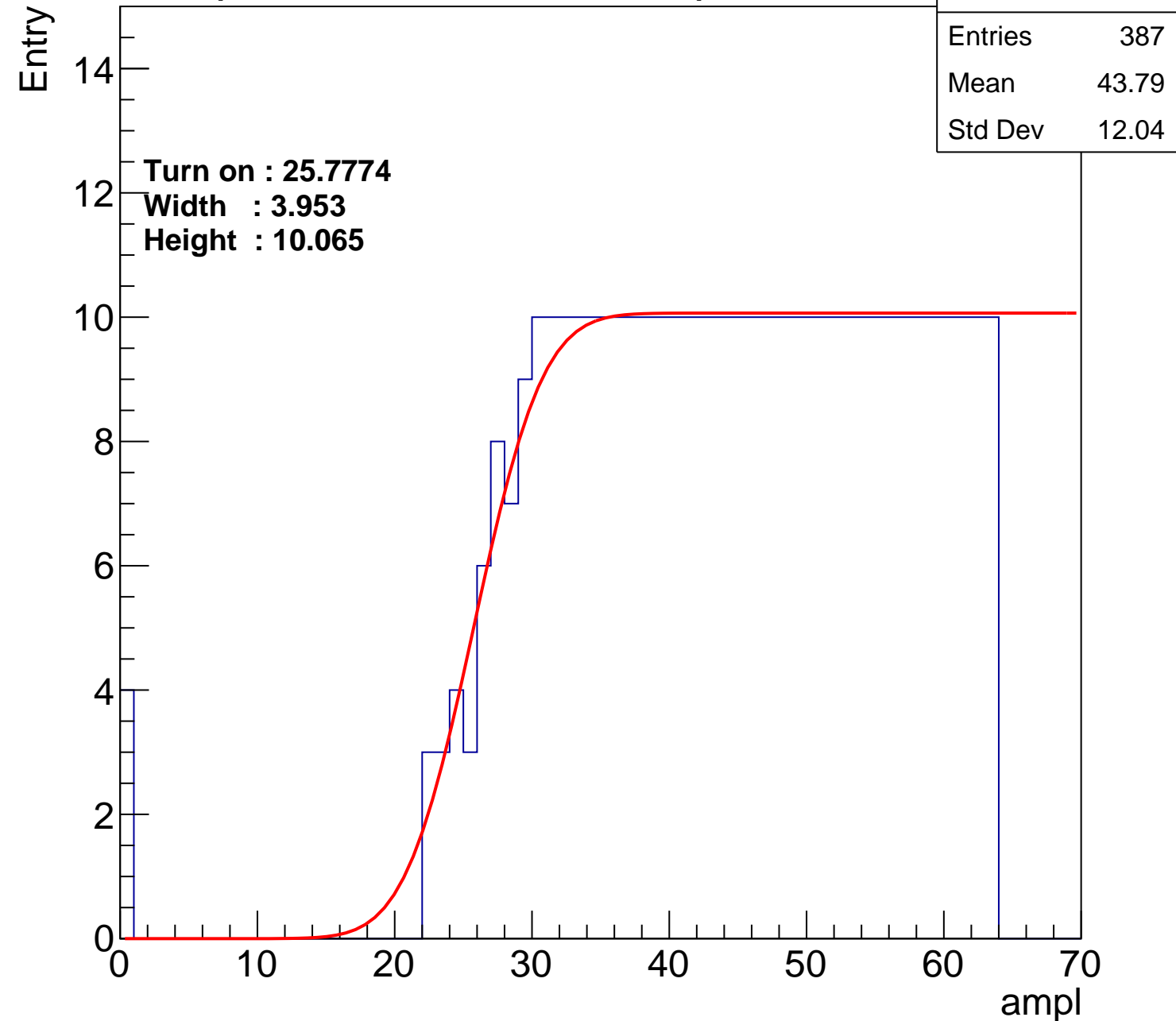
Width : 3.953

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch22

calib_packv5_042523_0143.root, FC#4, port A2

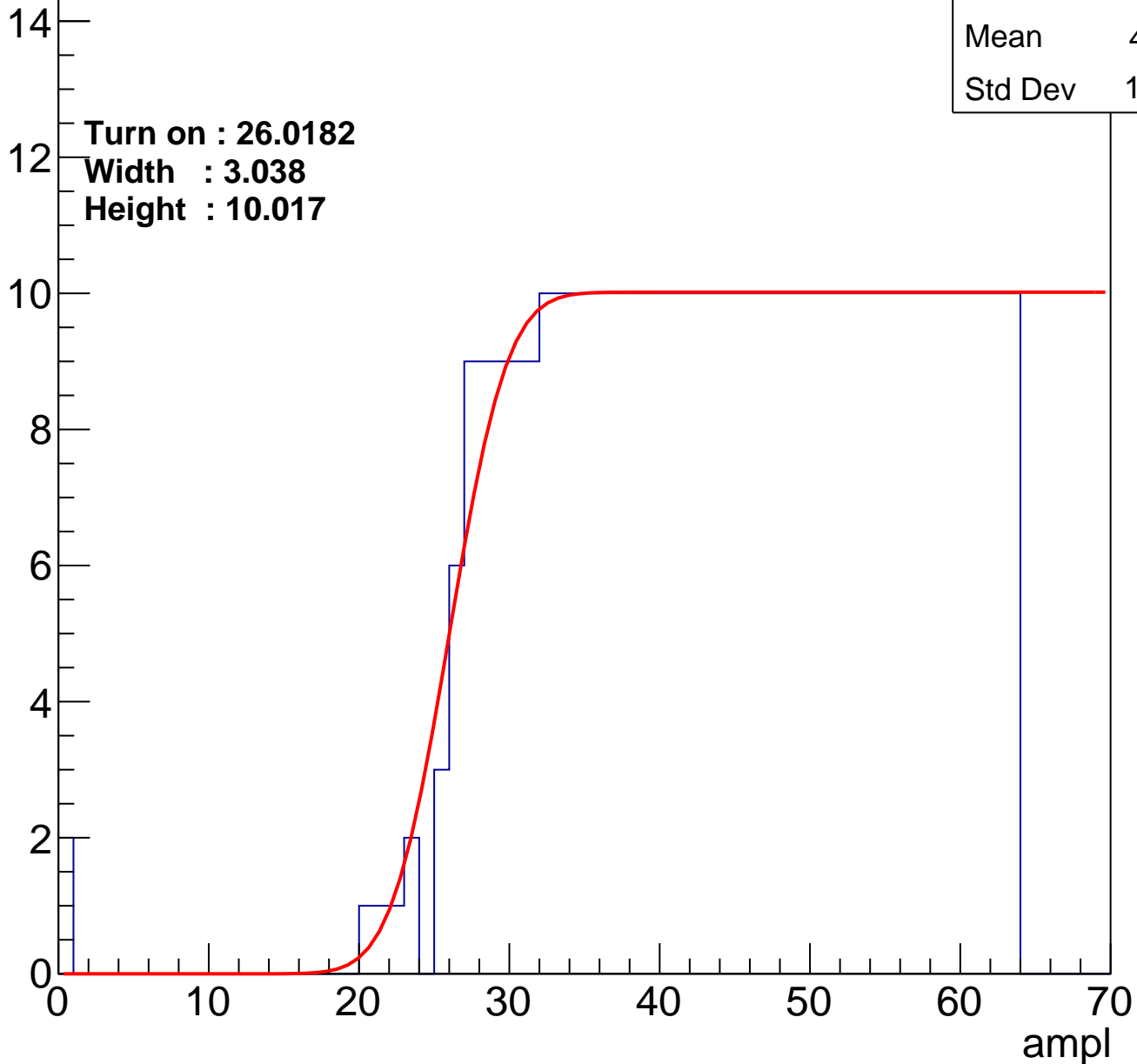
Entries	381
Mean	44.21
Std Dev	11.54

Turn on : 26.0182

Width : 3.038

Height : 10.017

Entry



B1L100S, U6-ch23

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.34
Std Dev	11.51

Turn on : 26.6774

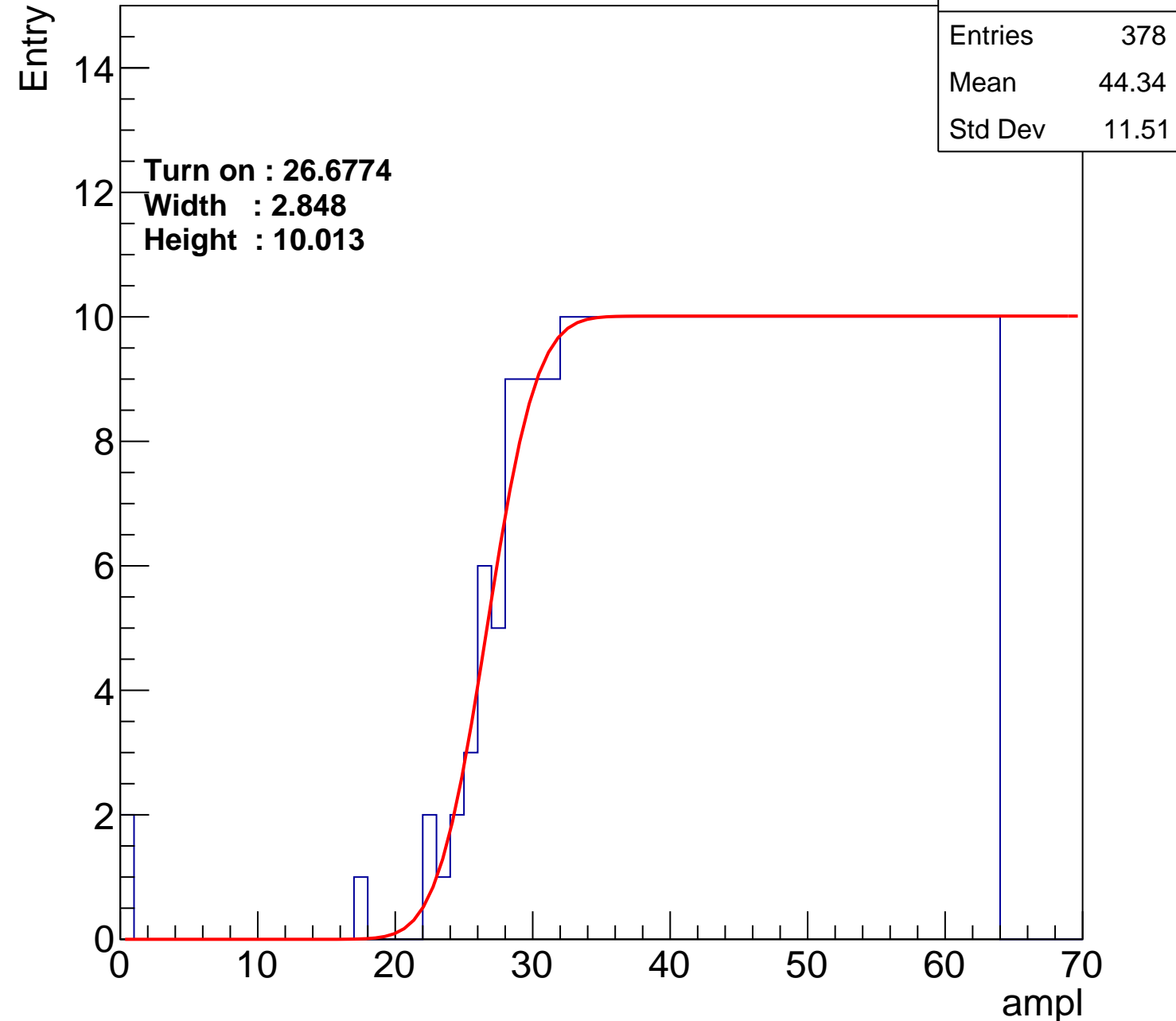
Width : 2.848

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch24

calib_packv5_042523_0143.root, FC#4, port A2

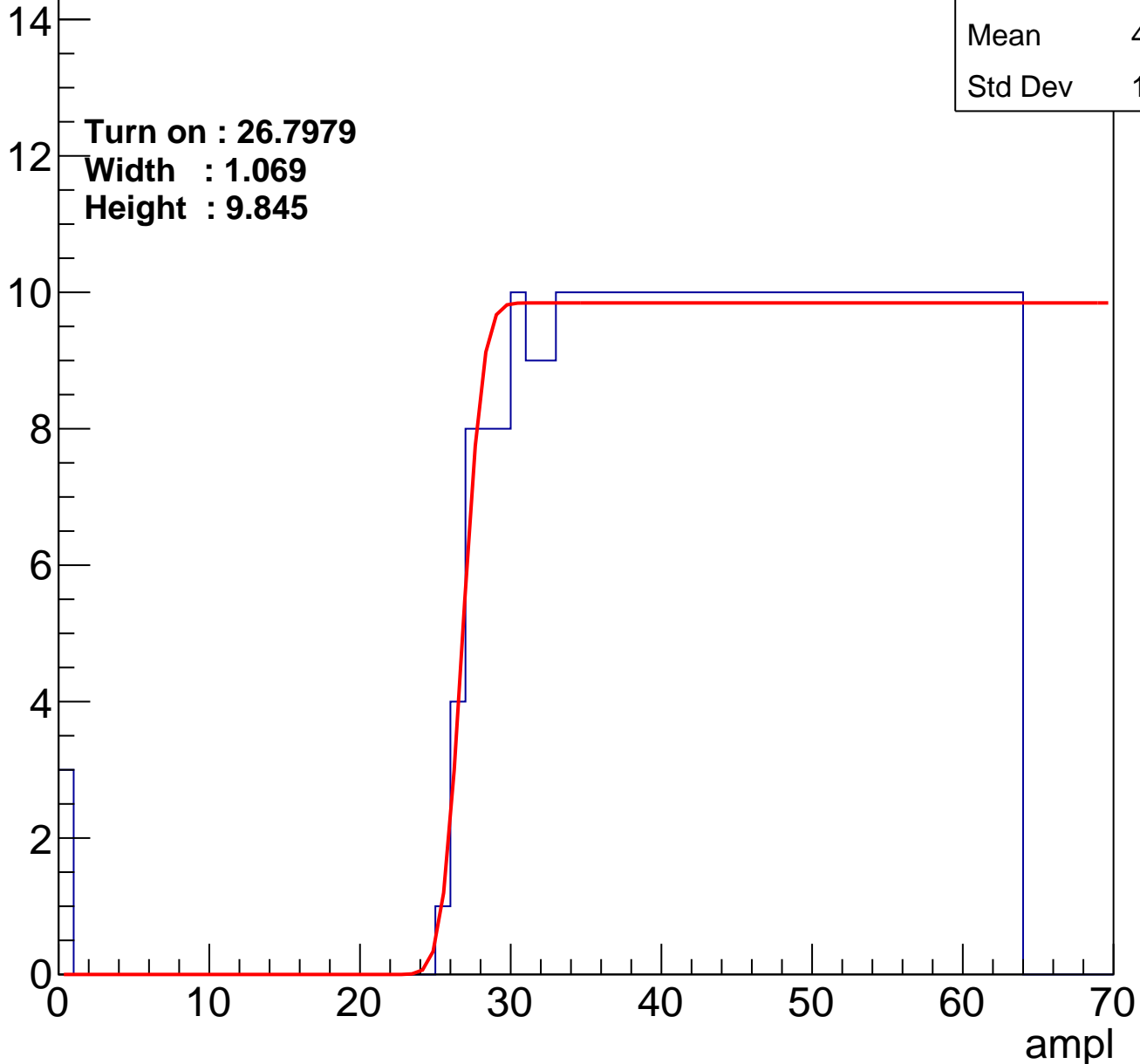
Entries	370
Mean	44.72
Std Dev	11.39

Turn on : 26.7979

Width : 1.069

Height : 9.845

Entry



B1L100S, U6-ch25

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.22
Std Dev	10.84

Turn on : 28.2987

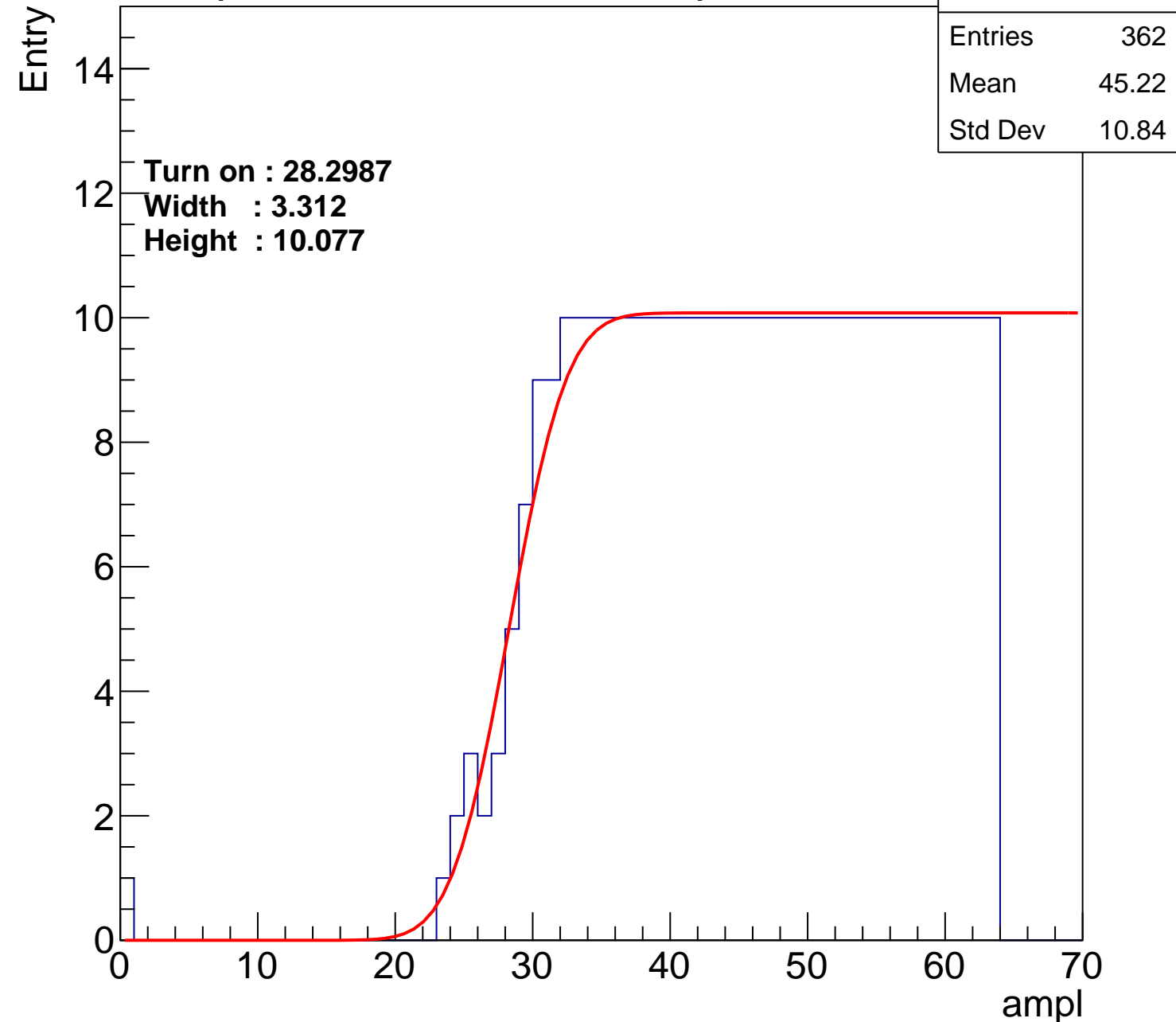
Width : 3.312

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch26

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.08
Std Dev	11.78

Turn on : 26.9803

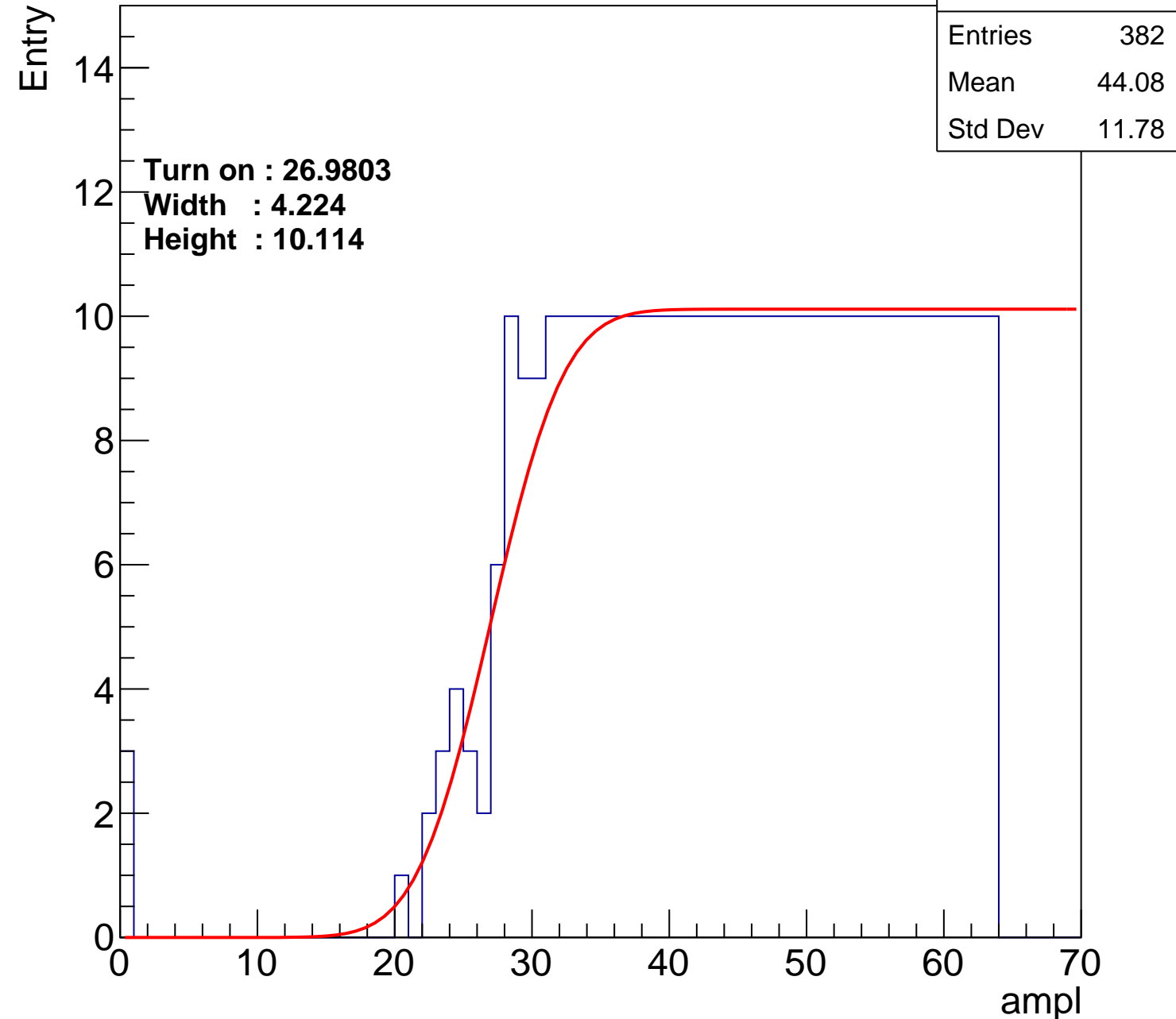
Width : 4.224

Height : 10.114

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch27

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.81
Std Dev	11.22

Turn on : 27.2397

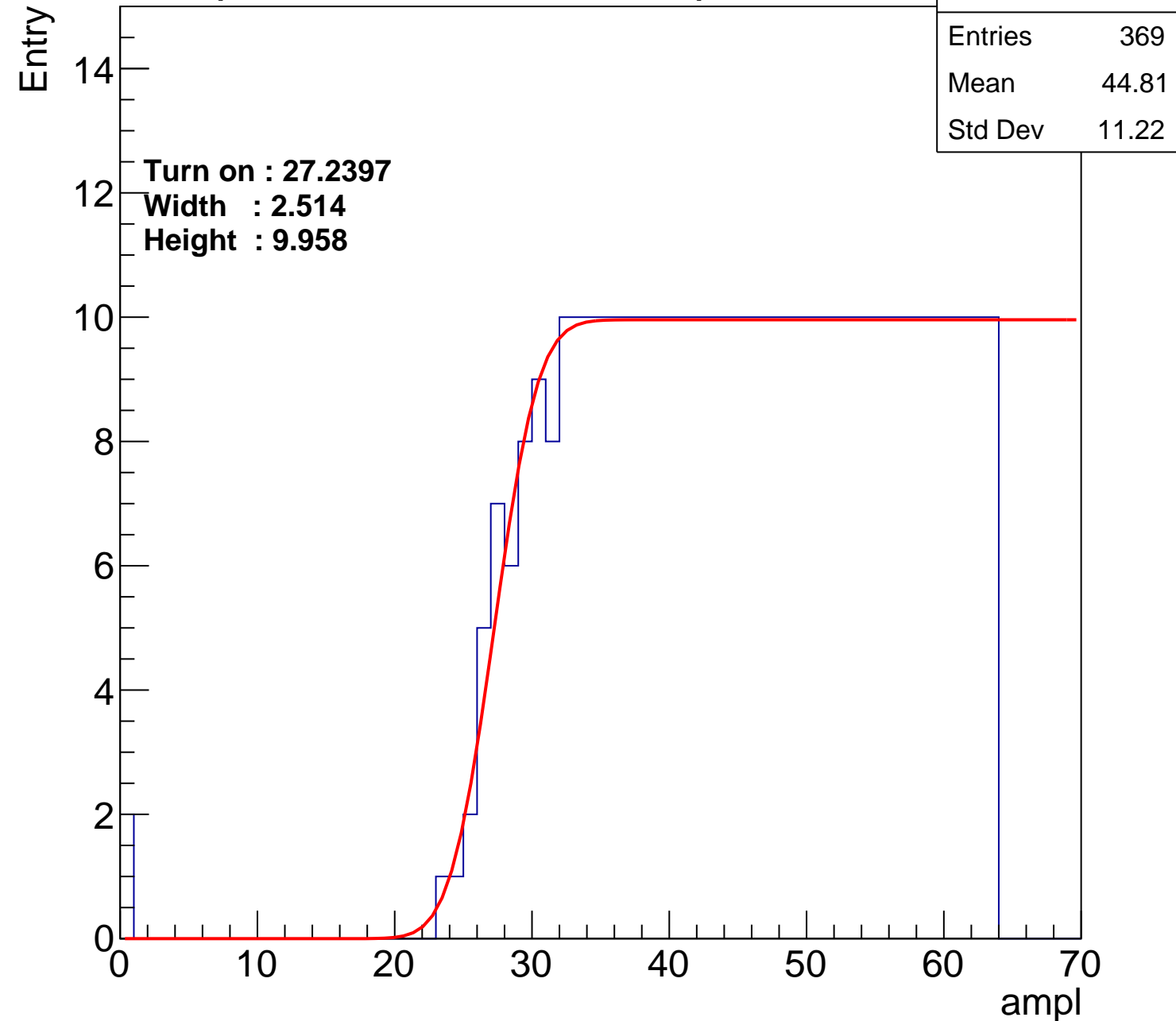
Width : 2.514

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch28

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	44.07
Std Dev	11.44

Turn on : 25.8663

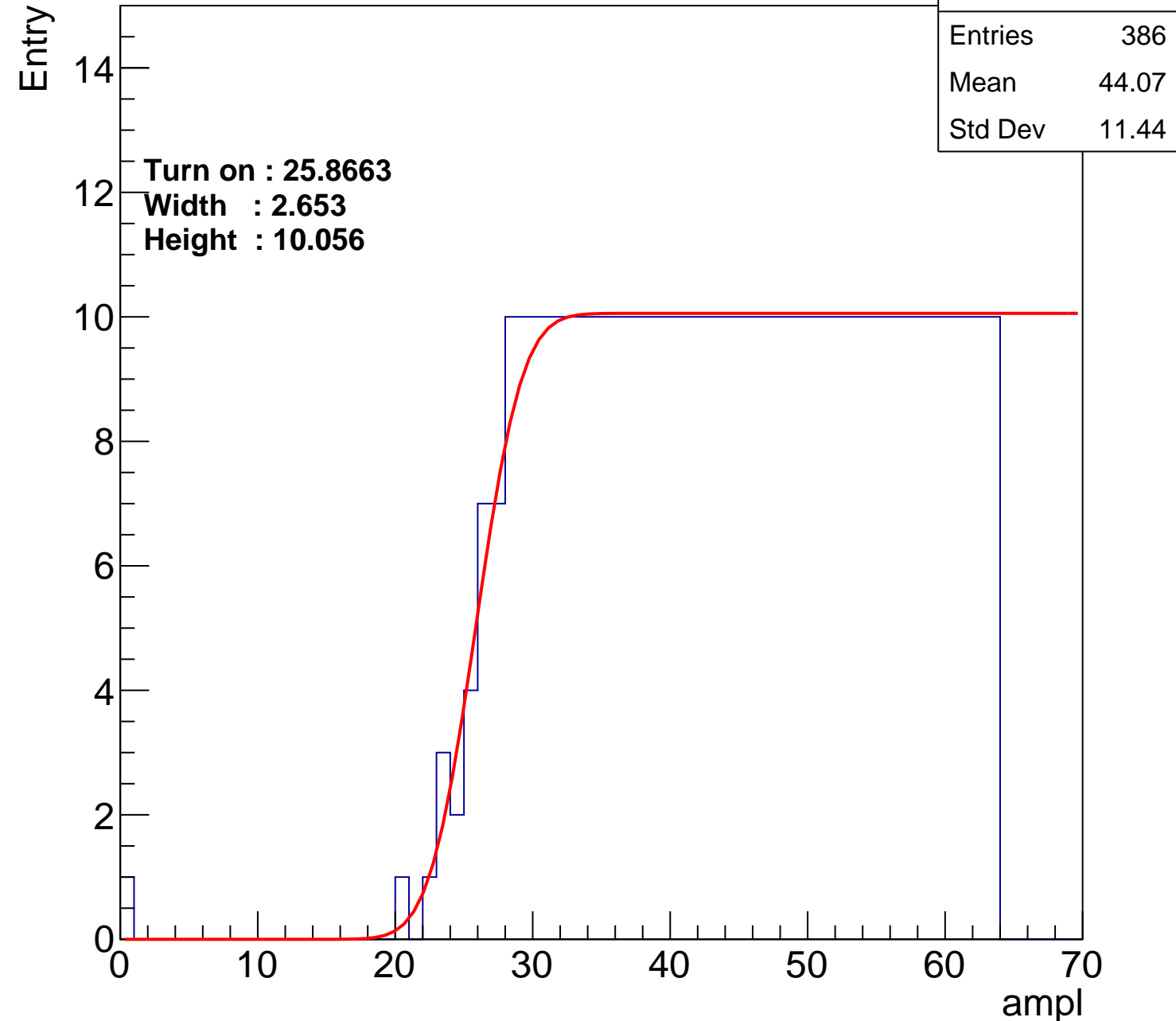
Width : 2.653

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch29

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.12
Std Dev	11.71

Turn on : 25.9532

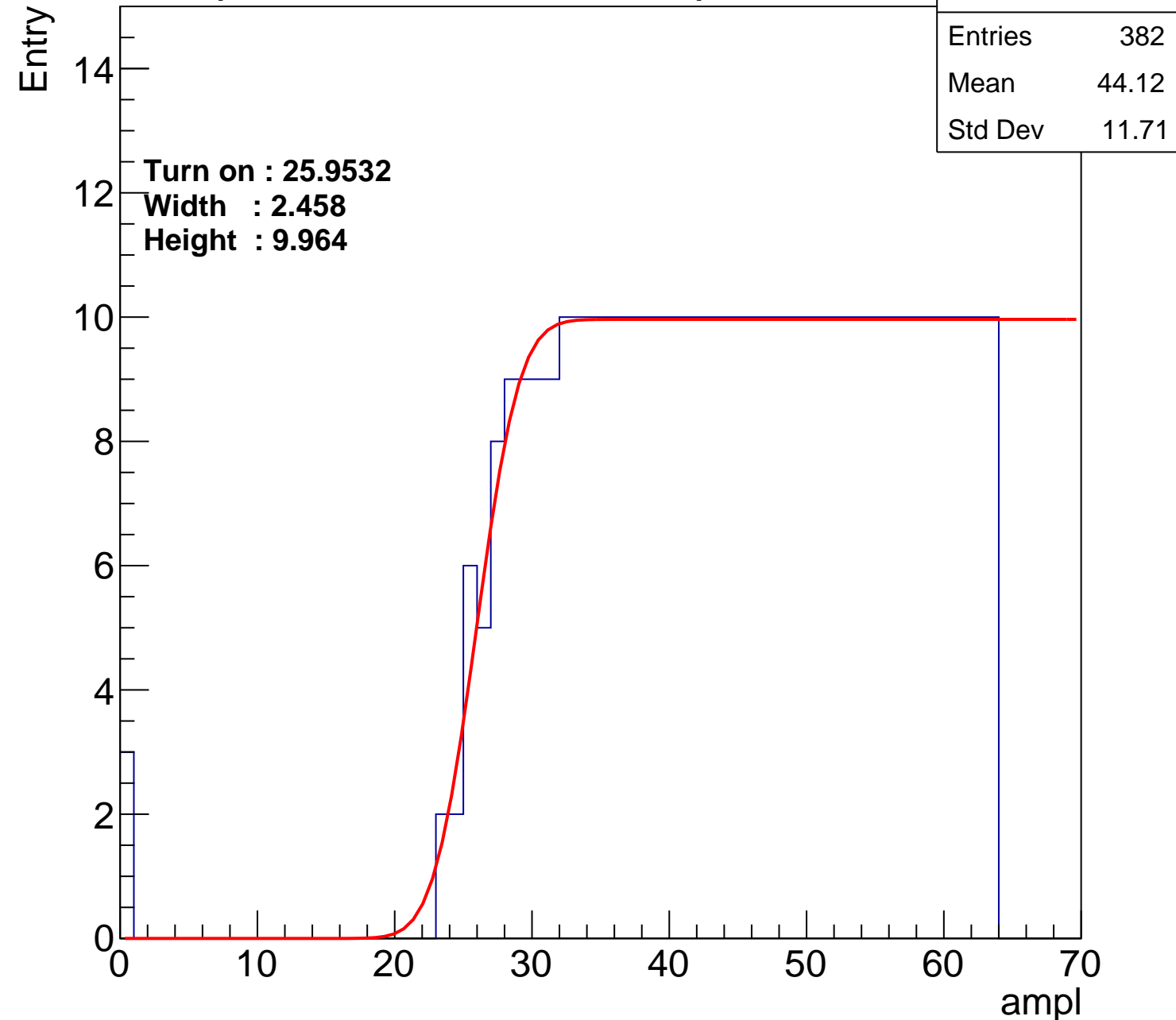
Width : 2.458

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch30

calib_packv5_042523_0143.root, FC#4, port A2

Entries	356
Mean	45.42
Std Dev	10.93

Turn on : 28.7534

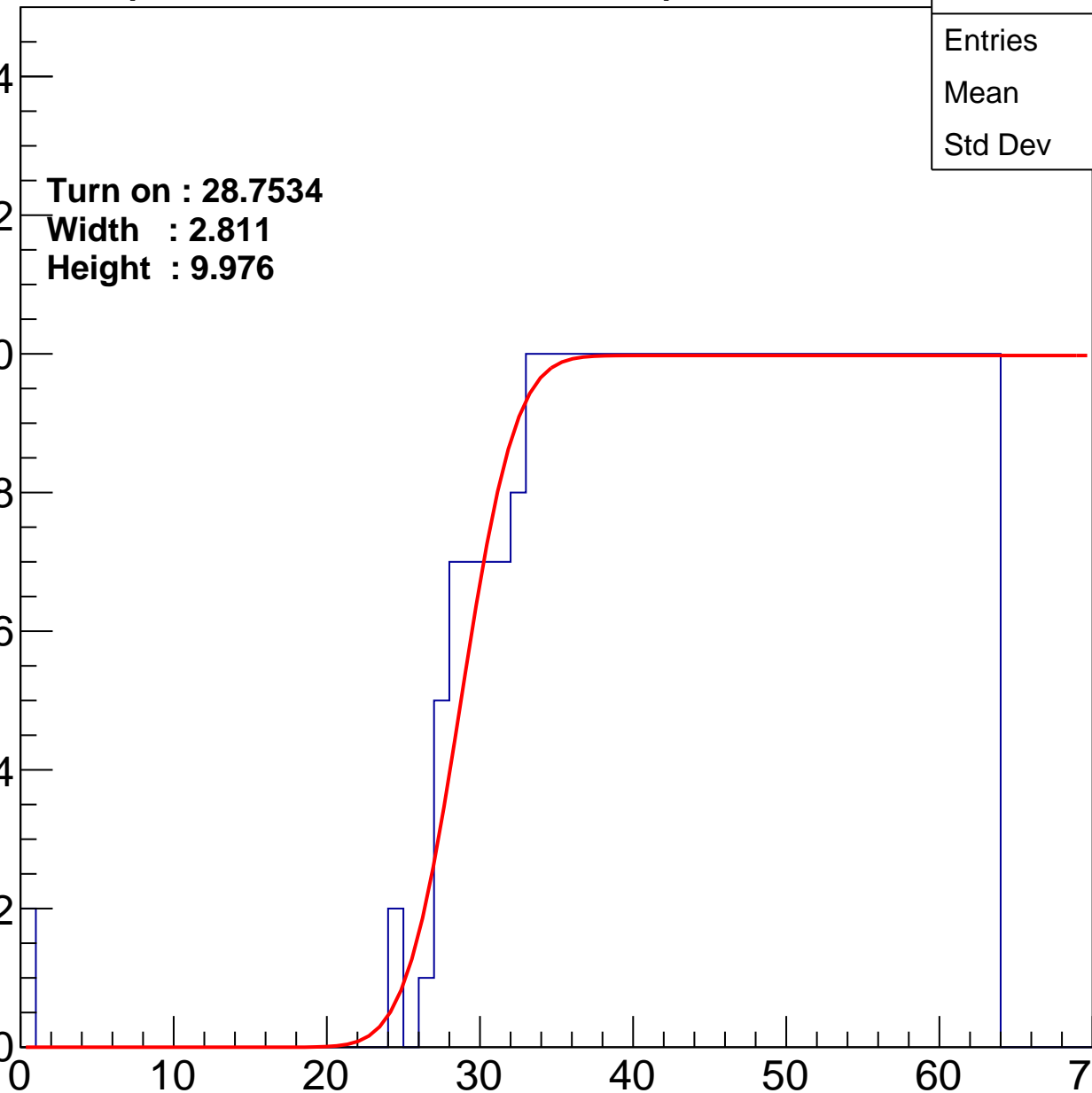
Width : 2.811

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch31

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.46
Std Dev	10.68

Turn on : 28.8484

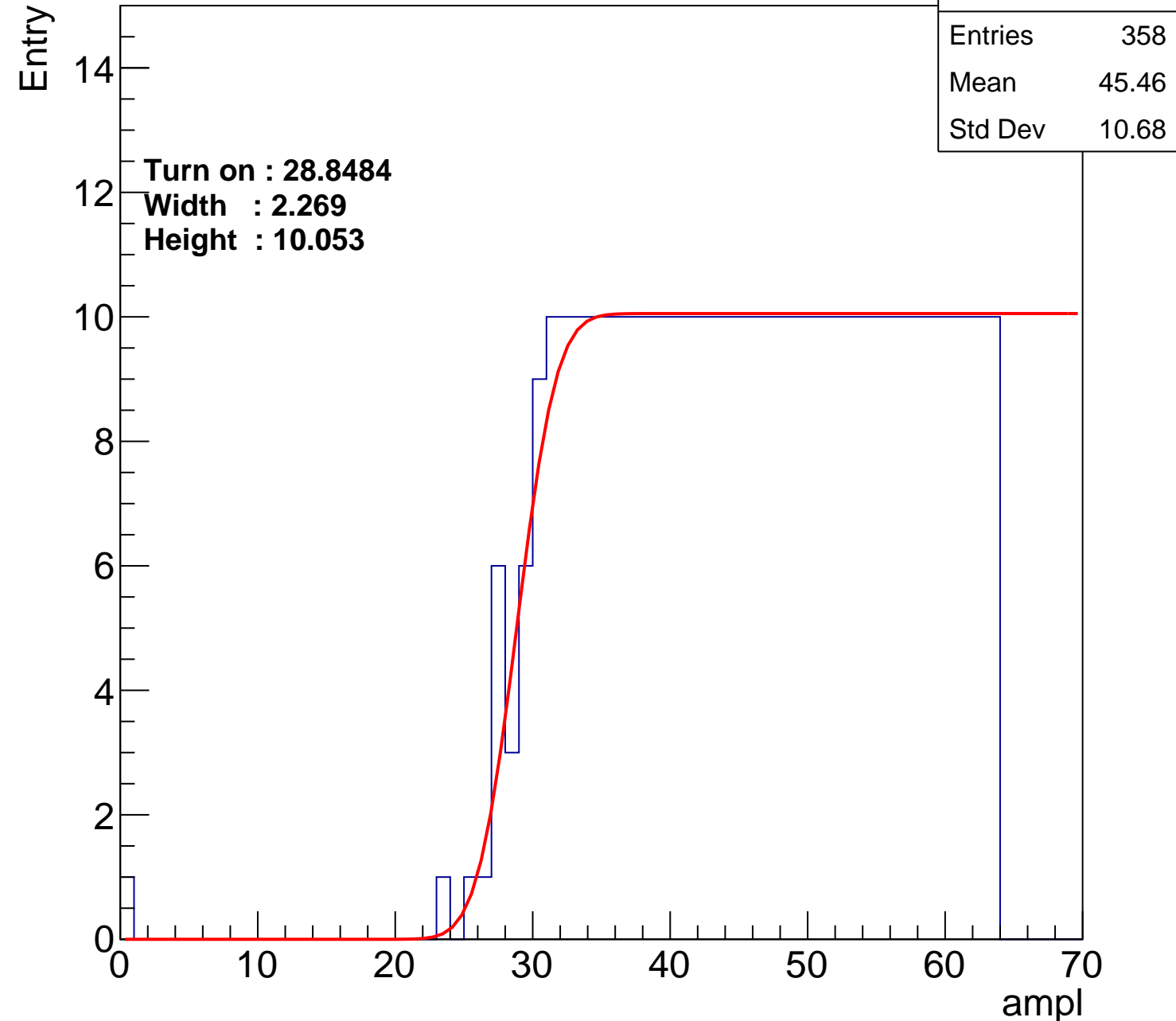
Width : 2.269

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch32

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.15
Std Dev	11.05

Turn on : 27.8070

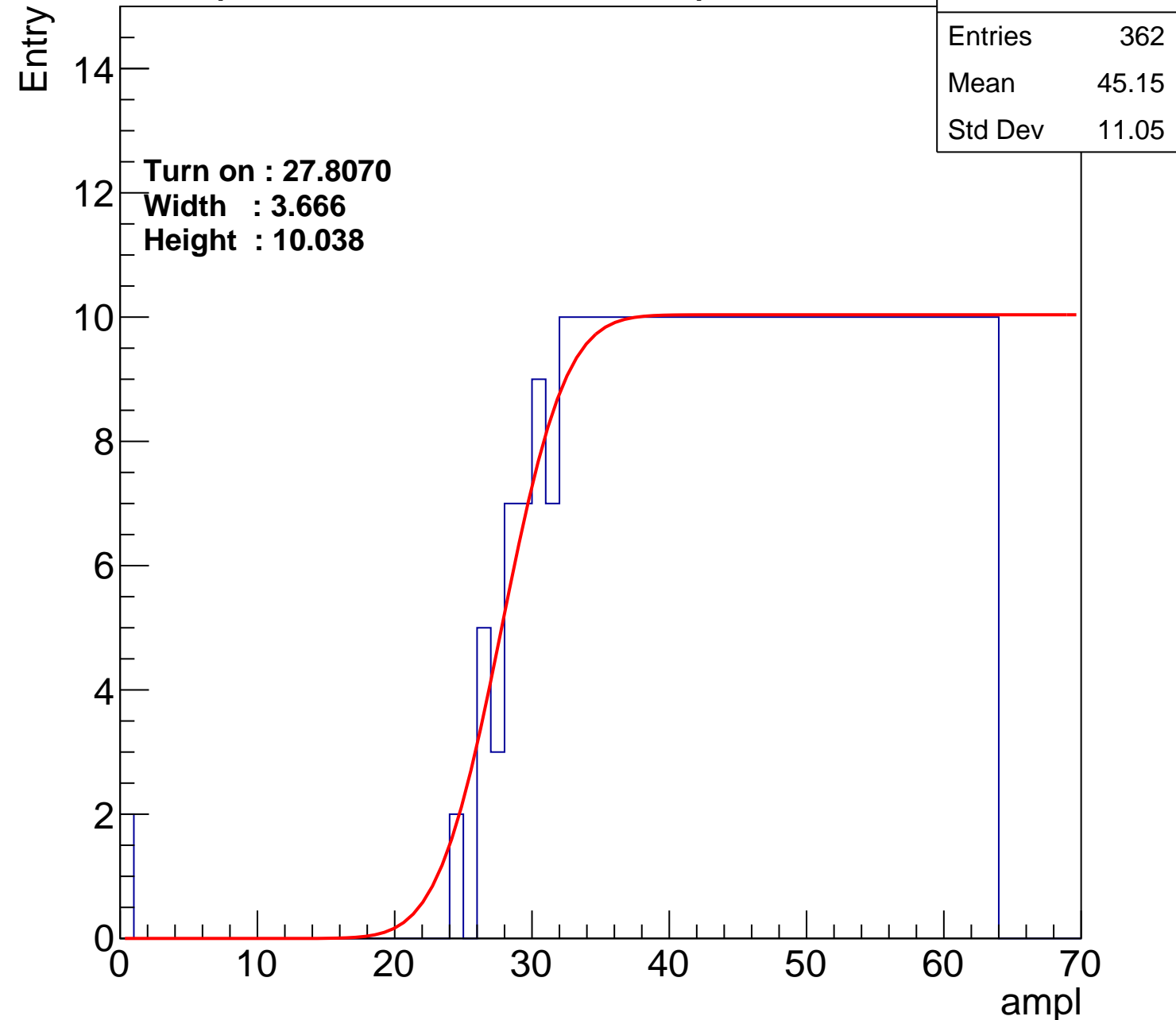
Width : 3.666

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch33

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.5033

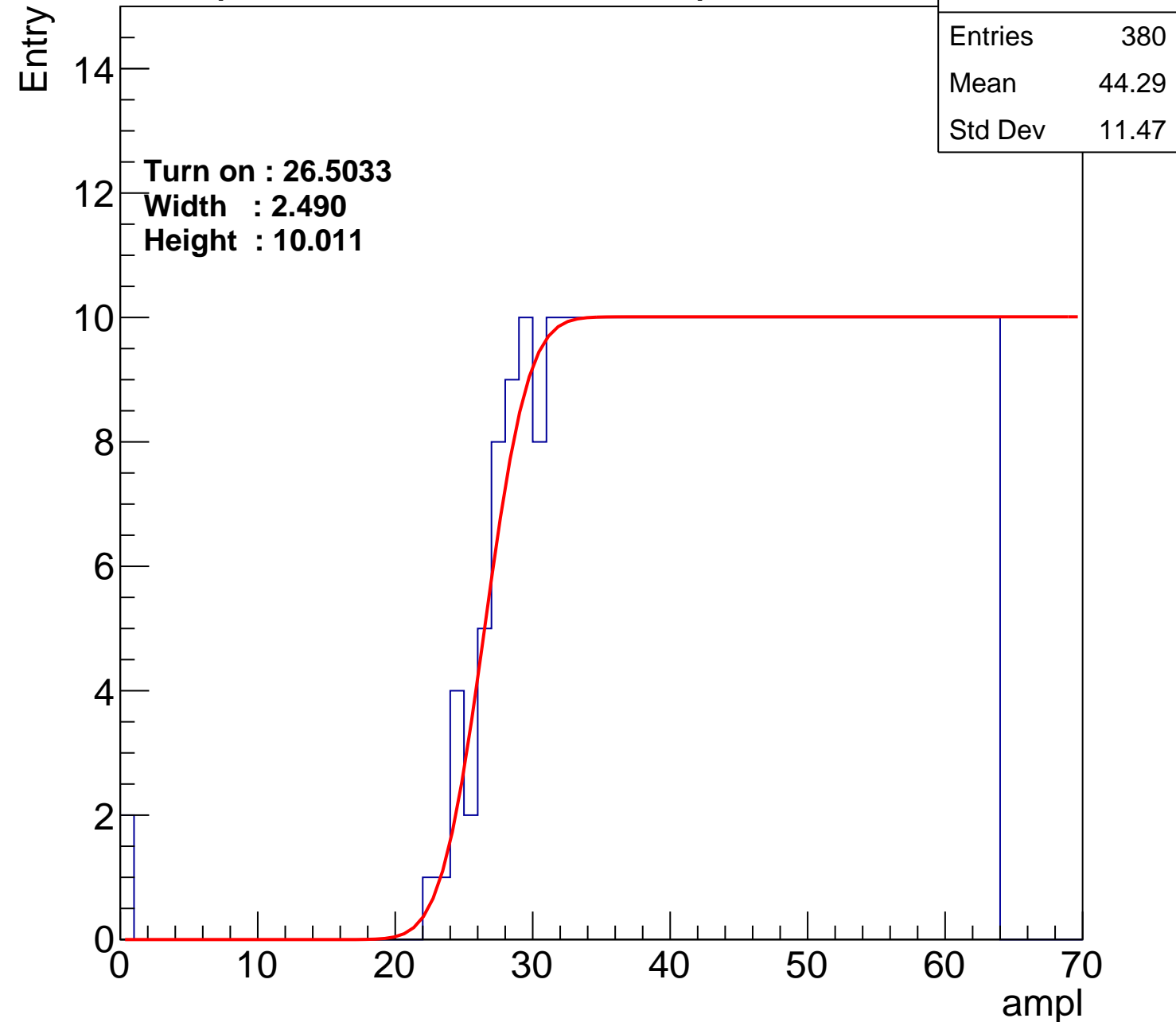
Width : 2.490

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch34

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 25.6819

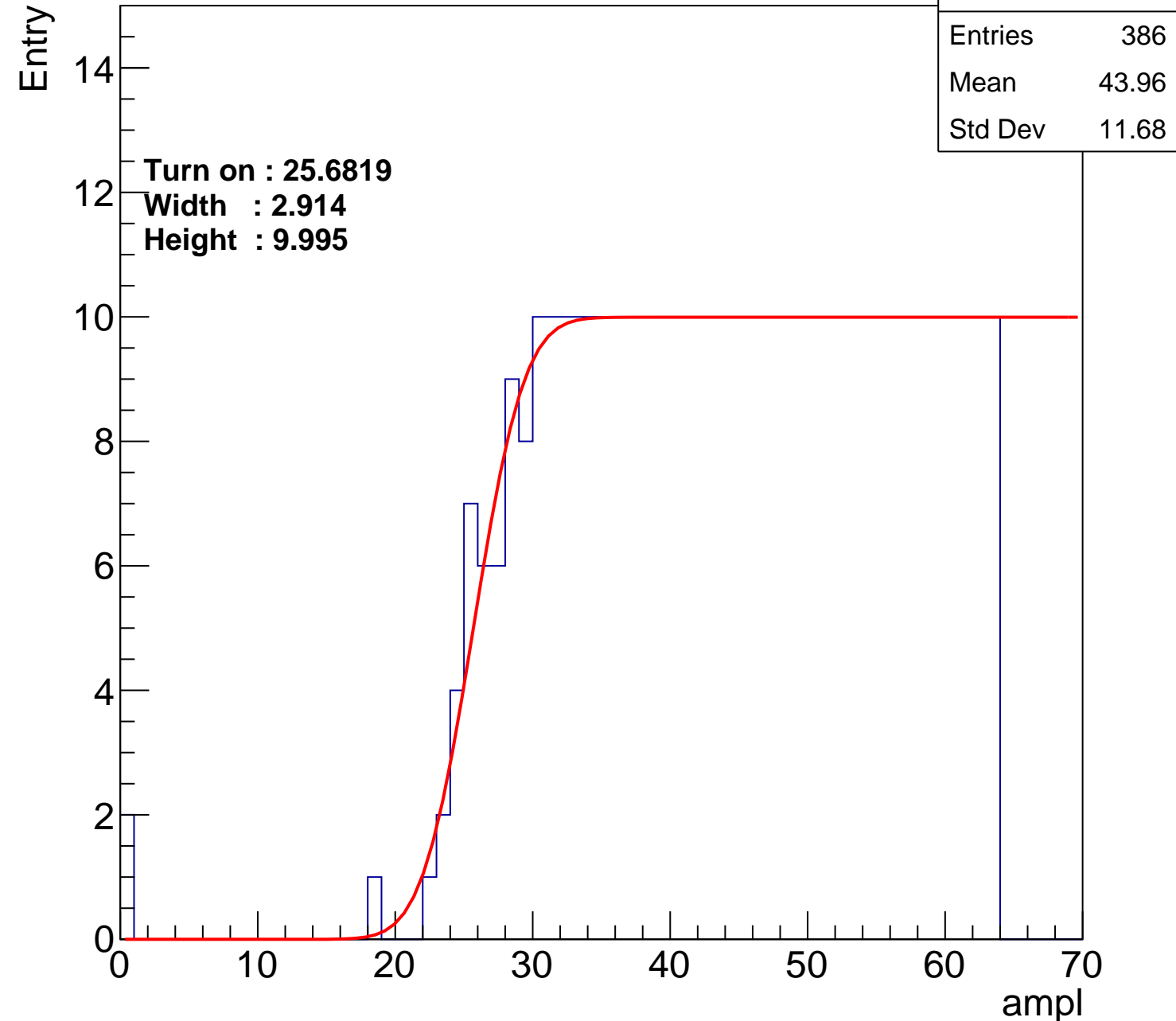
Width : 2.914

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch35

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.03
Std Dev	11.08

Turn on : 28.0961

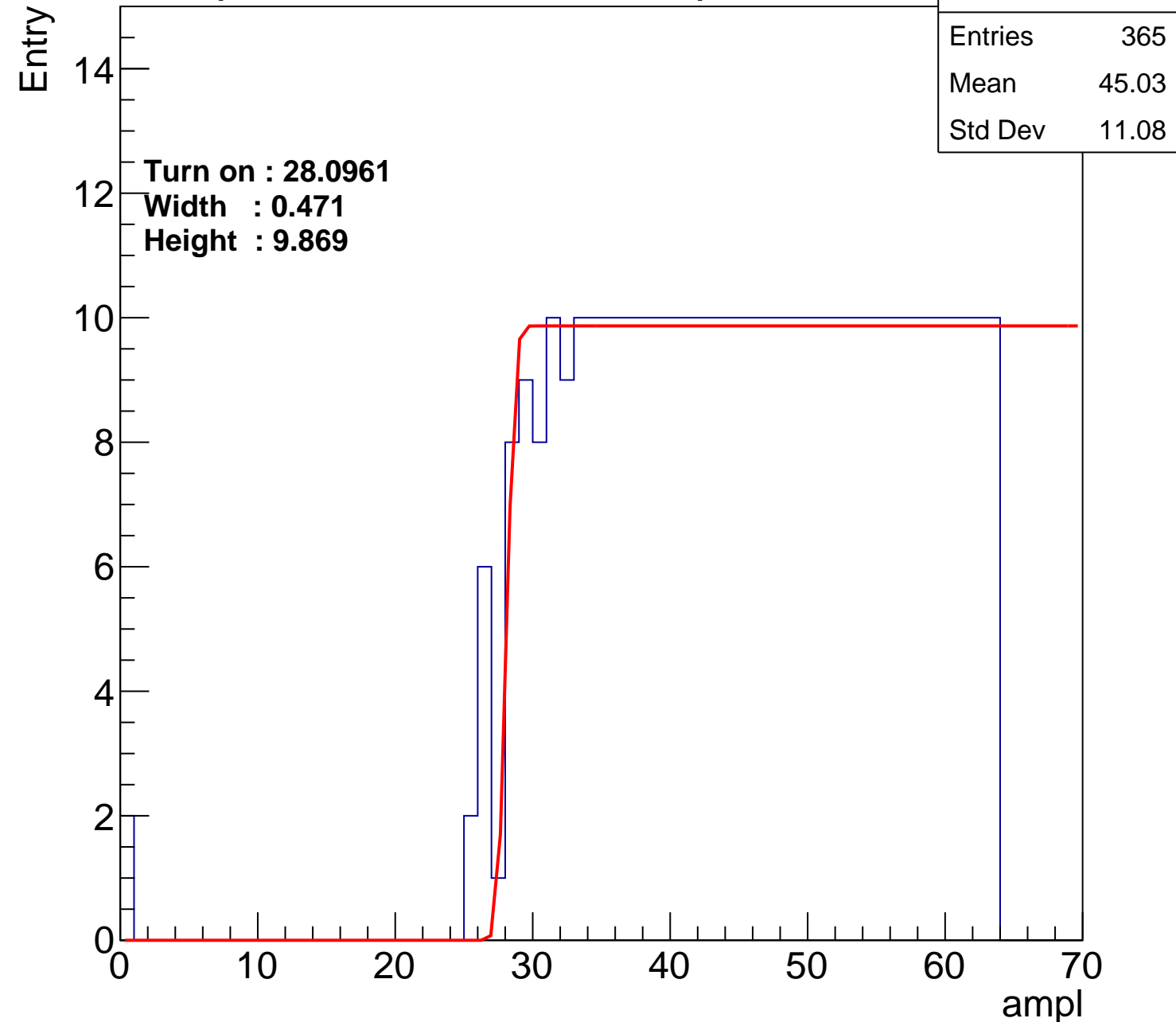
Width : 0.471

Height : 9.869

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch36

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 26.1662

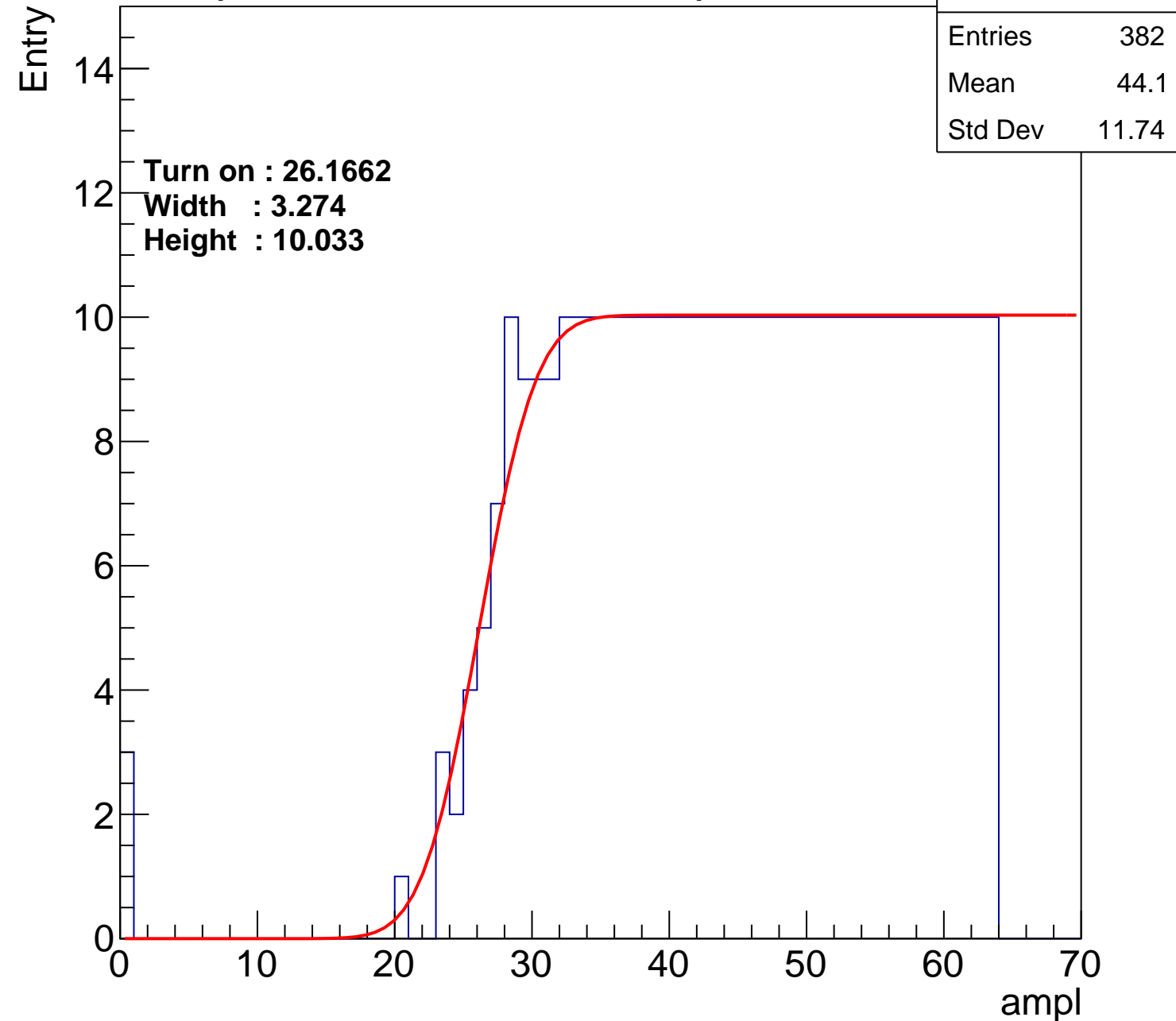
Width : 3.274

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch37

calib_packv5_042523_0143.root, FC#4, port A2

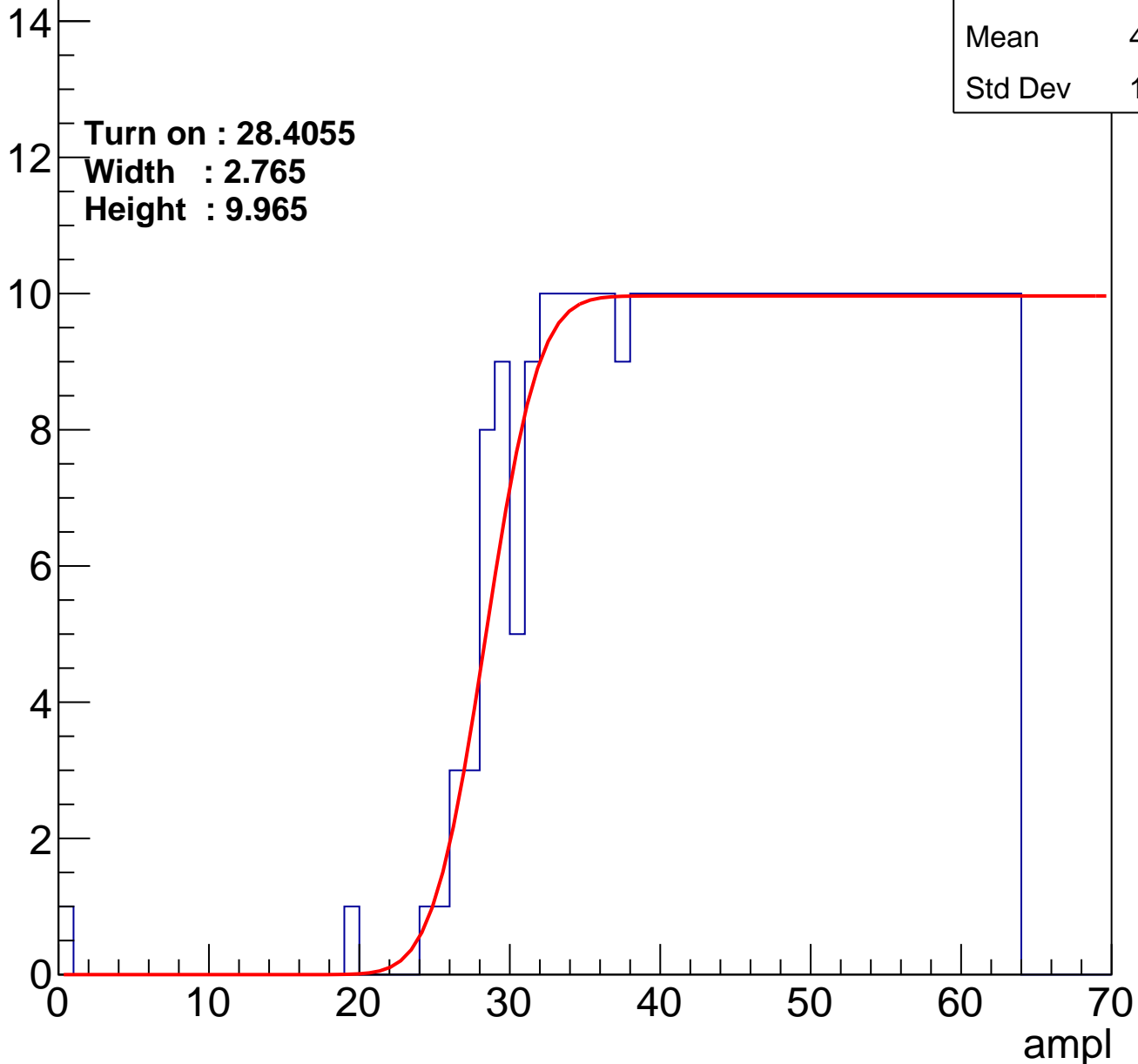
Entries	360
Mean	45.29
Std Dev	10.84

Turn on : 28.4055

Width : 2.765

Height : 9.965

Entry



B1L100S, U6-ch38

calib_packv5_042523_0143.root, FC#4, port A2

Entries	363
Mean	45.19
Std Dev	10.83

Turn on : 27.6281

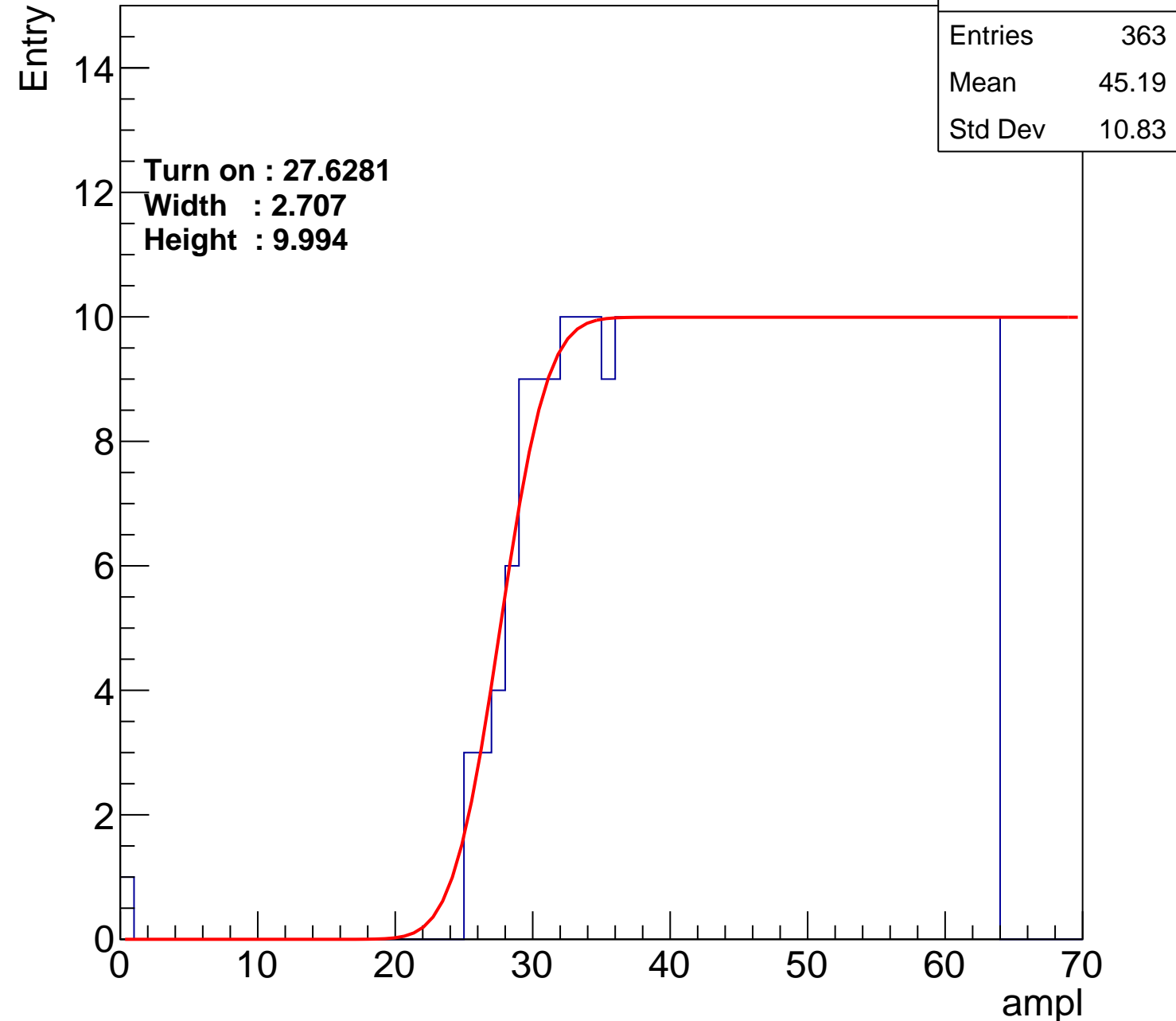
Width : 2.707

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch39

calib_packv5_042523_0143.root, FC#4, port A2

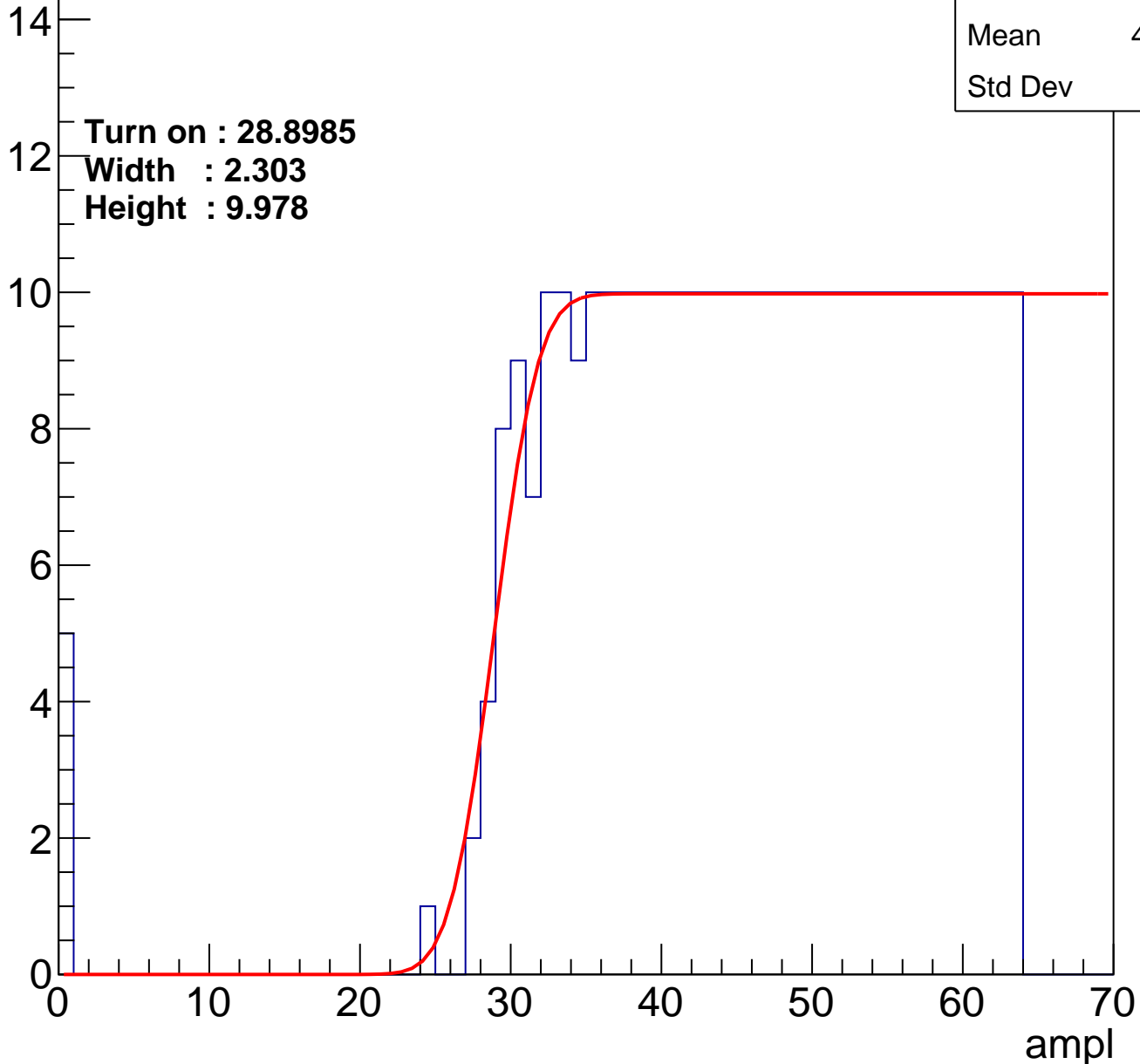
Entries	355
Mean	45.28
Std Dev	11.5

Turn on : 28.8985

Width : 2.303

Height : 9.978

Entry



B1L100S, U6-ch40

calib_packv5_042523_0143.root, FC#4, port A2

Entries	393
Mean	43.56
Std Dev	11.95

Turn on : 25.6274

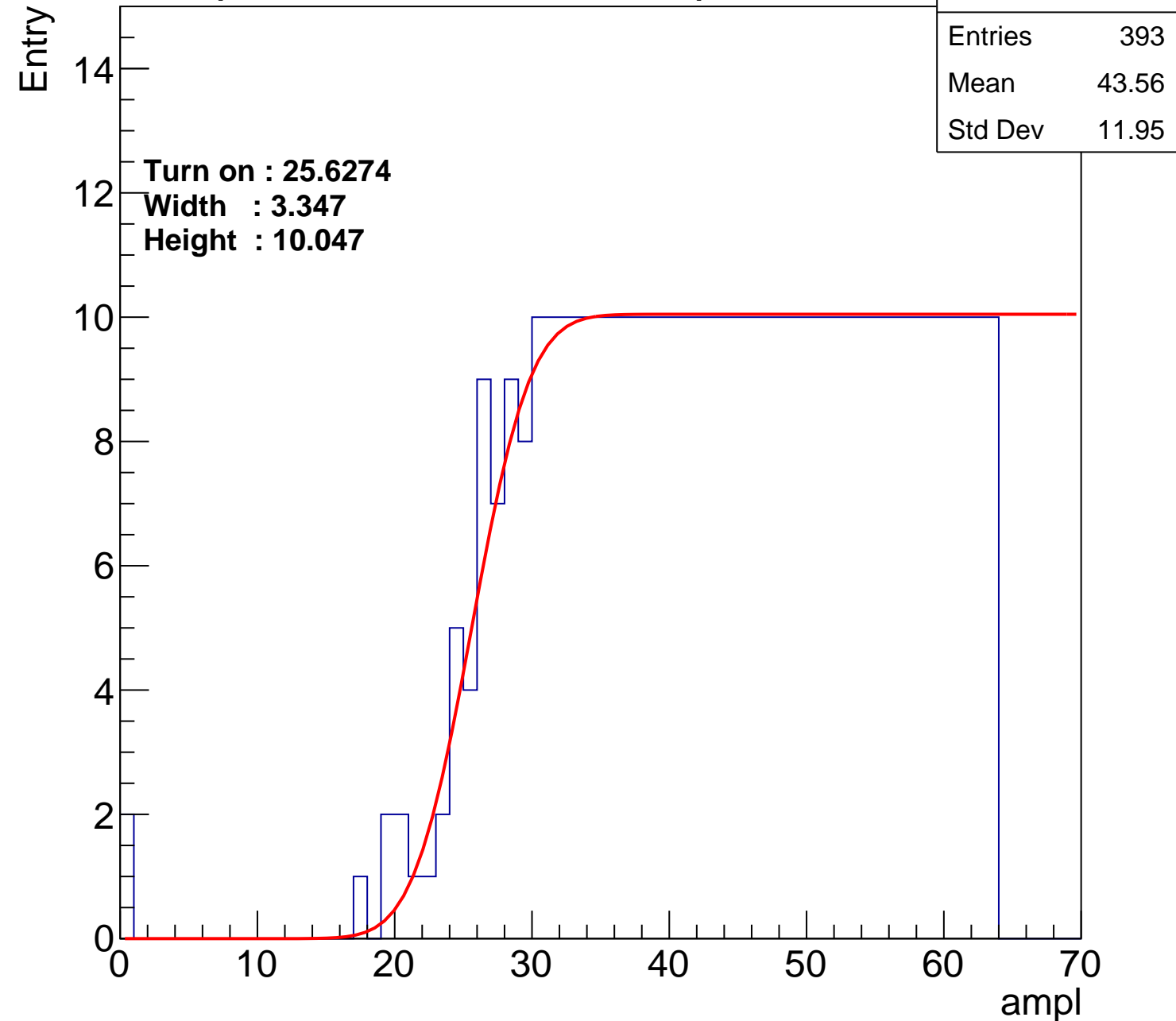
Width : 3.347

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch41

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	44.01
Std Dev	11.48

Turn on : 25.6867

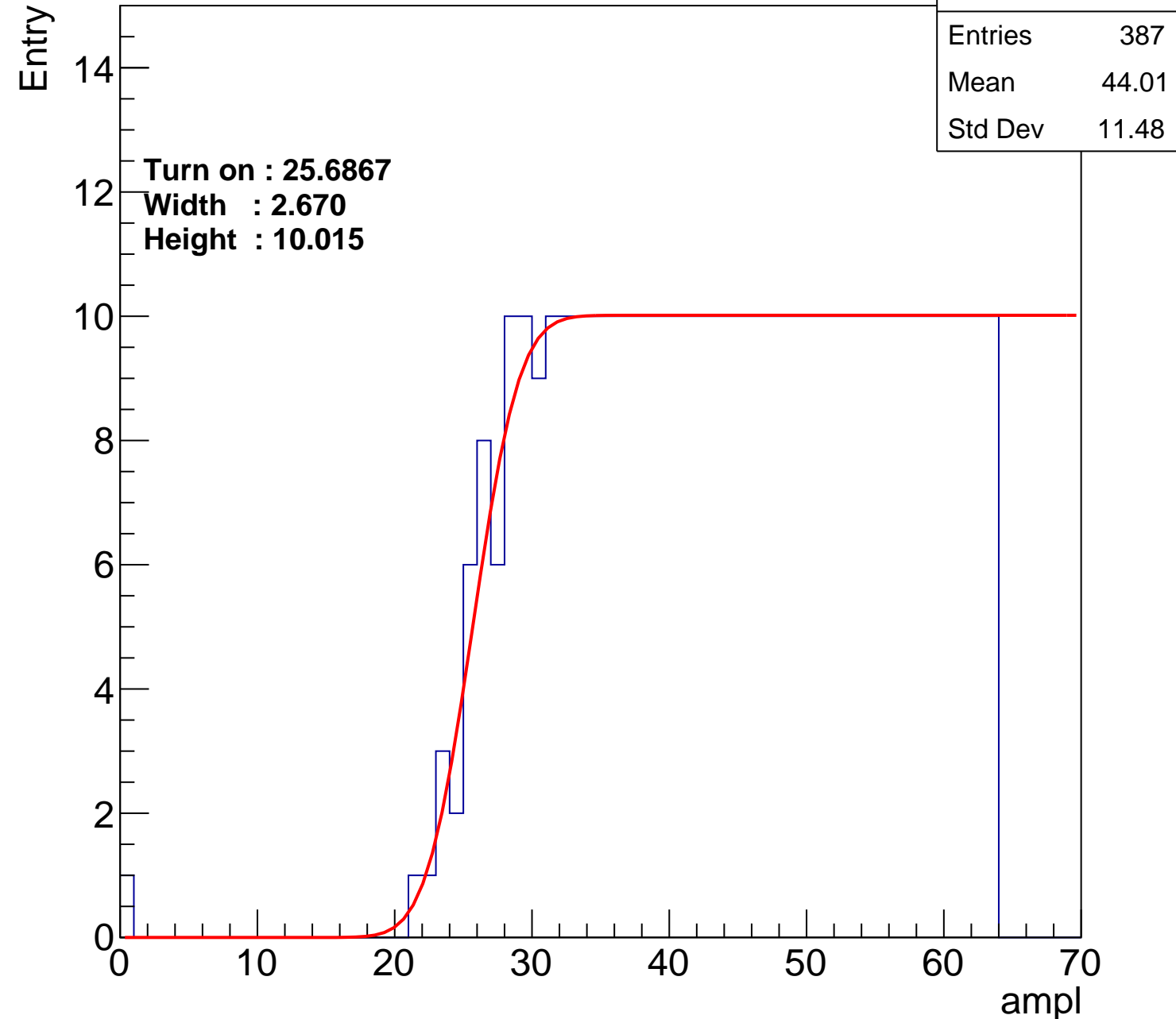
Width : 2.670

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch42

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.99
Std Dev	11.1

Turn on : 27.9462

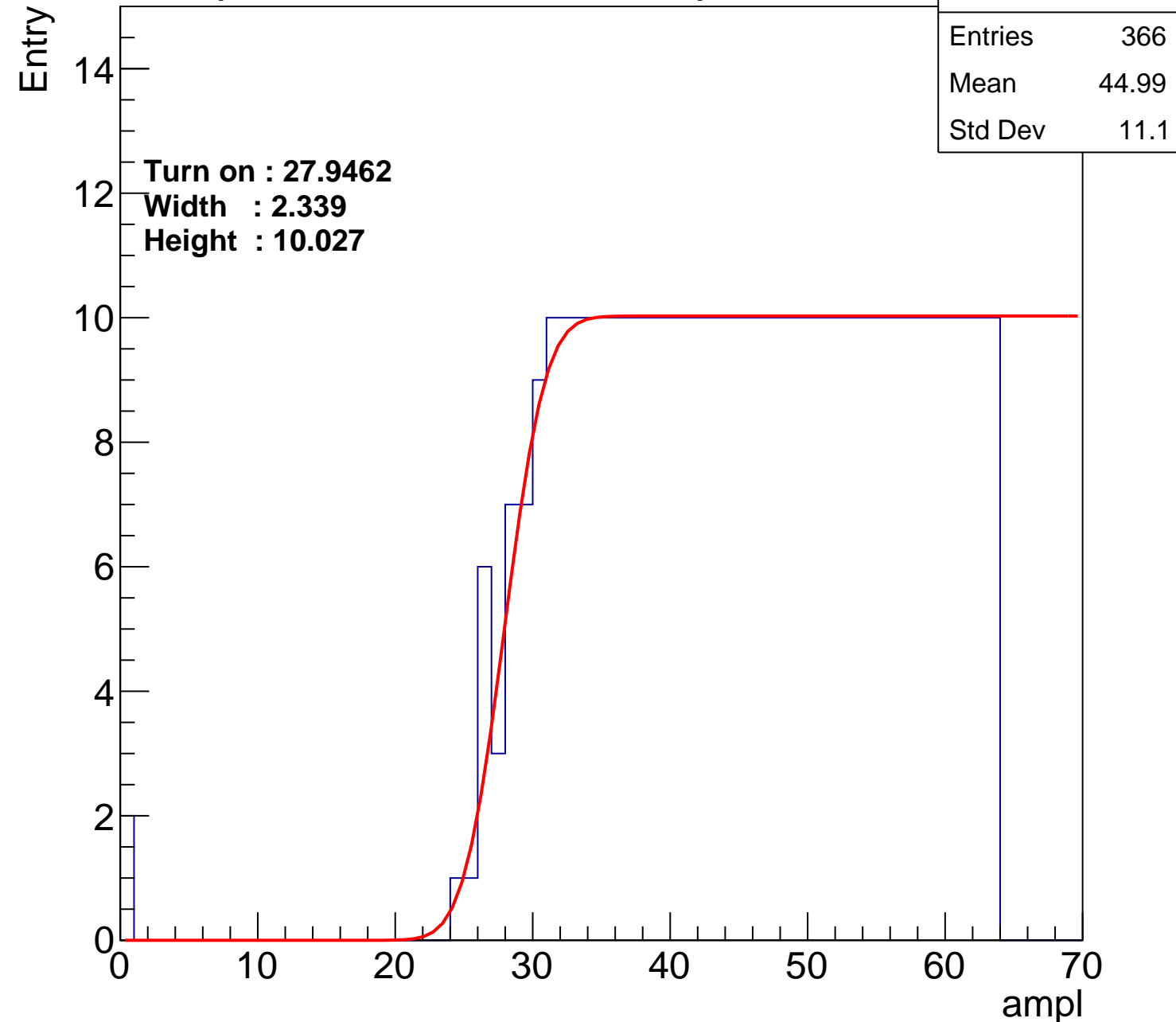
Width : 2.339

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch43

calib_packv5_042523_0143.root, FC#4, port A2

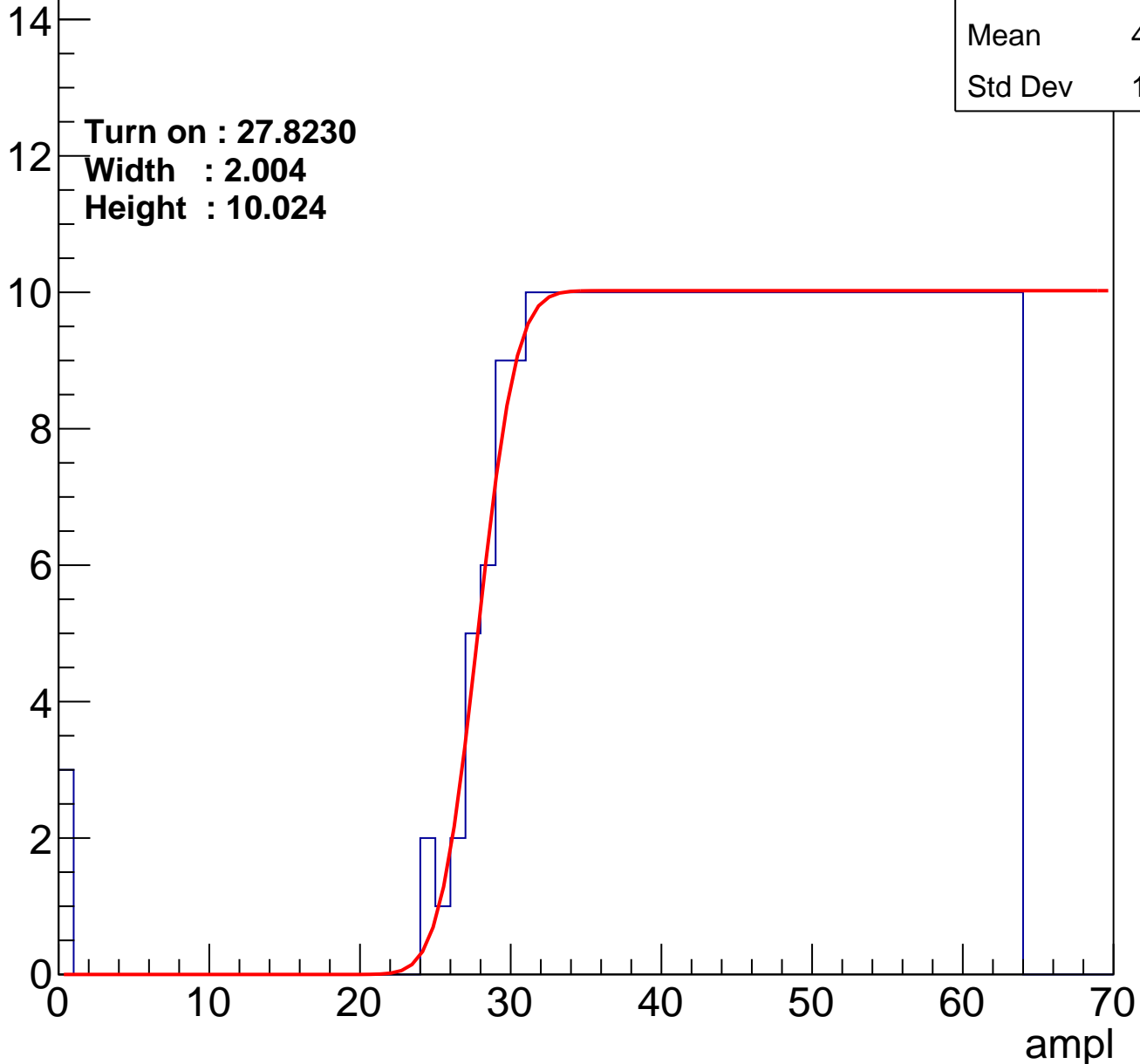
Entries	367
Mean	44.87
Std Dev	11.32

Turn on : 27.8230

Width : 2.004

Height : 10.024

Entry



B1L100S, U6-ch44

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
---------	-----

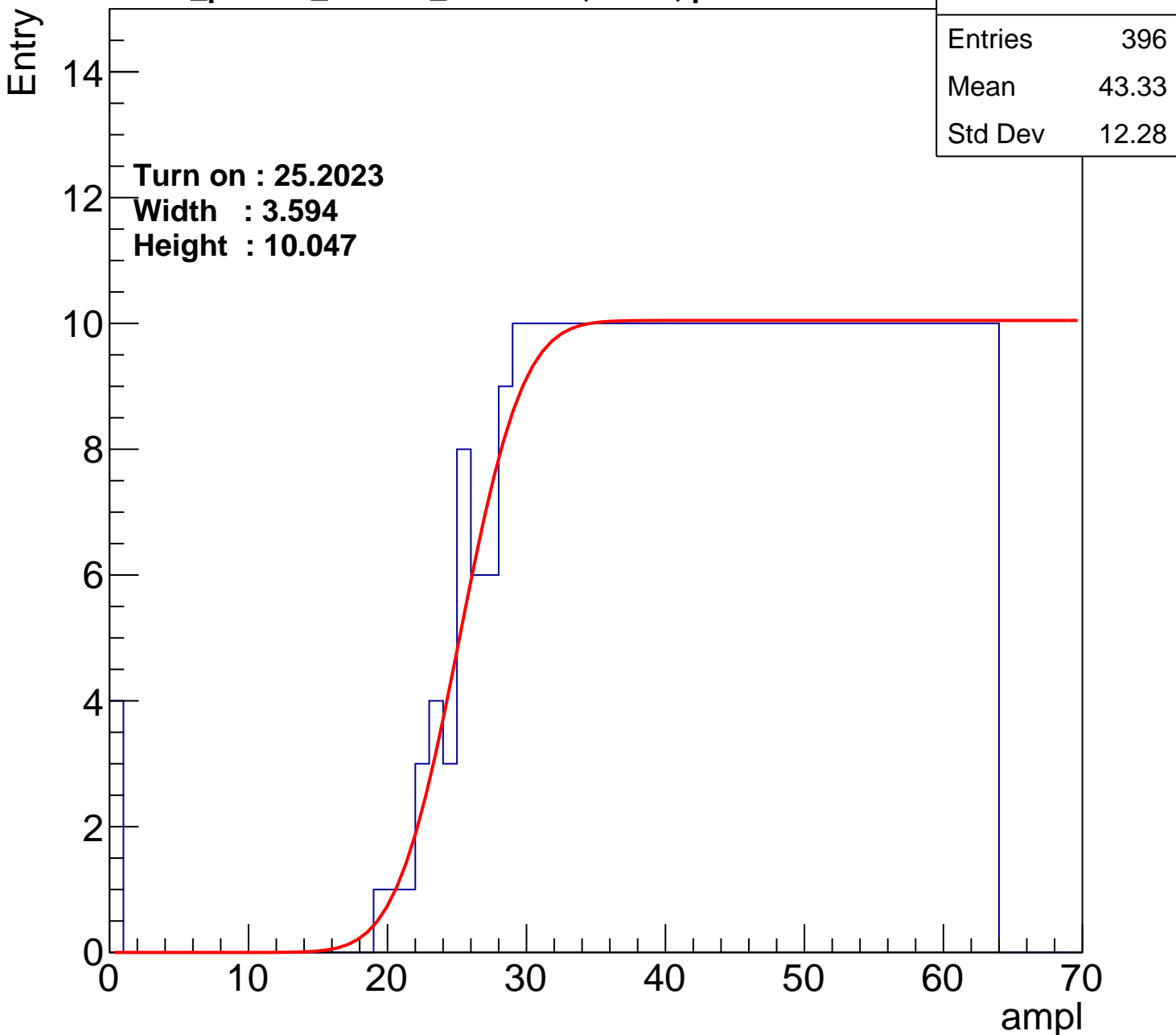
Mean	43.33
------	-------

Std Dev	12.28
---------	-------

Turn on : 25.2023

Width : 3.594

Height : 10.047



B1L100S, U6-ch45

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.68
Std Dev	11.76

Turn on : 24.8693

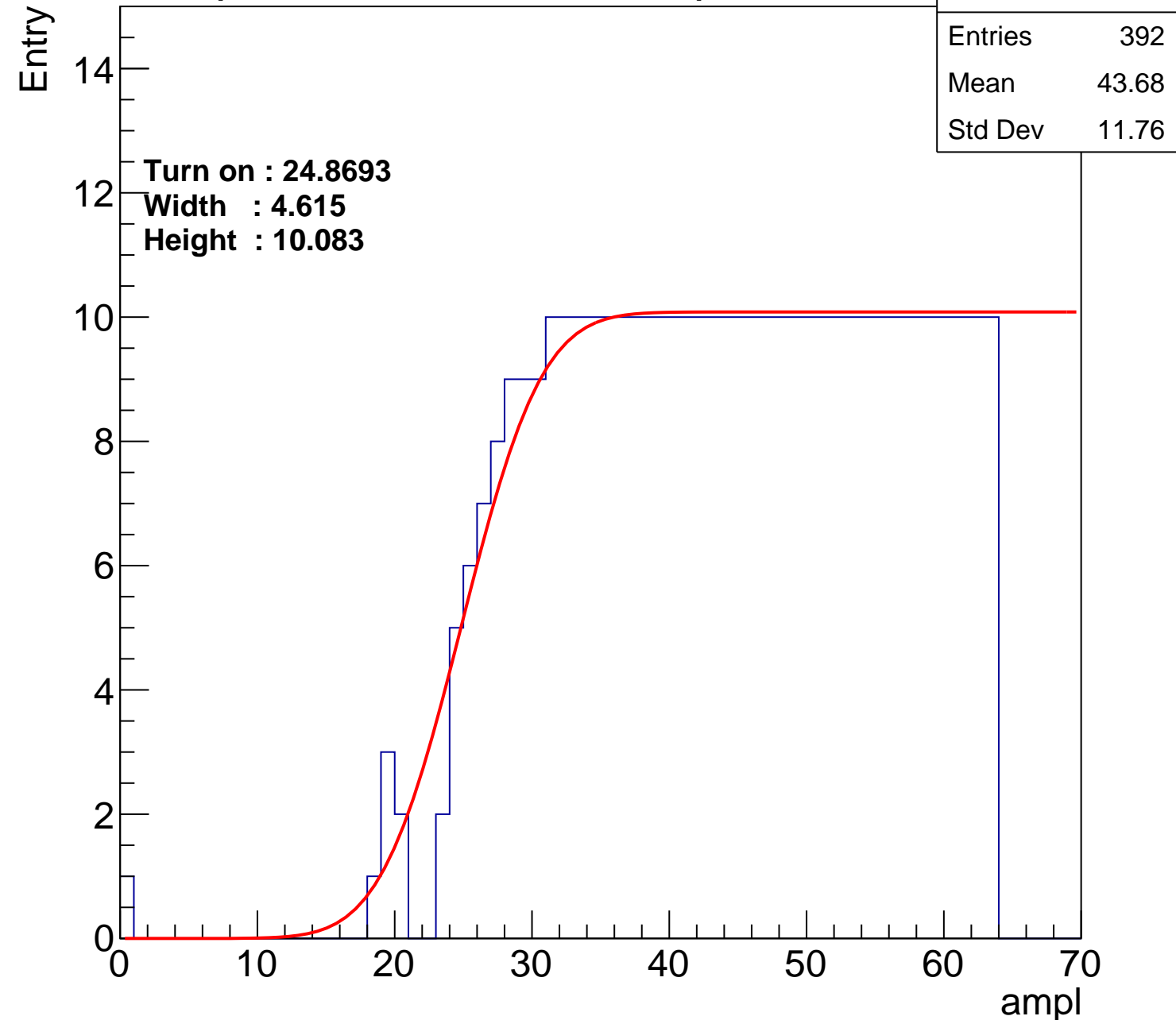
Width : 4.615

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch46

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.5841

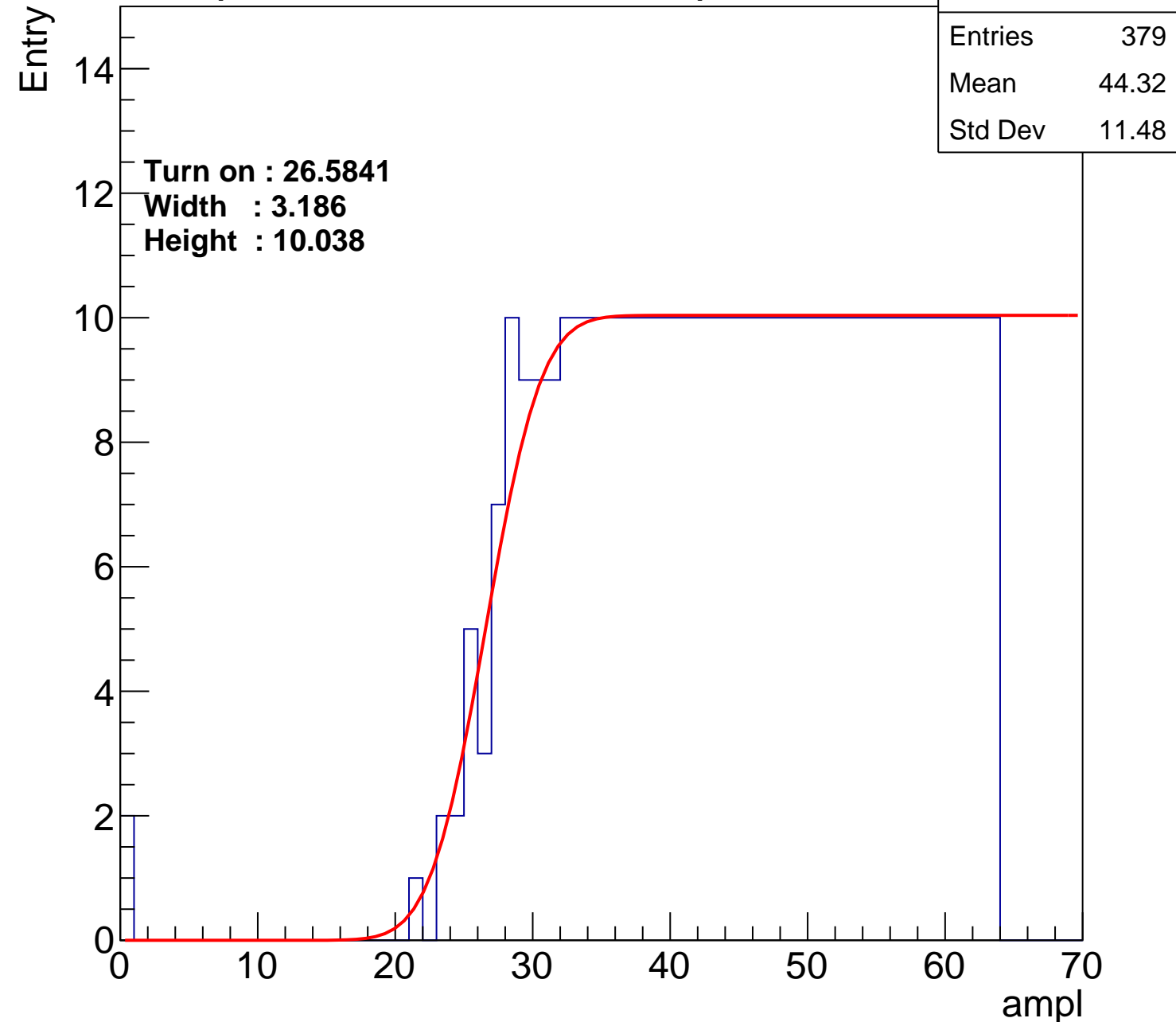
Width : 3.186

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch47

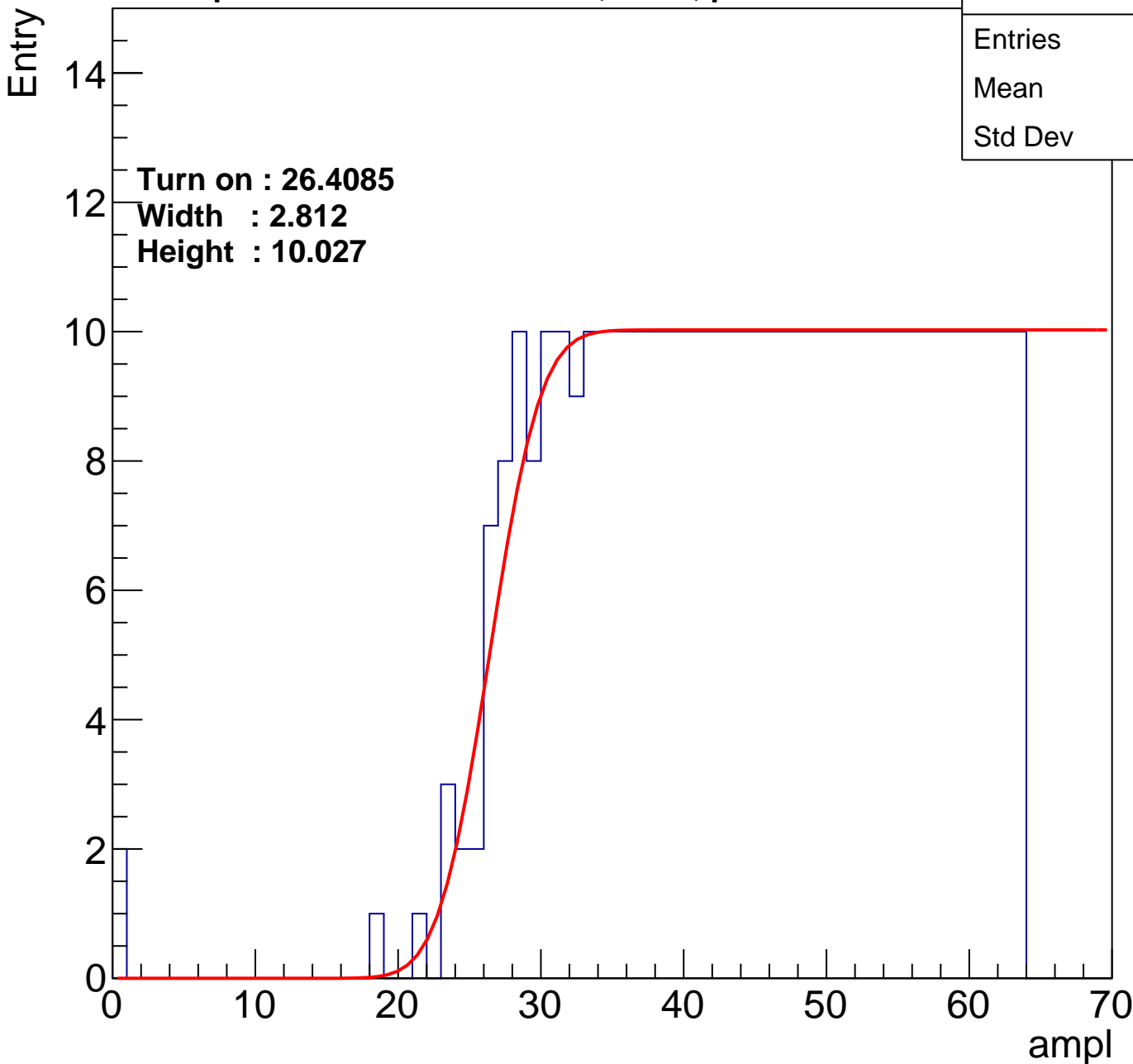
calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 26.4085

Width : 2.812

Height : 10.027



B1L100S, U6-ch48

calib_packv5_042523_0143.root, FC#4, port A2

Entries	402
Mean	43.21
Std Dev	12.03

Turn on : 23.9339

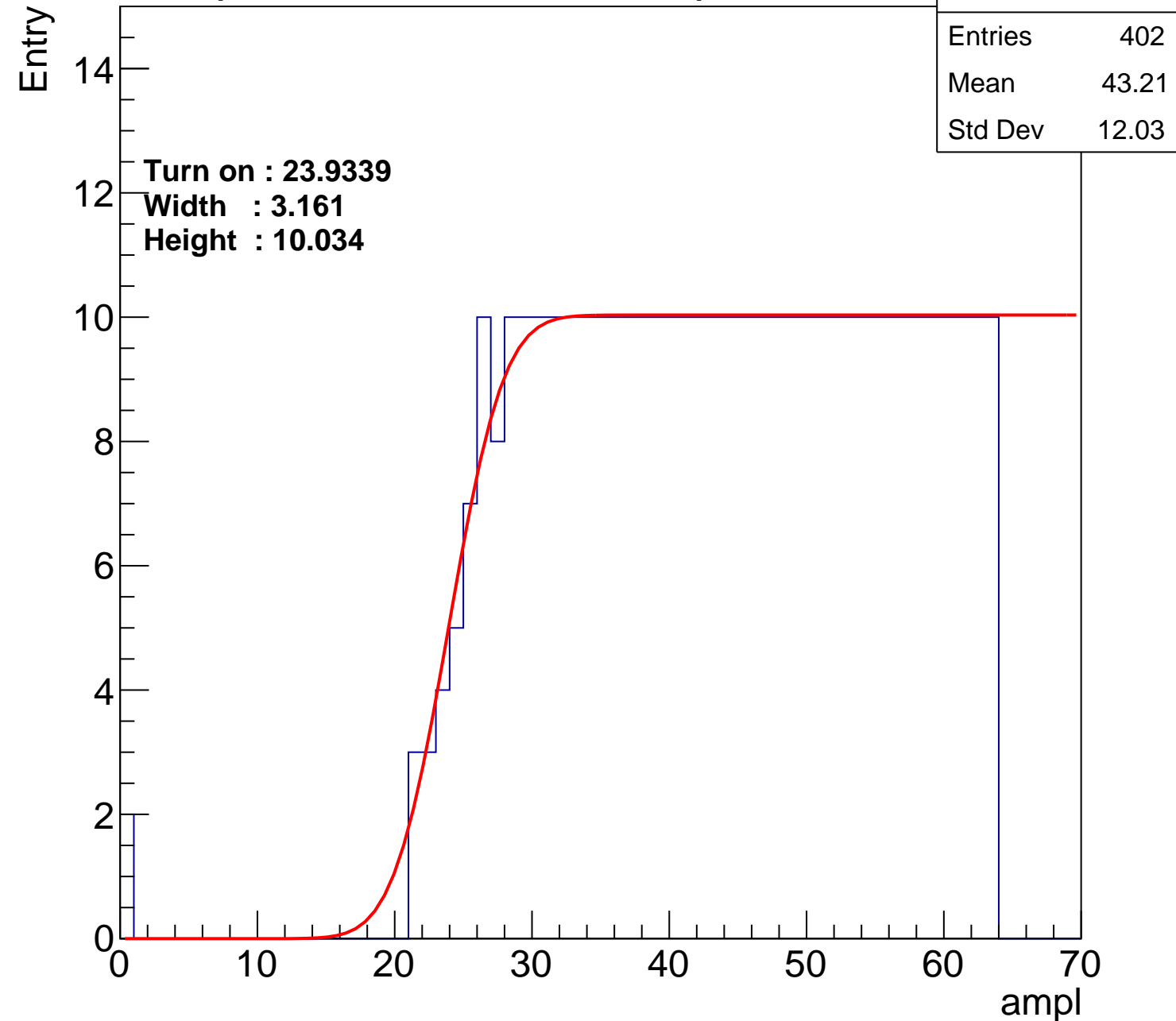
Width : 3.161

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch49

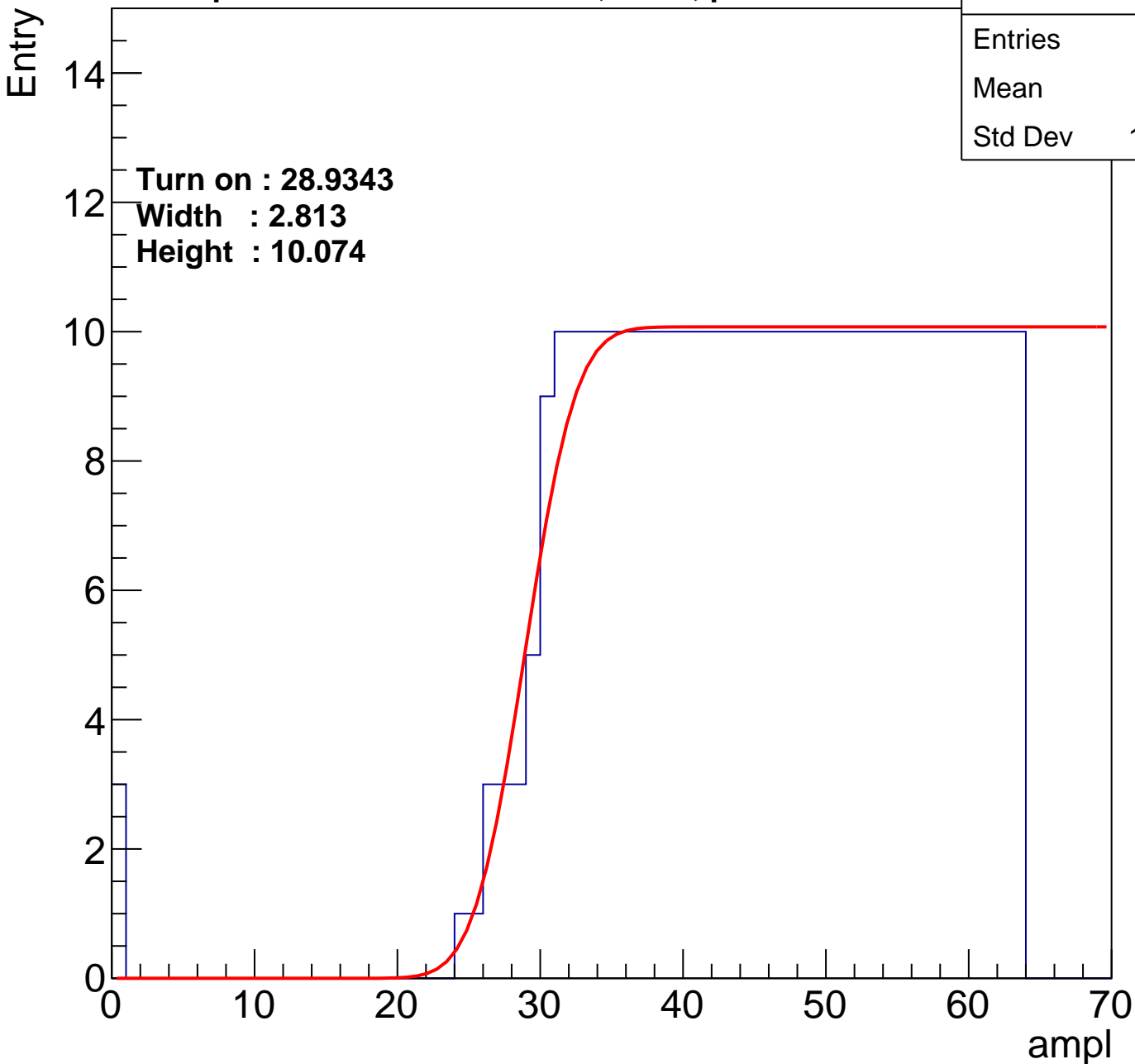
calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 28.9343

Width : 2.813

Height : 10.074

Entries	358
Mean	45.3
Std Dev	11.13



B1L100S, U6-ch50

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.94
Std Dev	11.59

Turn on : 25.8823

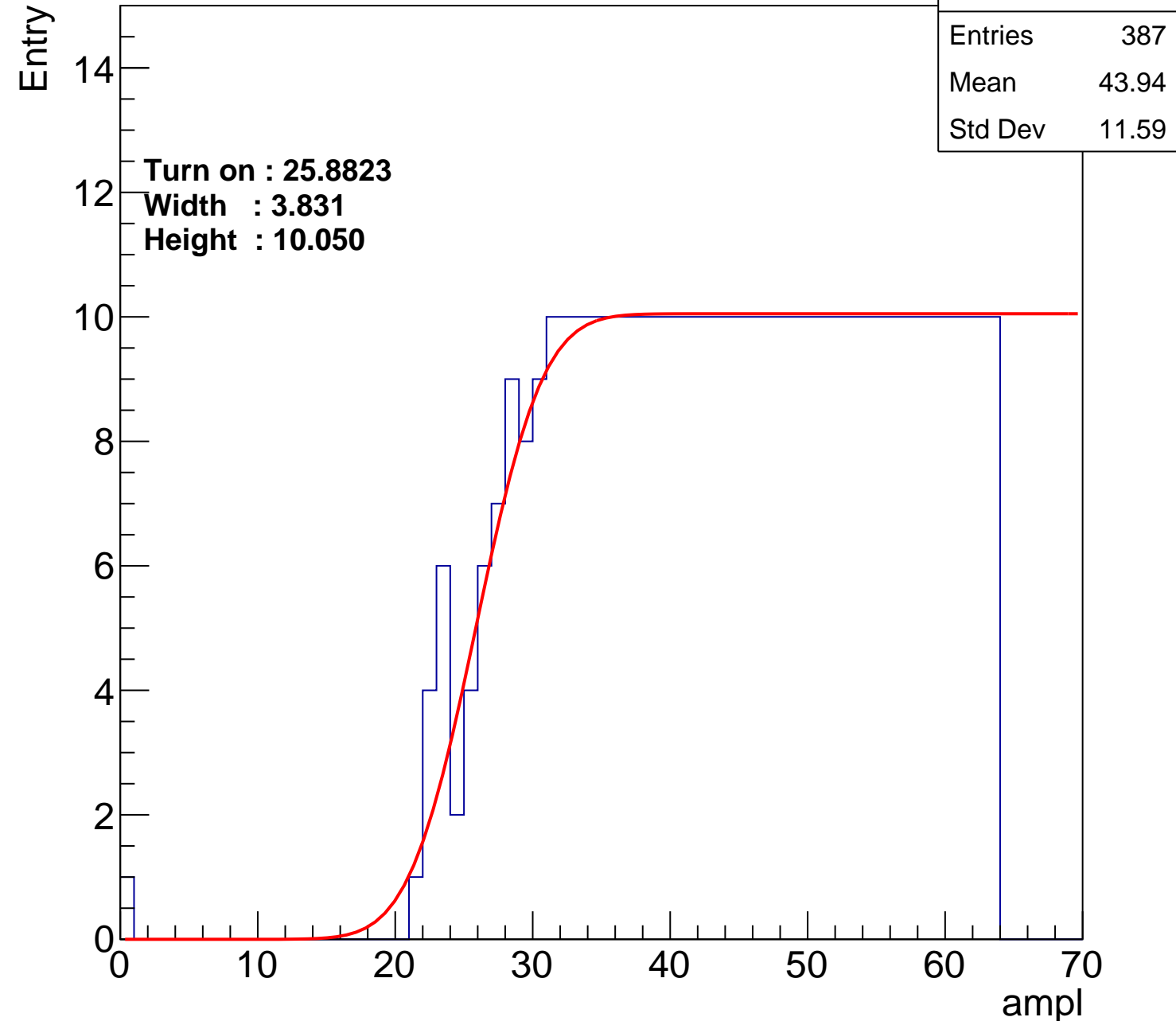
Width : 3.831

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch51

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	44.03
Std Dev	11.74

Turn on : 26.2359

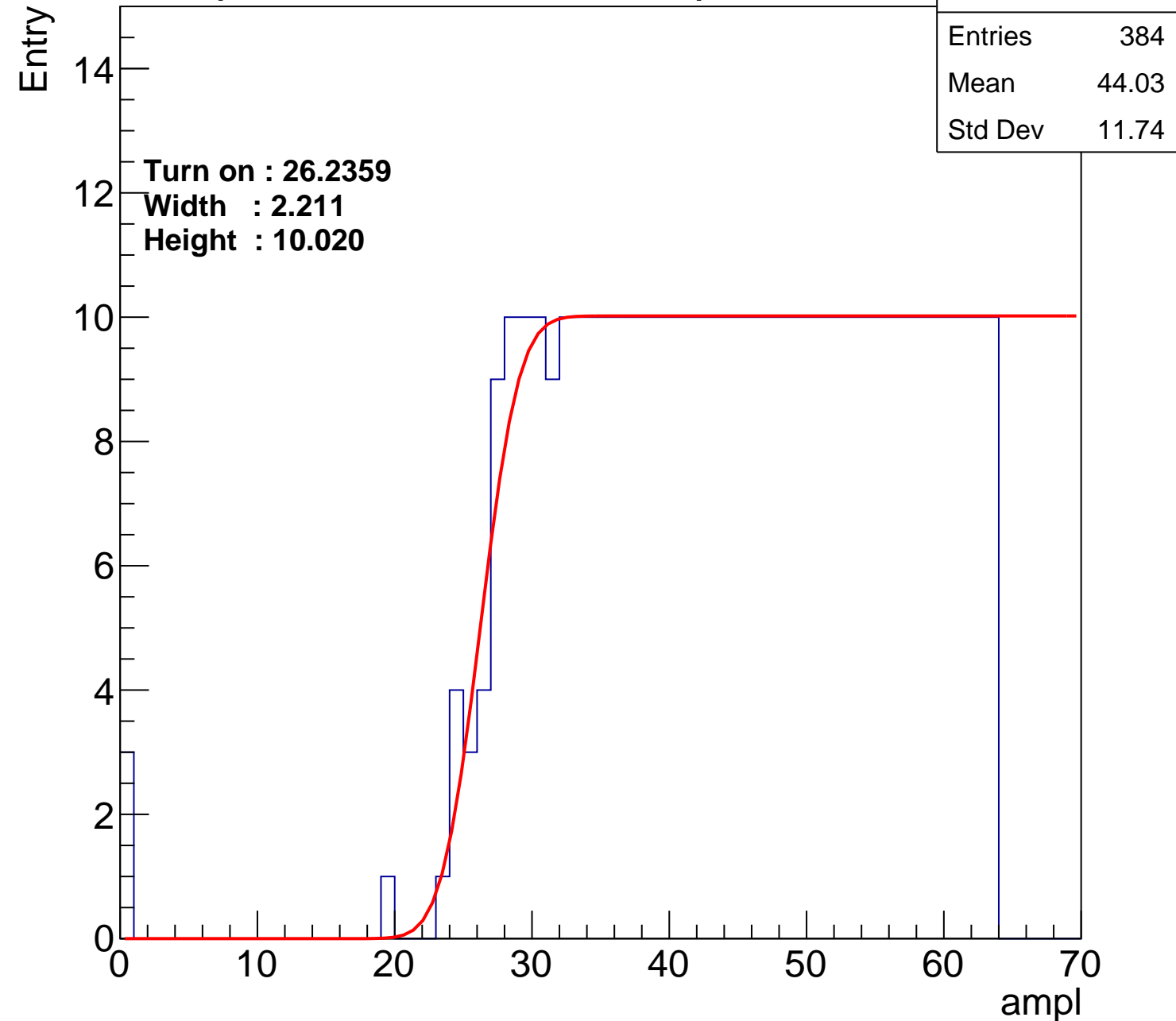
Width : 2.211

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch52

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.57
Std Dev	11.83

Turn on : 24.8603

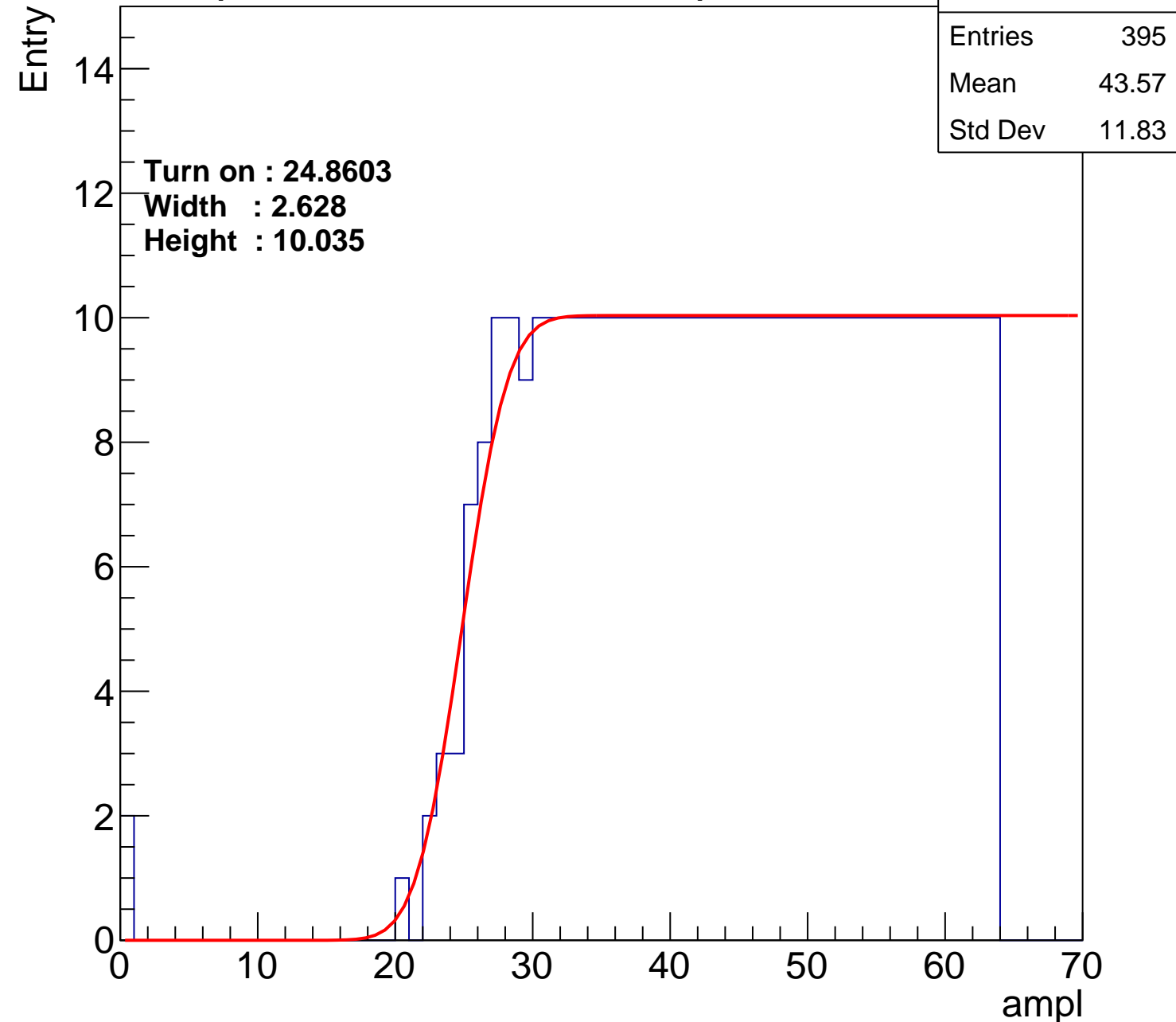
Width : 2.628

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch53

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45
Std Dev	11.12

Turn on : 27.3913

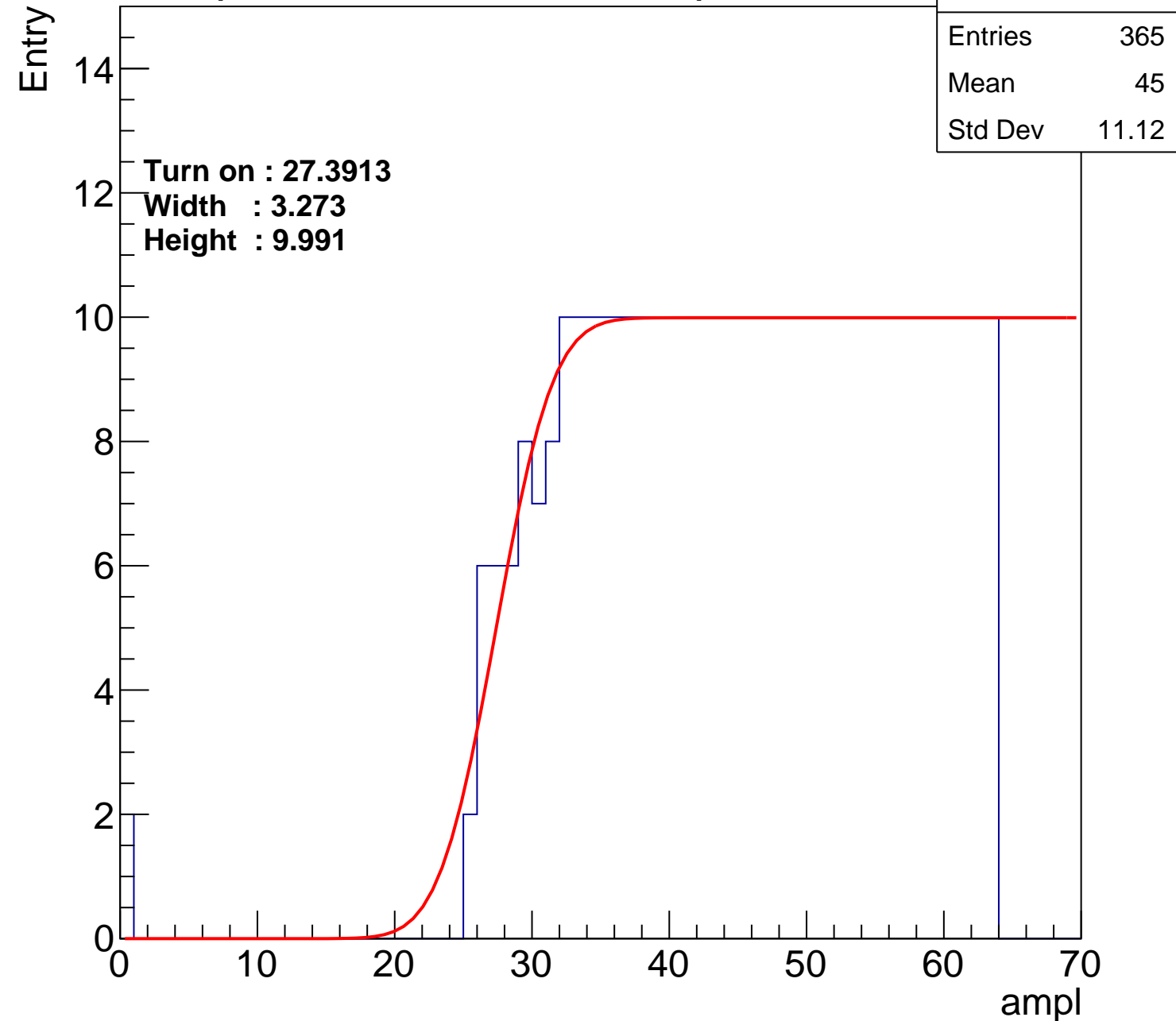
Width : 3.273

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch54

calib_packv5_042523_0143.root, FC#4, port A2

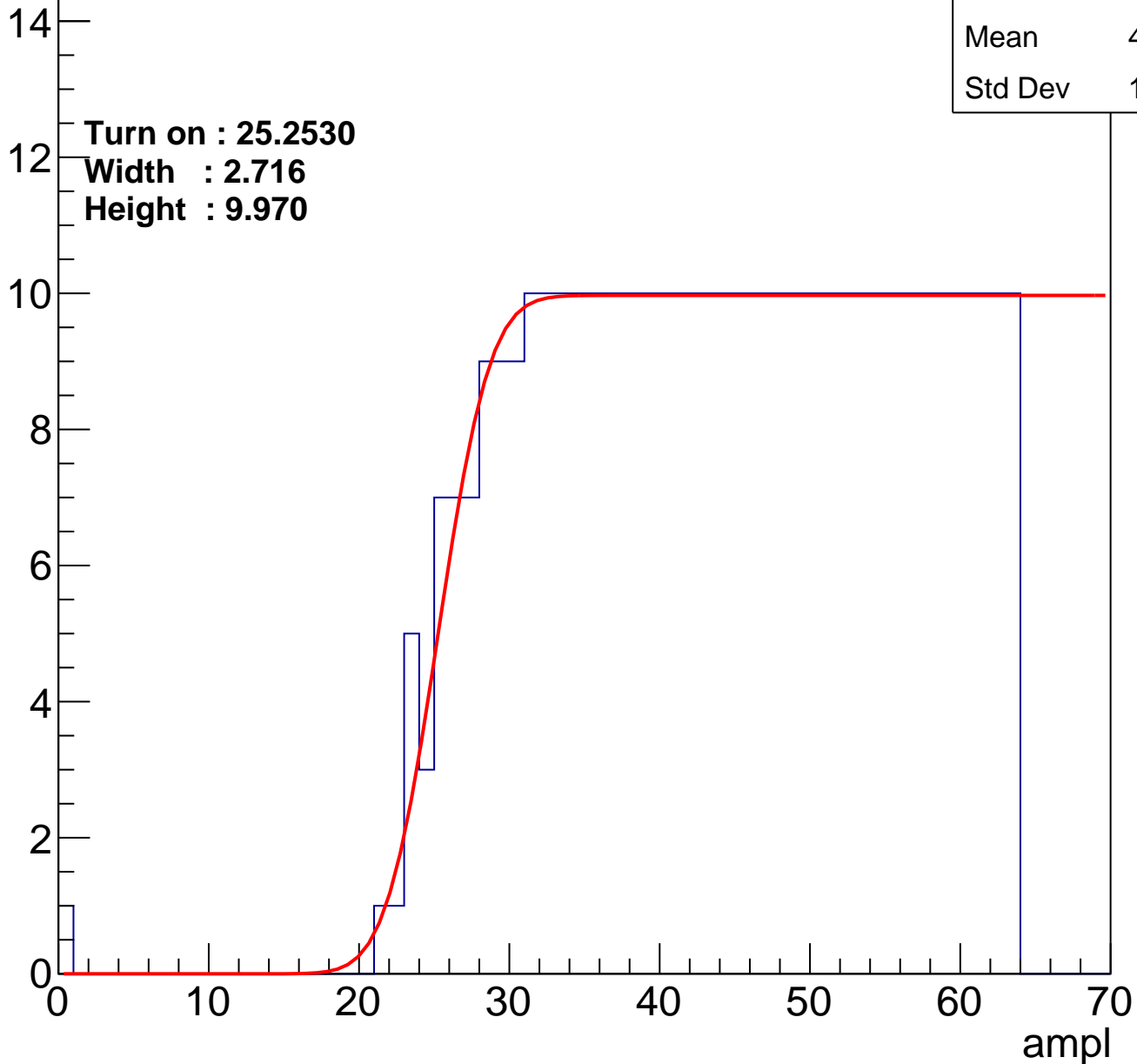
Entries	389
Mean	43.88
Std Dev	11.58

Turn on : 25.2530

Width : 2.716

Height : 9.970

Entry



B1L100S, U6-ch55

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.83
Std Dev	11.18

Turn on : 27.5492

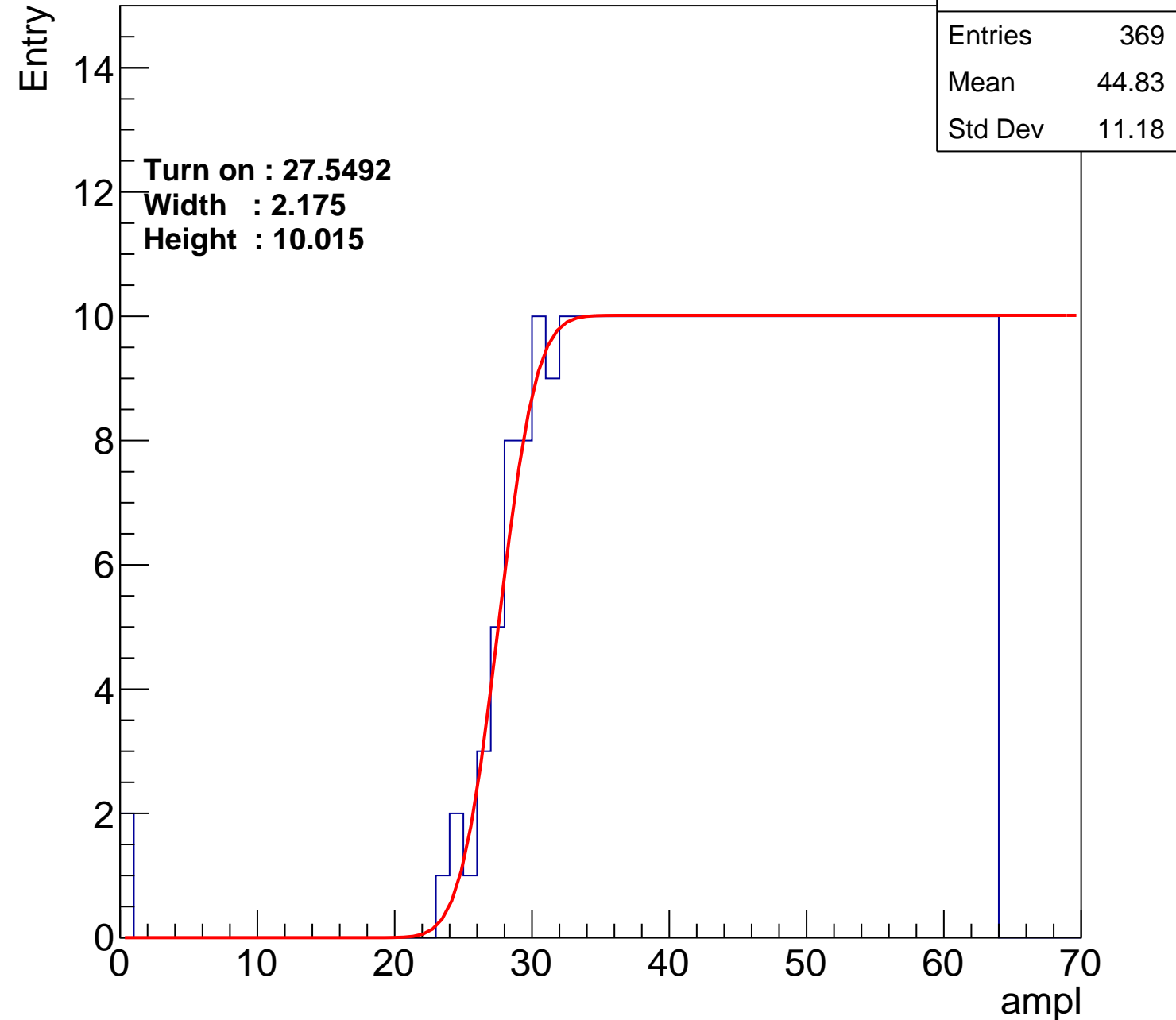
Width : 2.175

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch56

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 26.2015

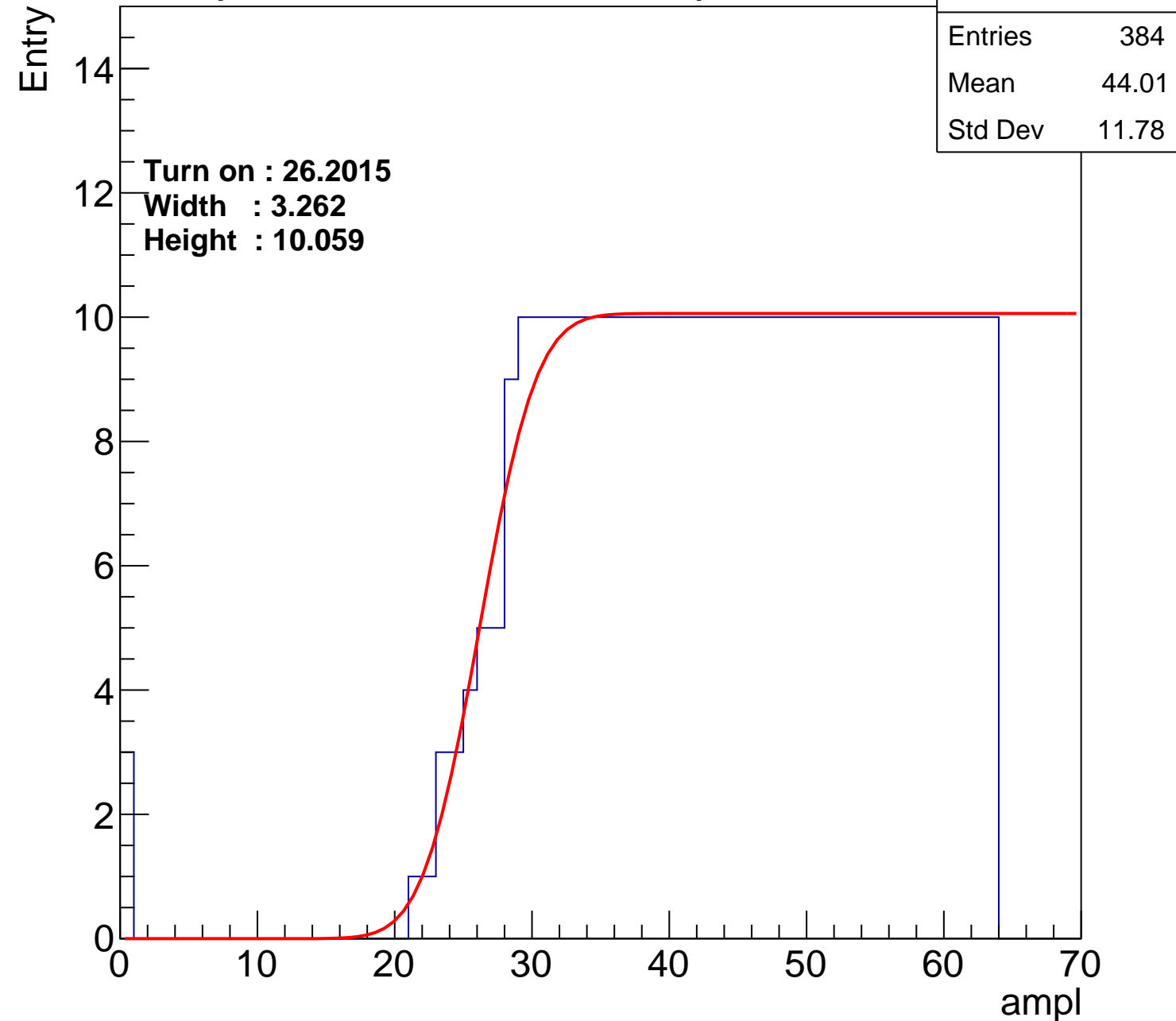
Width : 3.262

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch57

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 27.7604

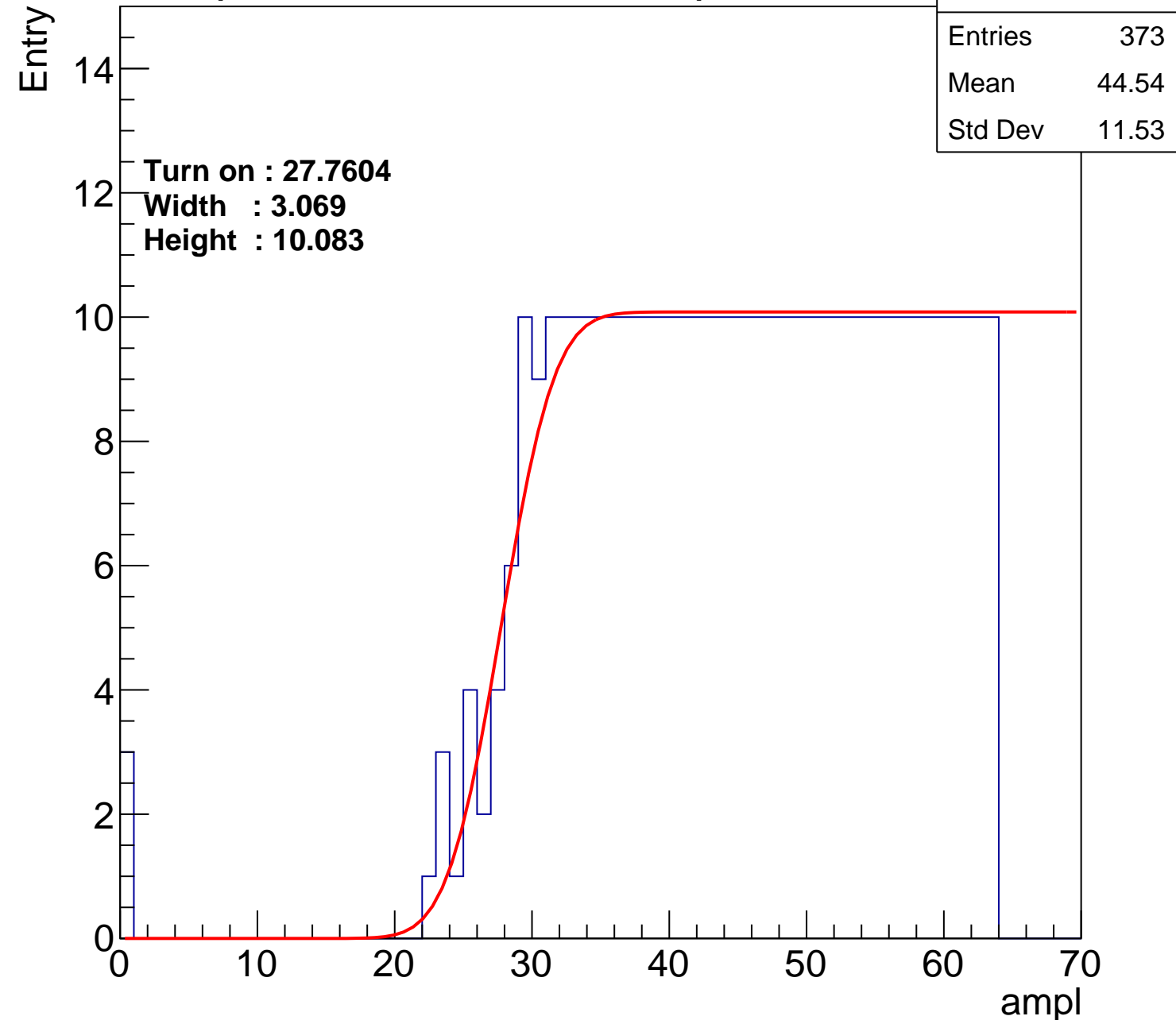
Width : 3.069

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch58

calib_packv5_042523_0143.root, FC#4, port A2

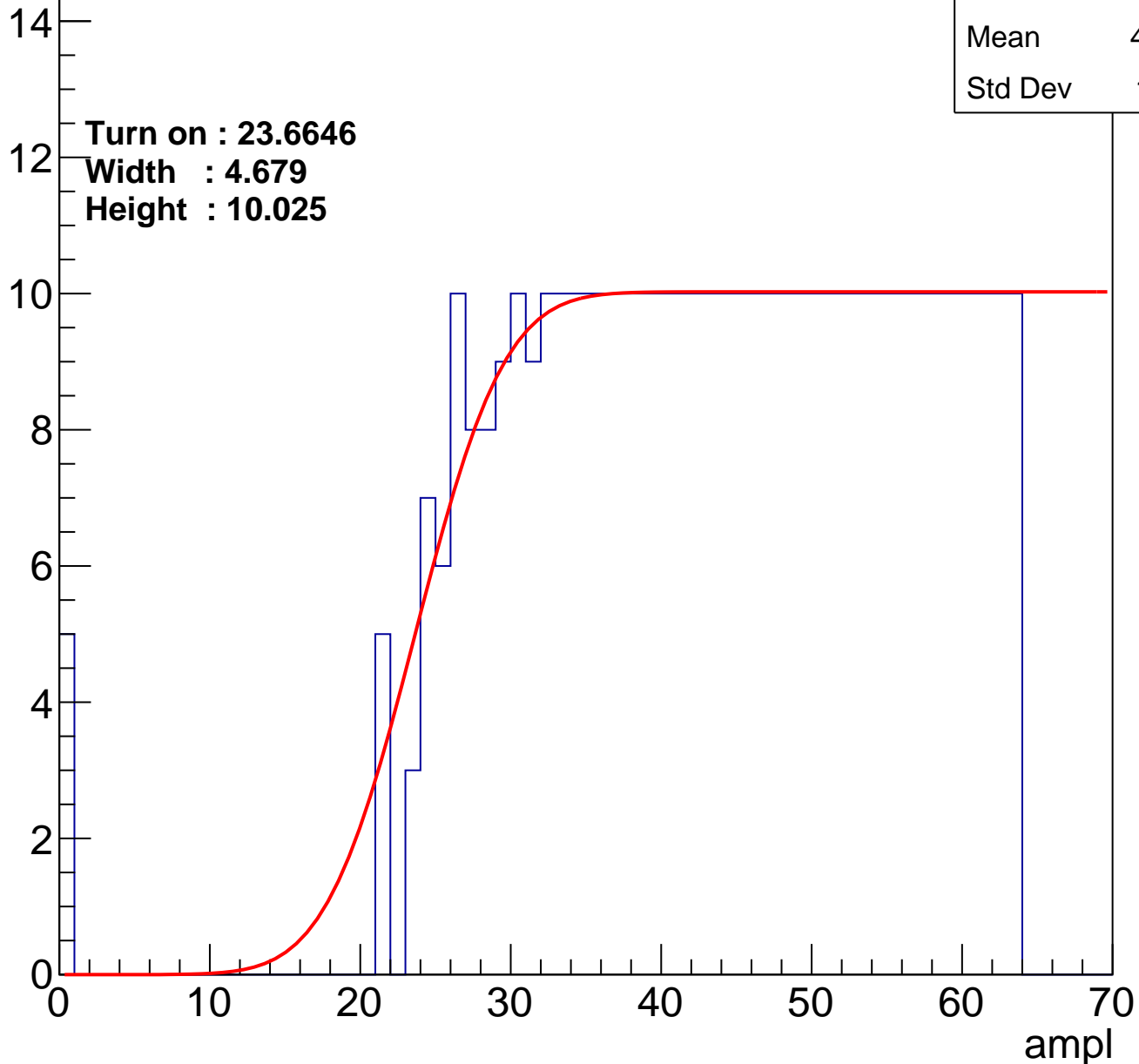
Entries	400
Mean	43.08
Std Dev	12.51

Turn on : 23.6646

Width : 4.679

Height : 10.025

Entry



B1L100S, U6-ch59

calib_packv5_042523_0143.root, FC#4, port A2

Entries	357
Mean	45.28
Std Dev	11.2

Turn on : 28.7672

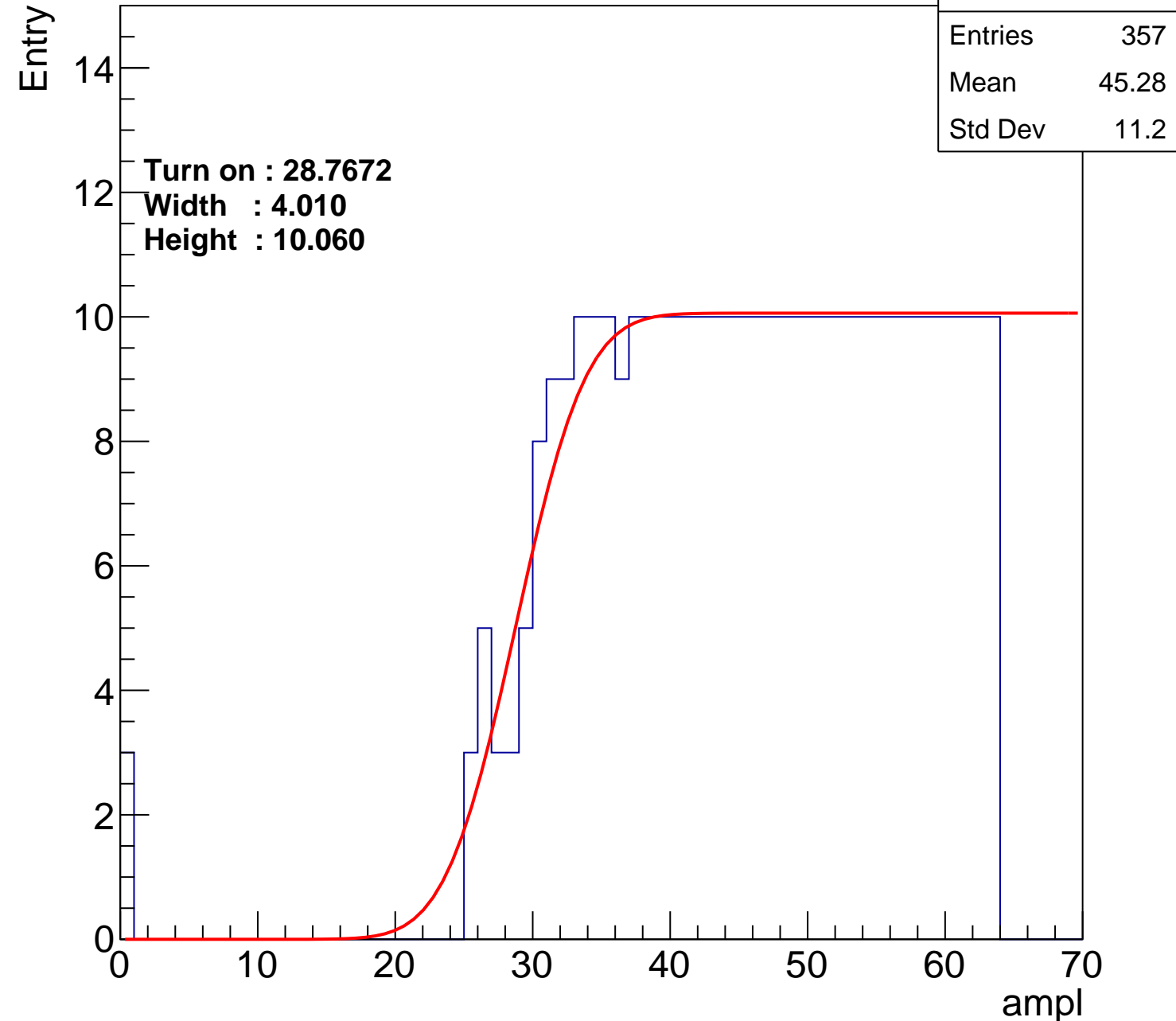
Width : 4.010

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch60

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.52
Std Dev	11.24

Turn on : 26.4609

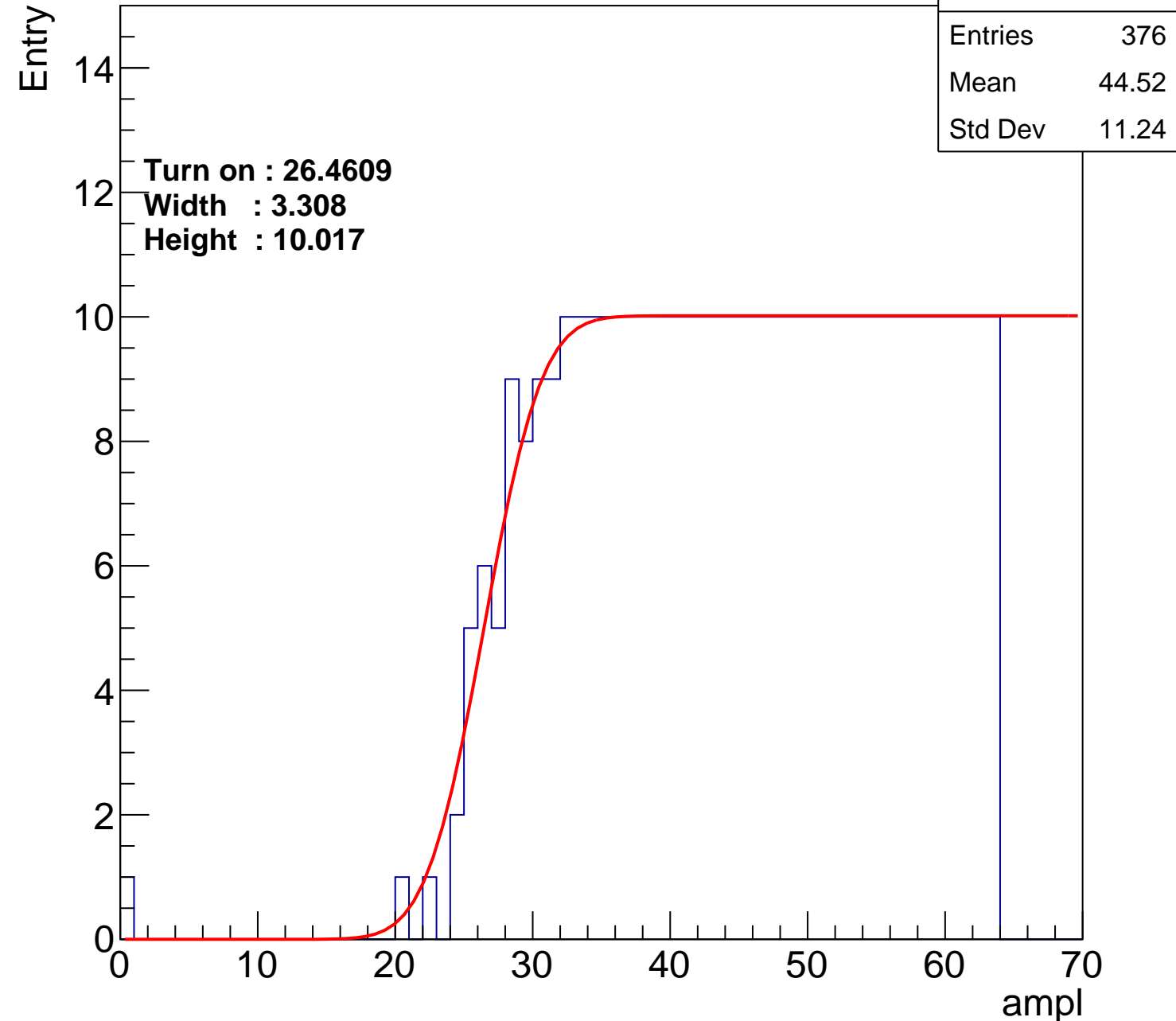
Width : 3.308

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch61

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.68
Std Dev	11.28

Turn on : 27.3662

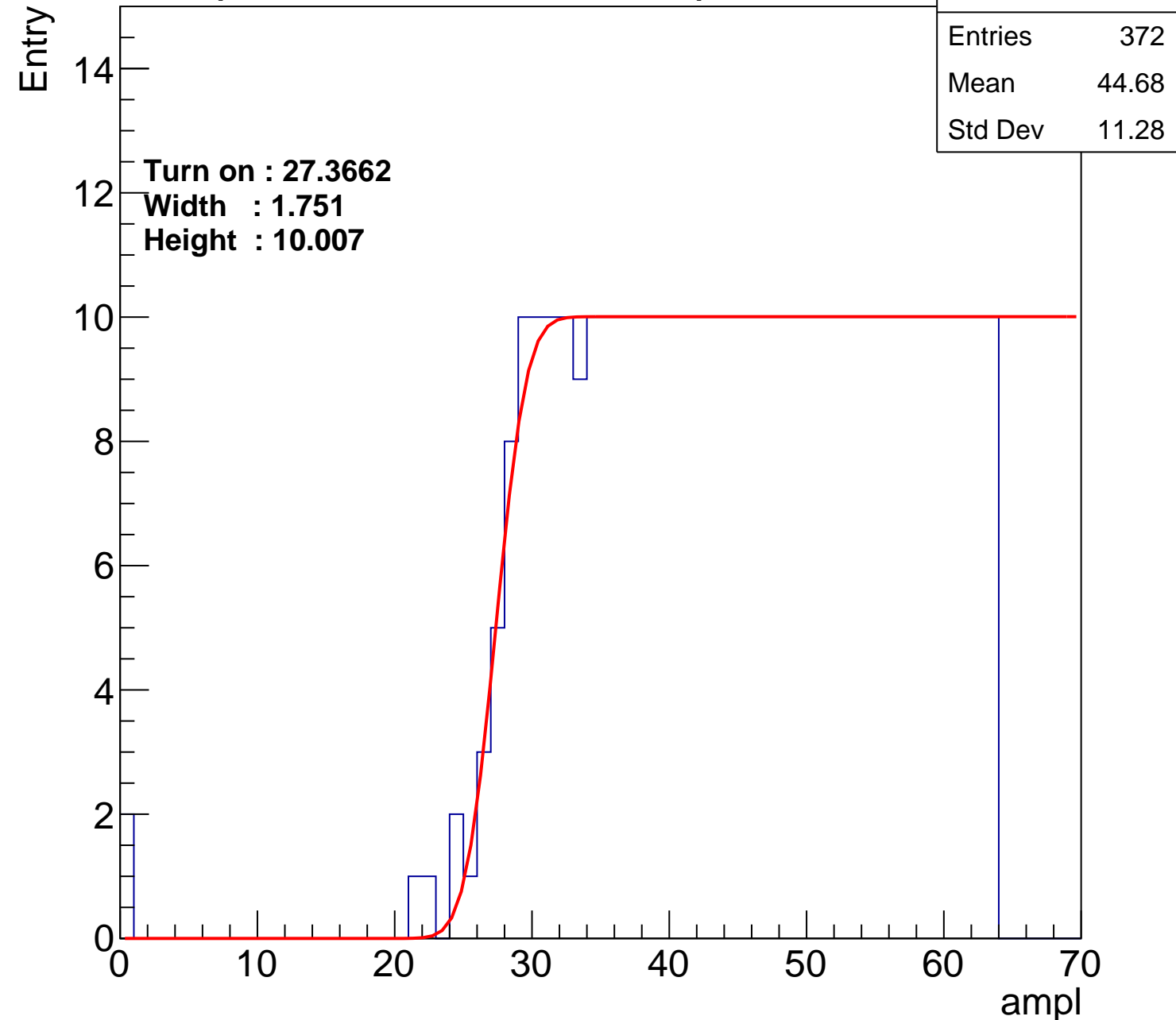
Width : 1.751

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch62

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.64
Std Dev	11.46

Turn on : 26.9052

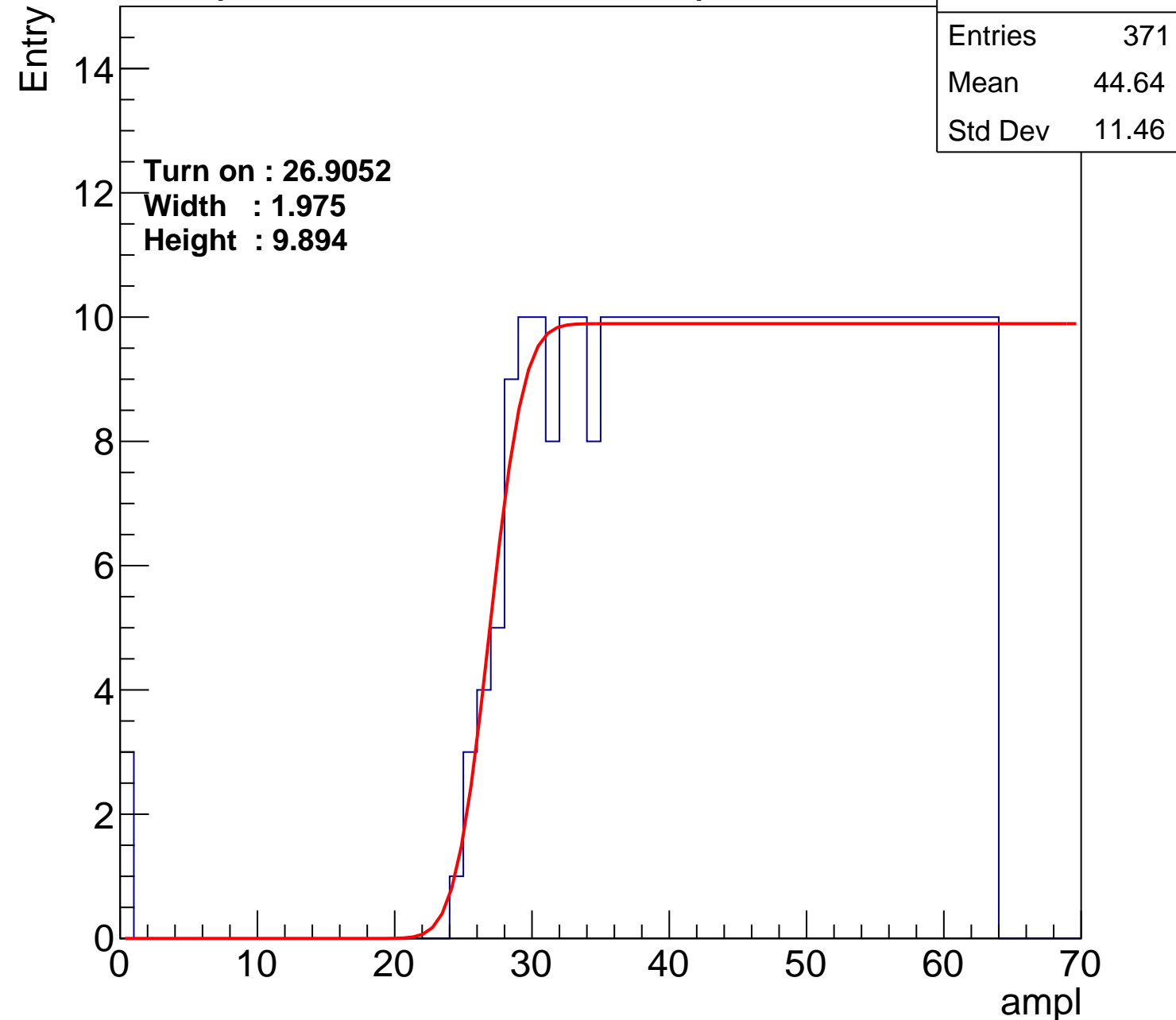
Width : 1.975

Height : 9.894

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch63

calib_packv5_042523_0143.root, FC#4, port A2

Entries	405
Mean	42.92
Std Dev	12.45

Turn on : 23.8589

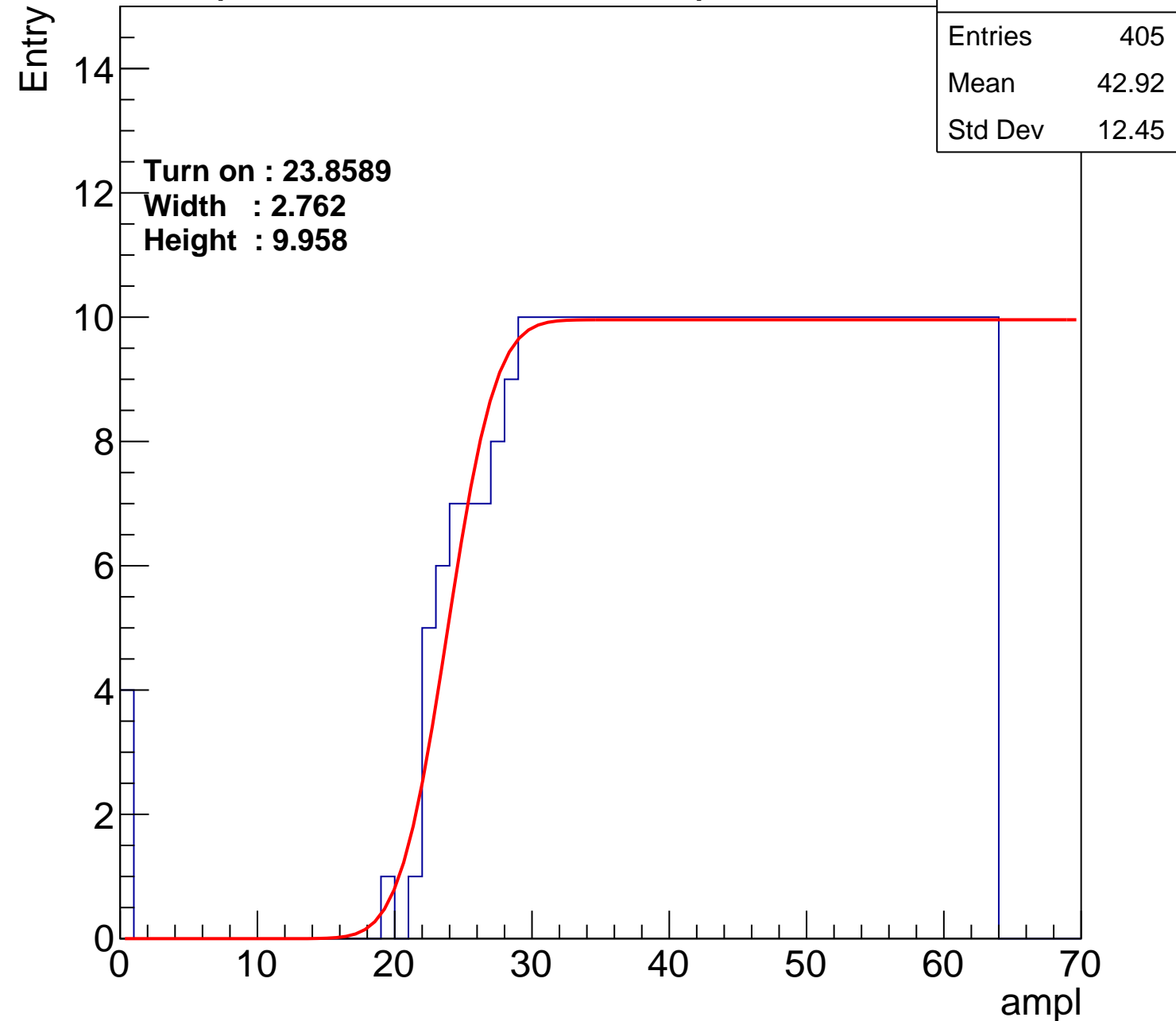
Width : 2.762

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch64

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	43.81
Std Dev	12.3

Turn on : 26.5647

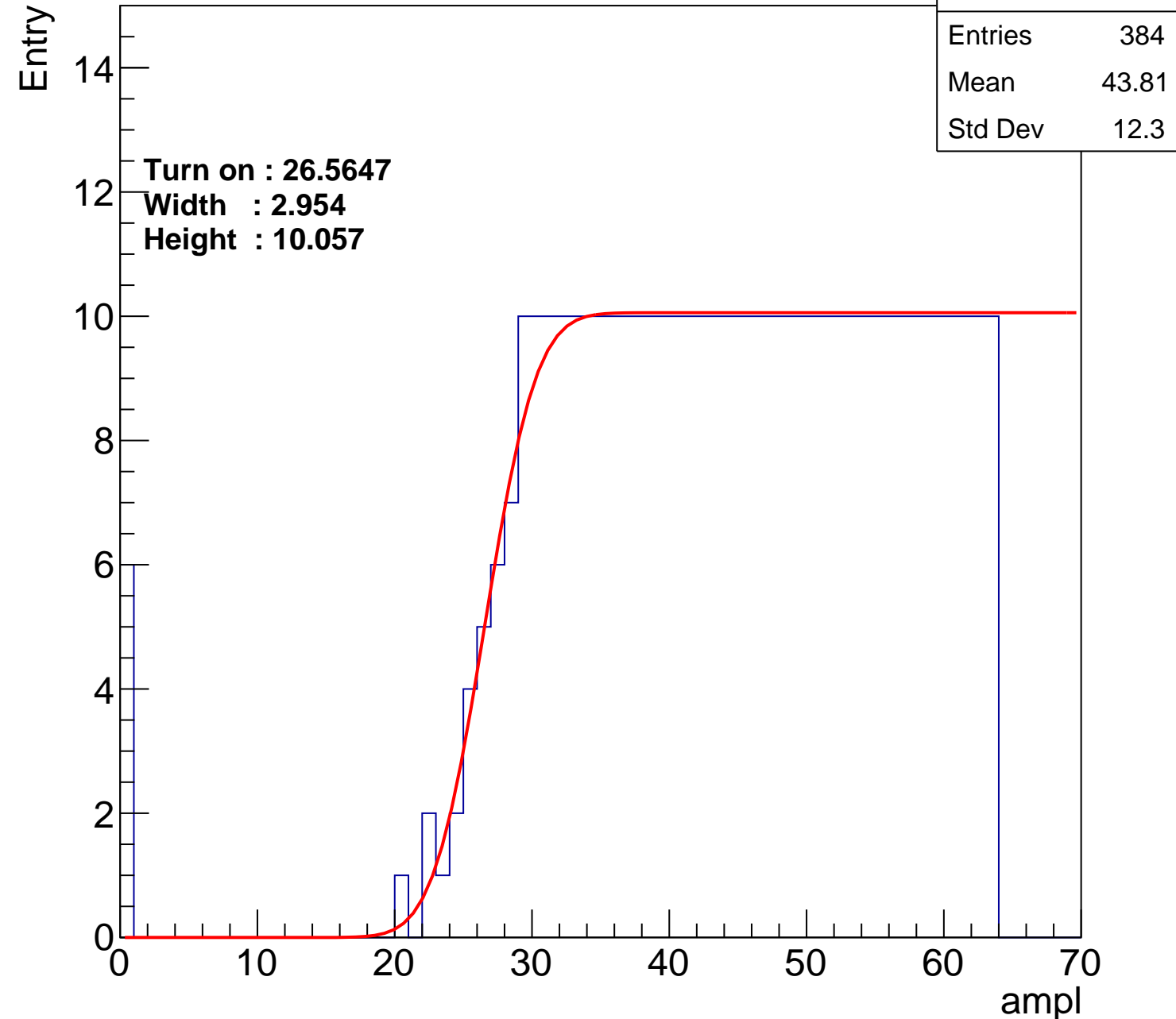
Width : 2.954

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch65

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.61
Std Dev	11.15

Turn on : 26.7212

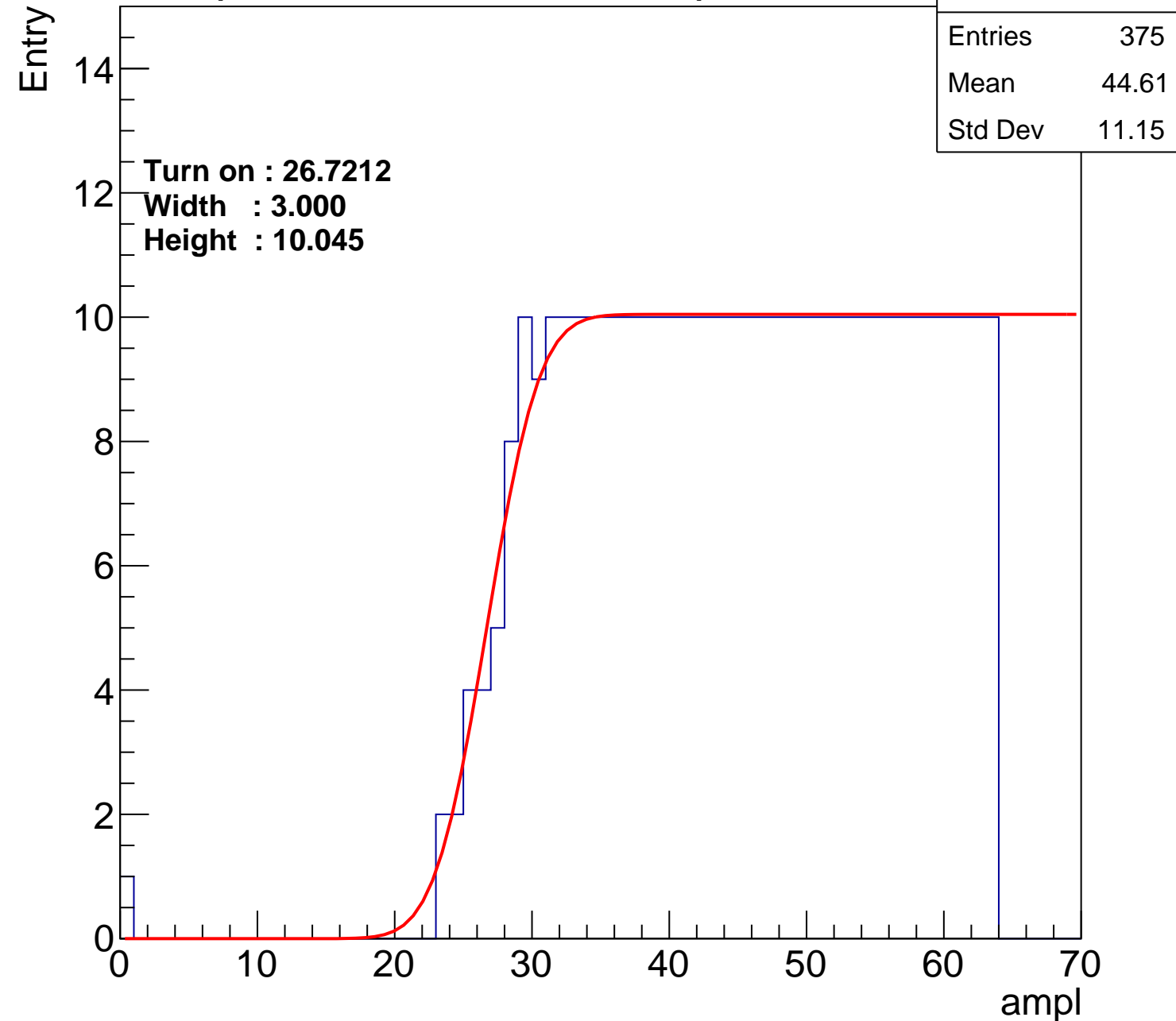
Width : 3.000

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch66

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.78
Std Dev	11.28

Turn on : 27.5987

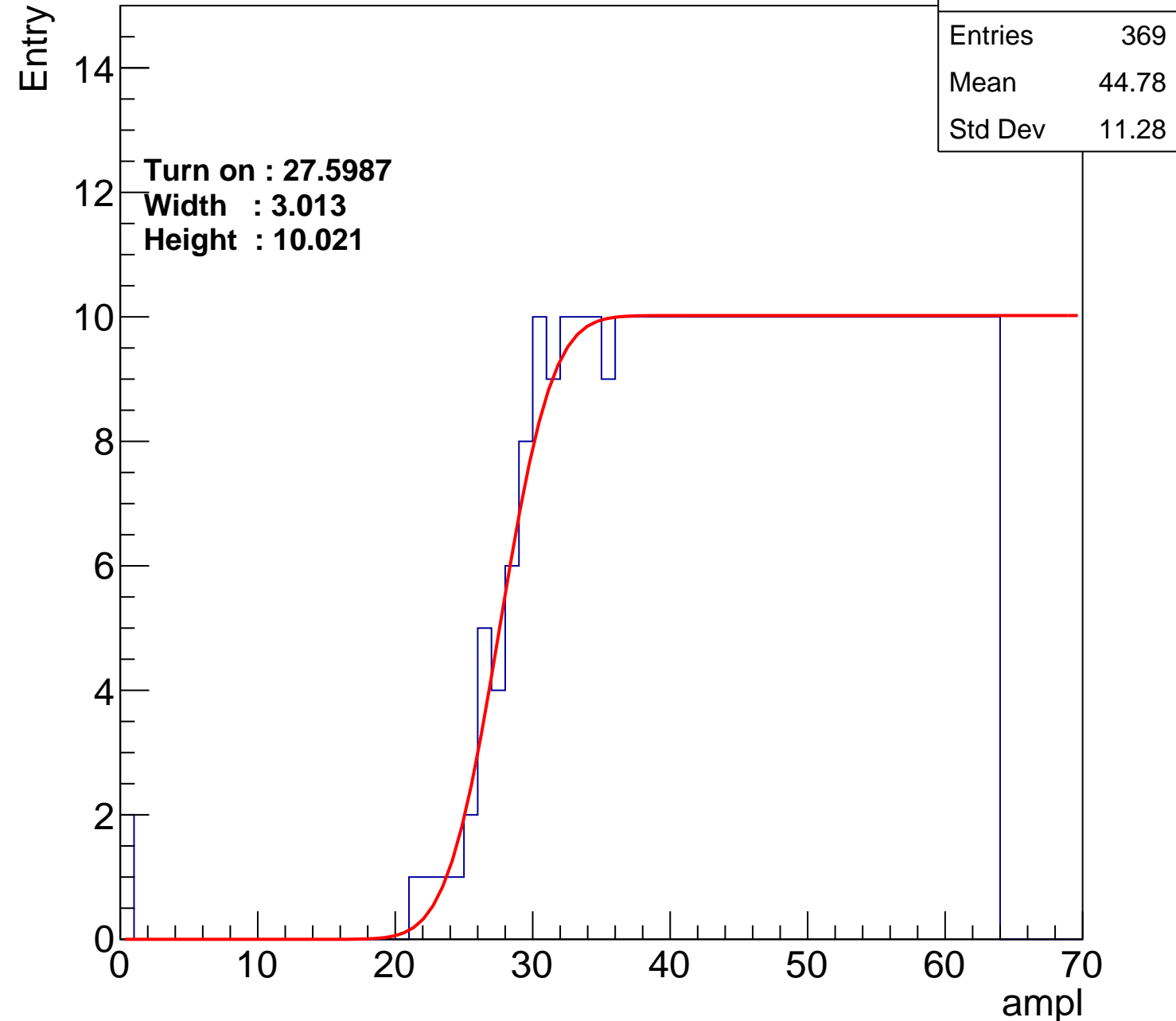
Width : 3.013

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch67

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 26.8671

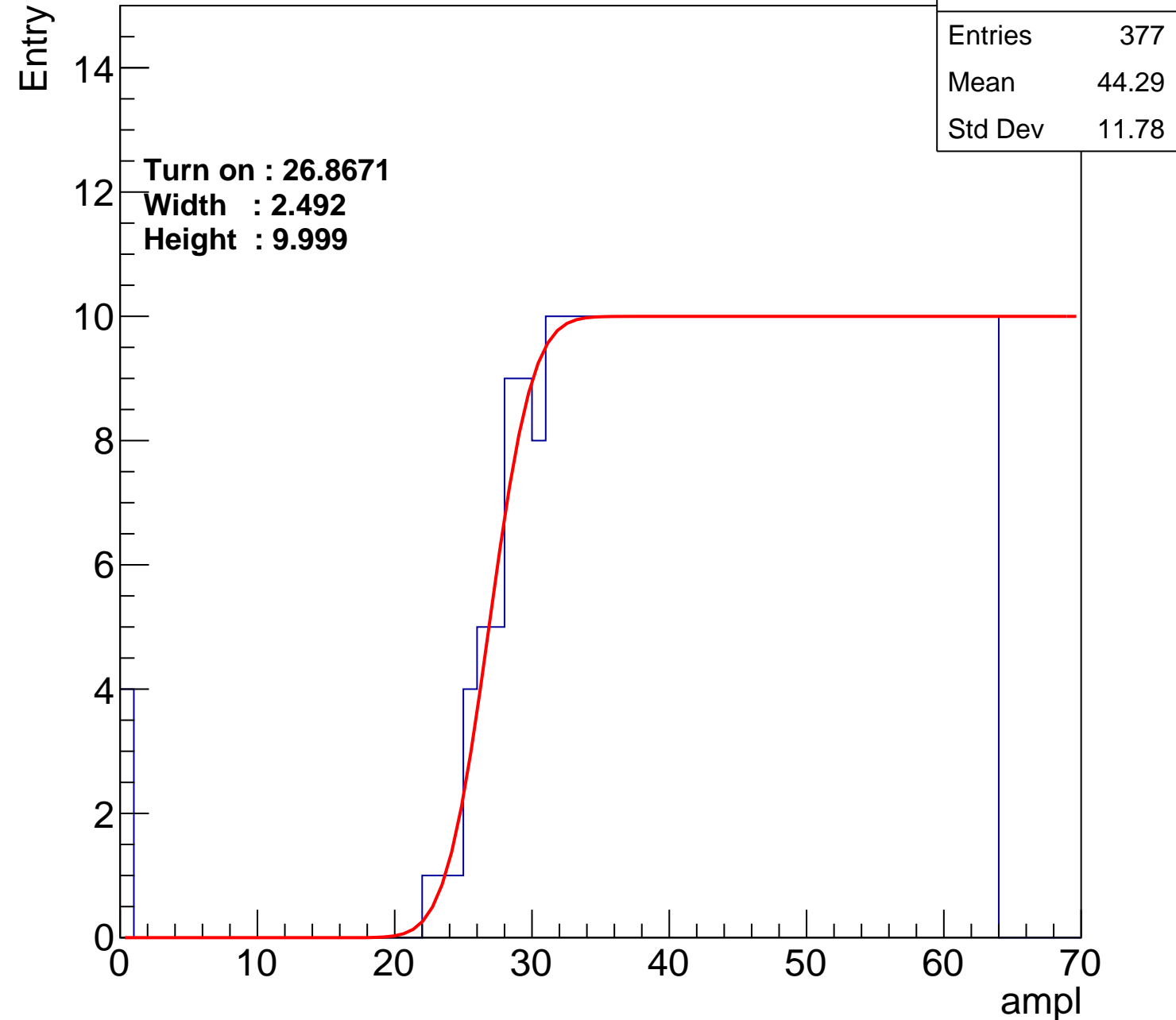
Width : 2.492

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch68

calib_packv5_042523_0143.root, FC#4, port A2

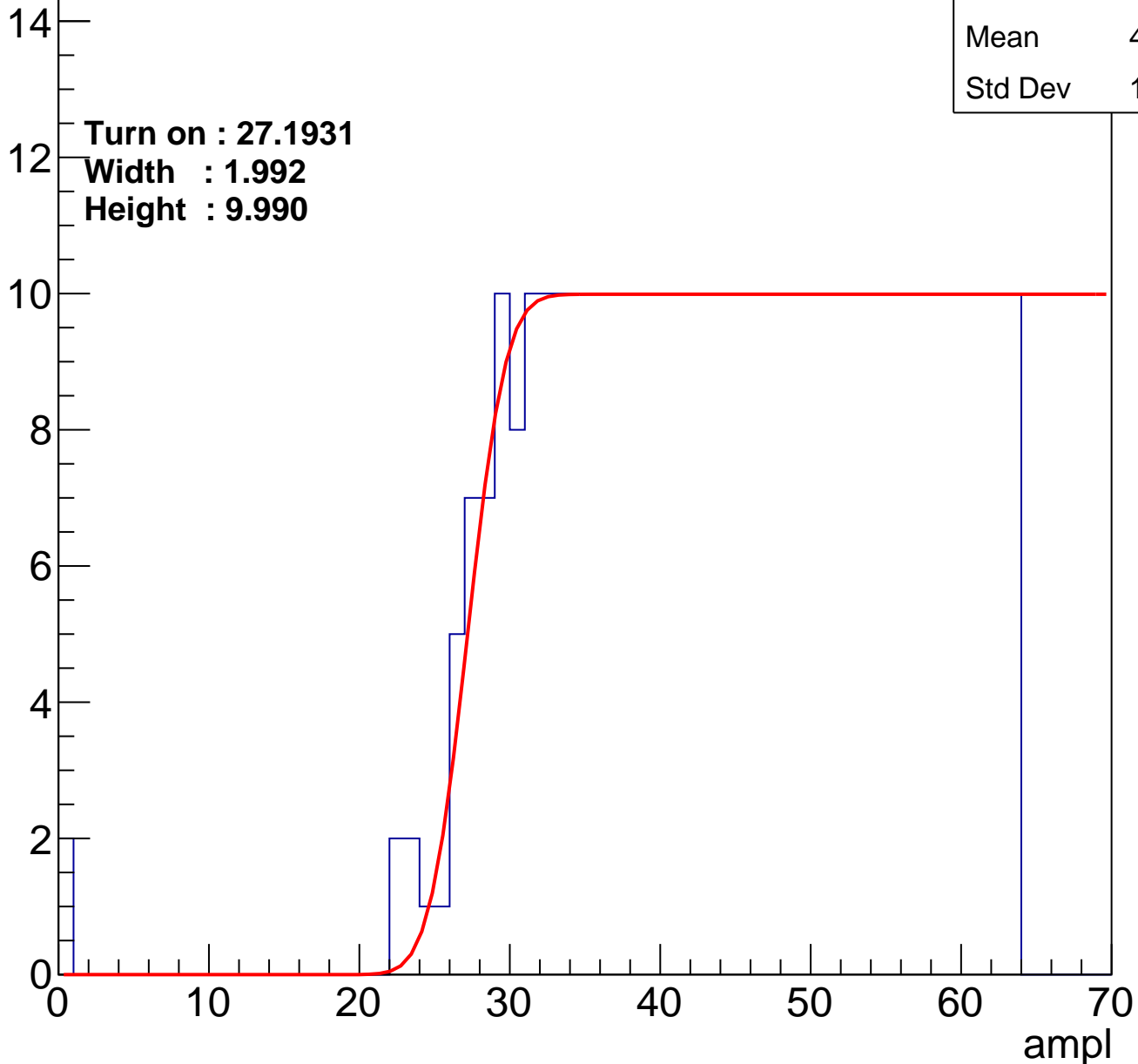
Entries	375
Mean	44.52
Std Dev	11.38

Turn on : 27.1931

Width : 1.992

Height : 9.990

Entry



B1L100S, U6-ch69

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.17
Std Dev	11.98

Turn on : 26.5732

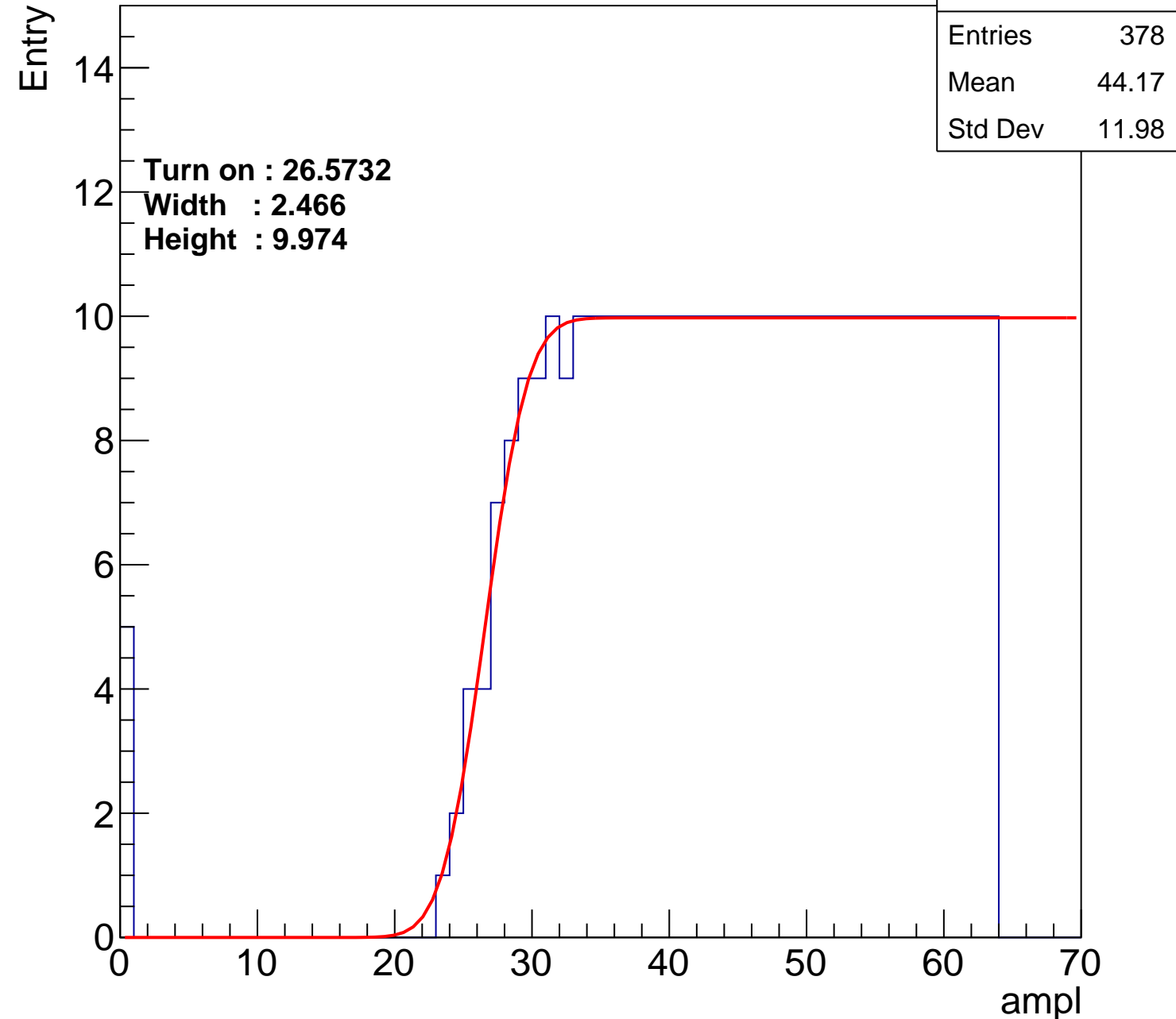
Width : 2.466

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch70

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.42
Std Dev	10.74

Turn on : 28.5896

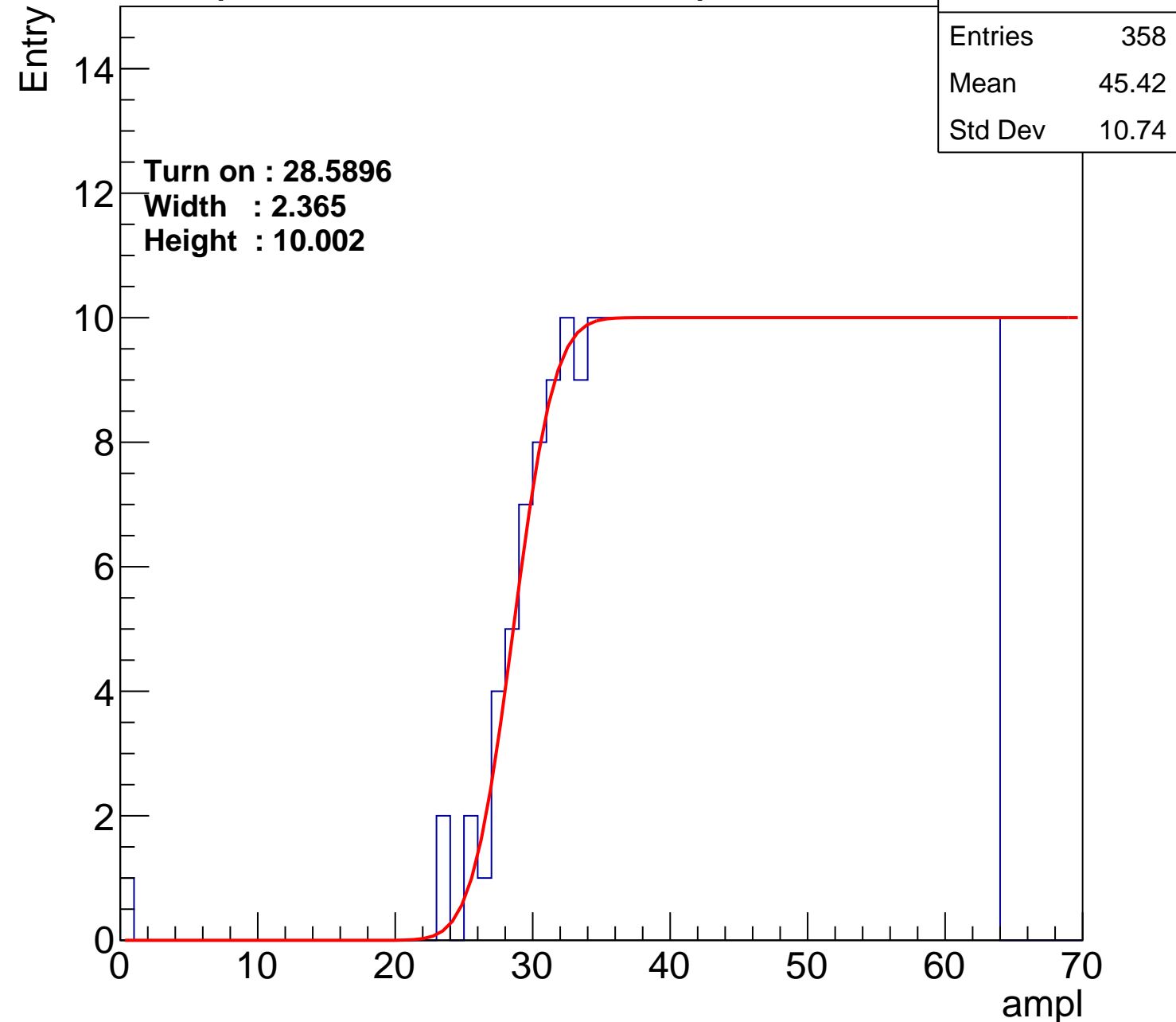
Width : 2.365

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch71

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.25
Std Dev	11.21

Turn on : 28.8004

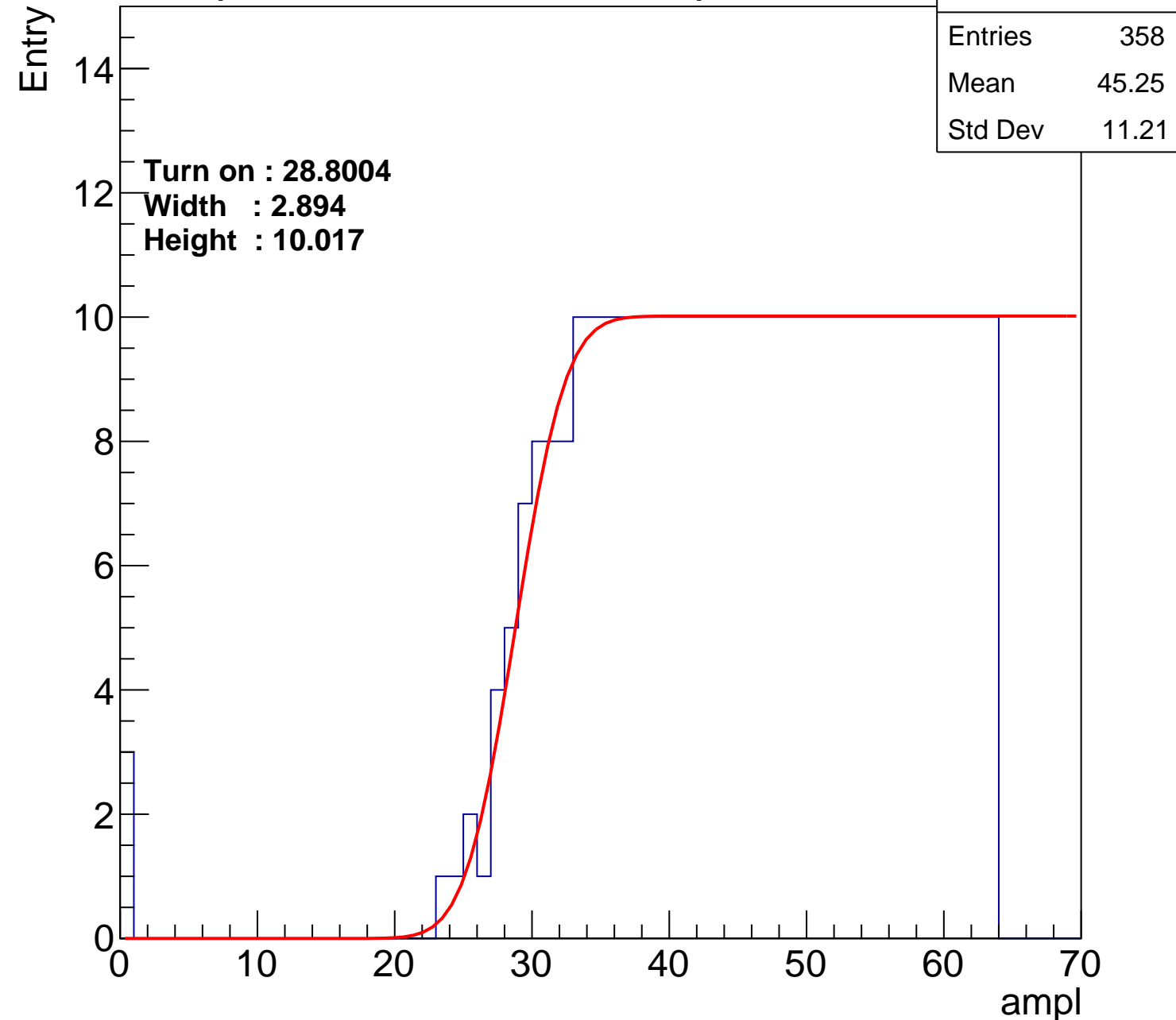
Width : 2.894

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch72

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.69
Std Dev	11.3

Turn on : 27.0031

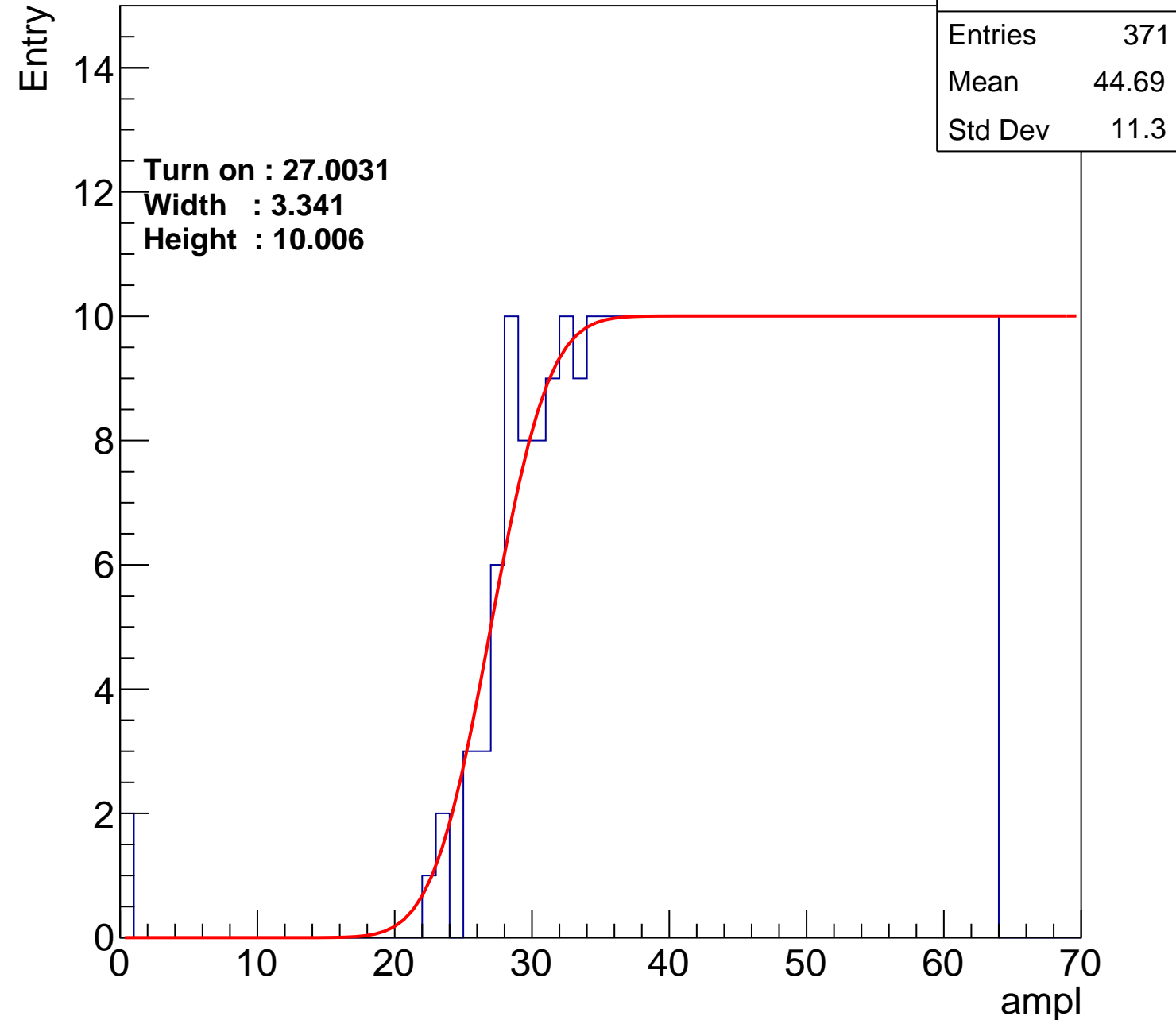
Width : 3.341

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch73

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.09
Std Dev	11.63

Turn on : 26.3452

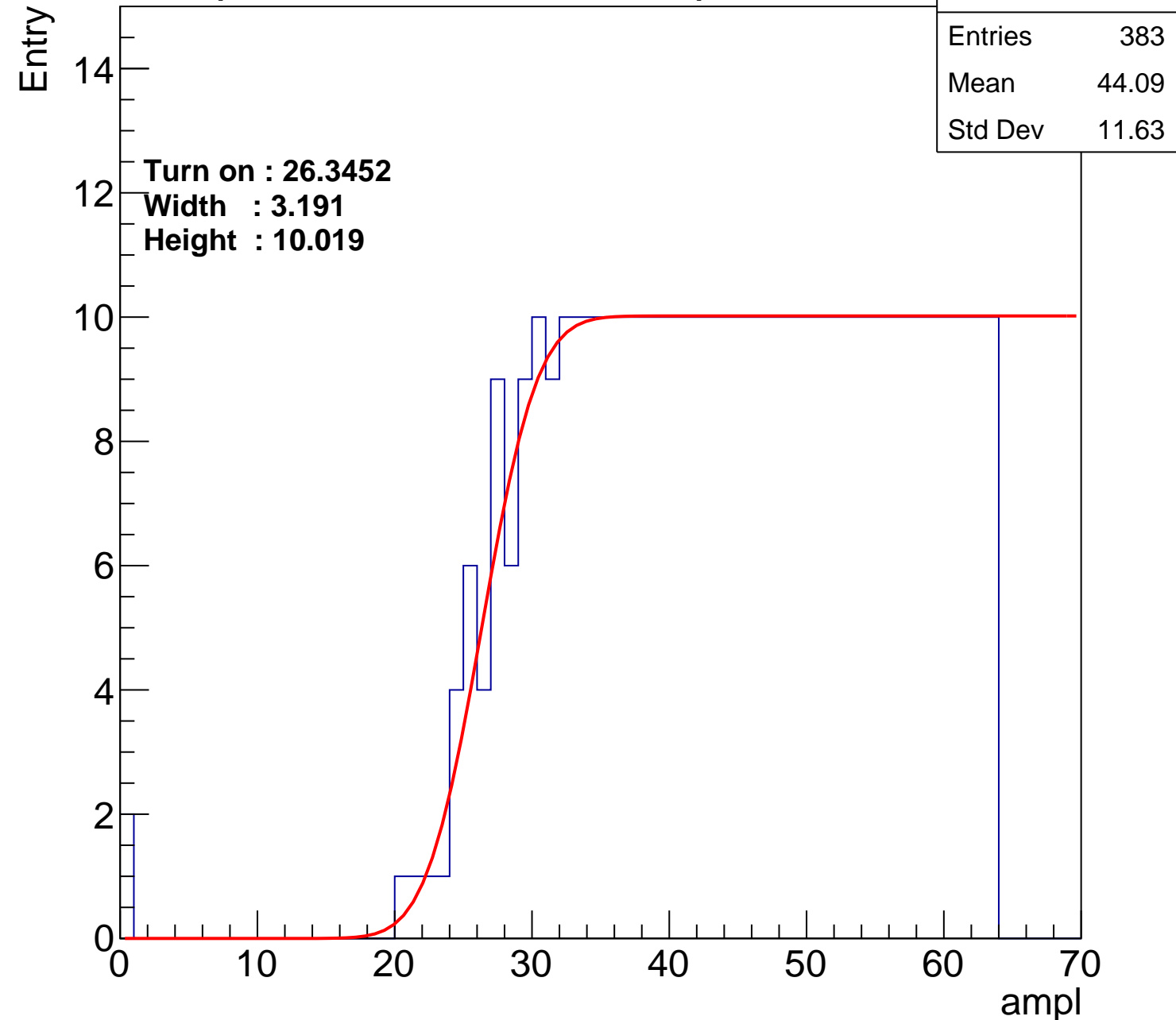
Width : 3.191

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch74

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.59
Std Dev	11.36

Turn on : 27.2536

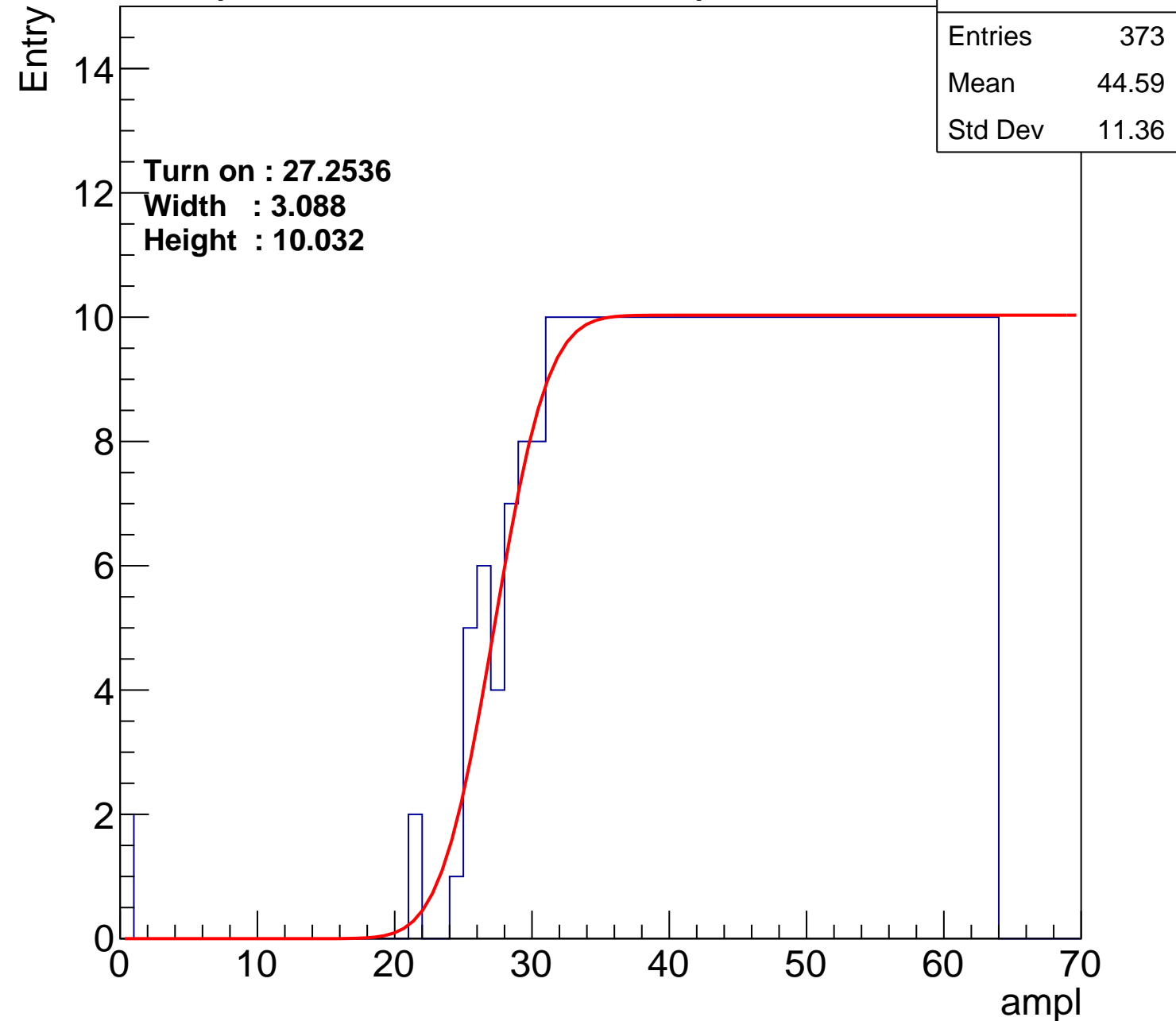
Width : 3.088

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch75

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.43
Std Dev	11.27

Turn on : 26.4217

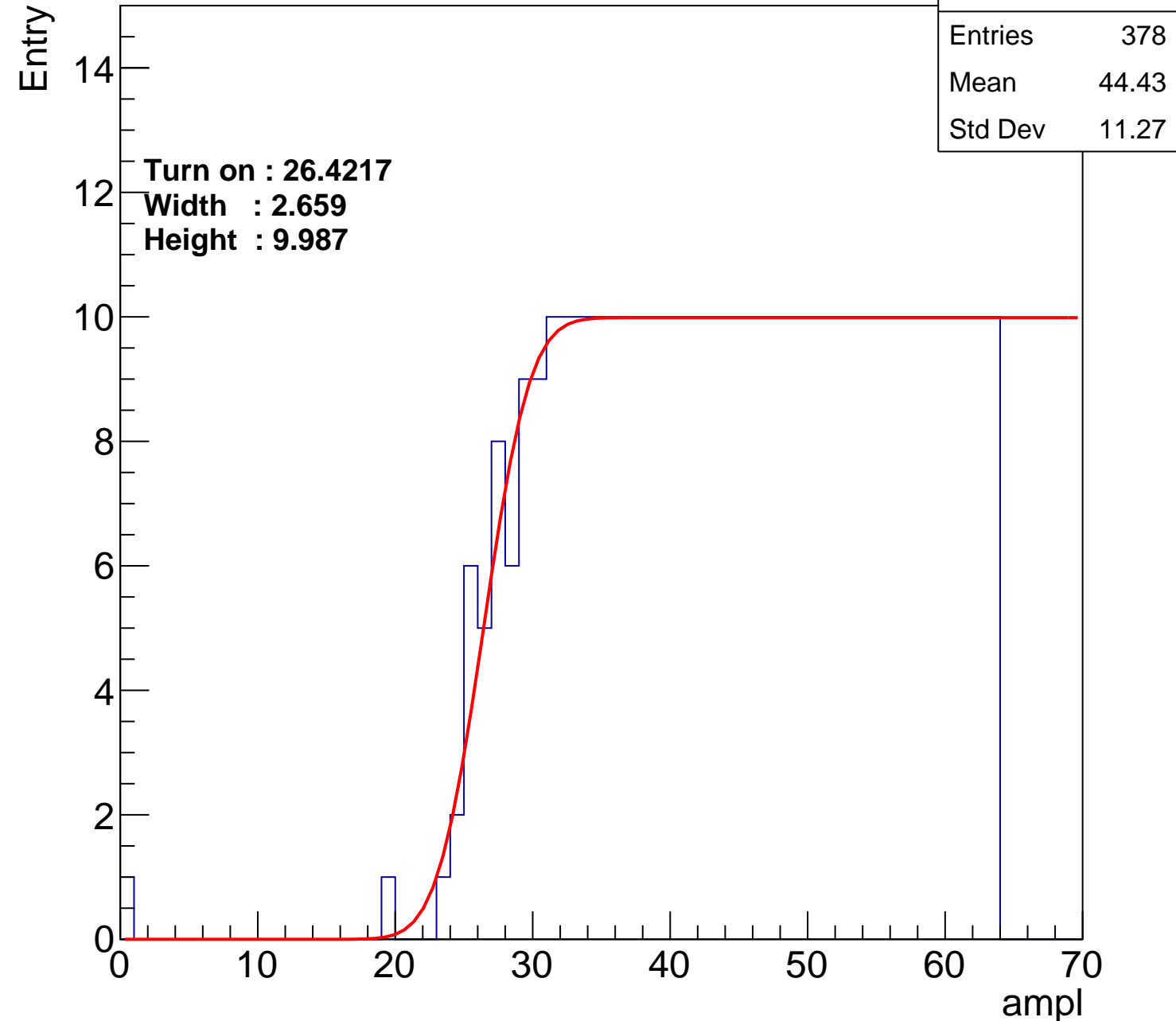
Width : 2.659

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch76

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	43.92
Std Dev	11.99

Turn on : 26.4763

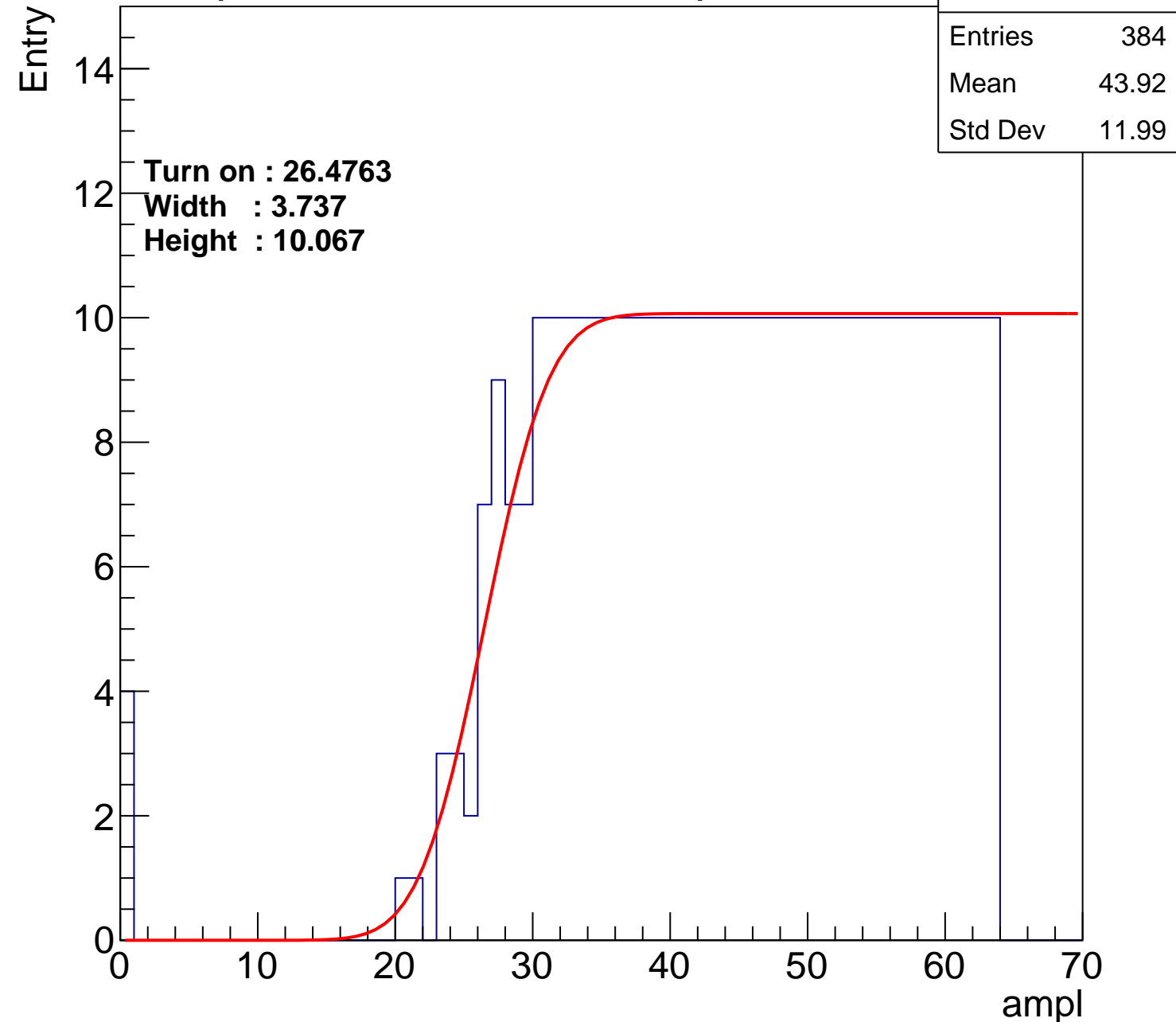
Width : 3.737

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch77

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.03
Std Dev	11.08

Turn on : 27.9164

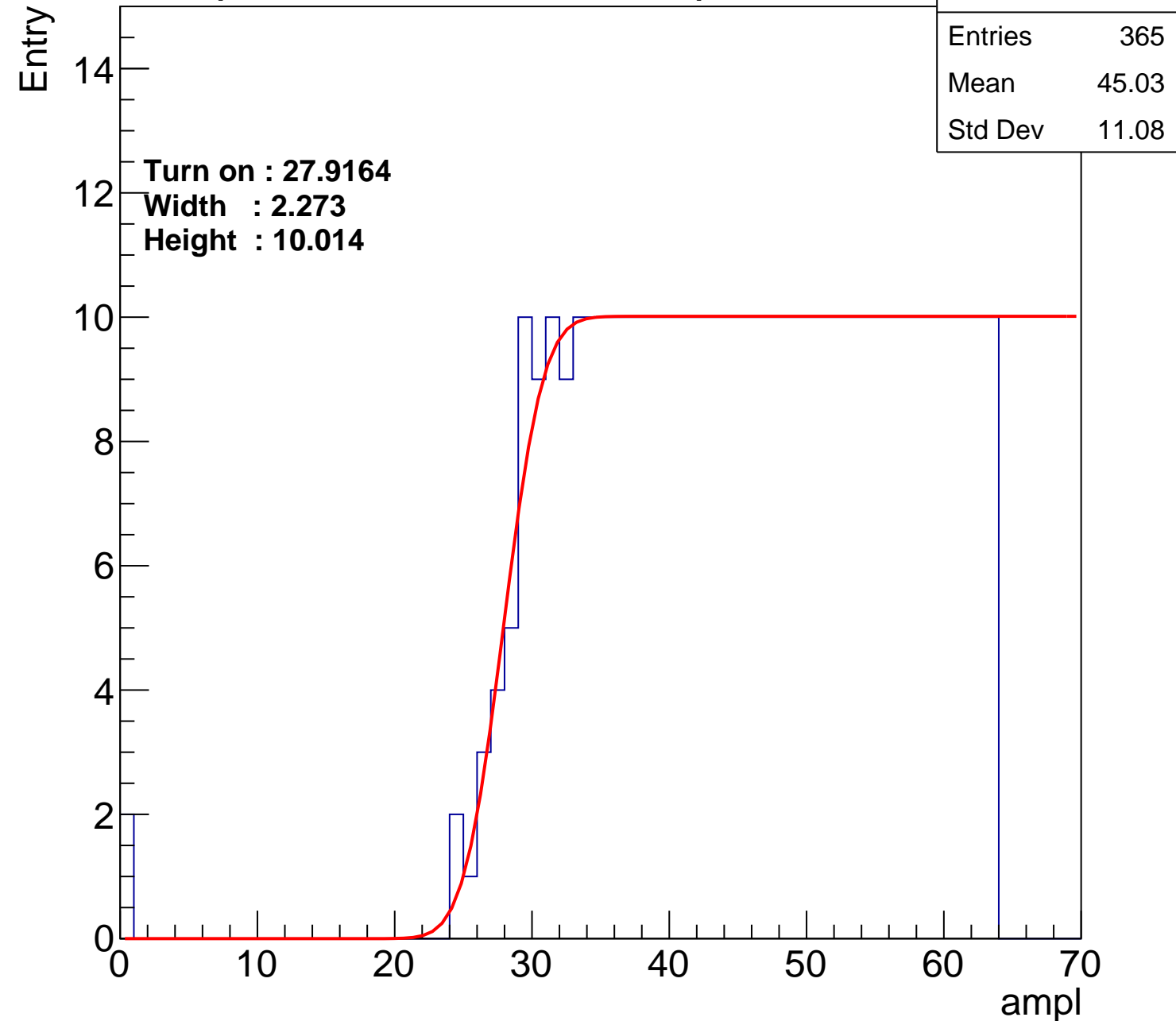
Width : 2.273

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch78

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	44.71
Std Dev	11.98

Turn on : 28.7993

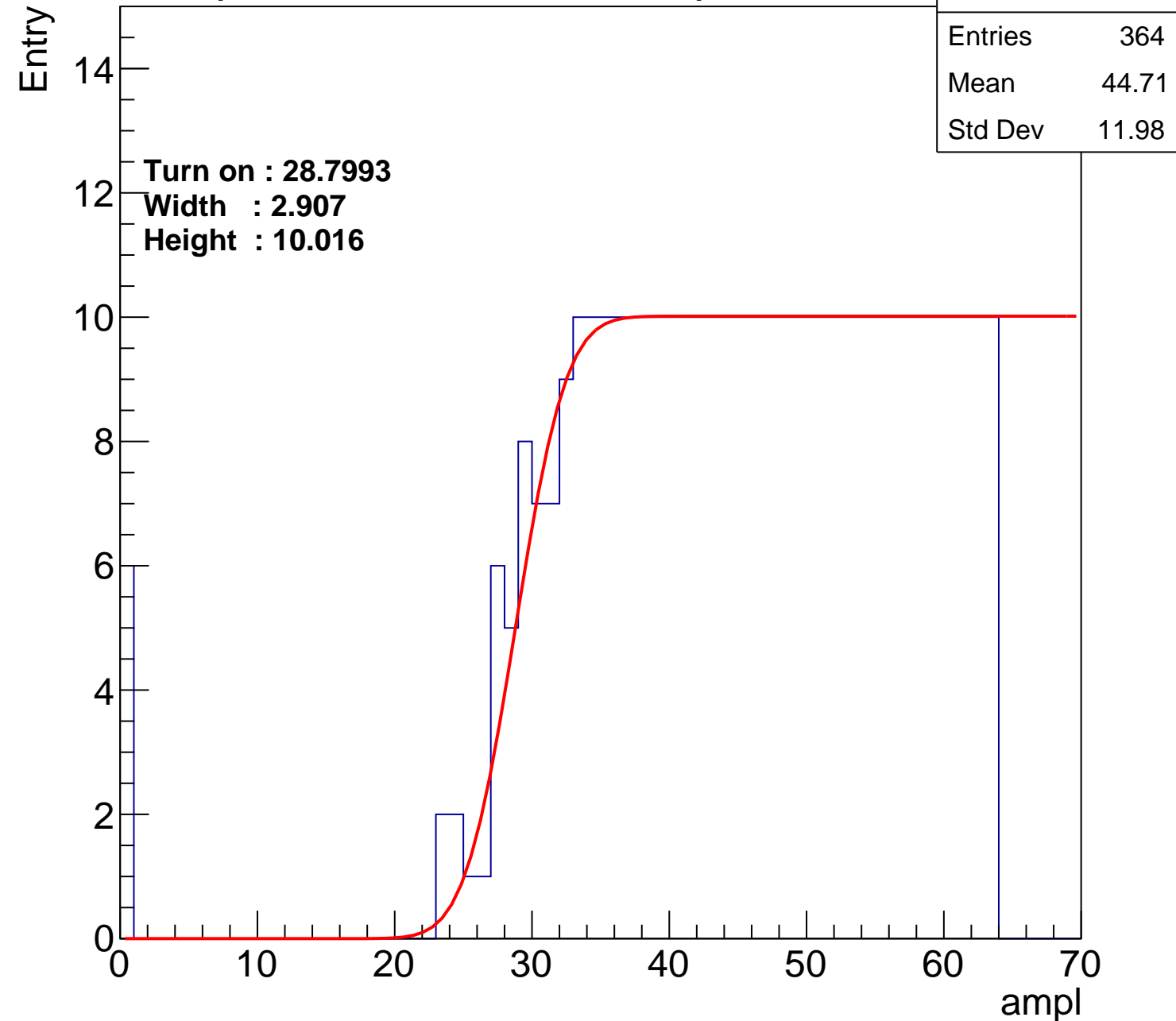
Width : 2.907

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch79

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.43
Std Dev	12.06

Turn on : 24.4777

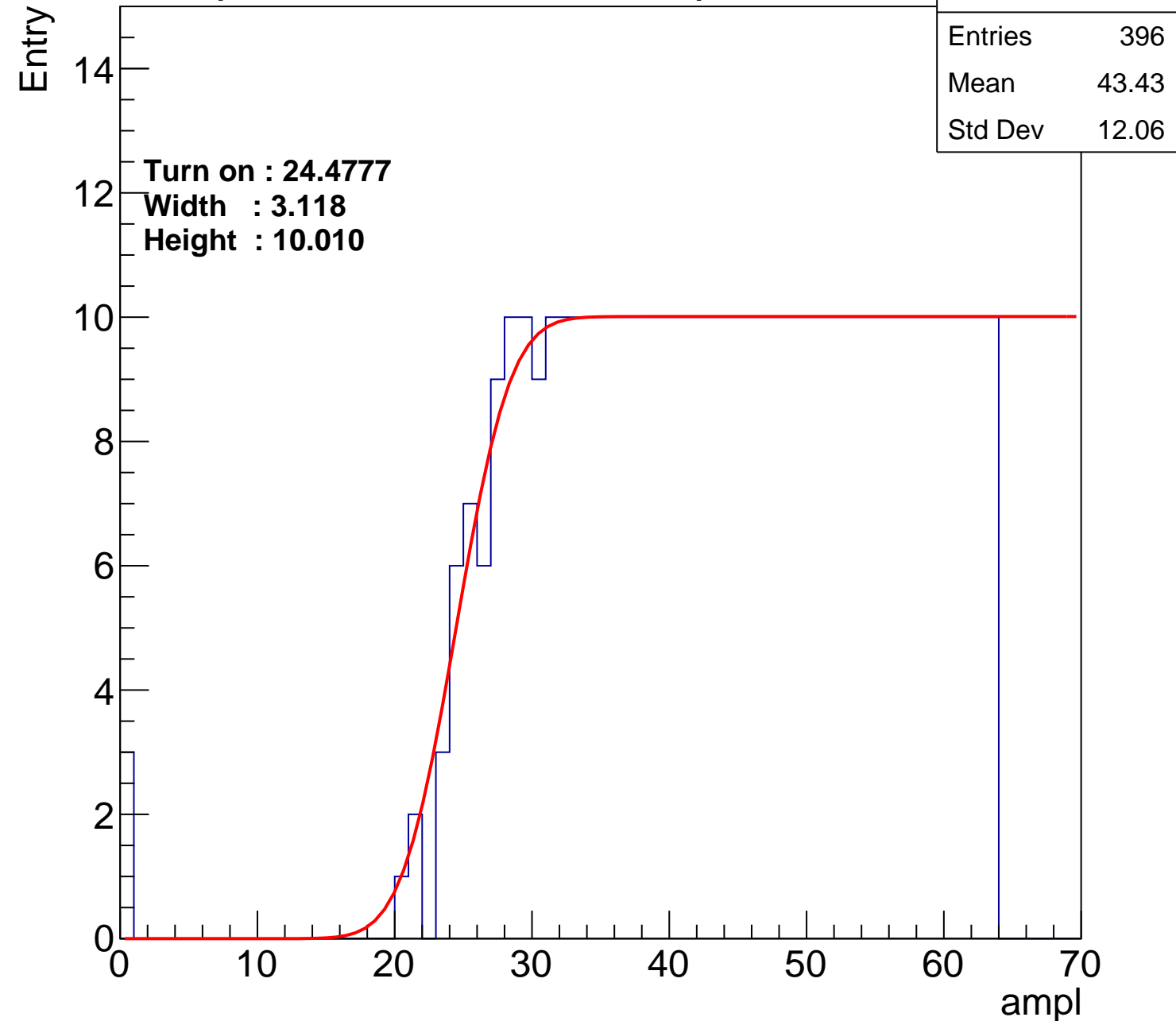
Width : 3.118

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch80

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.36
Std Dev	12.61

Turn on : 25.4211

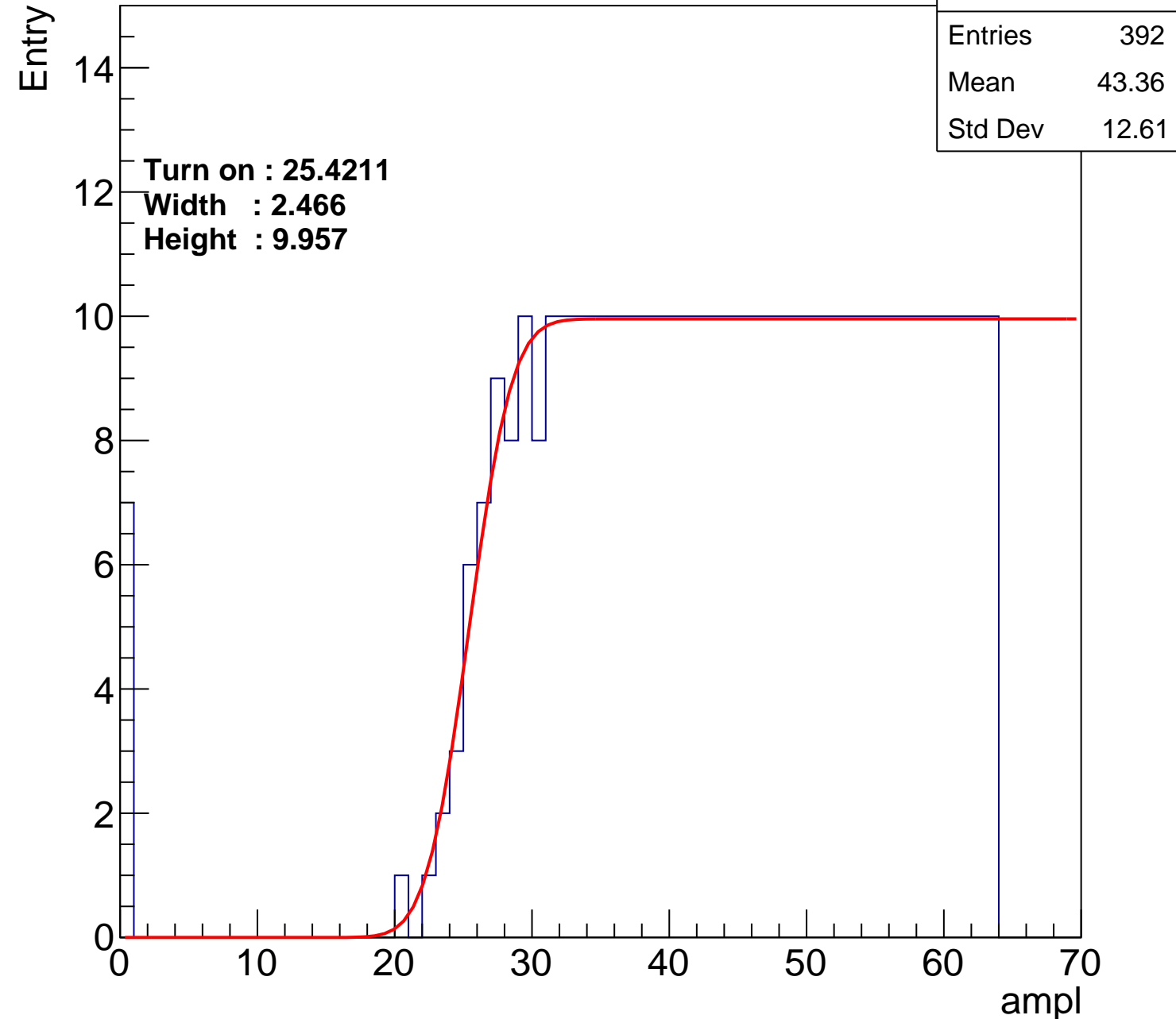
Width : 2.466

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch81

calib_packv5_042523_0143.root, FC#4, port A2

Entries	397
Mean	43.45
Std Dev	11.91

Turn on : 24.6973

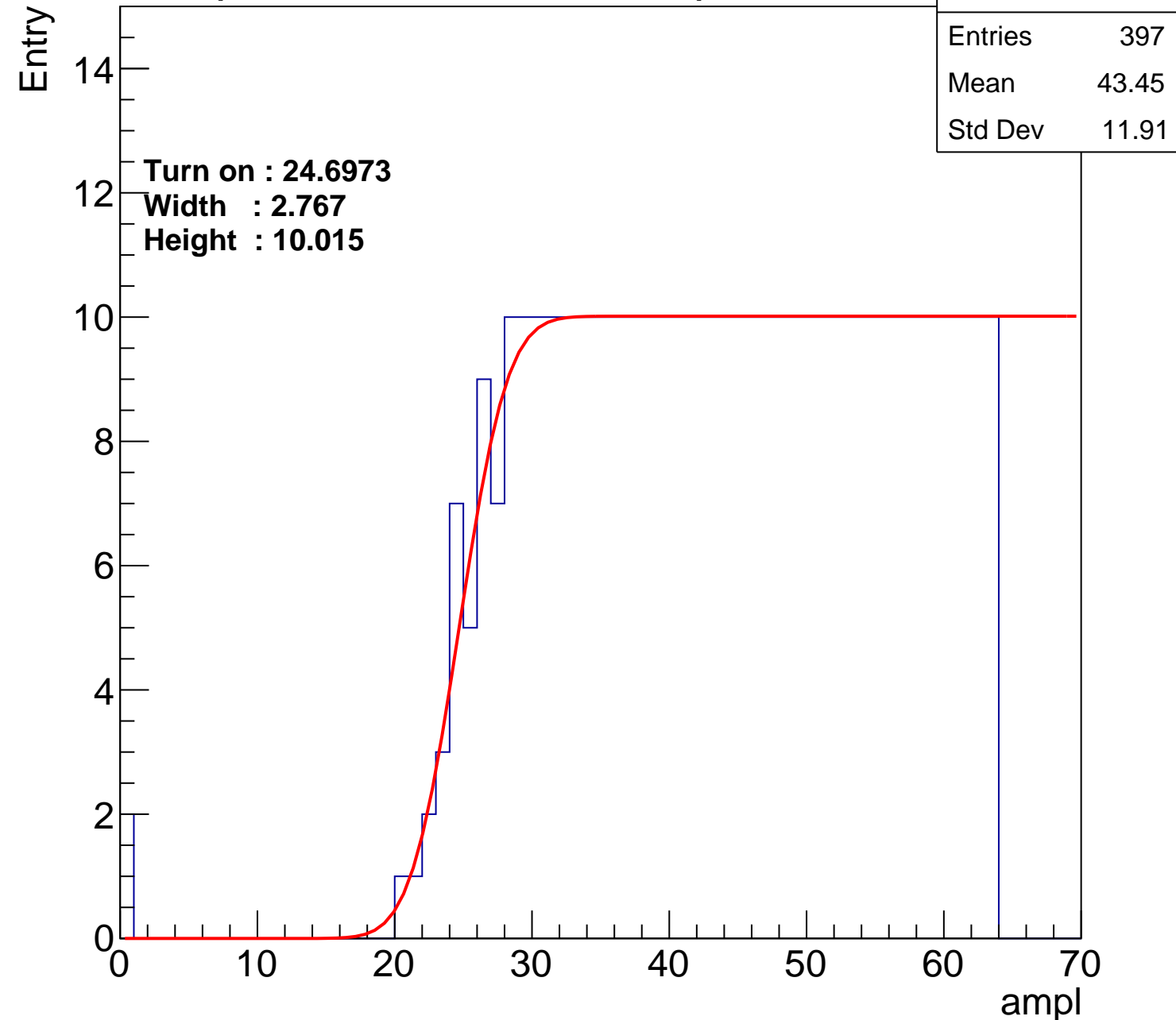
Width : 2.767

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch82

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.94
Std Dev	11.79

Turn on : 25.4951

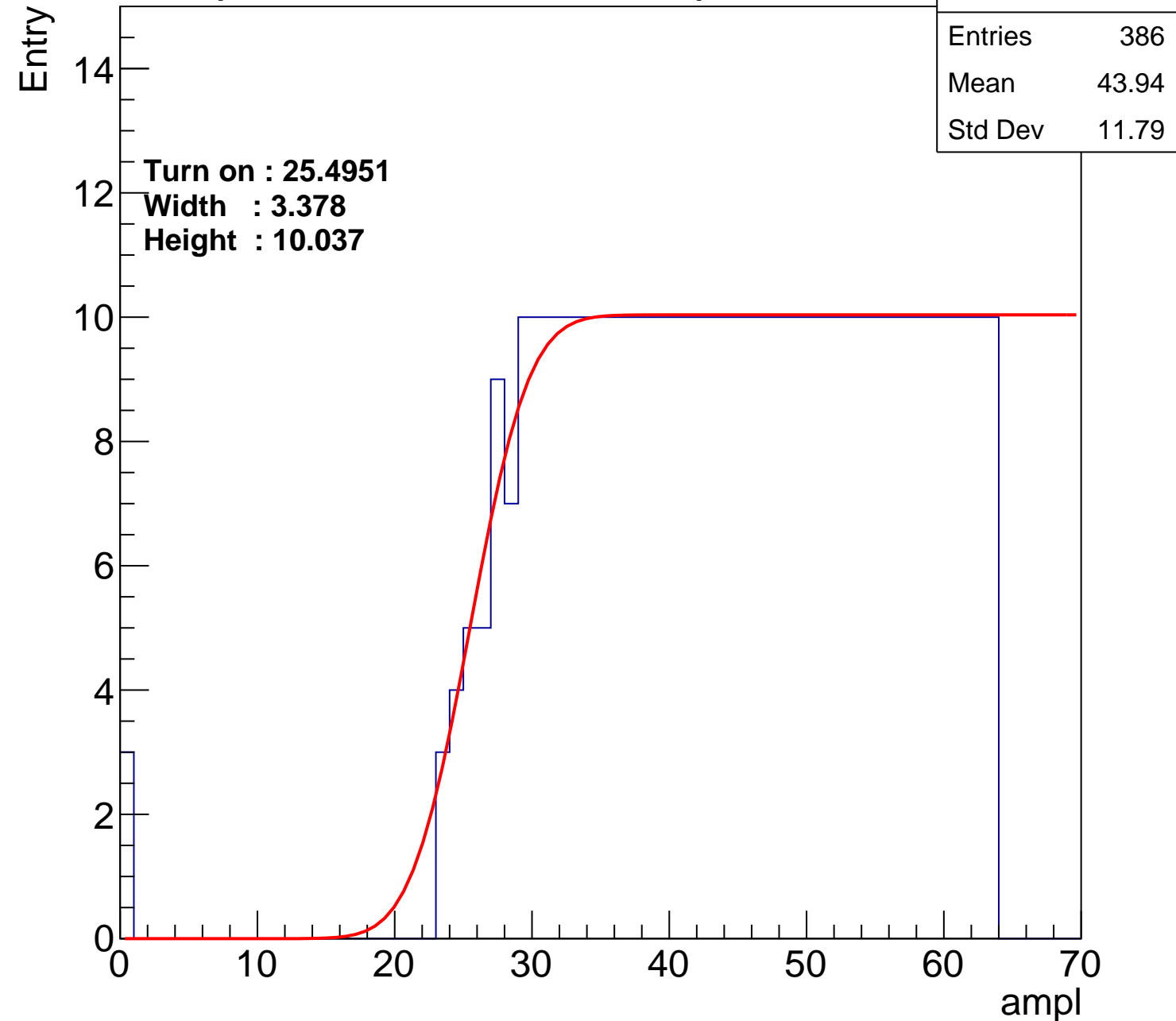
Width : 3.378

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch83

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.92
Std Dev	11.21

Turn on : 27.6904

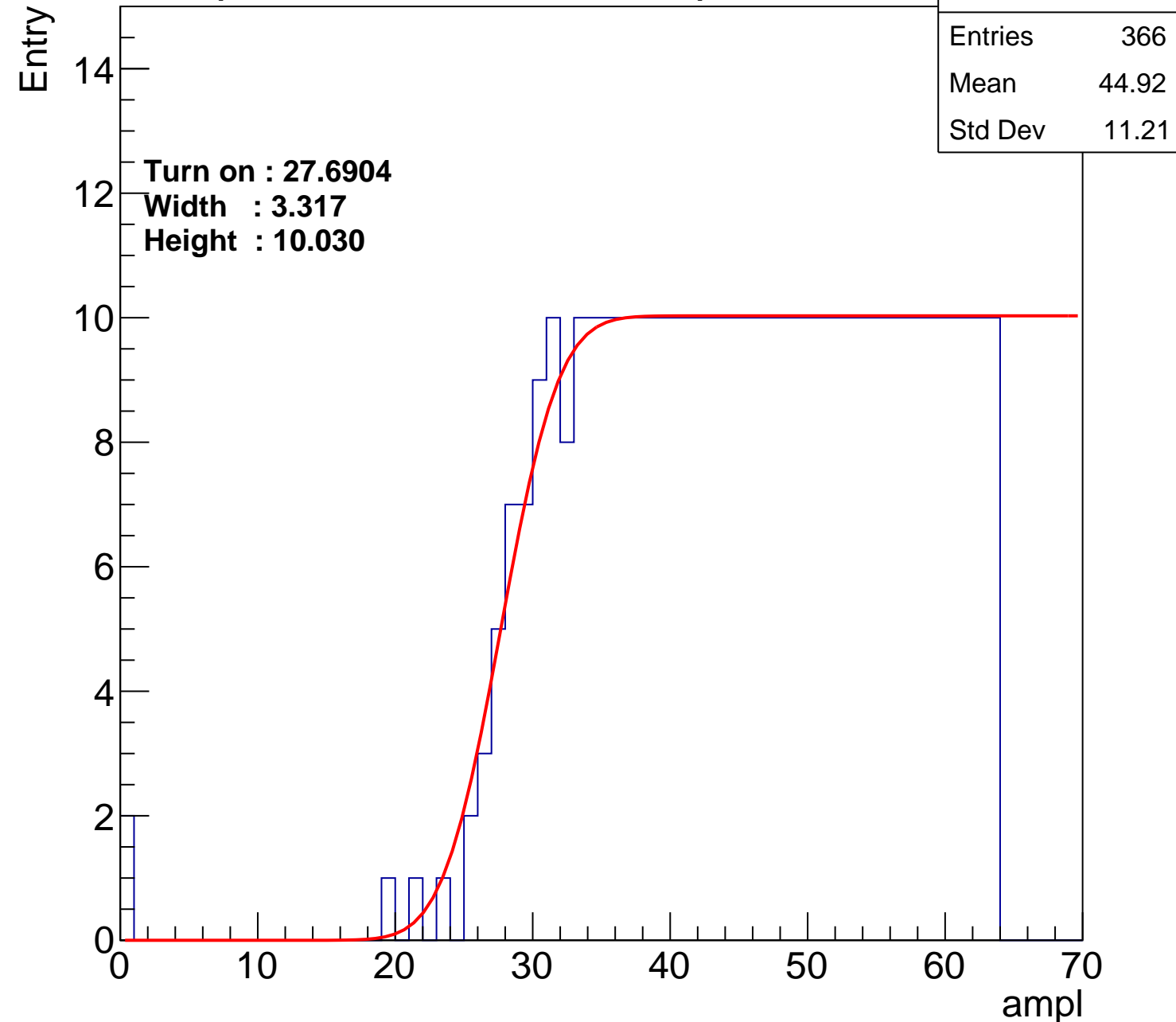
Width : 3.317

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch84

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.78
Std Dev	12

Turn on : 25.7142

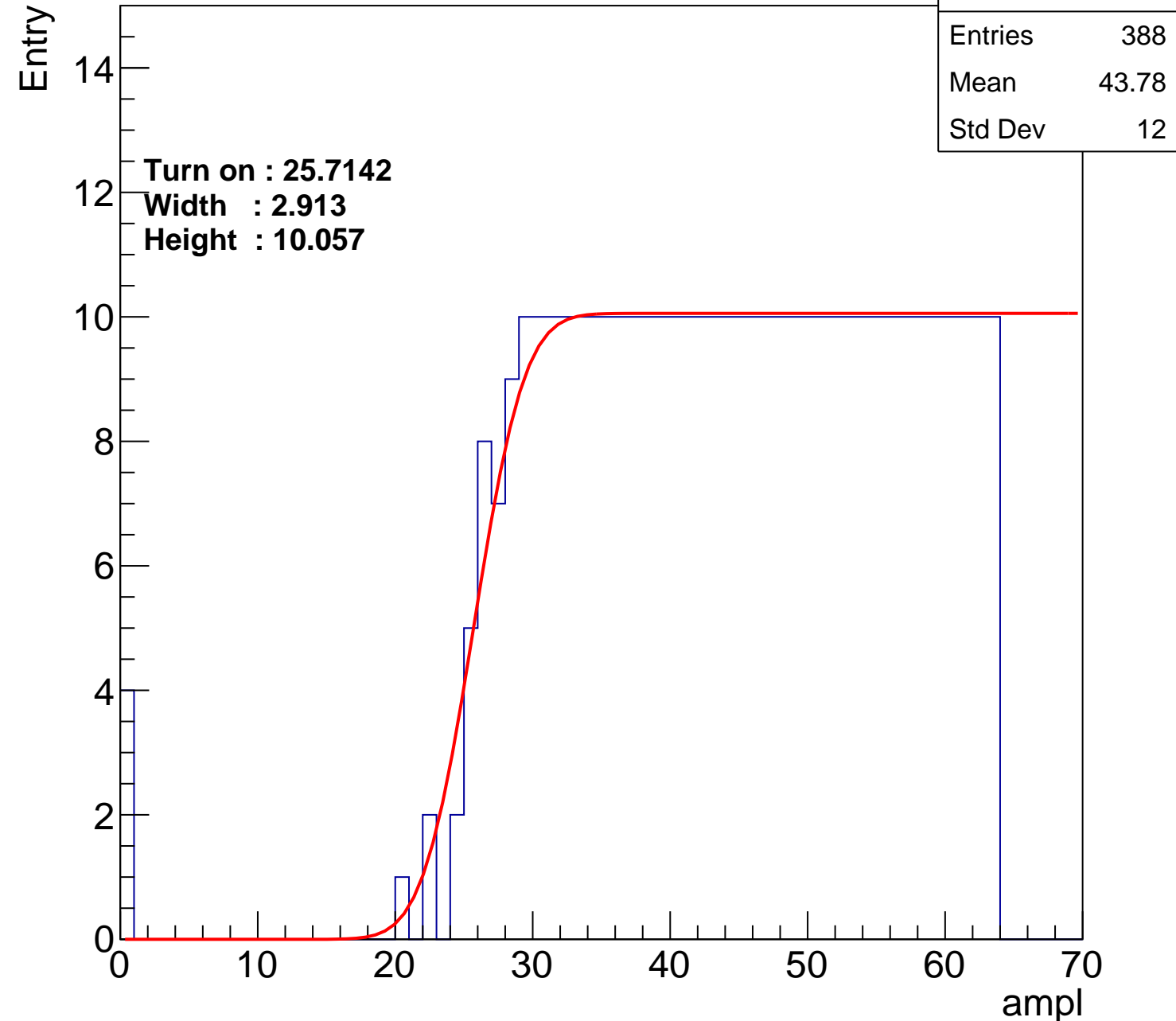
Width : 2.913

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch85

calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 25.5331

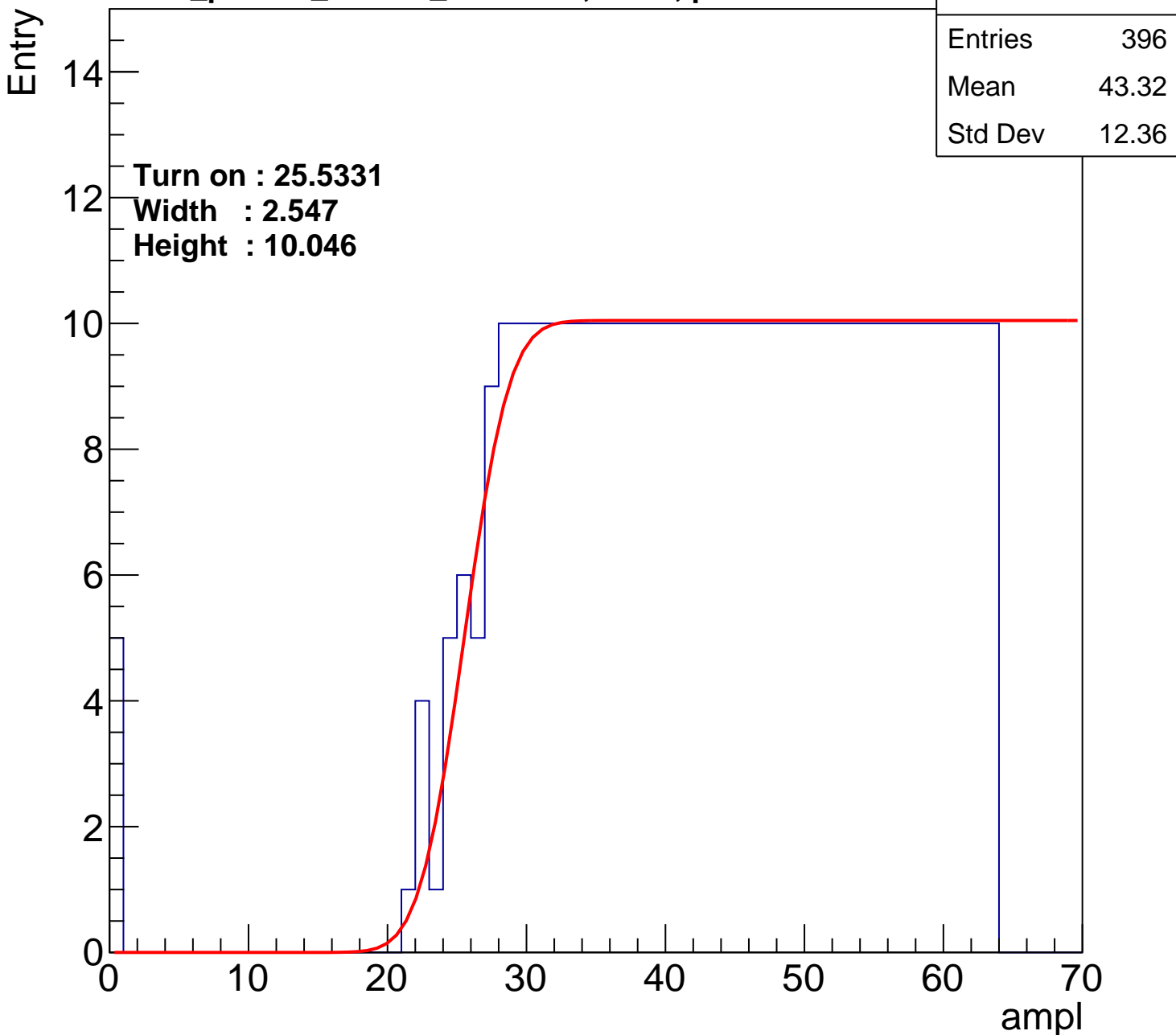
Width : 2.547

Height : 10.046

Entries 396

Mean 43.32

Std Dev 12.36



B1L100S, U6-ch86

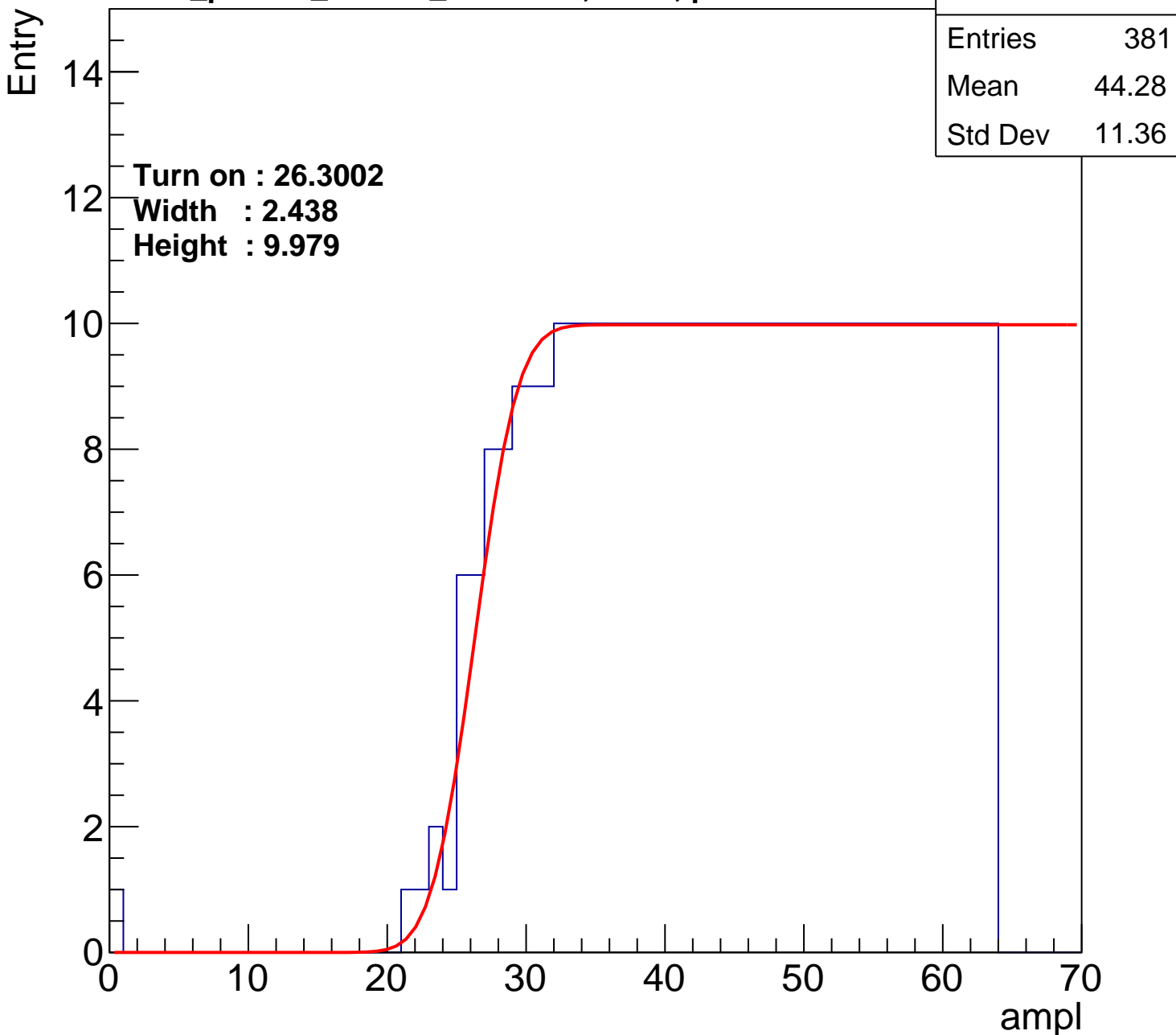
calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 26.3002

Width : 2.438

Height : 9.979

Entries	381
Mean	44.28
Std Dev	11.36



B1L100S, U6-ch87

calib_packv5_042523_0143.root, FC#4, port A2

Entries	372
Mean	44.66
Std Dev	11.32

Turn on : 27.3591

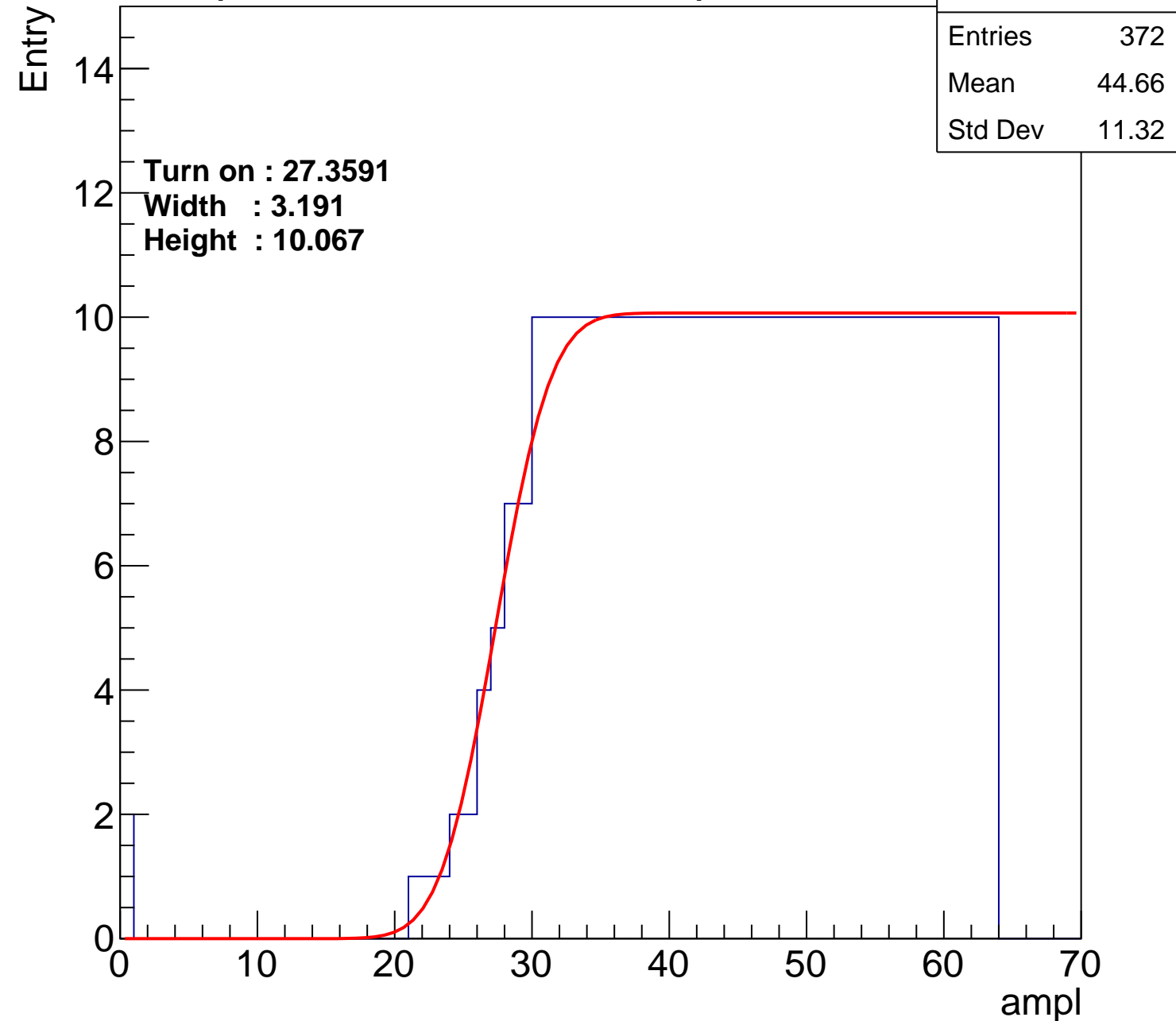
Width : 3.191

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch88

calib_packv5_042523_0143.root, FC#4, port A2

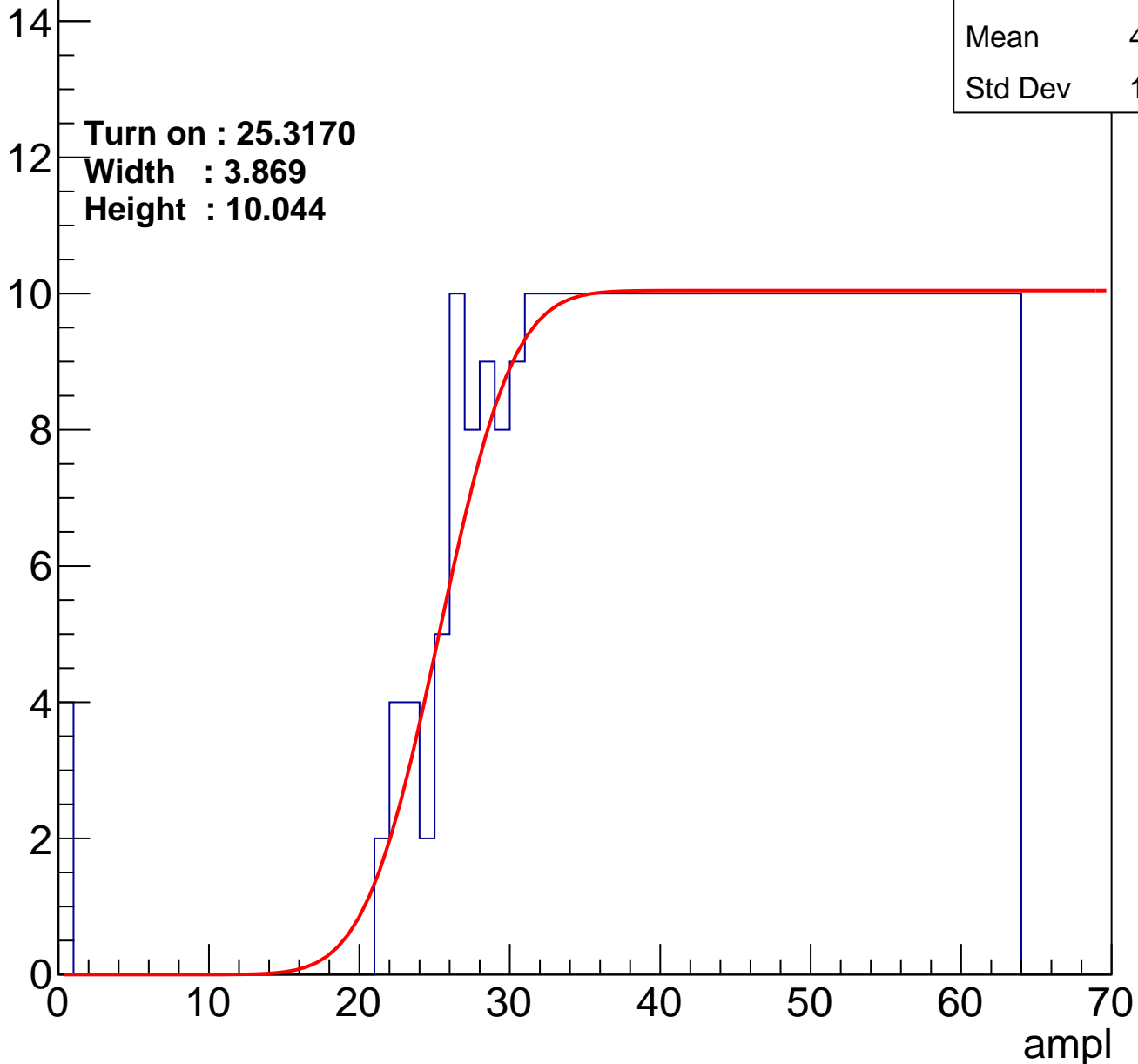
Entries	395
Mean	43.38
Std Dev	12.25

Turn on : 25.3170

Width : 3.869

Height : 10.044

Entry



B1L100S, U6-ch89

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.73
Std Dev	11.25

Turn on : 27.1687

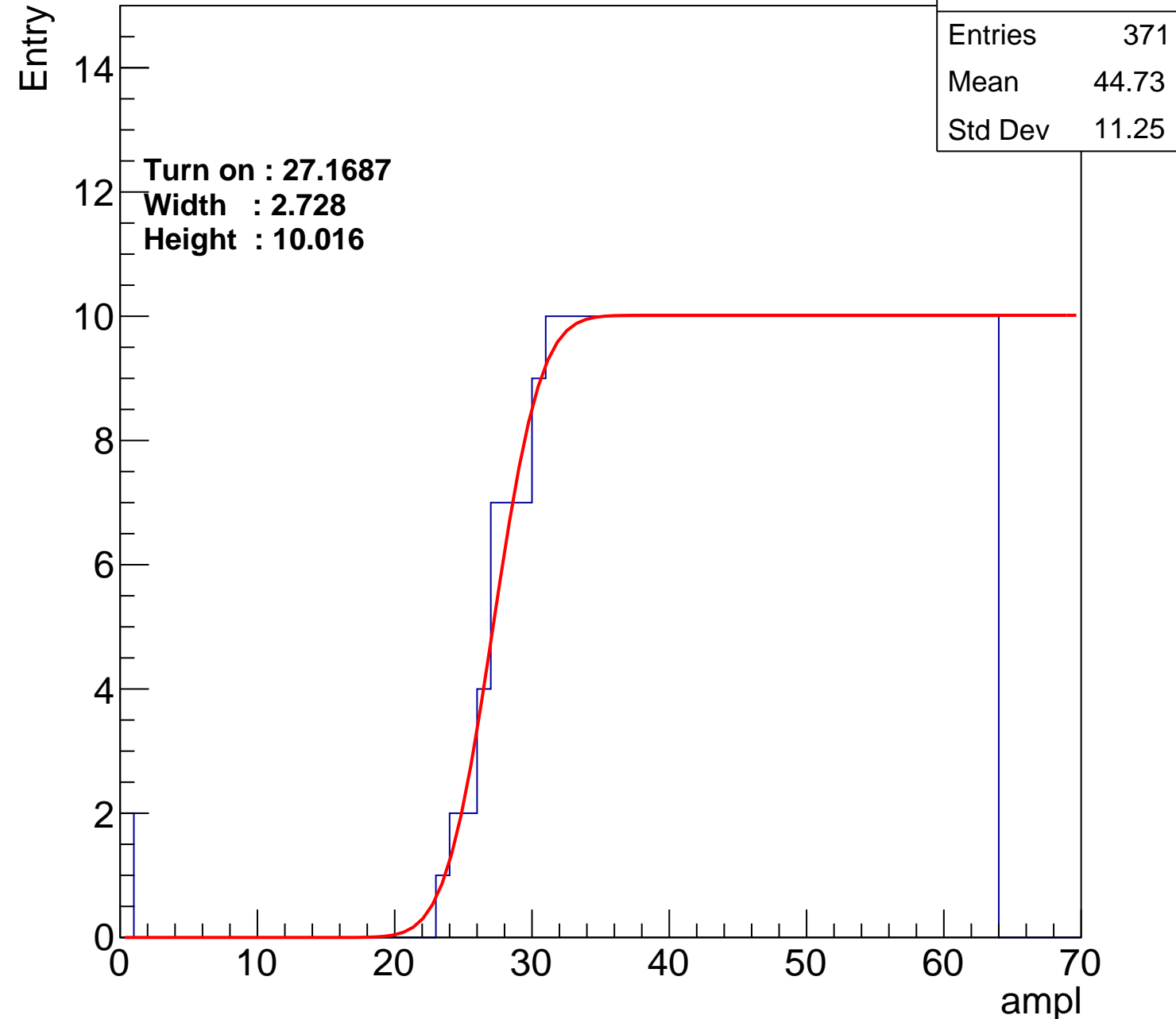
Width : 2.728

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch90

calib_packv5_042523_0143.root, FC#4, port A2

Entries	392
Mean	43.67
Std Dev	11.89

Turn on : 25.2708

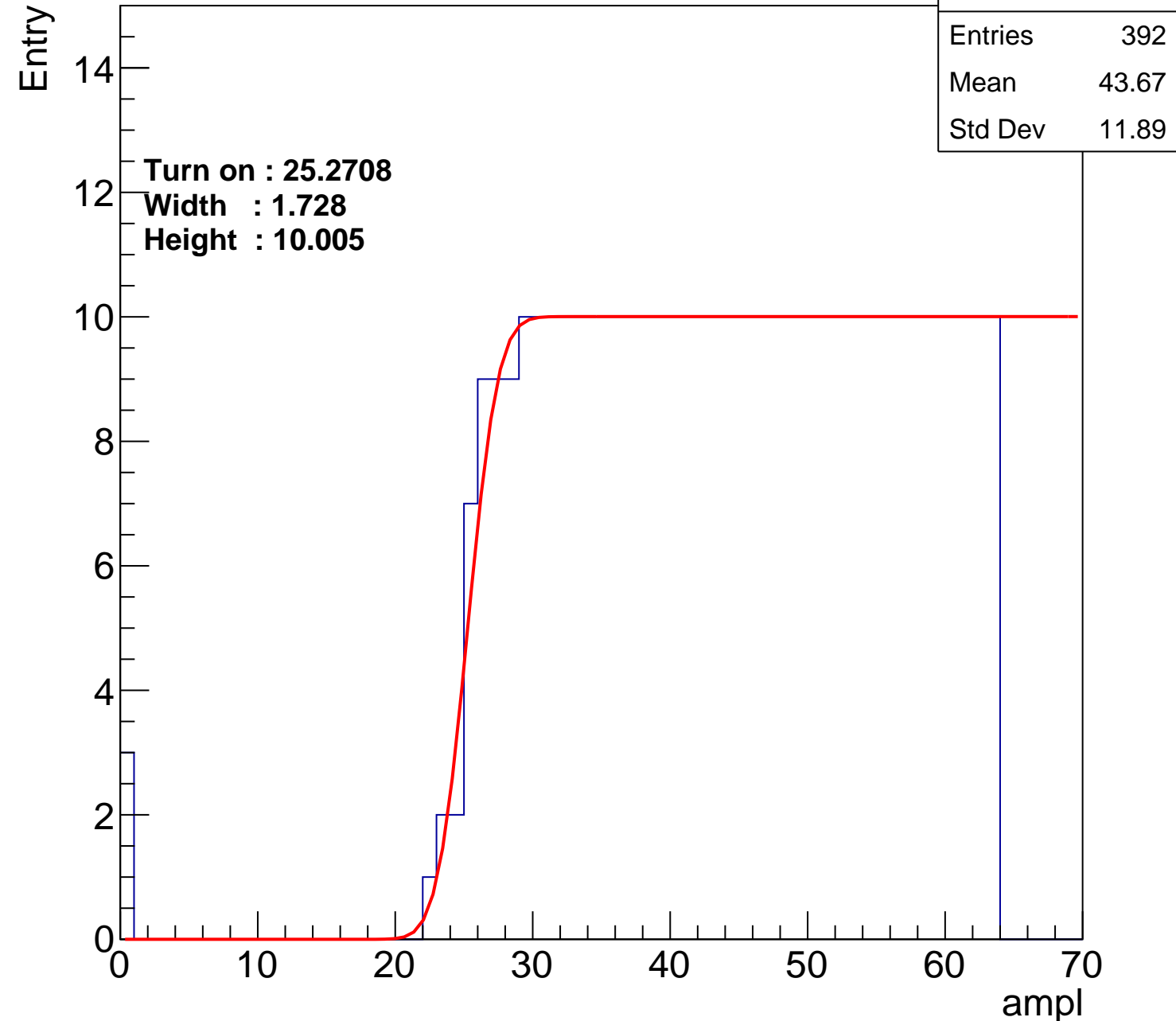
Width : 1.728

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch91

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.85
Std Dev	11.22

Turn on : 27.9700

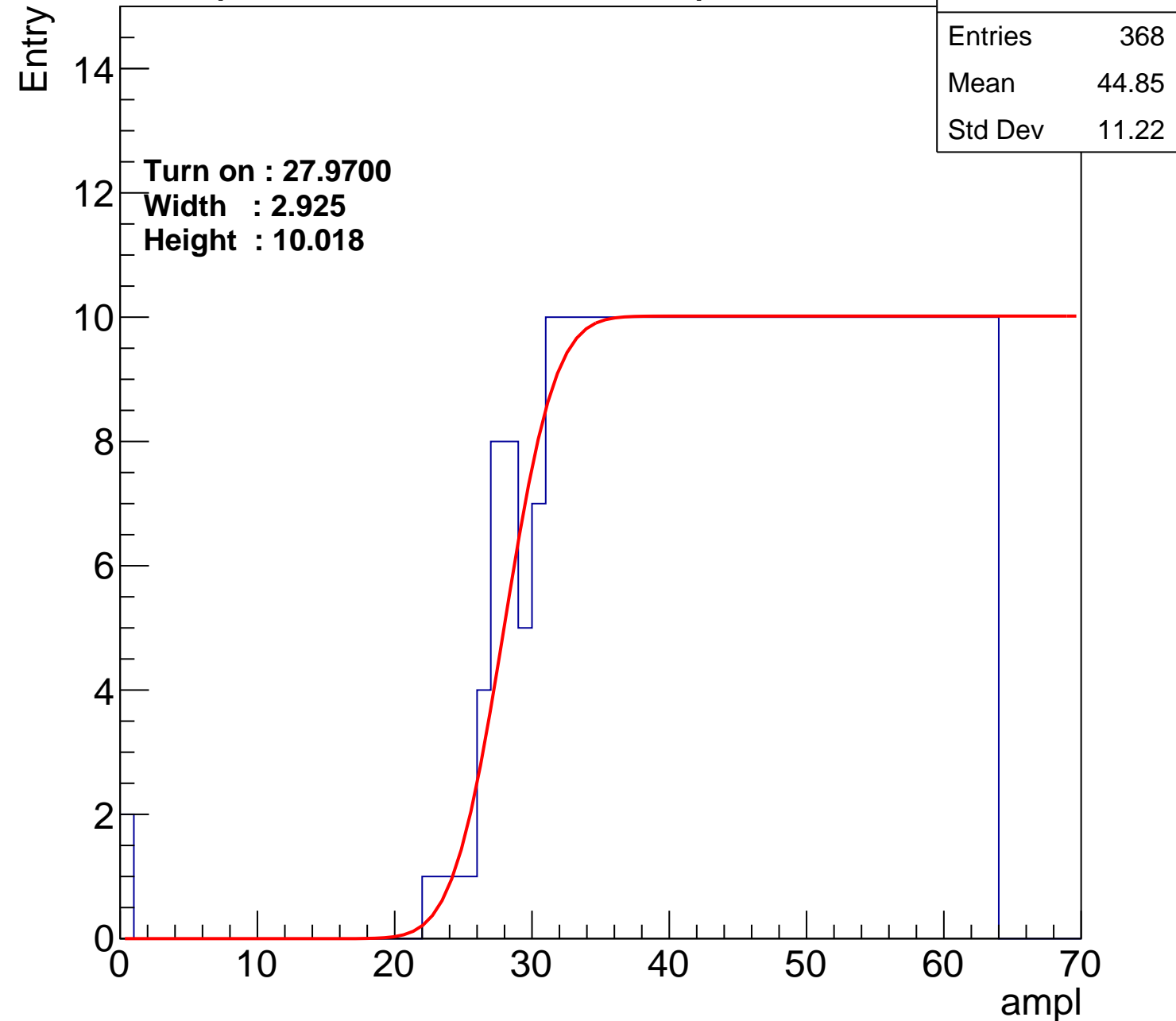
Width : 2.925

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch92

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 25.6415

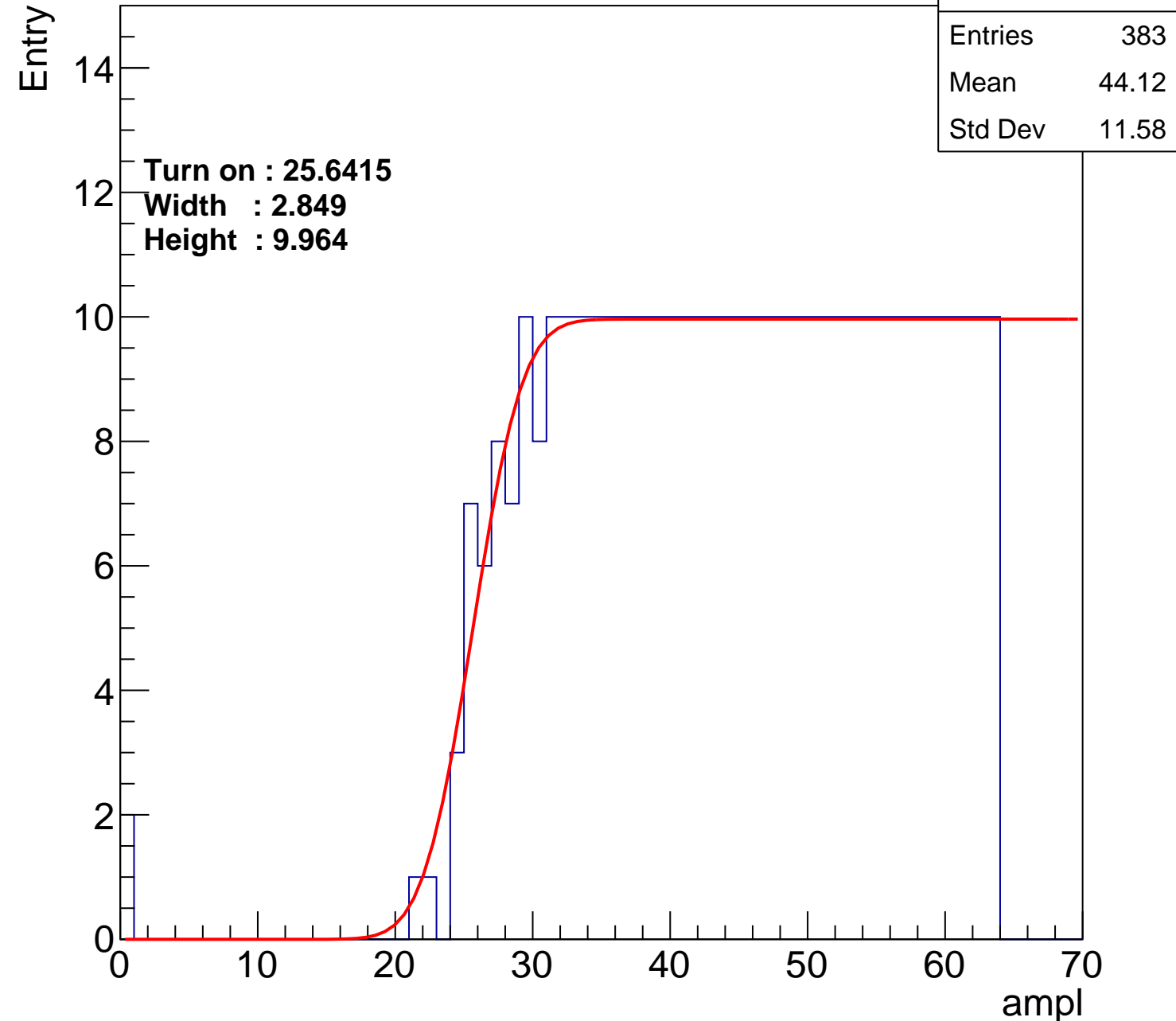
Width : 2.849

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch93

calib_packv5_042523_0143.root, FC#4, port A2

Entries	399
Mean	43.28
Std Dev	12.15

Turn on : 24.8825

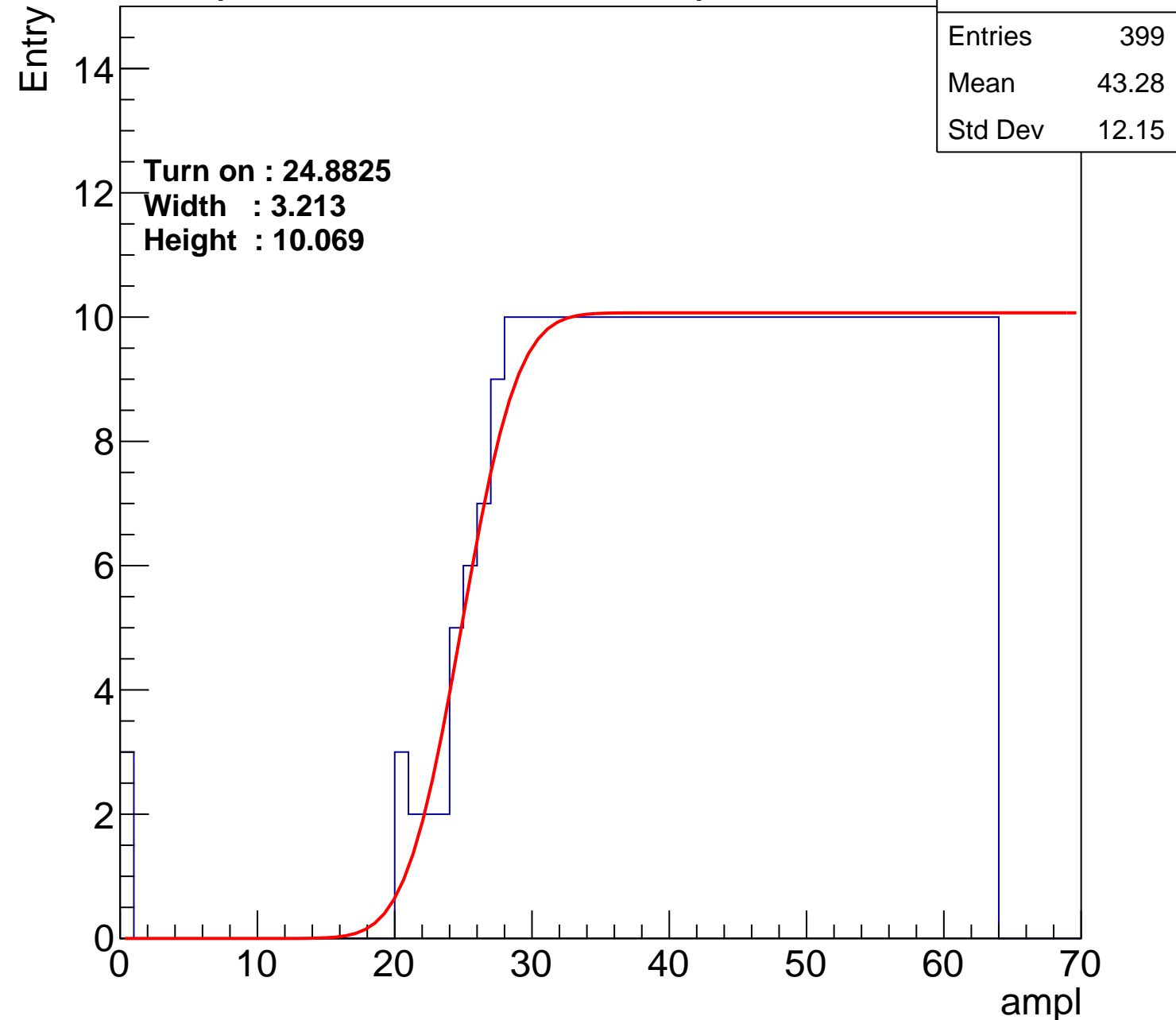
Width : 3.213

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch94

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
---------	-----

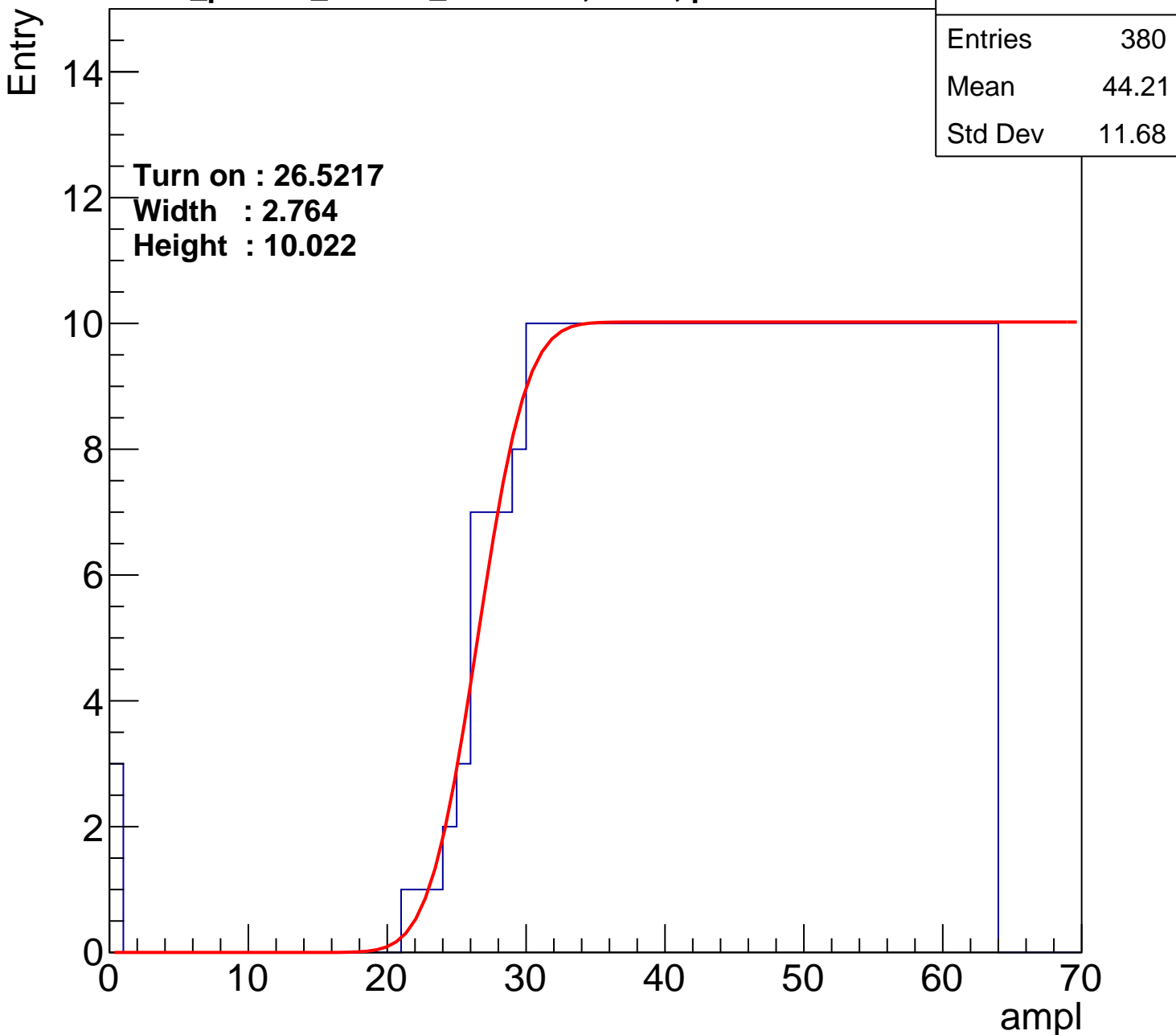
Mean	44.21
------	-------

Std Dev	11.68
---------	-------

Turn on : 26.5217

Width : 2.764

Height : 10.022



B1L100S, U6-ch95

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.6512

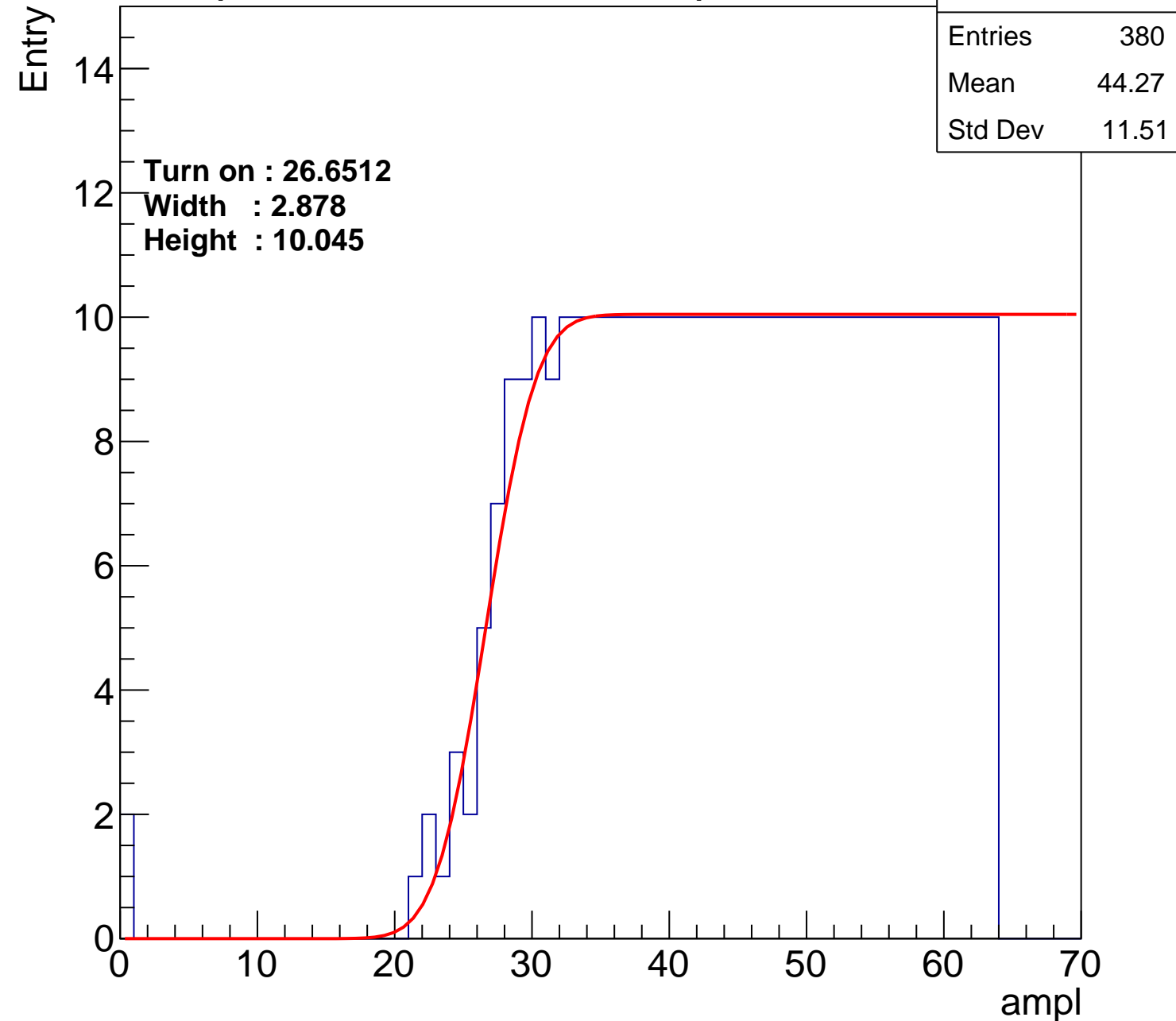
Width : 2.878

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch96

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.74
Std Dev	11.27

Turn on : 27.1323

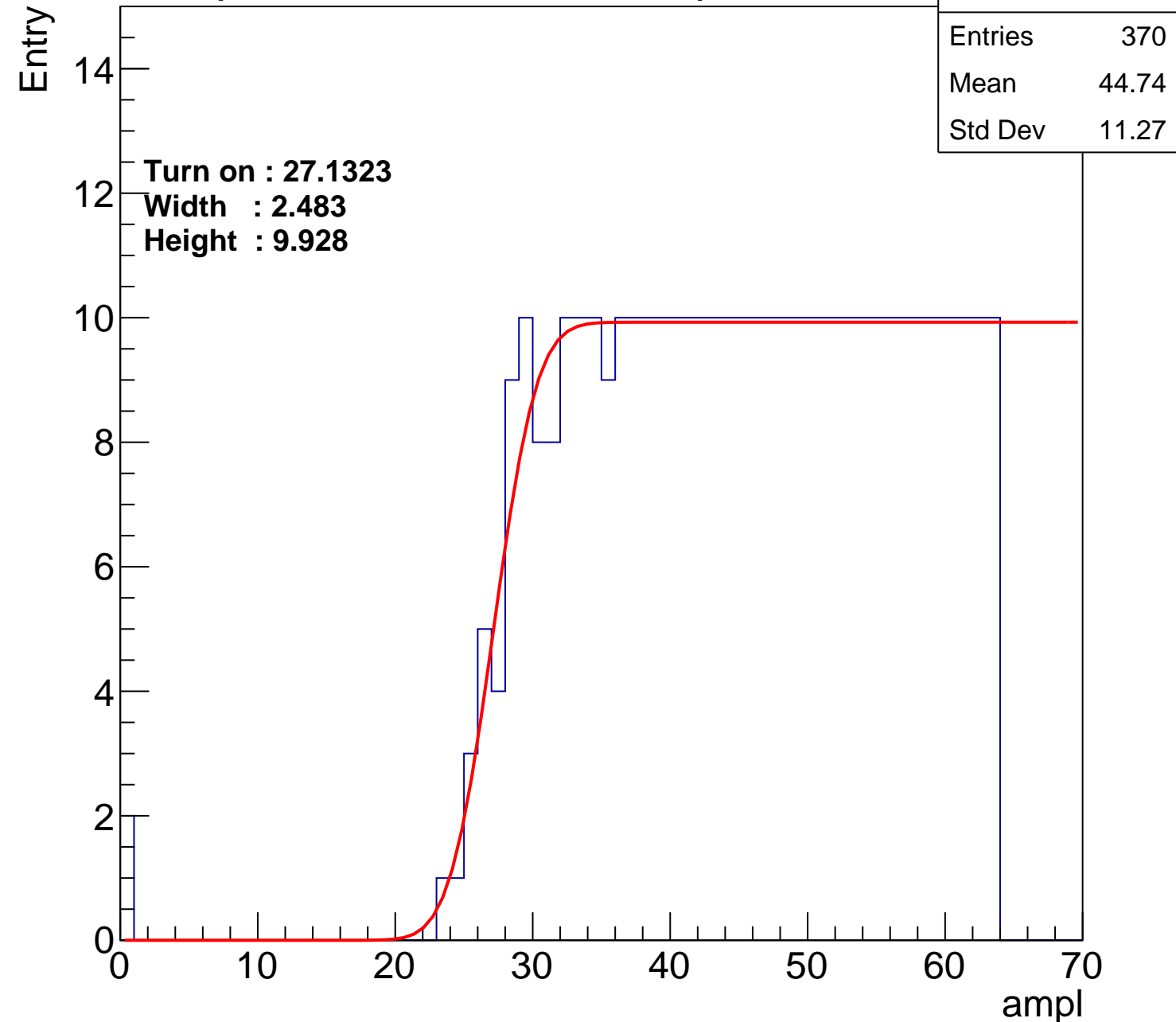
Width : 2.483

Height : 9.928

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch97

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.2
Std Dev	11.99

Turn on : 27.0729

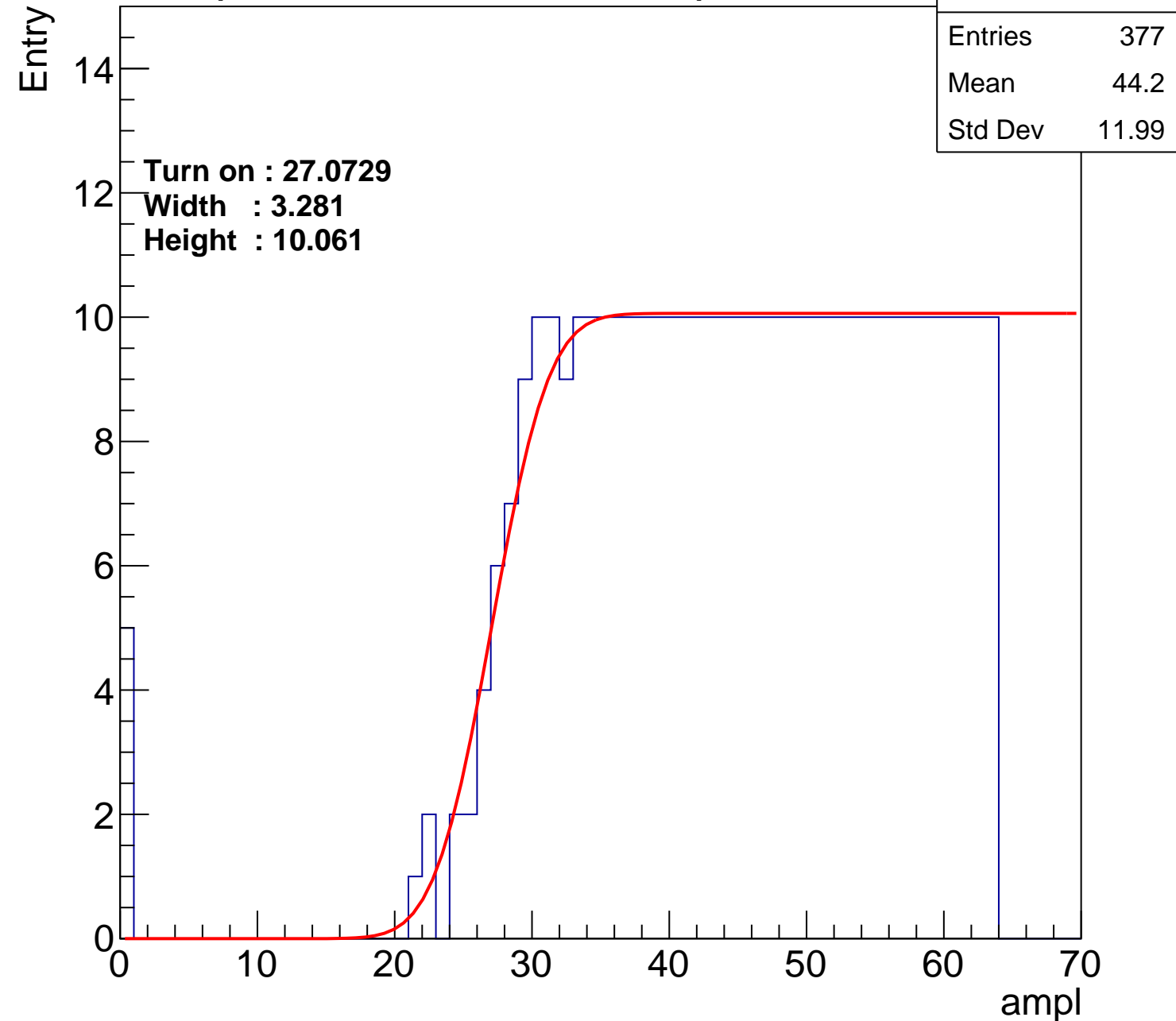
Width : 3.281

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch98

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.12
Std Dev	11.11

Turn on : 28.4254

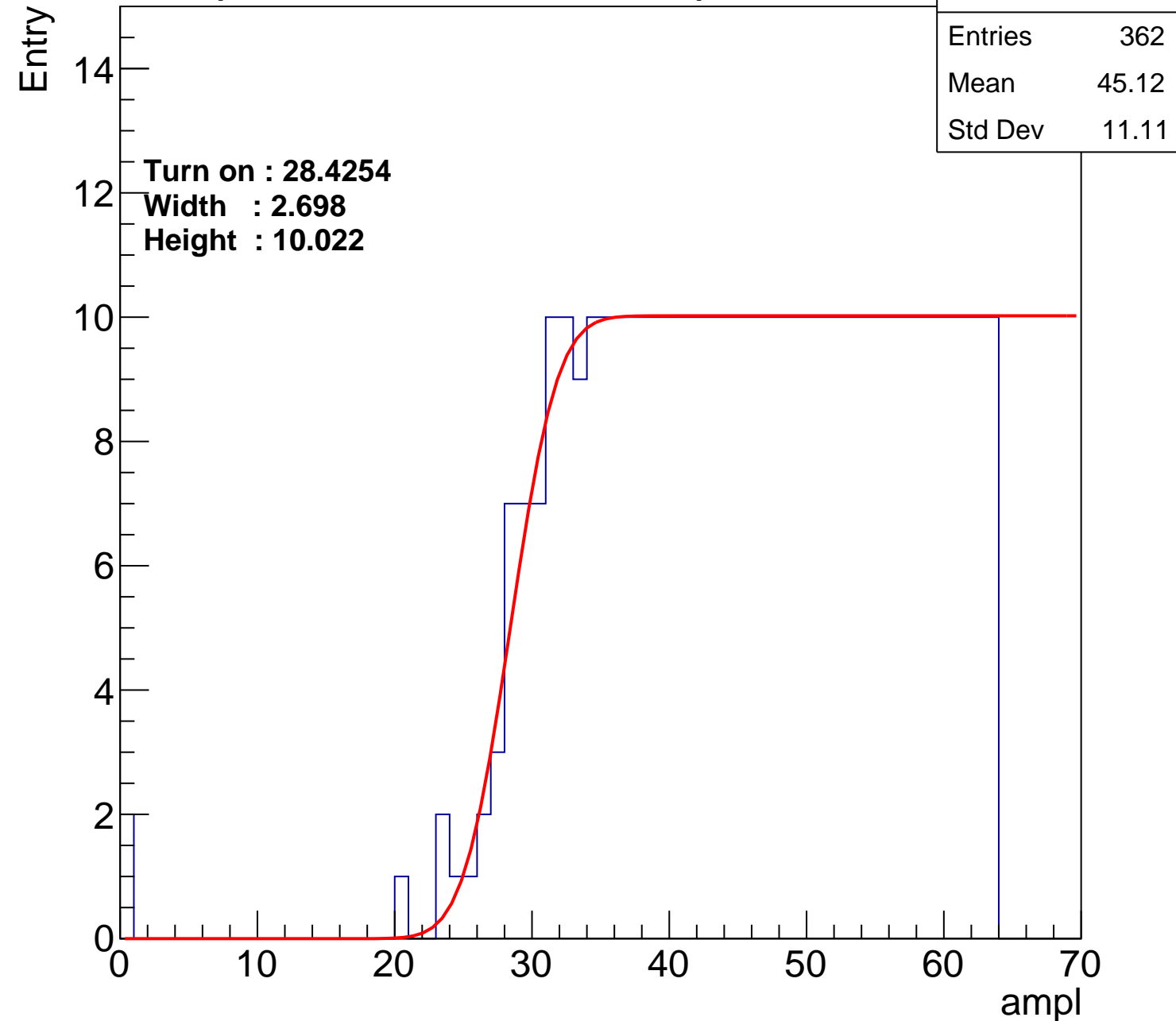
Width : 2.698

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch99

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.7
Std Dev	11.83

Turn on : 25.2487

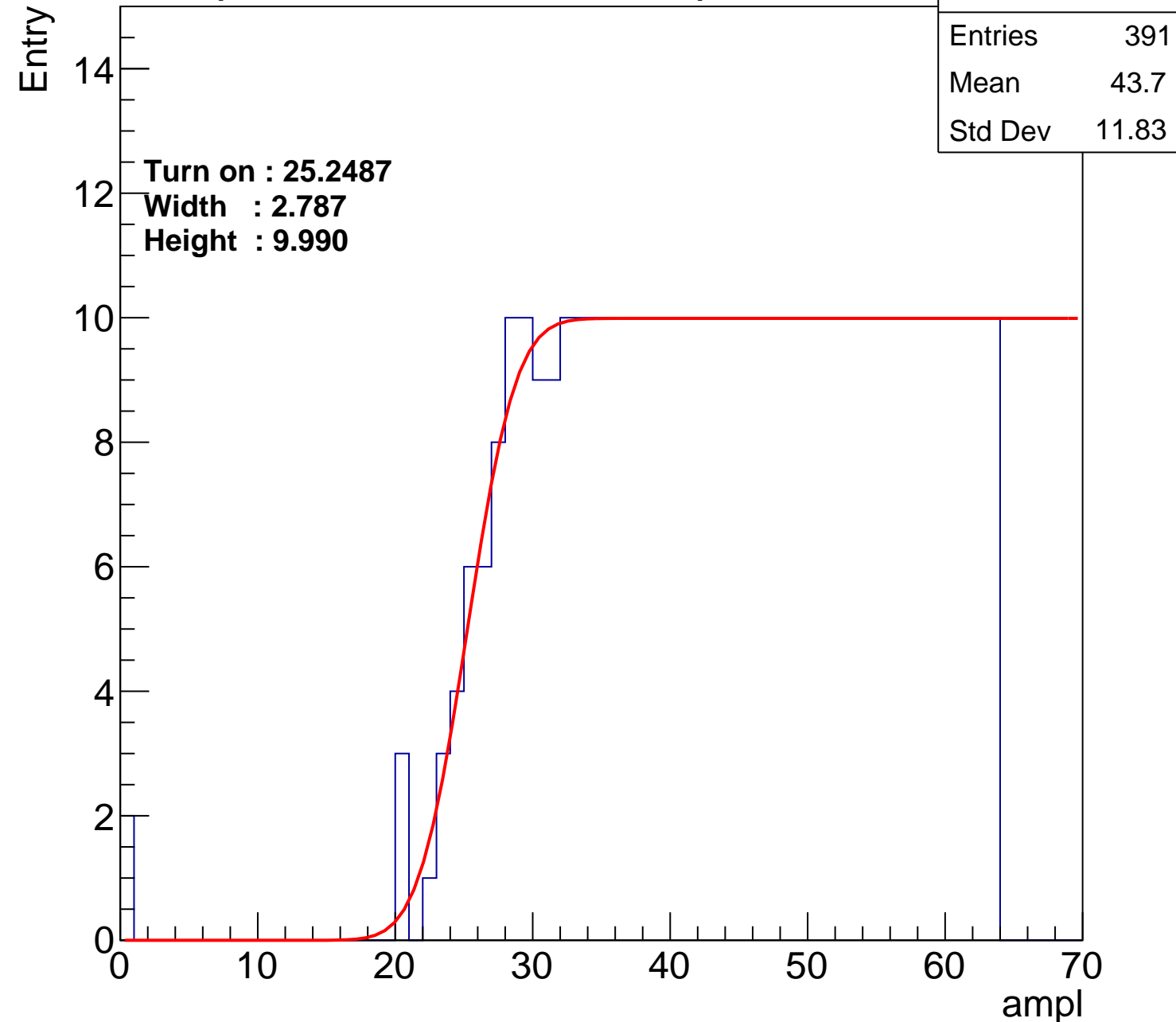
Width : 2.787

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch100

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.7
Std Dev	11.43

Turn on : 27.6633

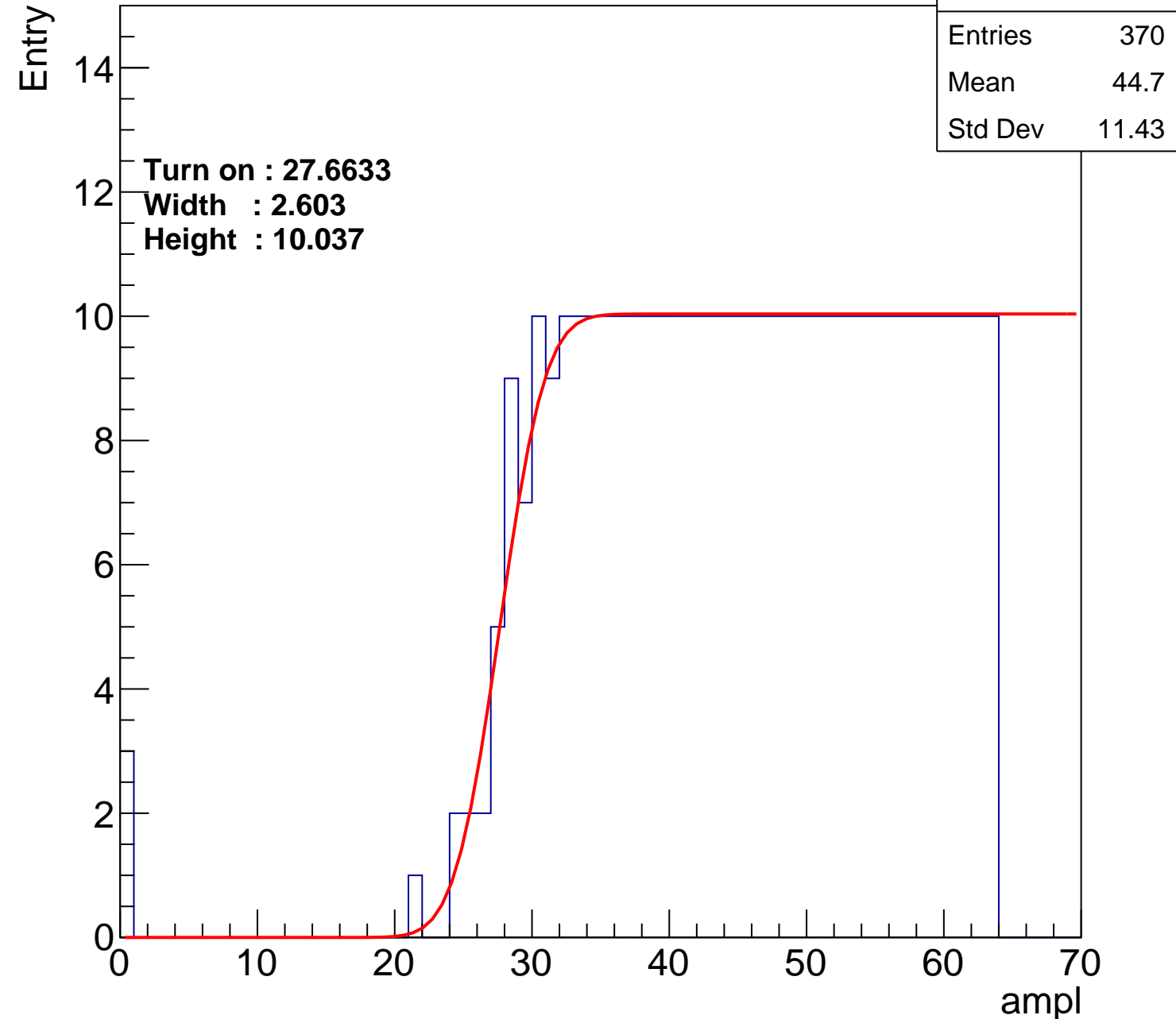
Width : 2.603

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch101

calib_packv5_042523_0143.root, FC#4, port A2

Entries	403
Mean	43.14
Std Dev	12.1

Turn on : 24.0953

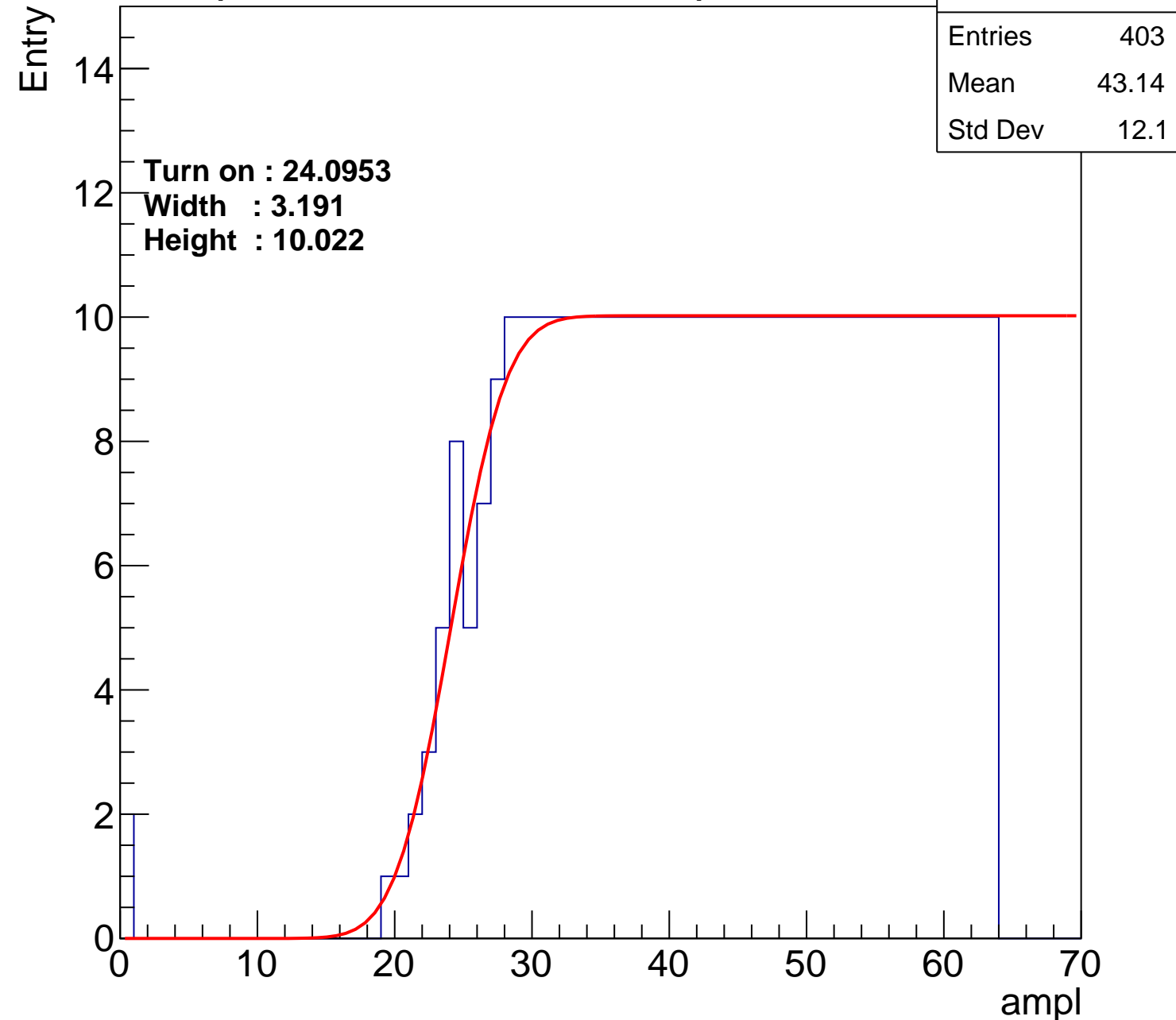
Width : 3.191

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch102

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.46
Std Dev	11.54

Turn on : 26.5346

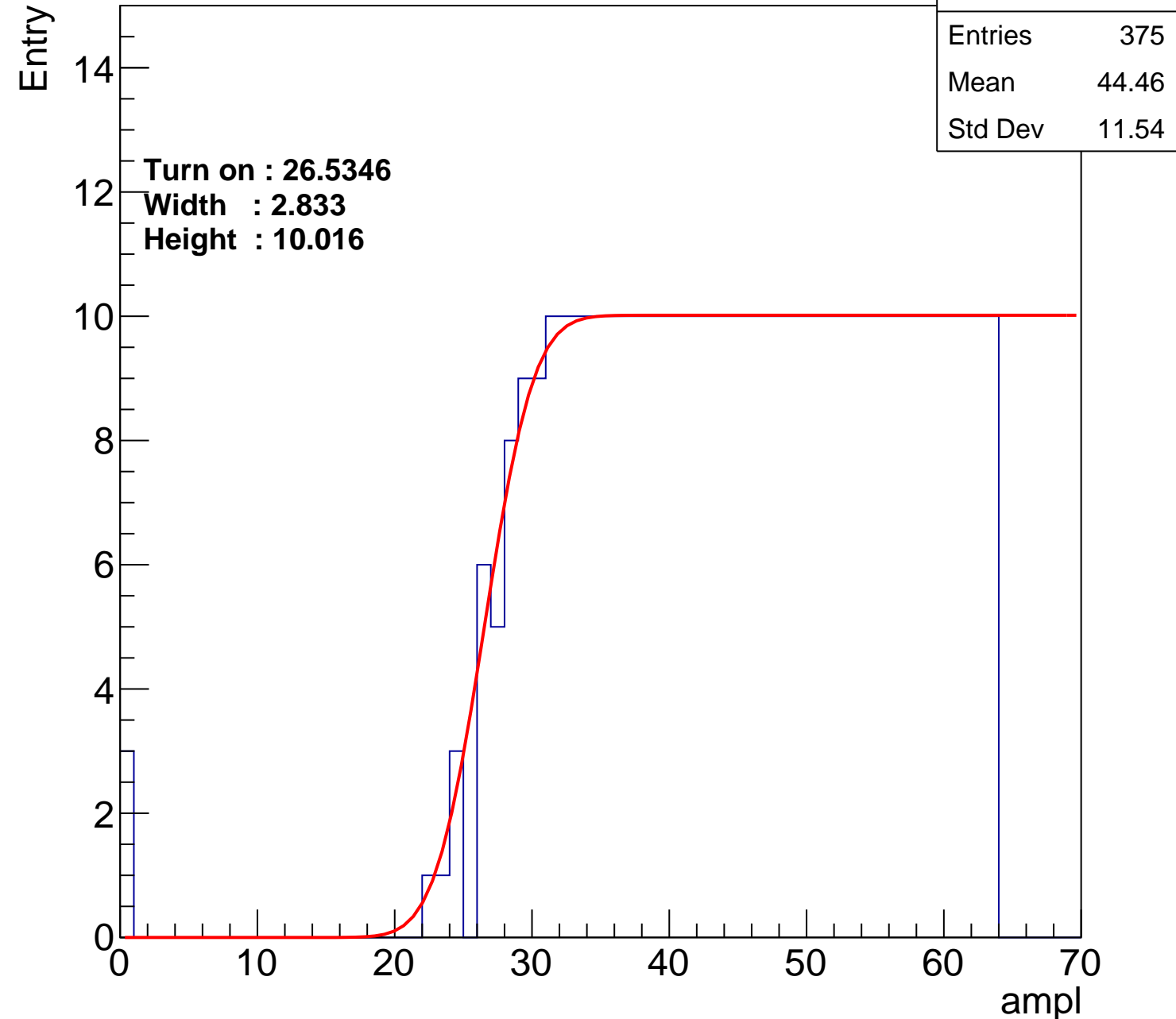
Width : 2.833

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch103

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.81
Std Dev	11.36

Turn on : 27.4863

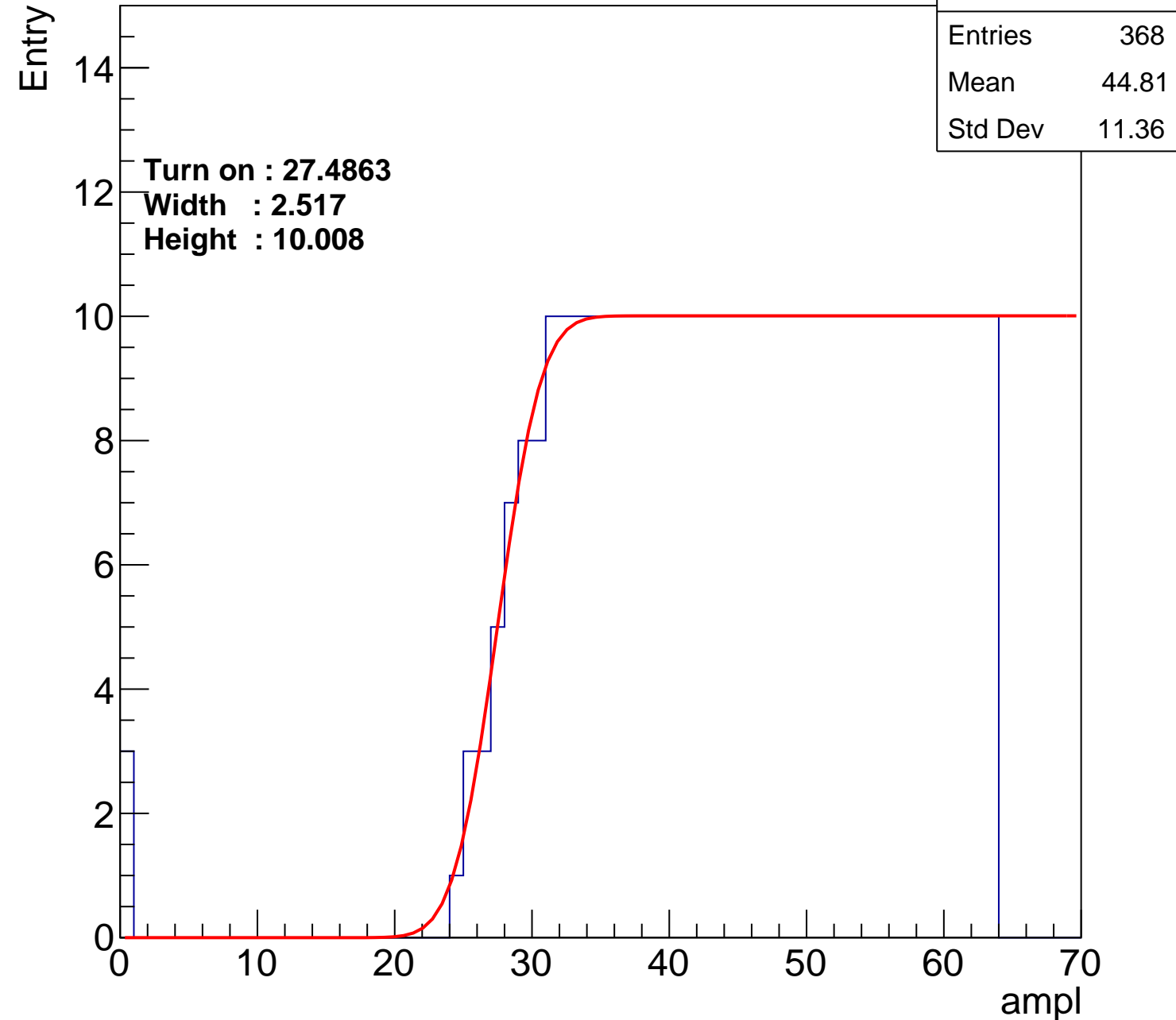
Width : 2.517

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch104

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.48
Std Dev	11.31

Turn on : 26.9994

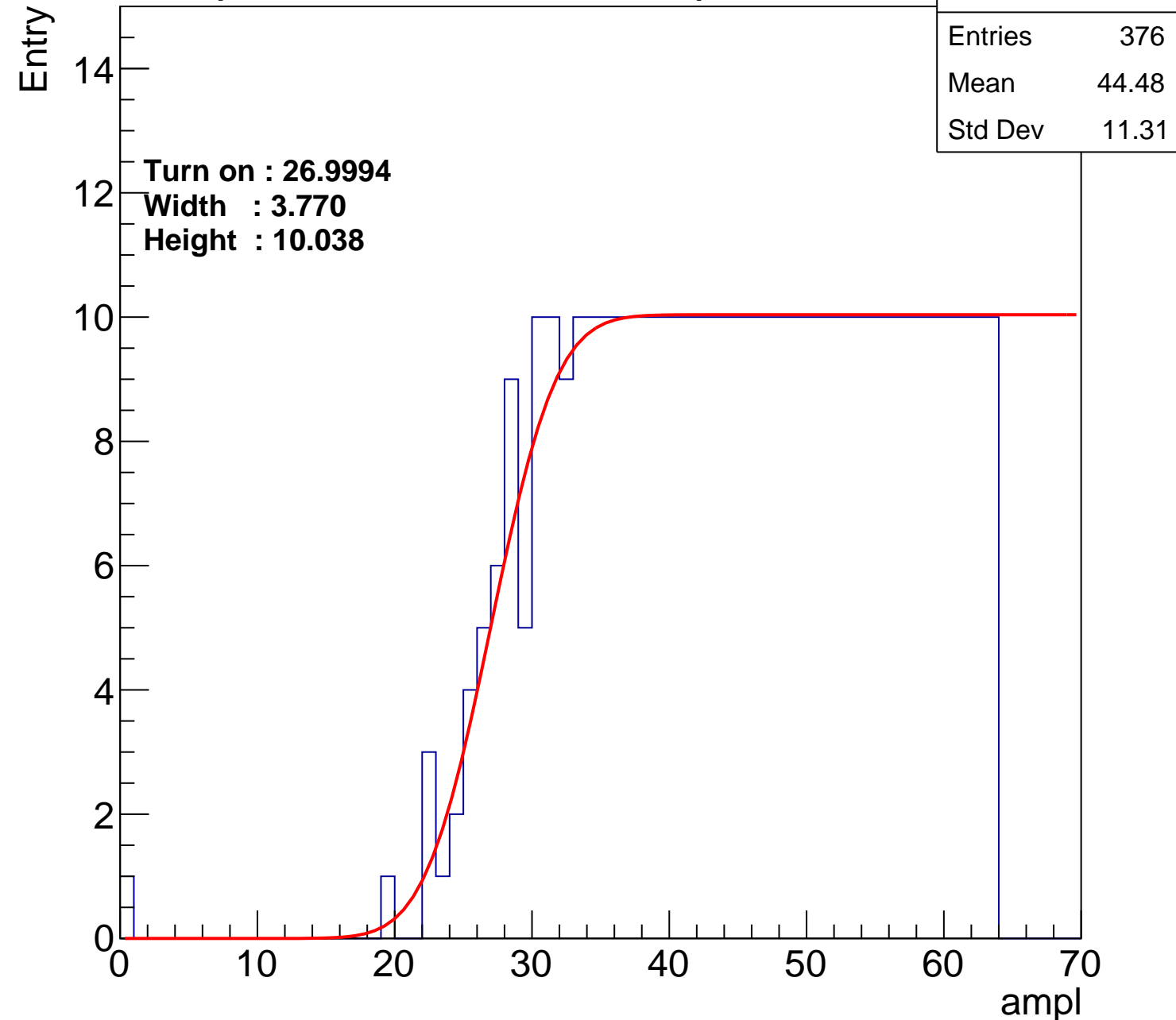
Width : 3.770

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch105

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.91
Std Dev	11.77

Turn on : 26.2138

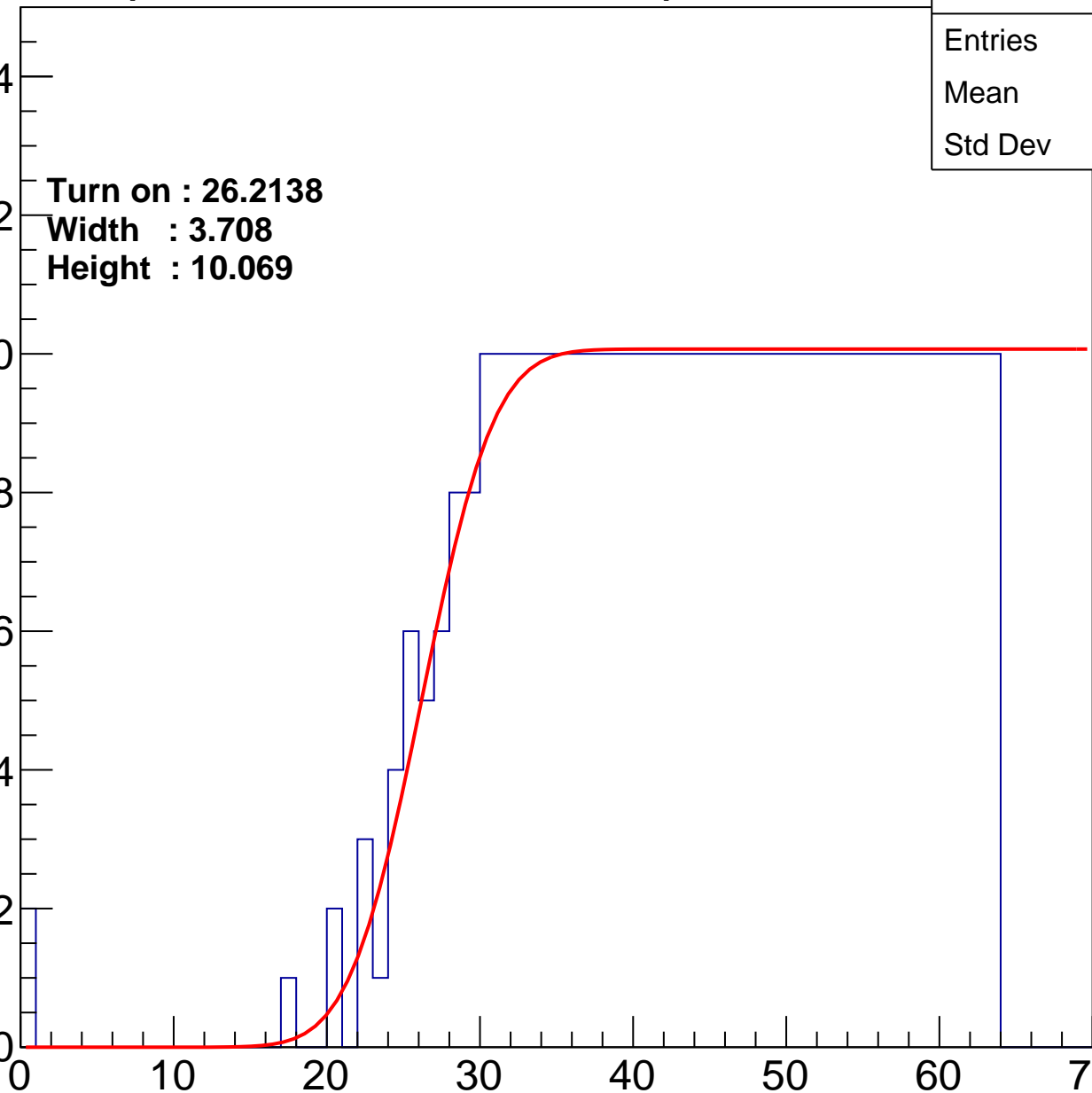
Width : 3.708

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch106

calib_packv5_042523_0143.root, FC#4, port A2

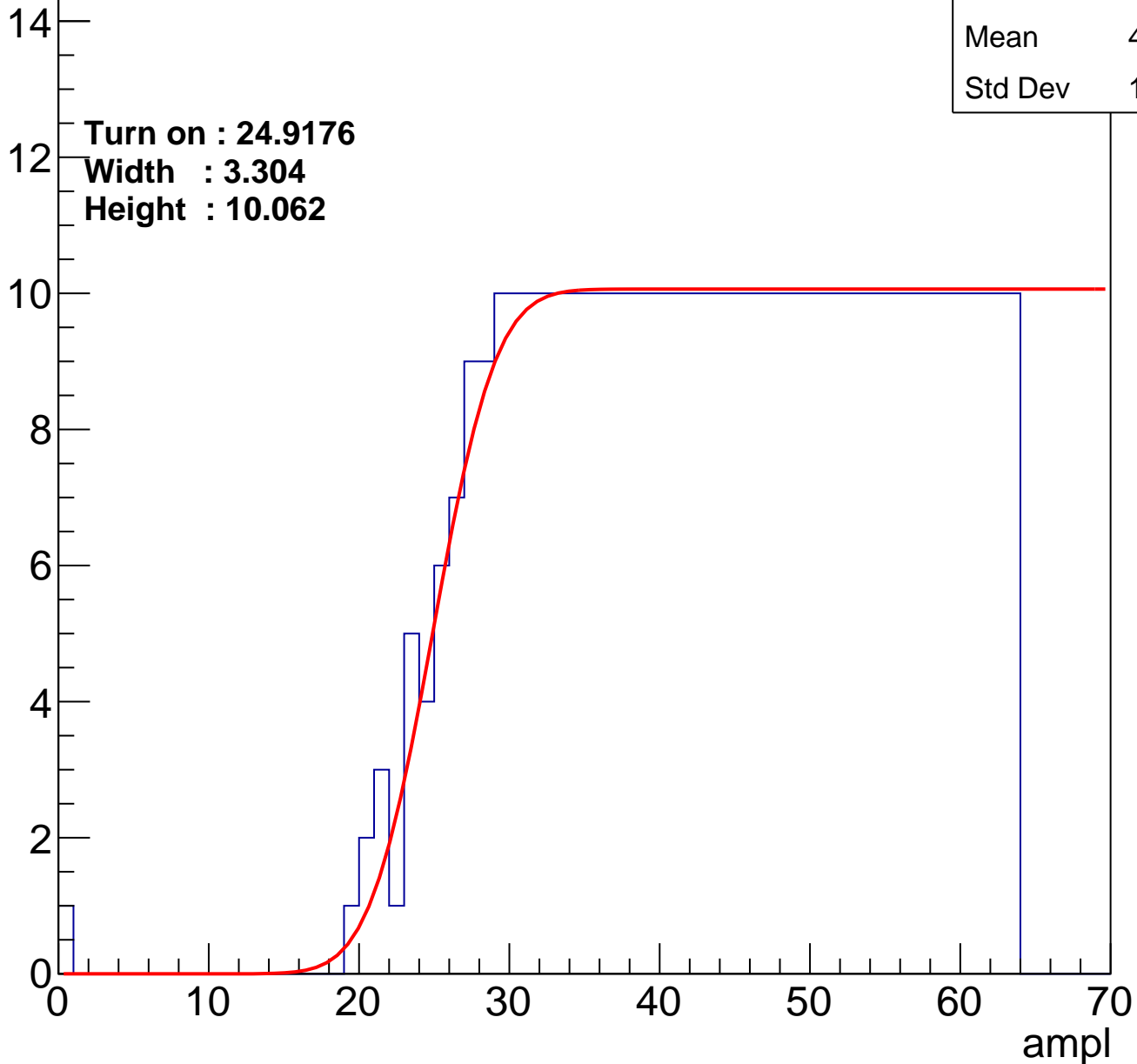
Entries	398
Mean	43.42
Std Dev	11.85

Turn on : 24.9176

Width : 3.304

Height : 10.062

Entry



B1L100S, U6-ch107

calib_packv5_042523_0143.root, FC#4, port A2

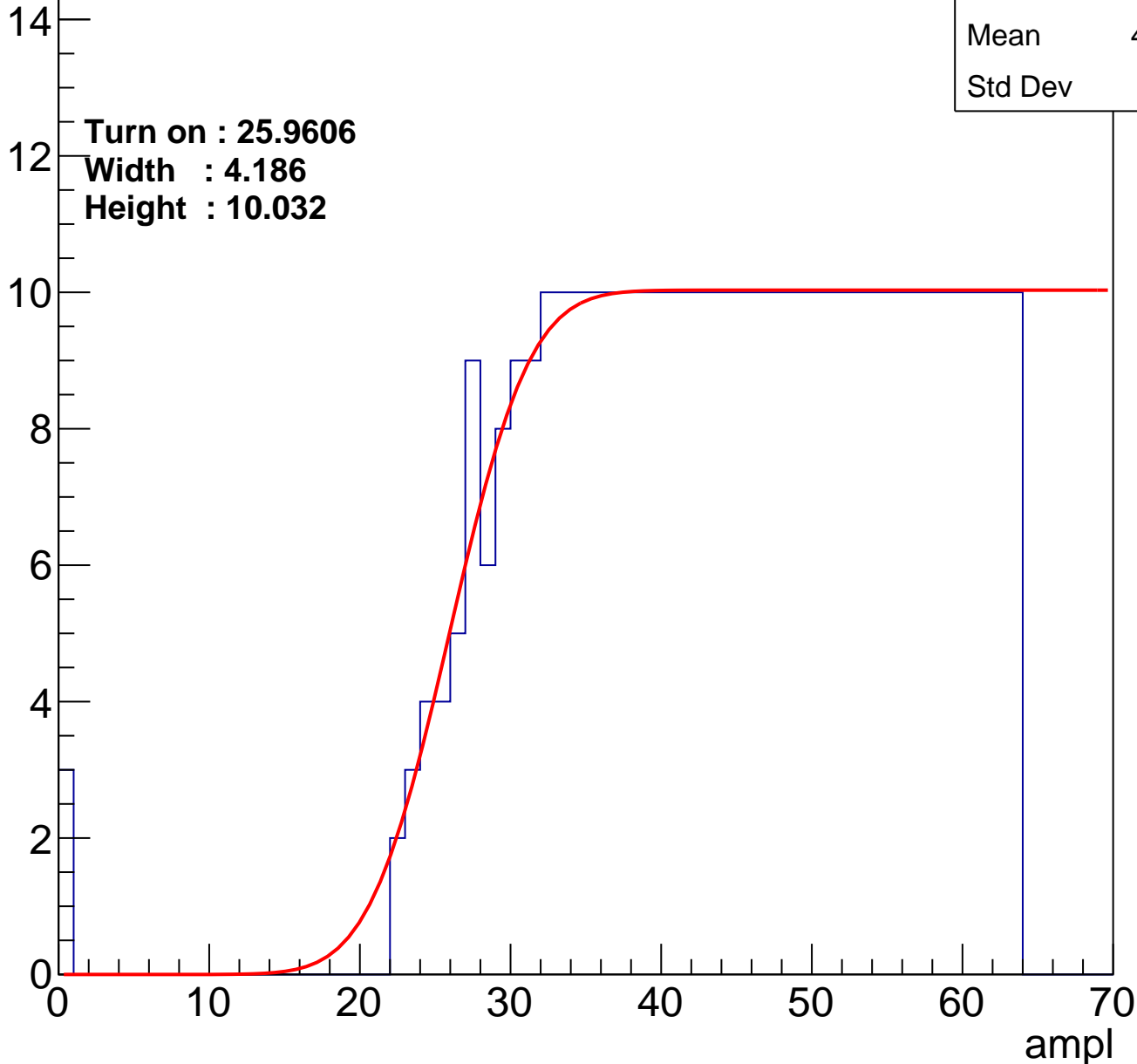
Entries	382
Mean	44.06
Std Dev	11.8

Turn on : 25.9606

Width : 4.186

Height : 10.032

Entry



B1L100S, U6-ch108

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.19
Std Dev	11.58

Turn on : 26.7265

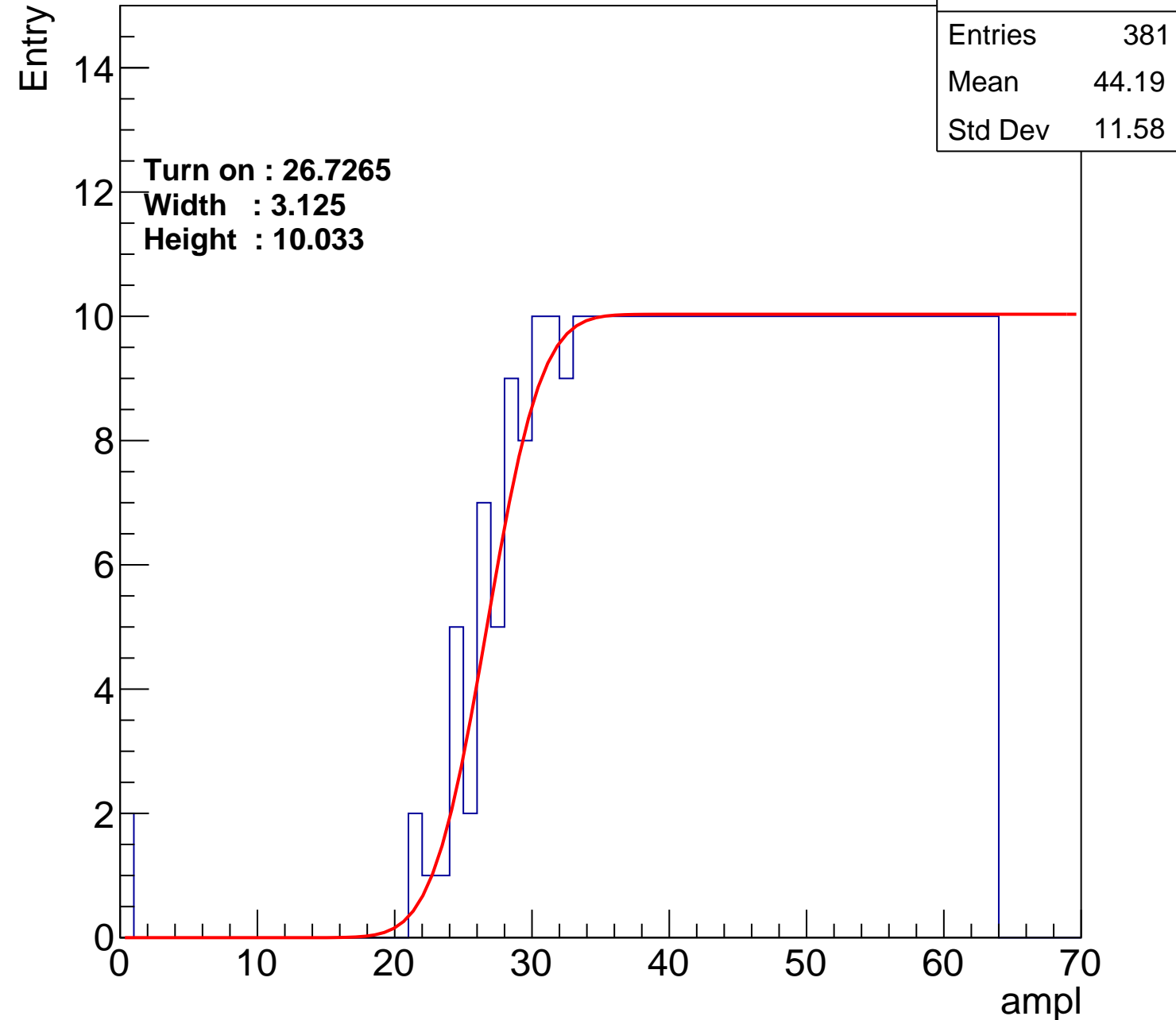
Width : 3.125

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch109

calib_packv5_042523_0143.root, FC#4, port A2

Entries	354
Mean	45.48
Std Dev	10.95

Turn on : 28.9338

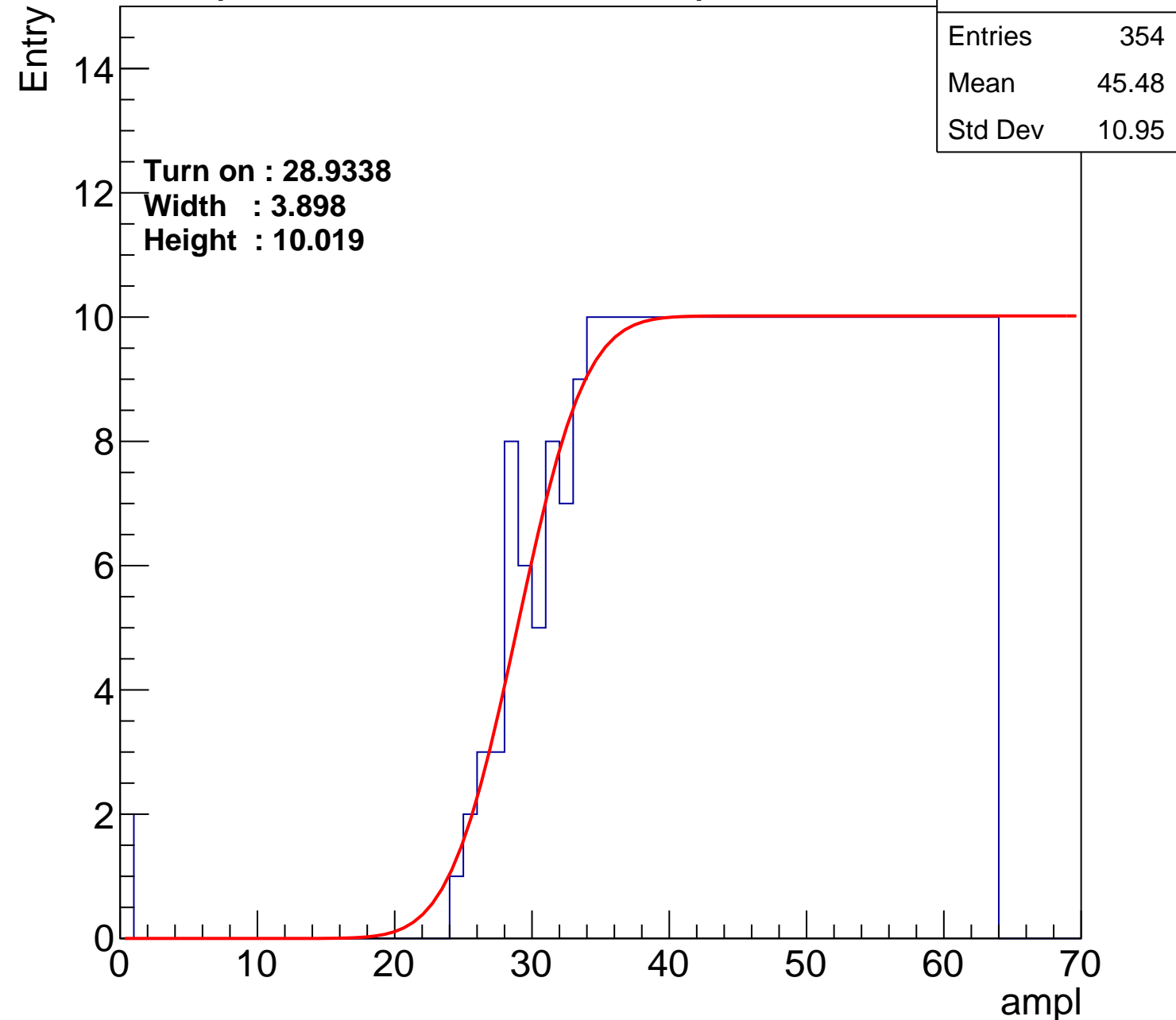
Width : 3.898

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch110

calib_packv5_042523_0143.root, FC#4, port A2

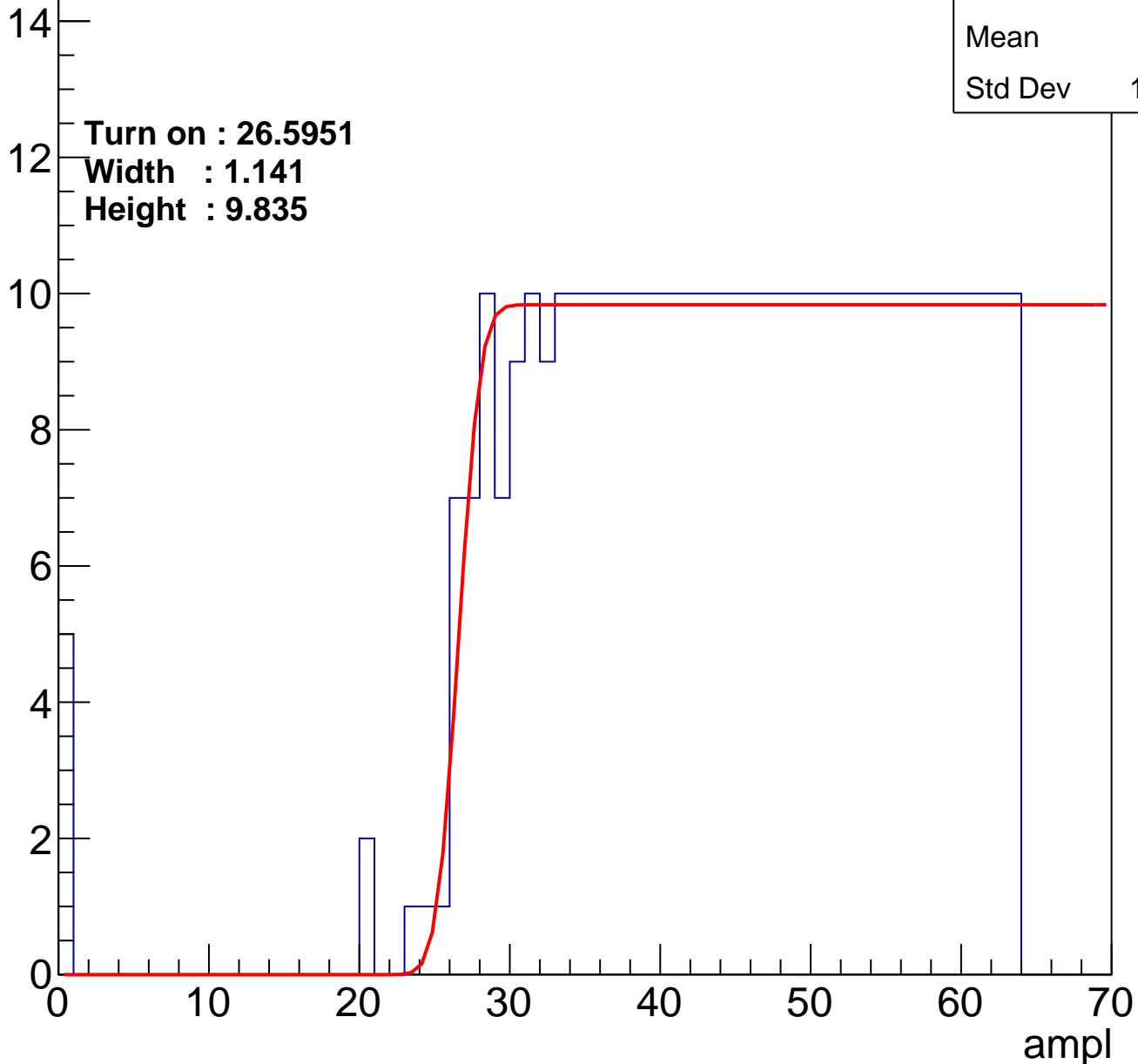
Entries	379
Mean	44.1
Std Dev	12.04

Turn on : 26.5951

Width : 1.141

Height : 9.835

Entry



B1L100S, U6-ch111

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 26.3508

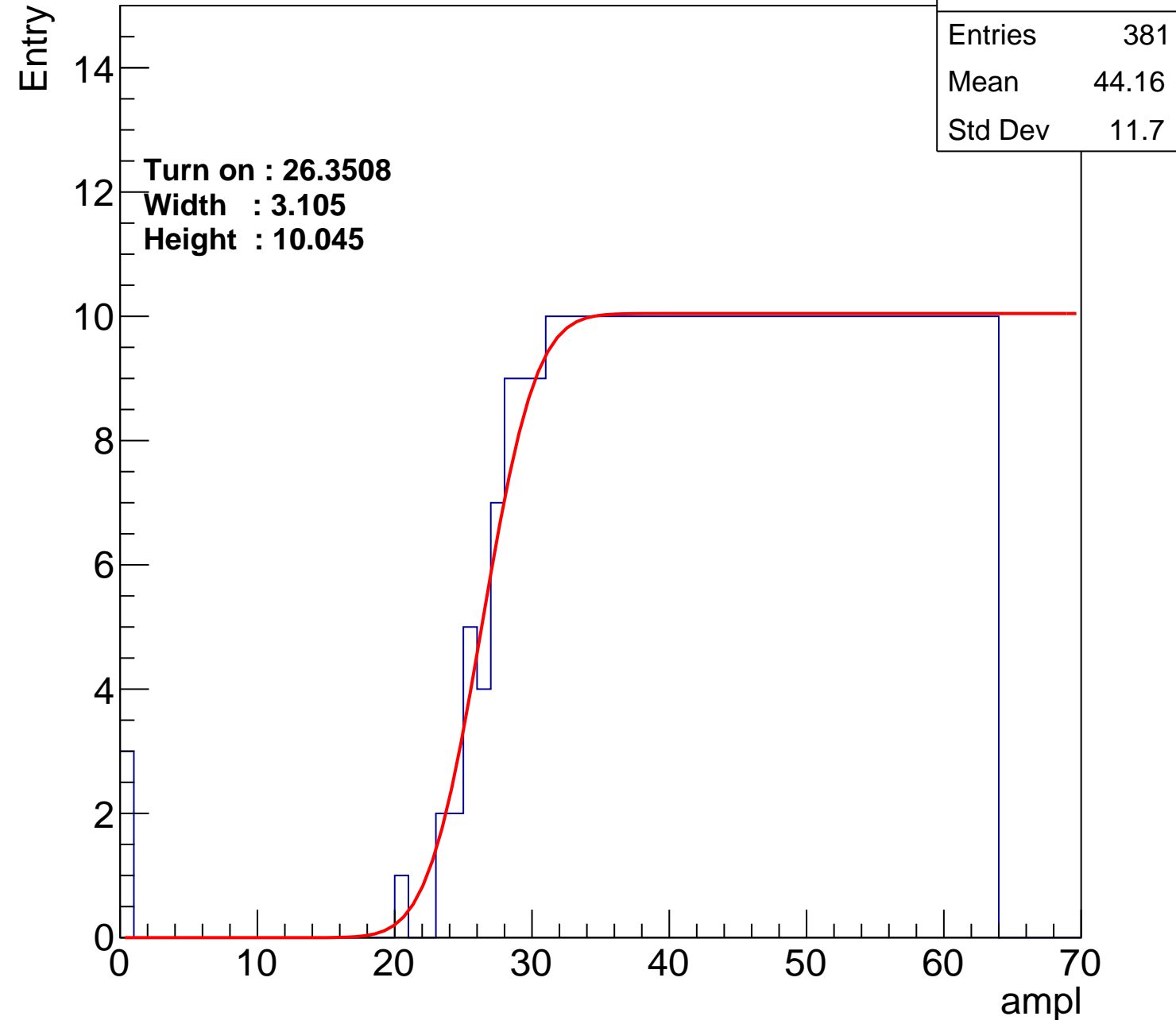
Width : 3.105

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch112

calib_packv5_042523_0143.root, FC#4, port A2

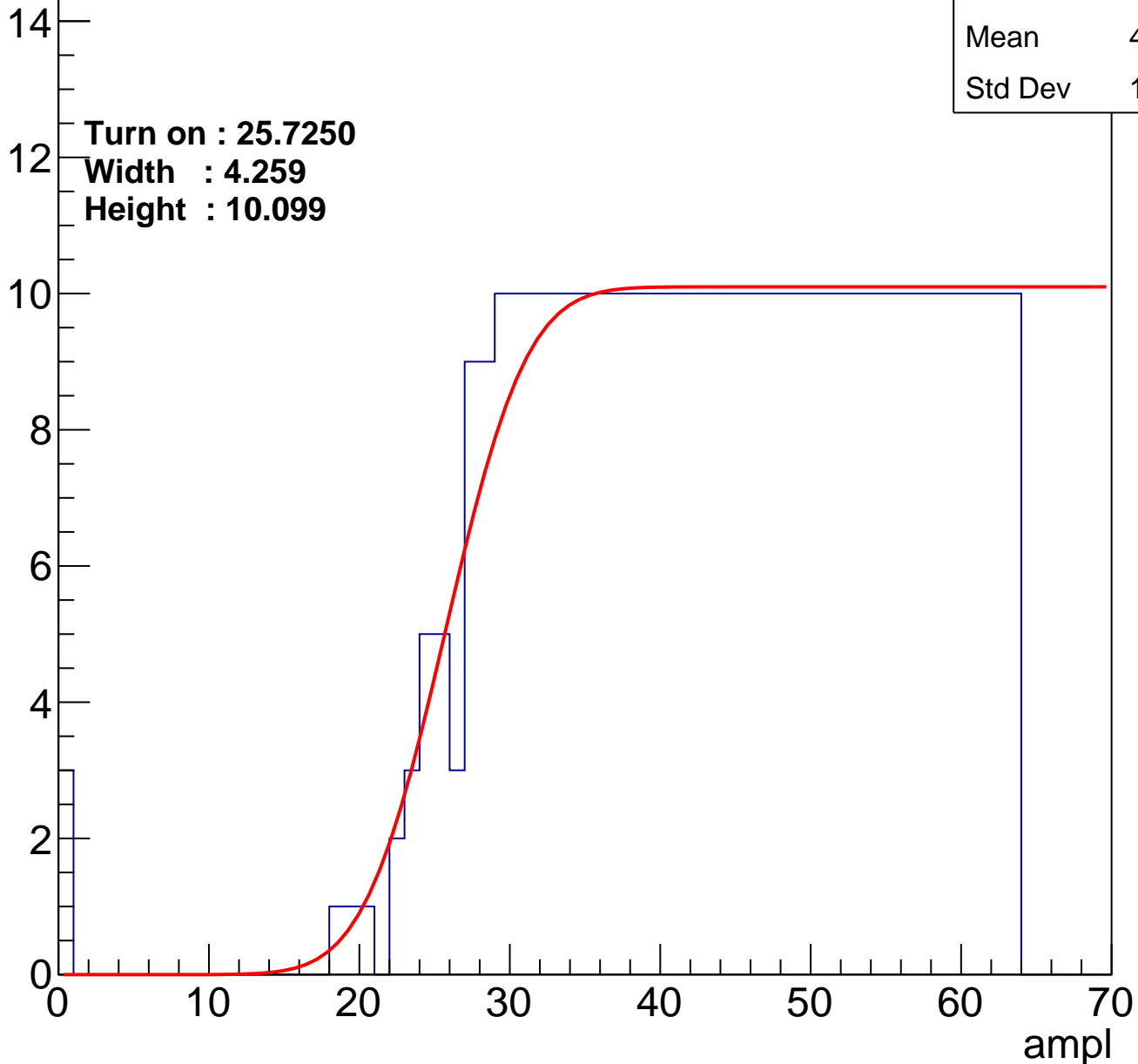
Entries	392
Mean	43.59
Std Dev	12.03

Turn on : 25.7250

Width : 4.259

Height : 10.099

Entry



B1L100S, U6-ch113

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.48
Std Dev	11.55

Turn on : 26.8948

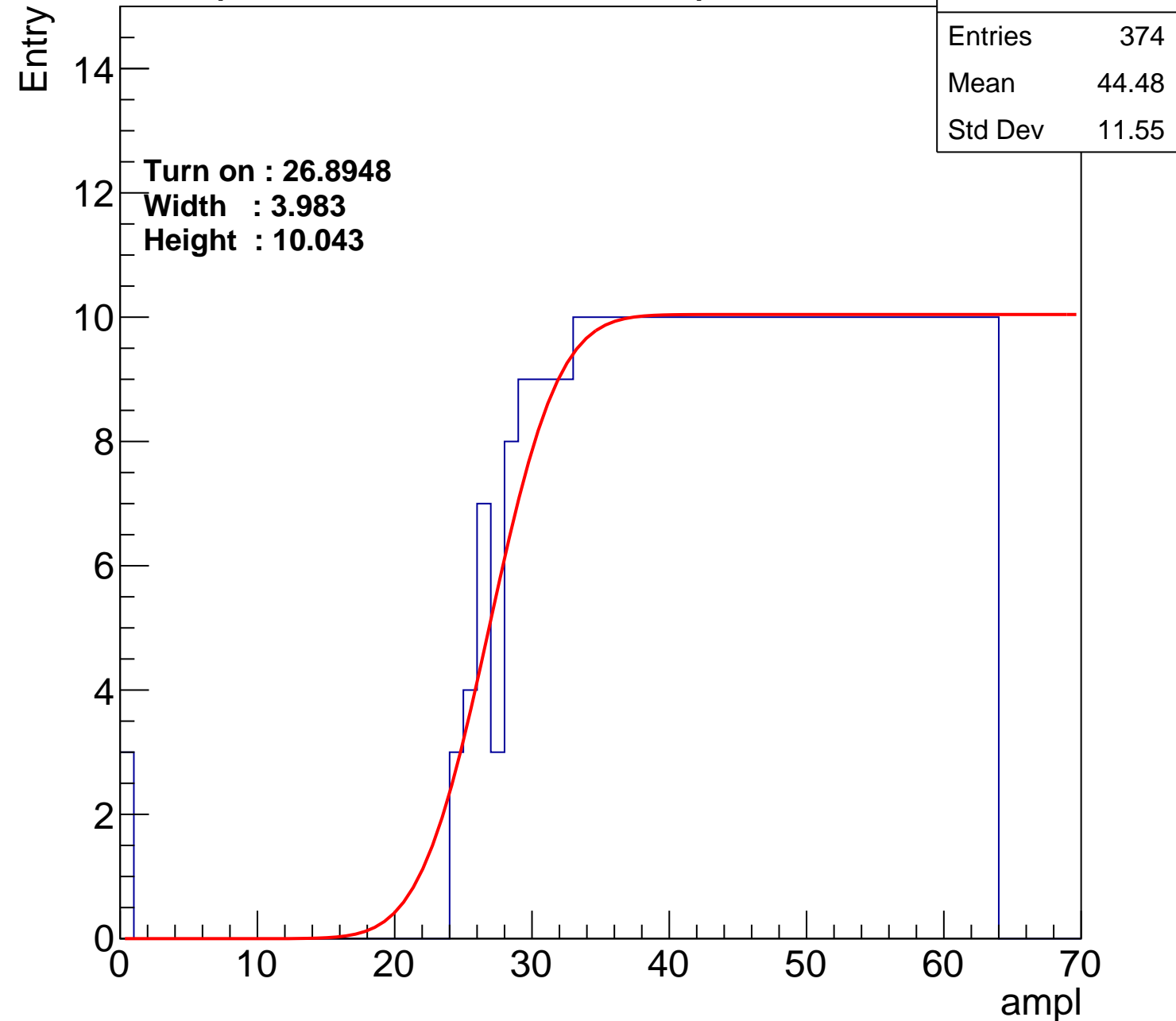
Width : 3.983

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch114

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.29
Std Dev	11.48

Turn on : 27.0196

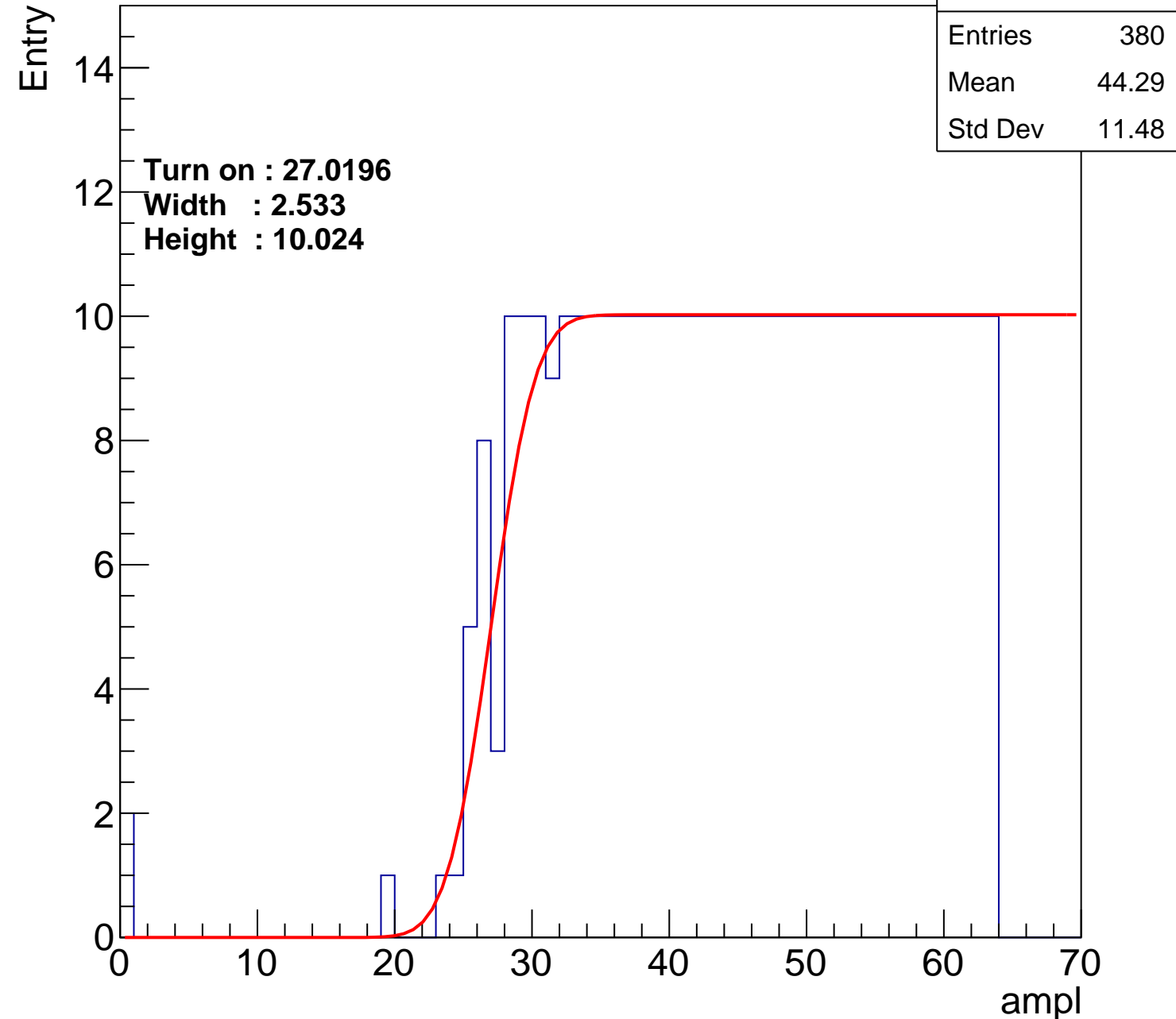
Width : 2.533

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch115

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.71
Std Dev	12.05

Turn on : 25.7712

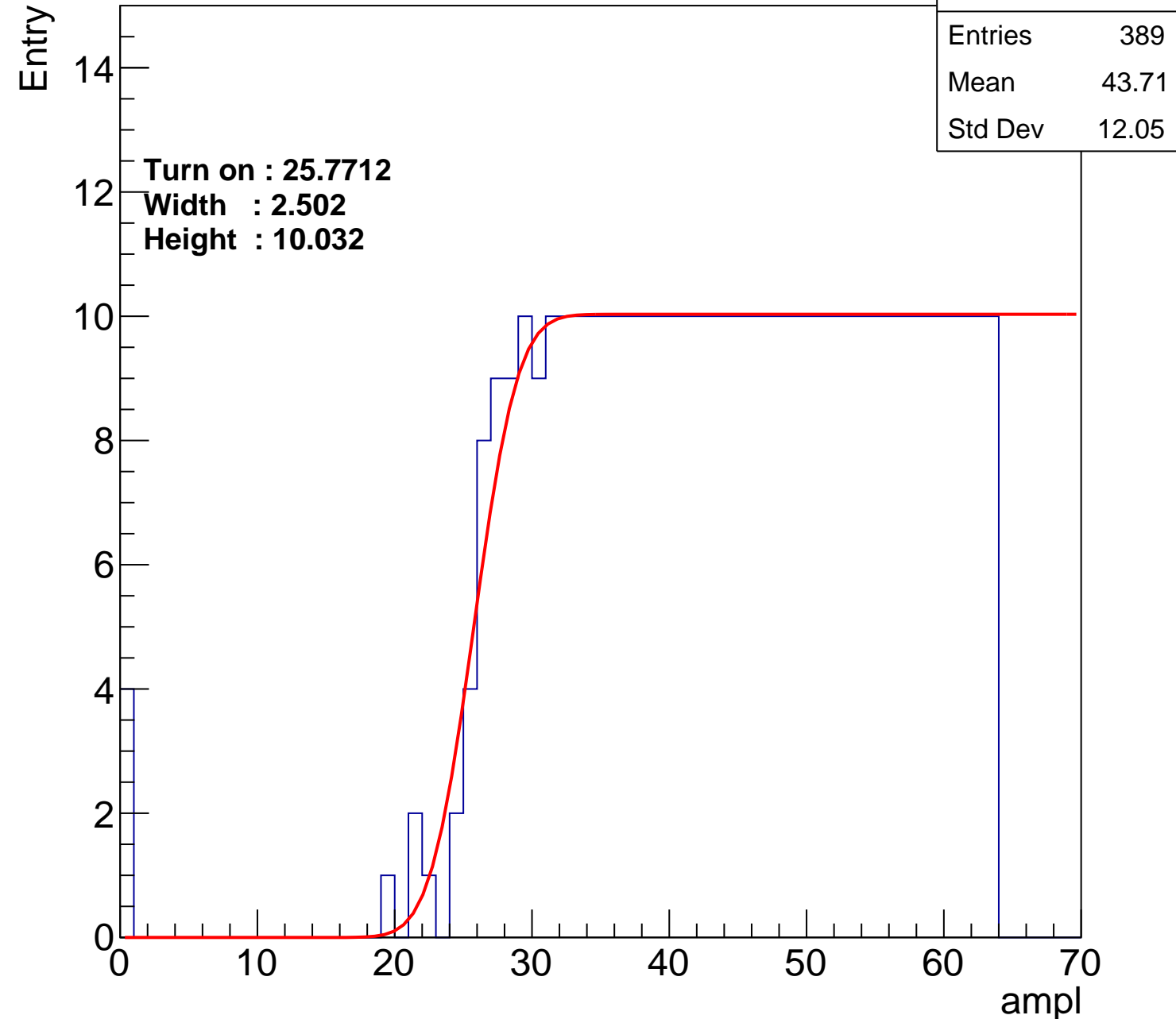
Width : 2.502

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch116

calib_packv5_042523_0143.root, FC#4, port A2

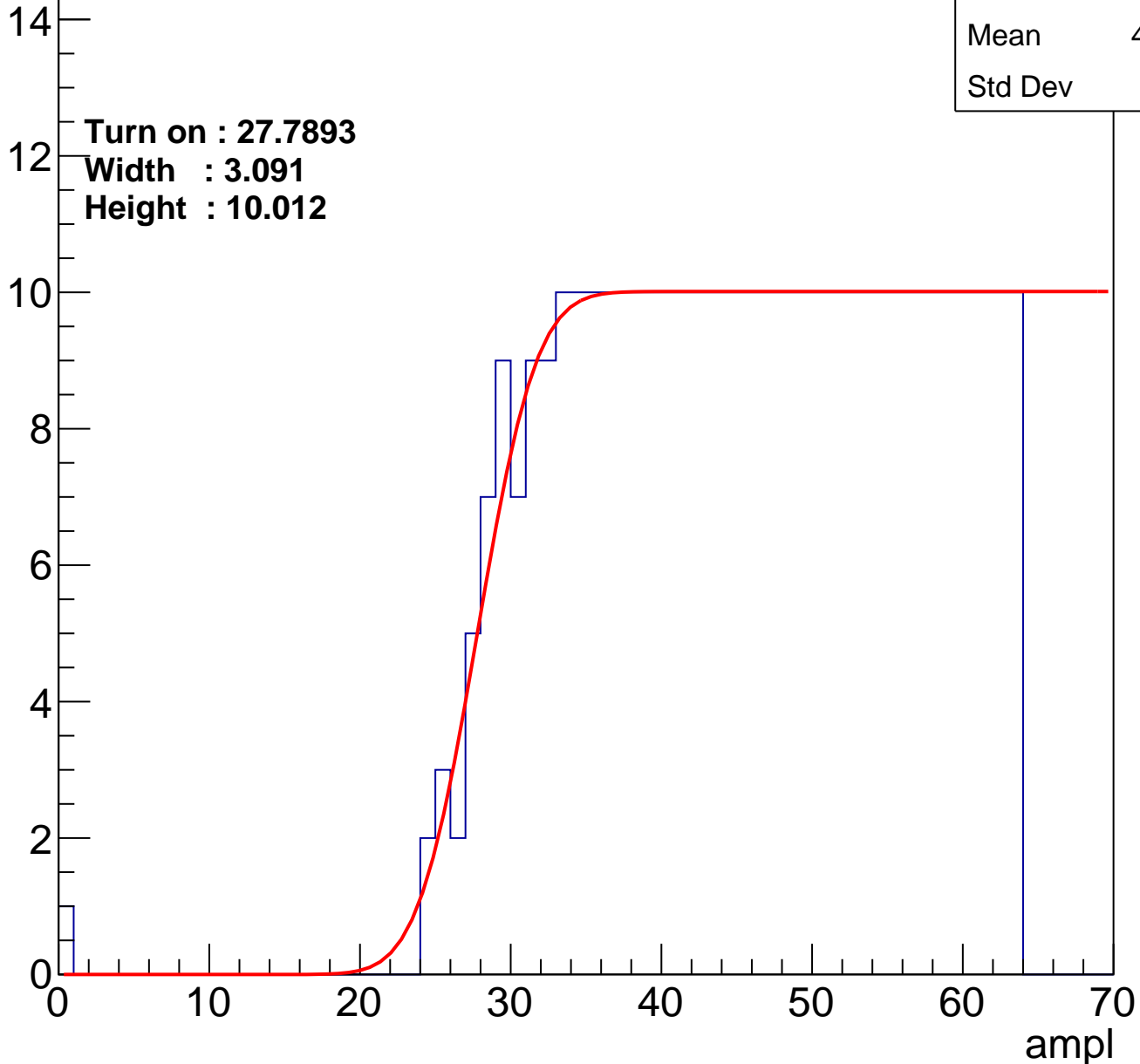
Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 27.7893

Width : 3.091

Height : 10.012

Entry



B1L100S, U6-ch117

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
Mean	45.24
Std Dev	11.01

Turn on : 28.5120

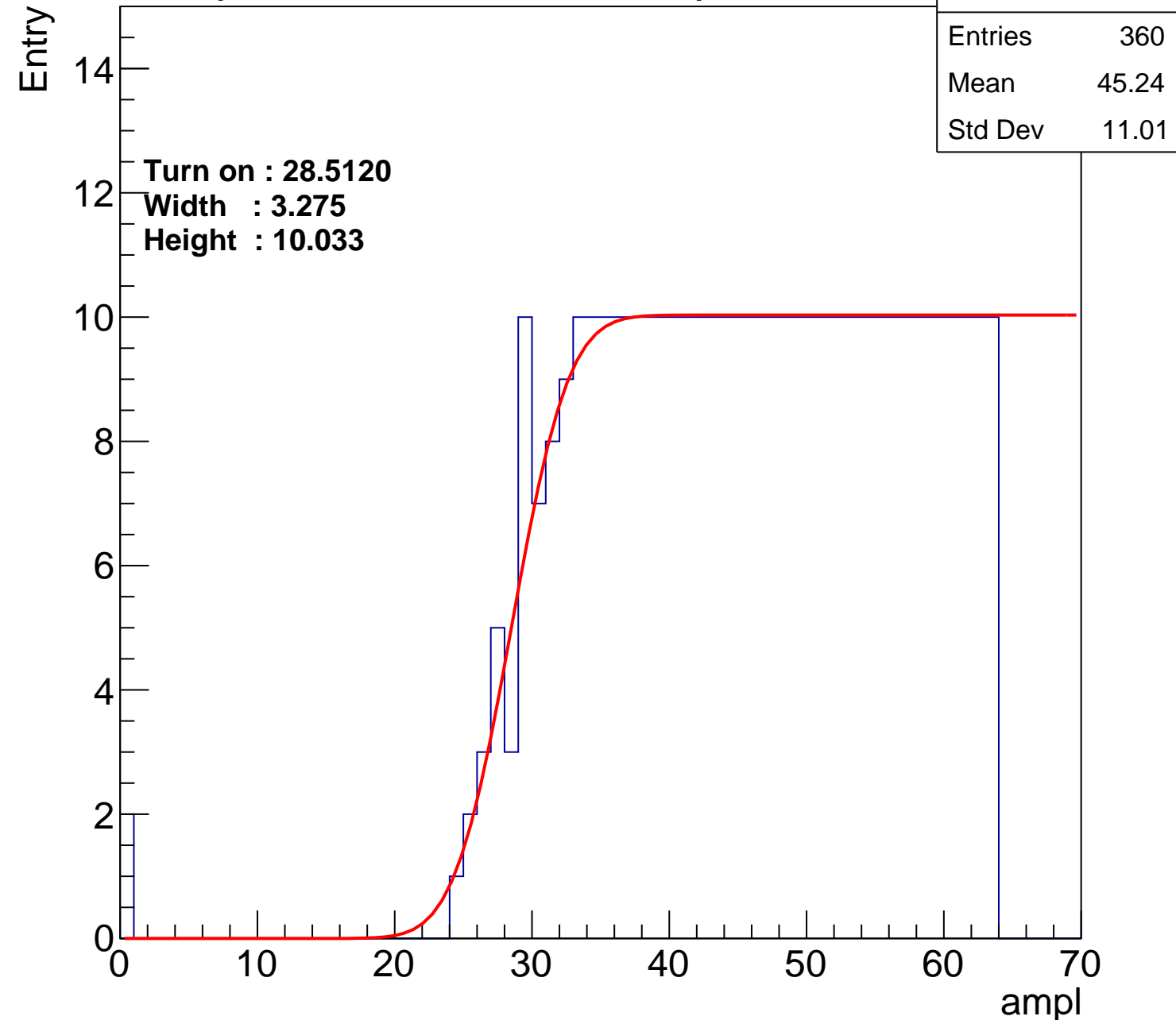
Width : 3.275

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch118

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.87
Std Dev	11.38

Turn on : 28.5278

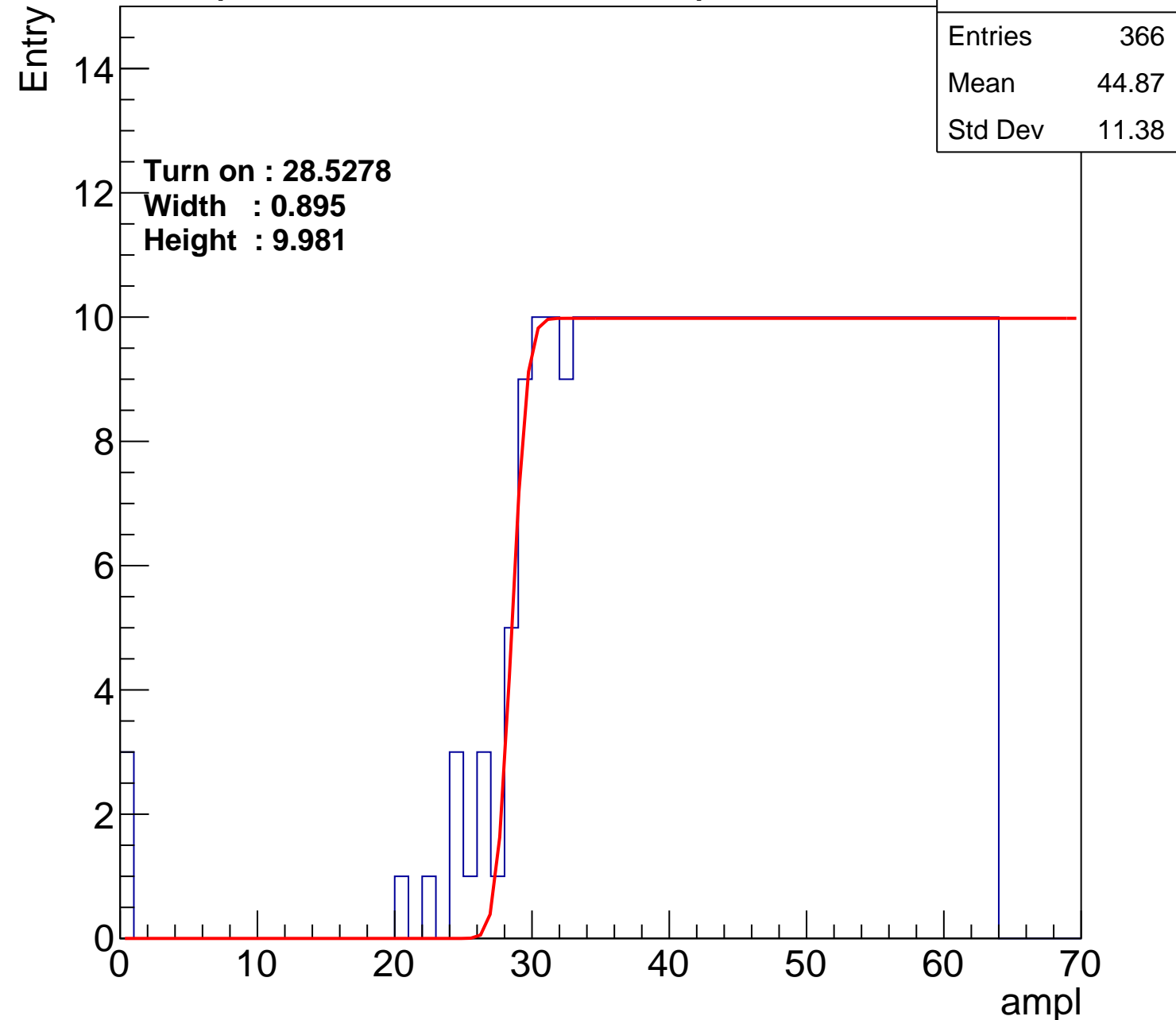
Width : 0.895

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch119

calib_packv5_042523_0143.root, FC#4, port A2

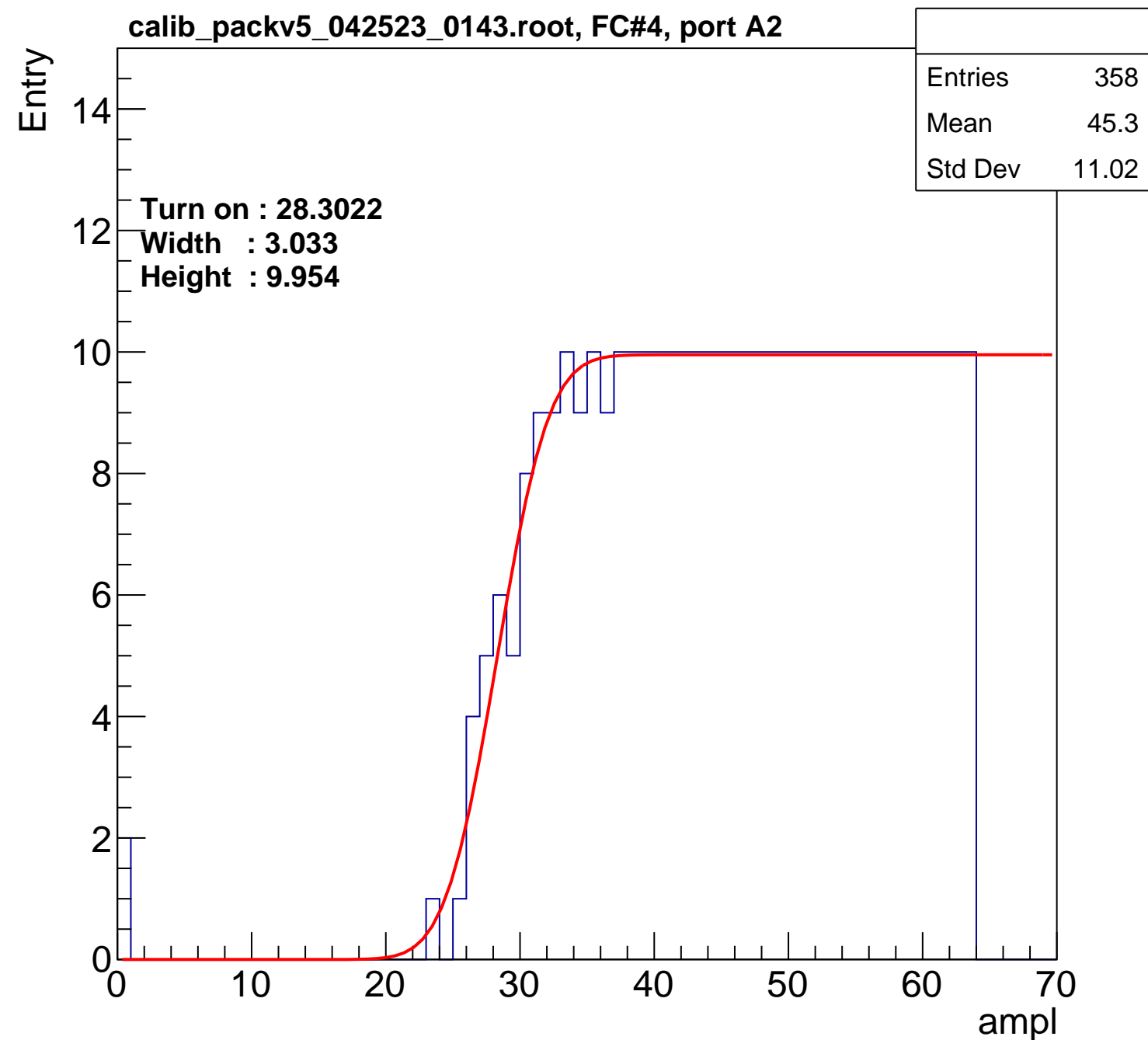
Entry

14
12
10
8
6
4
2
0

Turn on : 28.3022
Width : 3.033
Height : 9.954

Entries	358
Mean	45.3
Std Dev	11.02

ampl



B1L100S, U6-ch120

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.9217

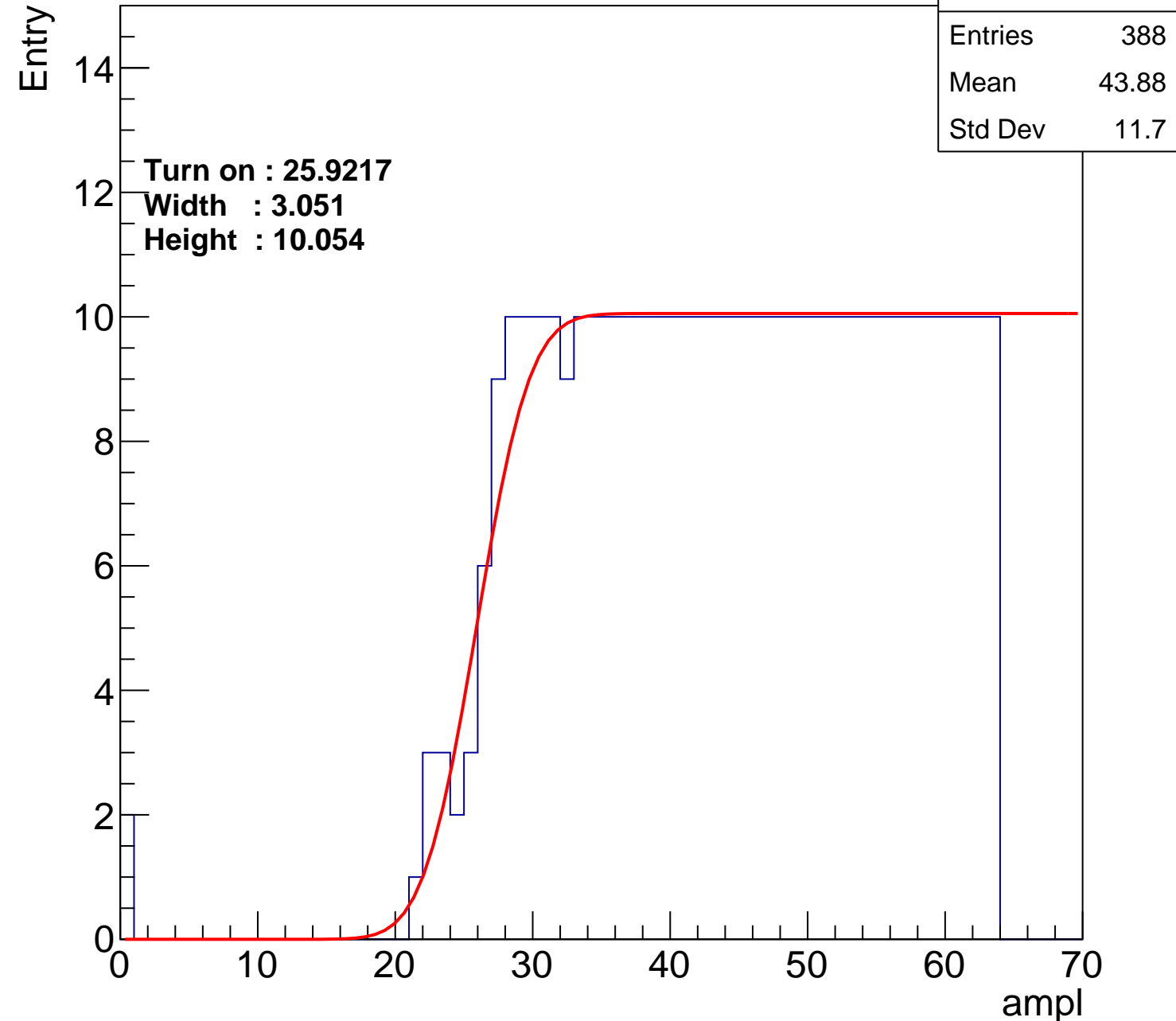
Width : 3.051

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch121

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.85
Std Dev	11.4

Turn on : 27.4788

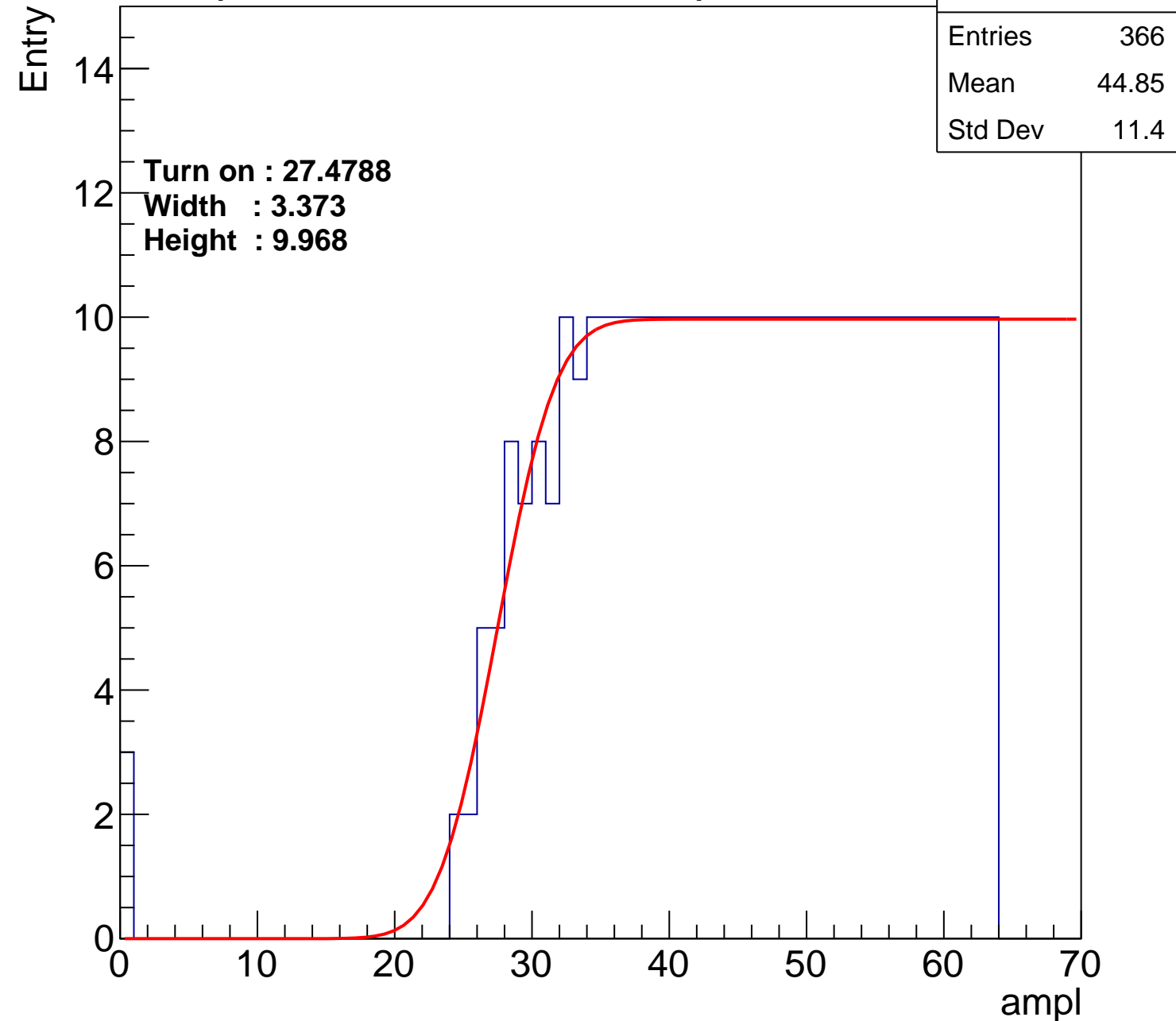
Width : 3.373

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch122

calib_packv5_042523_0143.root, FC#4, port A2

Entries	394
Mean	43.5
Std Dev	12.06

Turn on : 25.1262

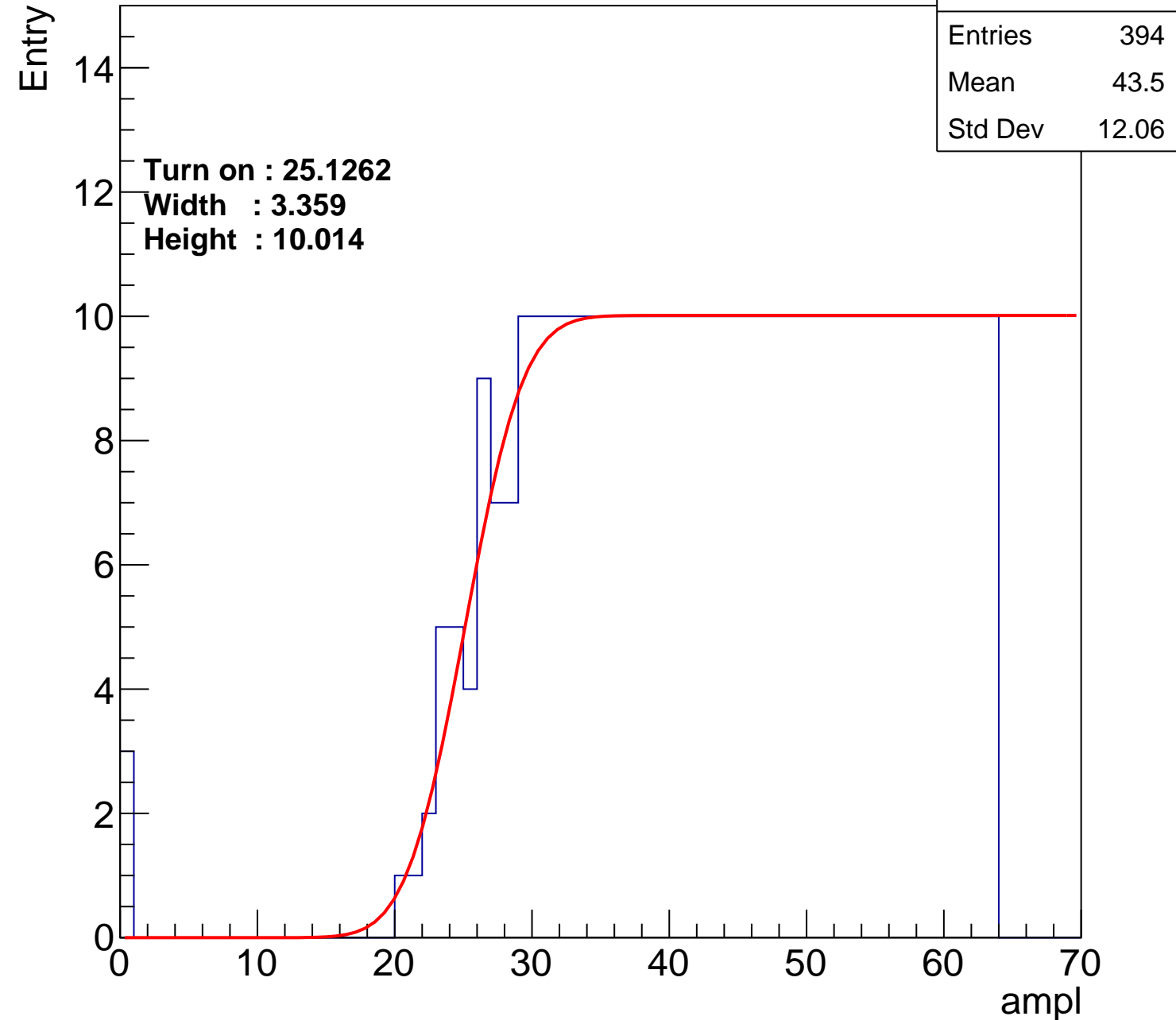
Width : 3.359

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch123

calib_packv5_042523_0143.root, FC#4, port A2

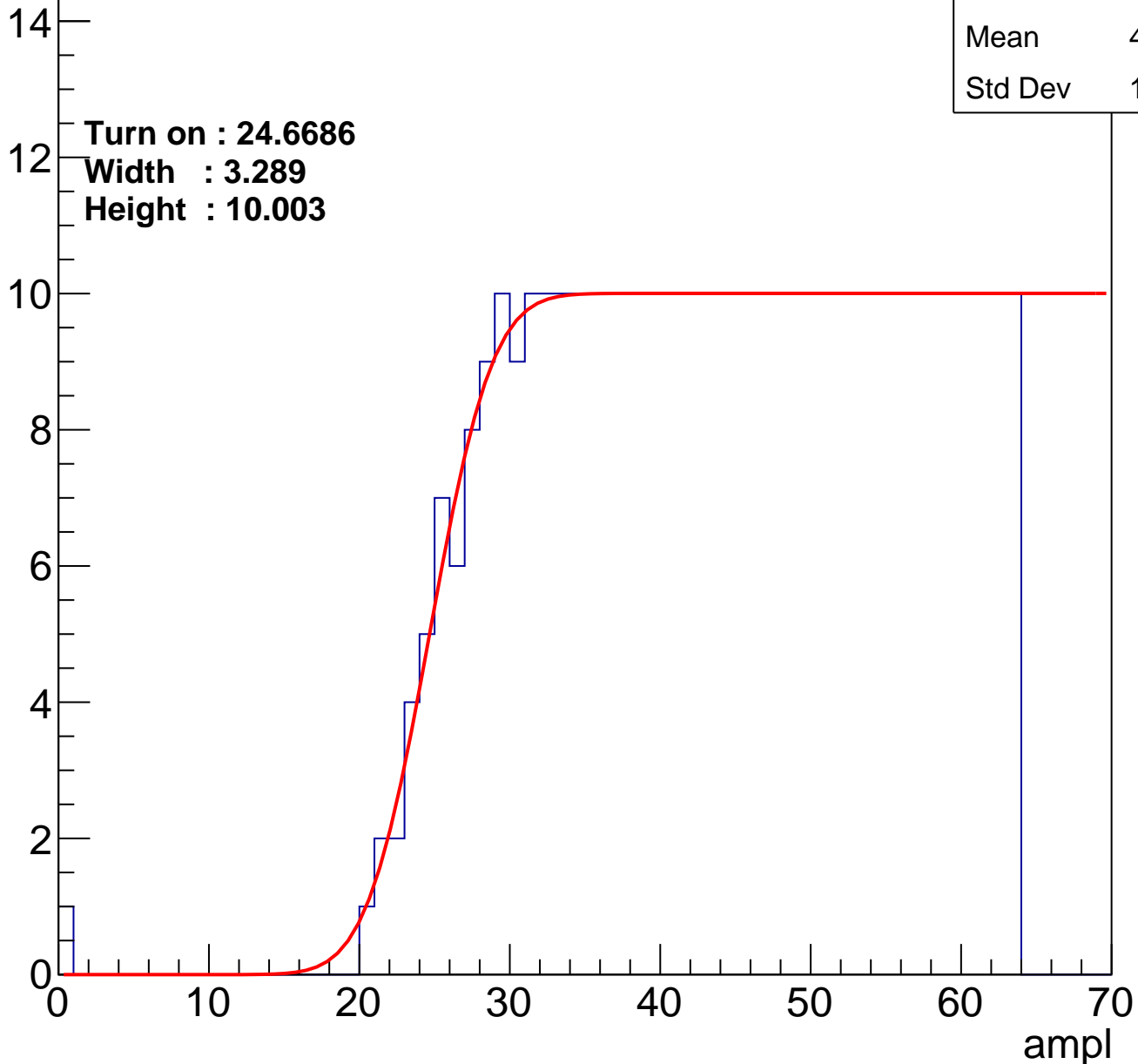
Entries	394
Mean	43.62
Std Dev	11.73

Turn on : 24.6686

Width : 3.289

Height : 10.003

Entry



B1L100S, U6-ch124

calib_packv5_042523_0143.root, FC#4, port A2

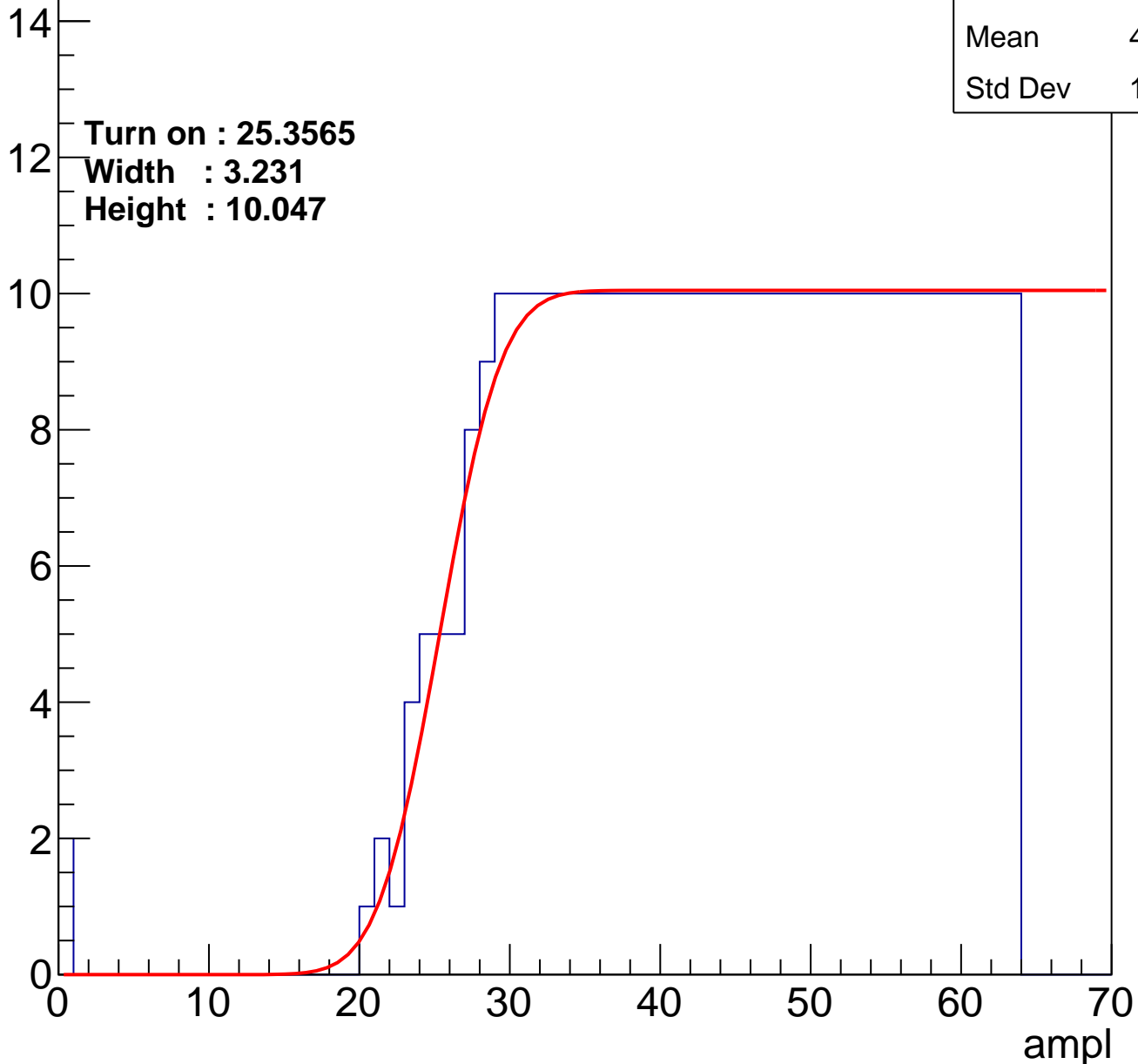
Entries	392
Mean	43.67
Std Dev	11.83

Turn on : 25.3565

Width : 3.231

Height : 10.047

Entry



B1L100S, U6-ch125

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.79
Std Dev	11.27

Turn on : 28.1437

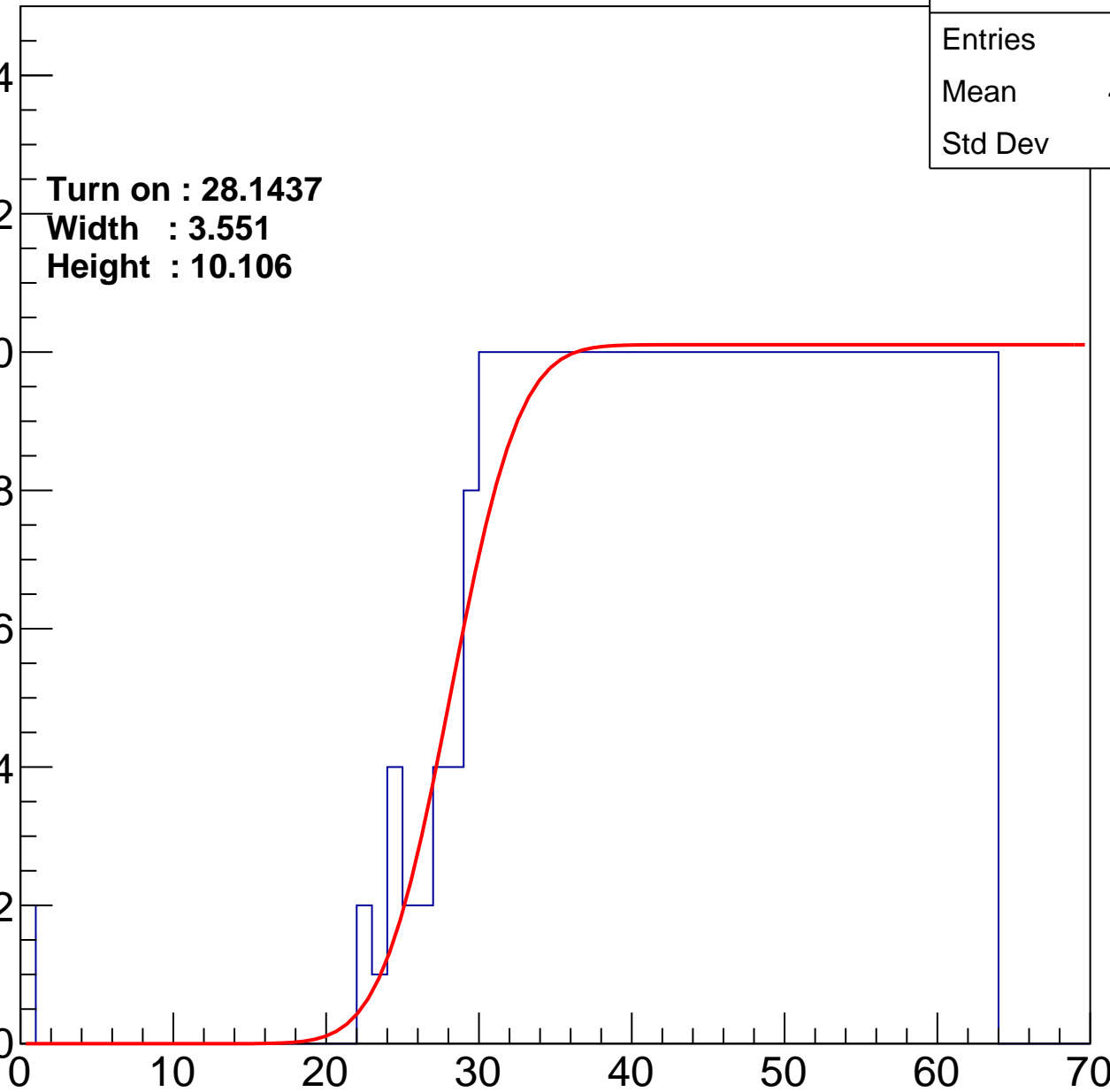
Width : 3.551

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch126

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.64
Std Dev	12.1

Turn on : 25.0525

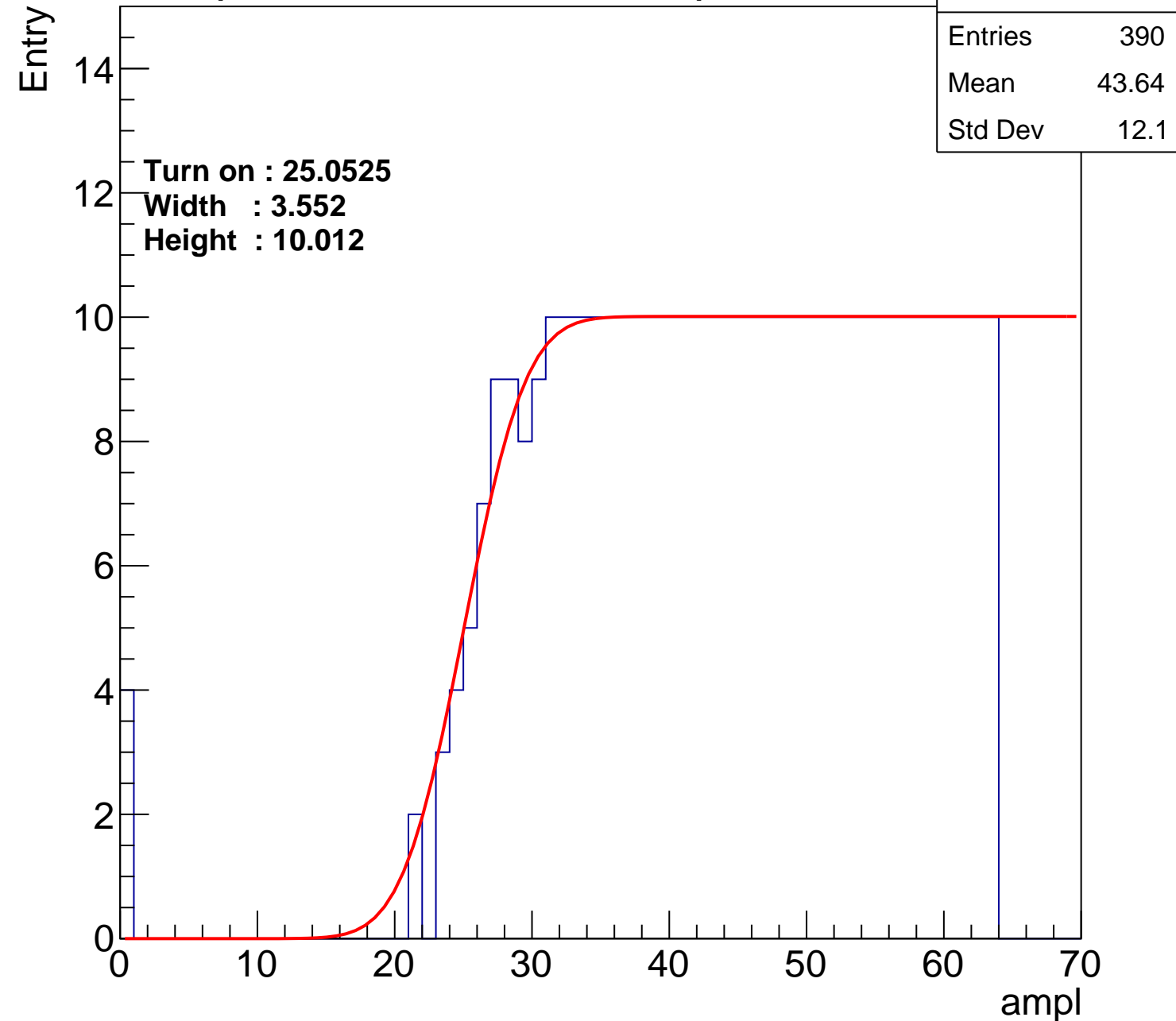
Width : 3.552

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.61
Std Dev	11.2

Turn on : 27.5869

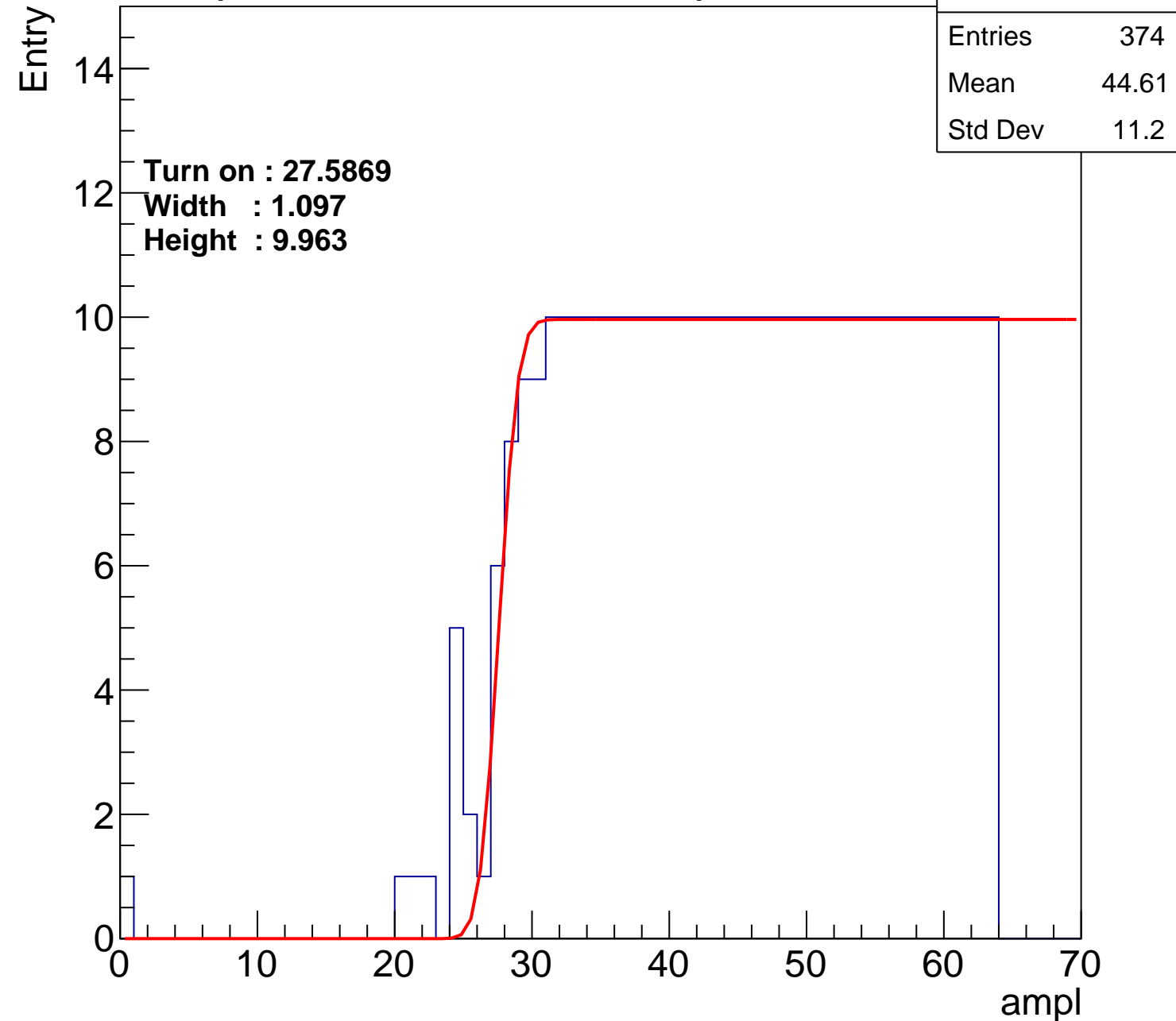
Width : 1.097

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U6-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.61
Std Dev	11.2

Turn on : 27.5869

Width : 1.097

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl

