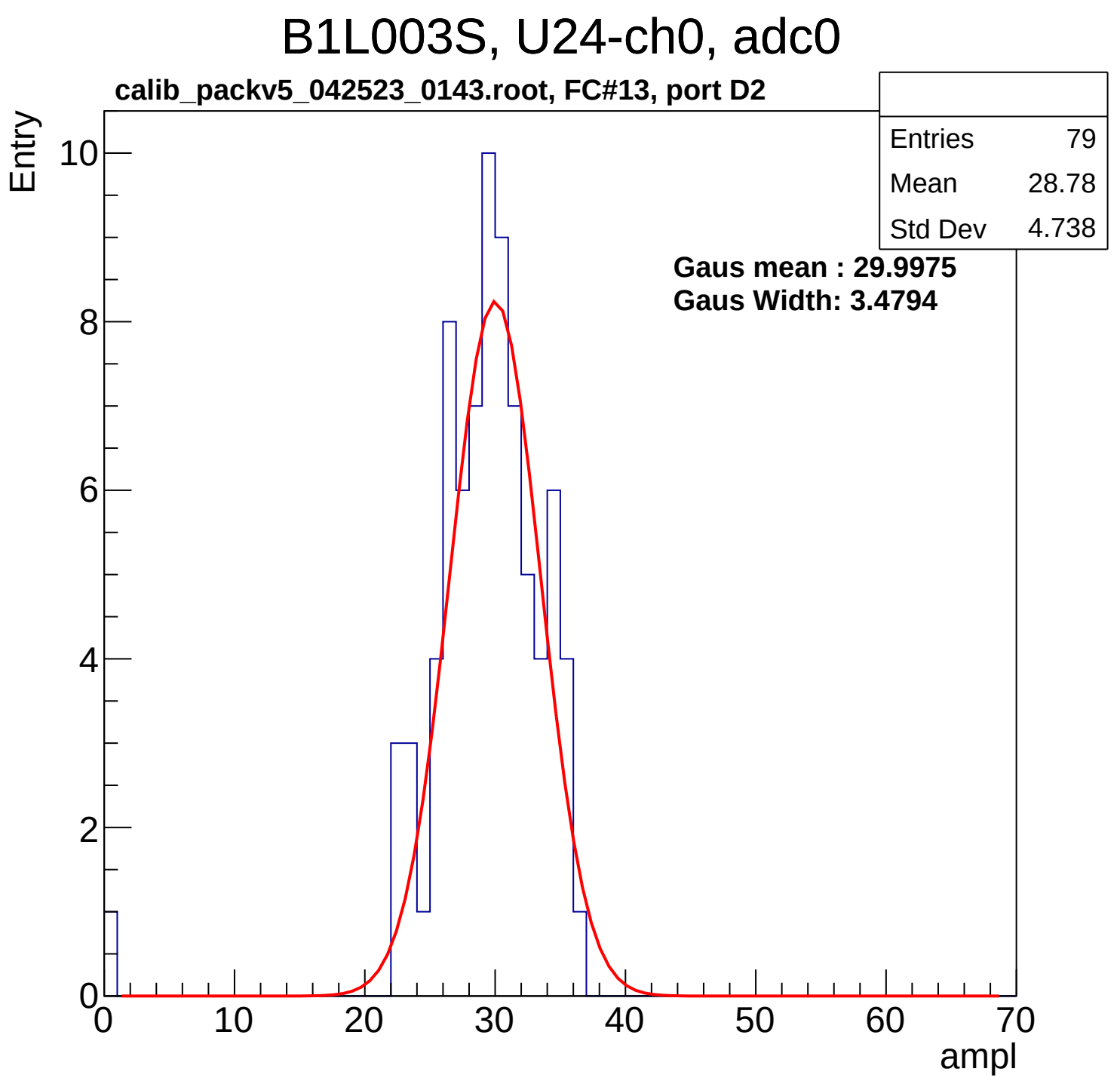
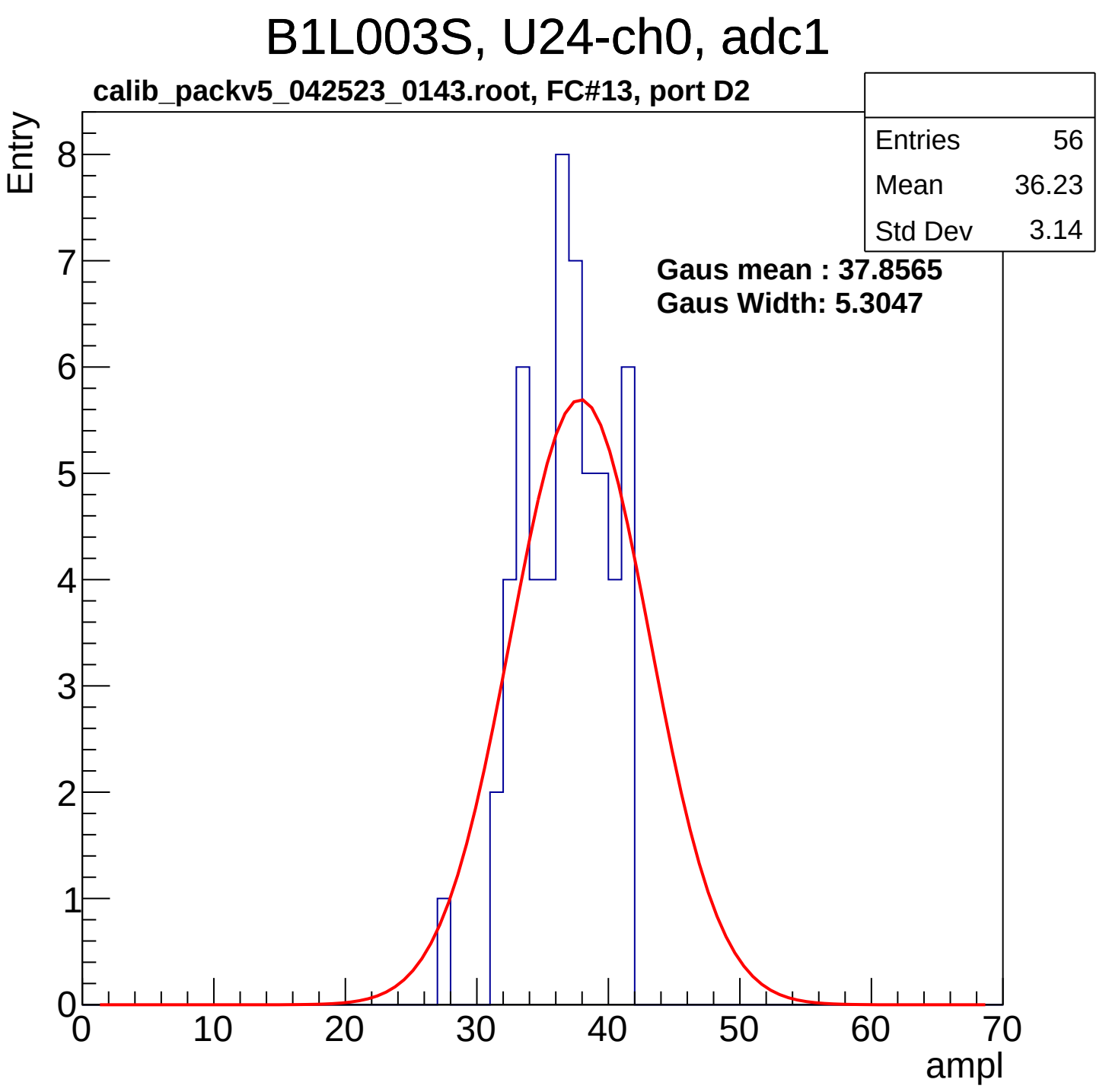

B1L003S, U24-ch0, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 28.78
Std Dev 4.738
Gaus mean : 29.9975
Gaus Width: 3.4794
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch0, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 36.23
Std Dev 3.14
Gaus mean : 37.8565
Gaus Width: 5.3047
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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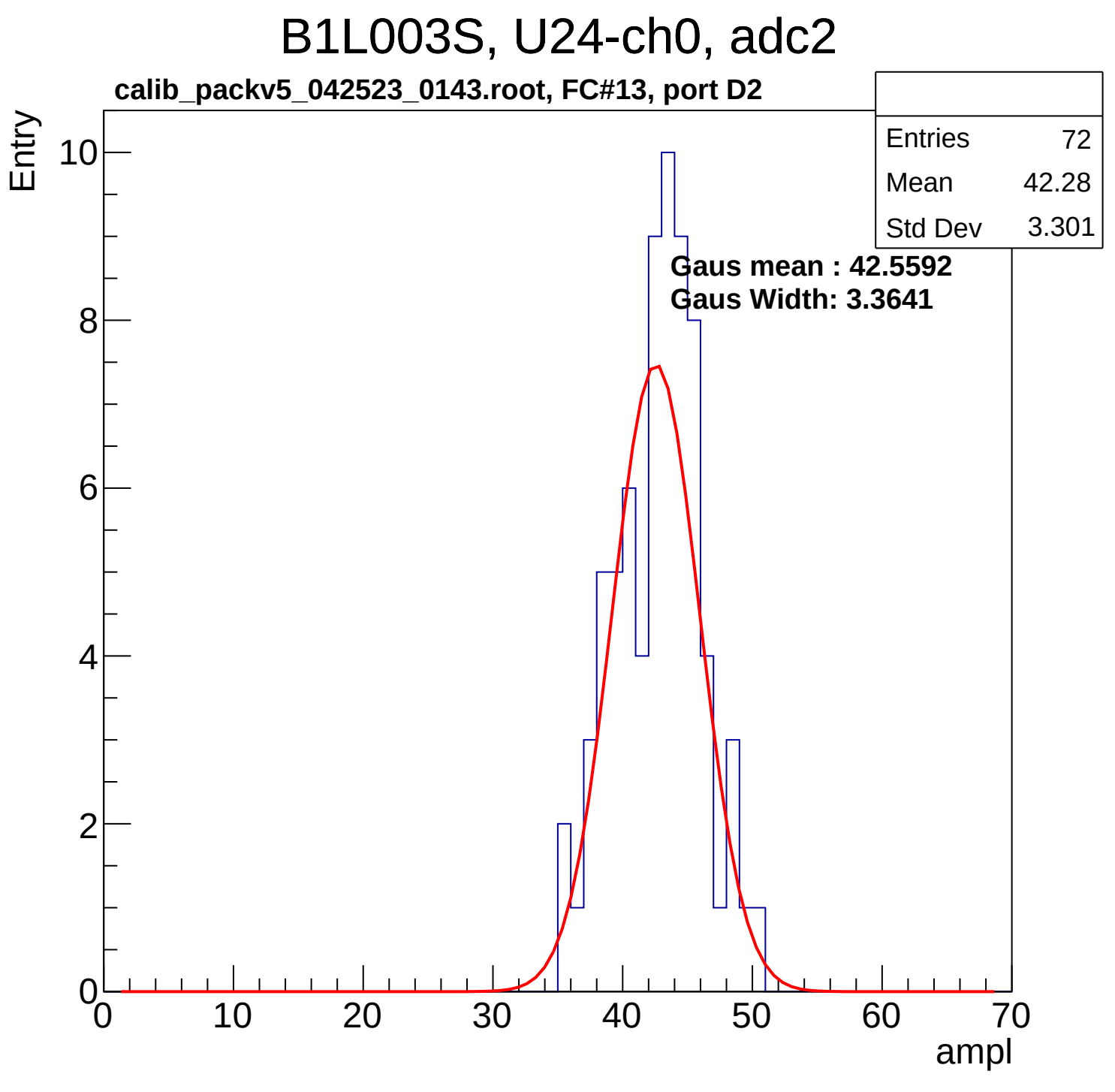
B1L003S, U24-ch0, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.28
Std Dev 3.301
Gaus mean : 42.5592
Gaus Width: 3.3641
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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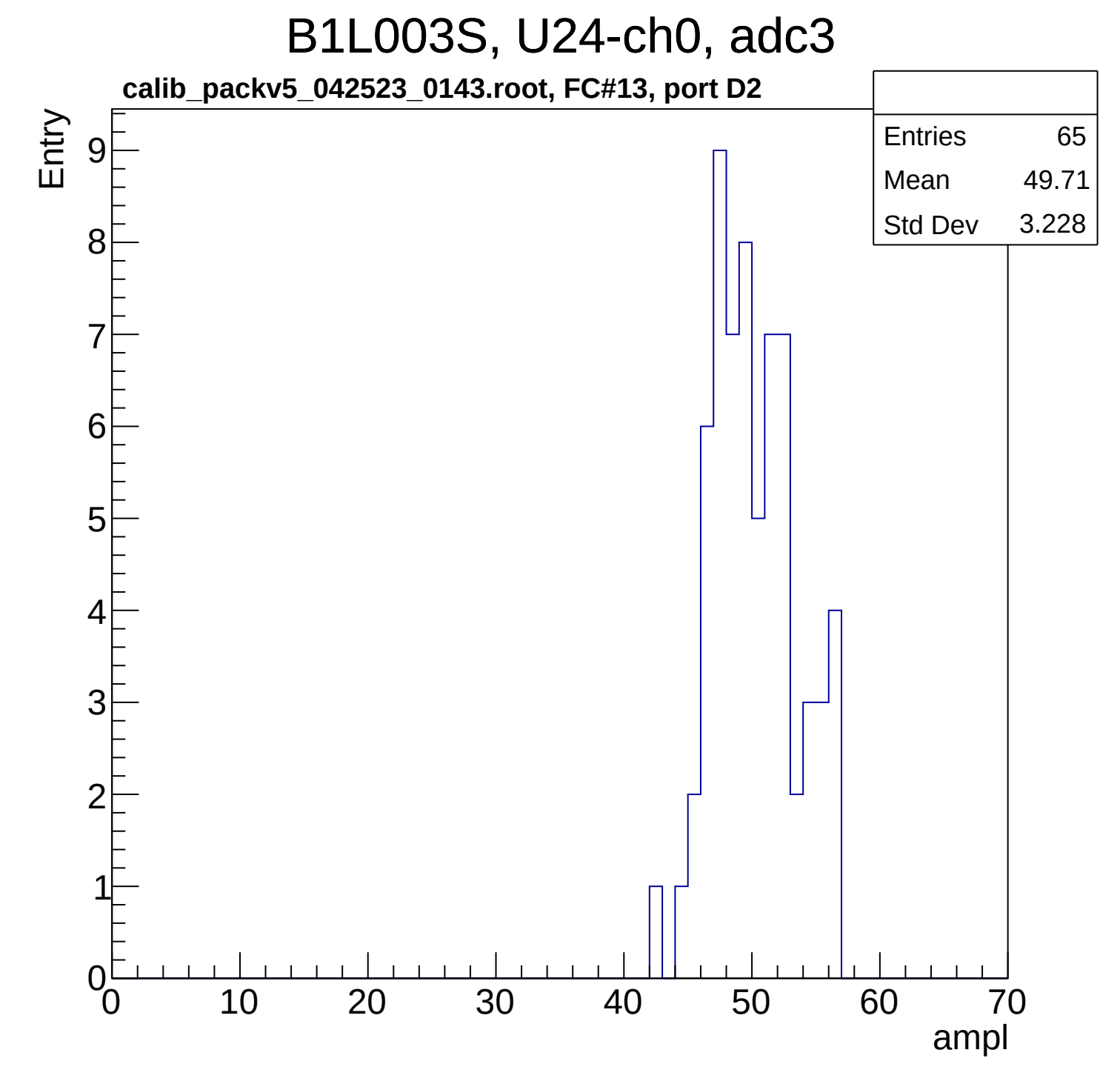
B1L003S, U24-ch0, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 49.71
Std Dev 3.228
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
50
60
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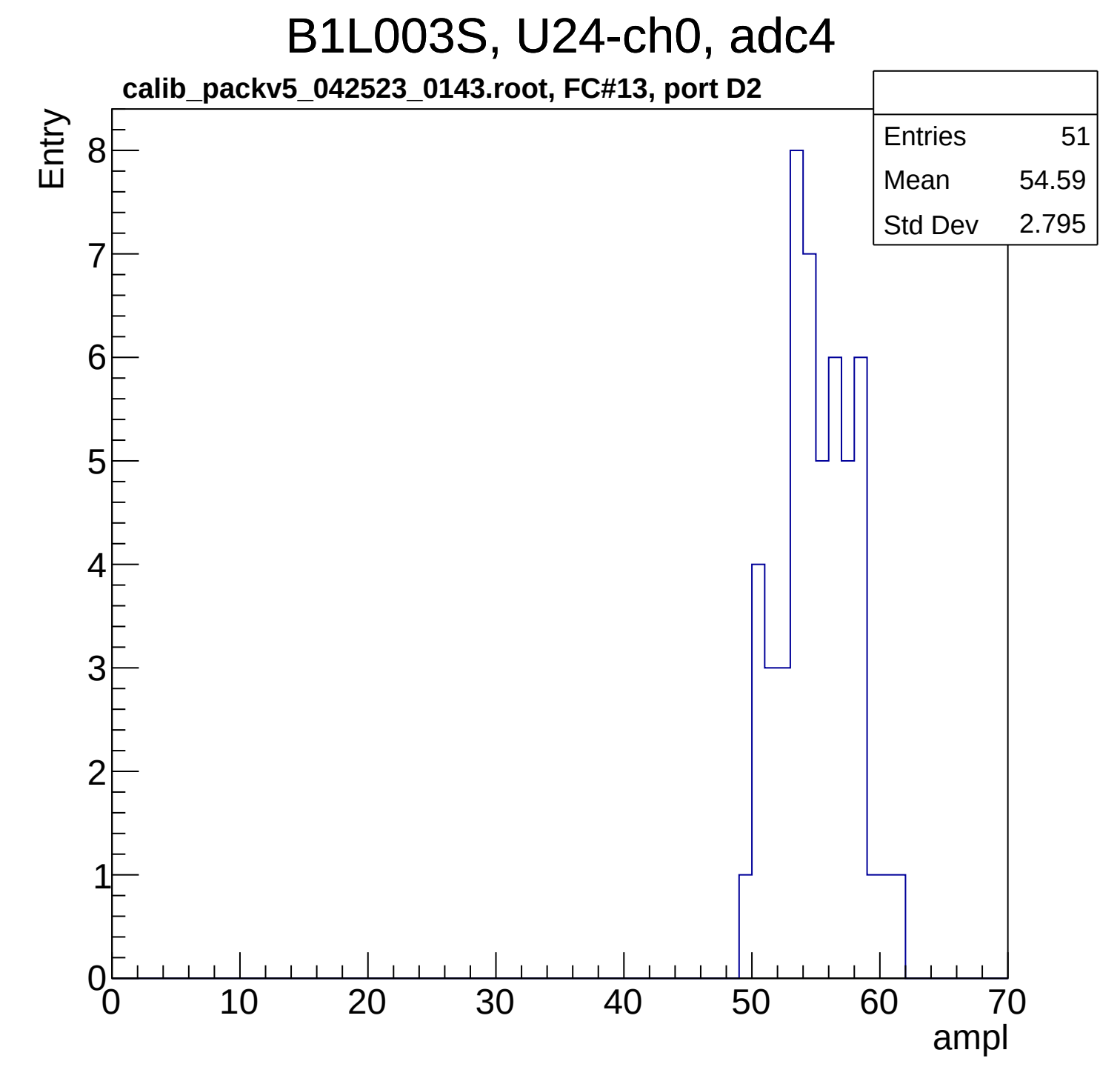
B1L003S, U24-ch0, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 54.59
Std Dev 2.795
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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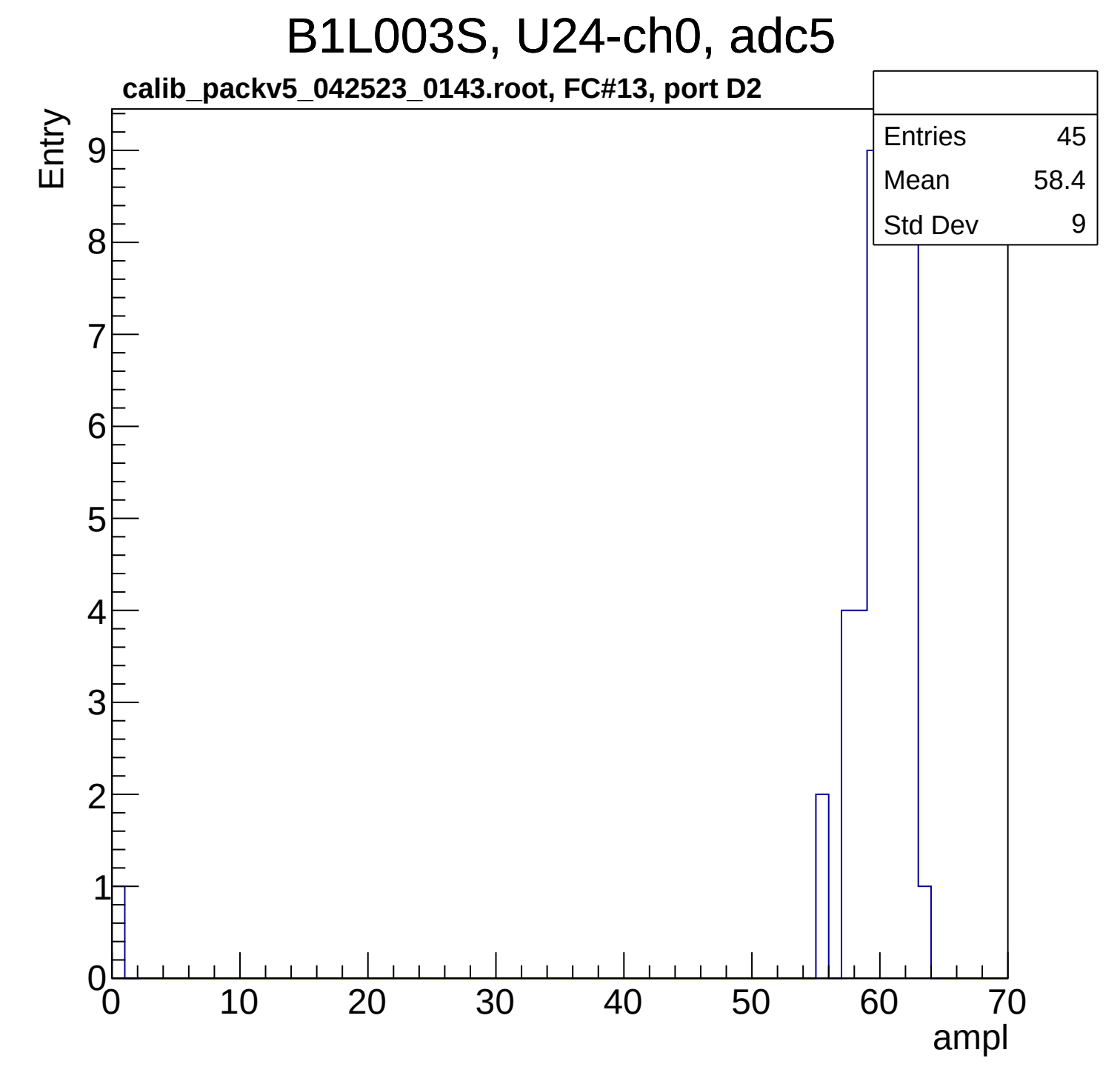
B1L003S, U24-ch0, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.4
Std Dev 9
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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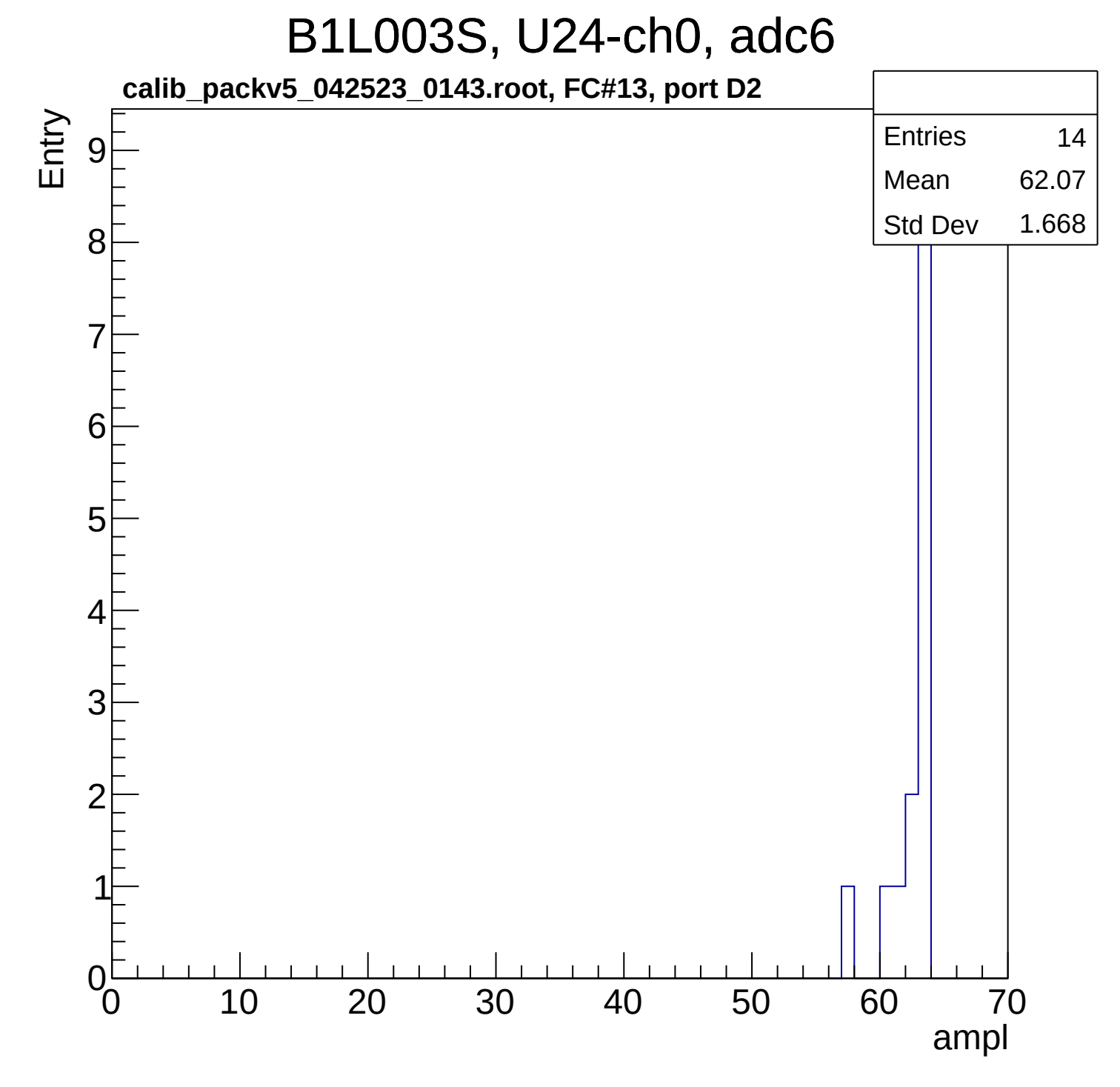
B1L003S, U24-ch0, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 62.07
Std Dev 1.668
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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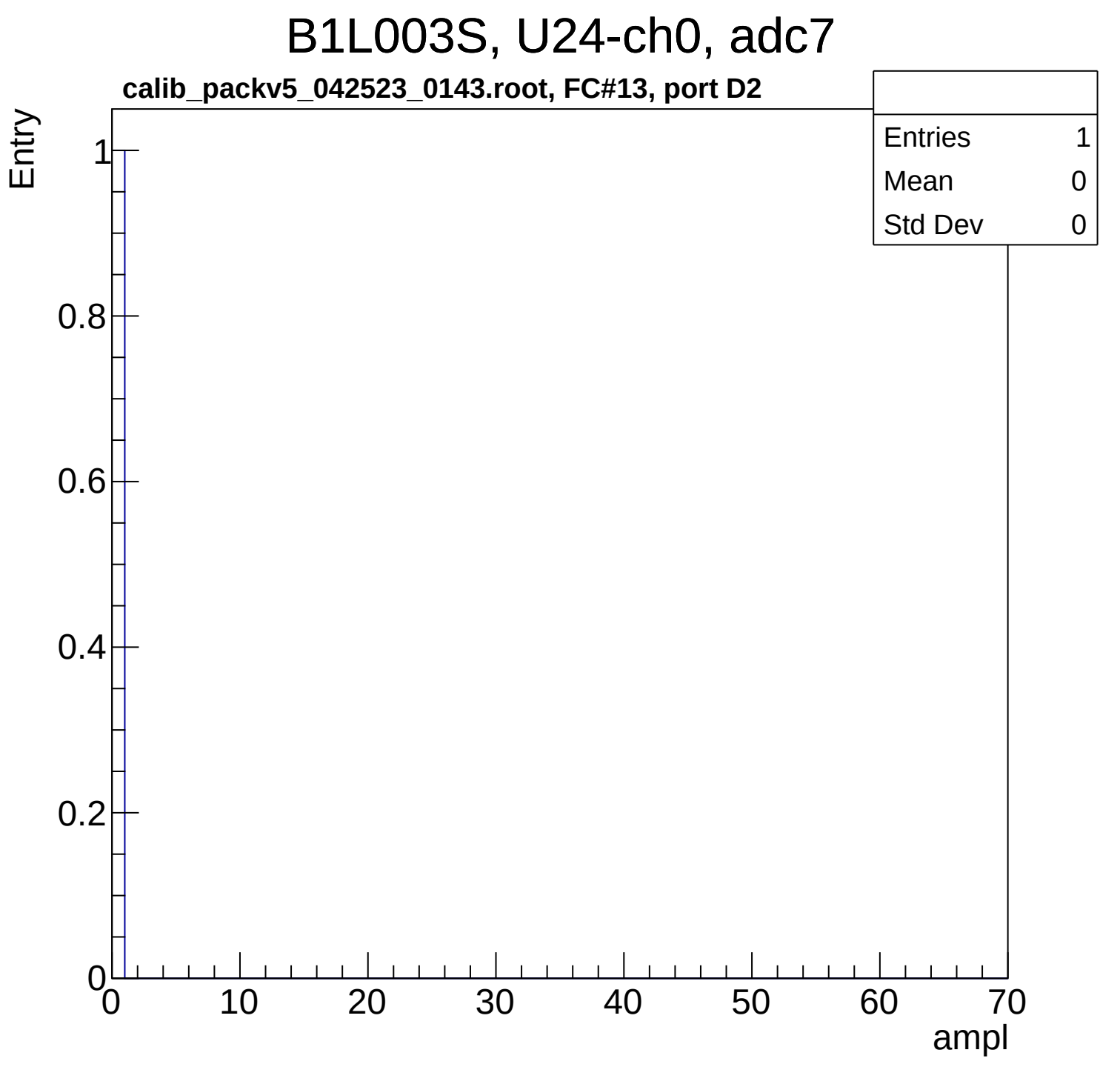
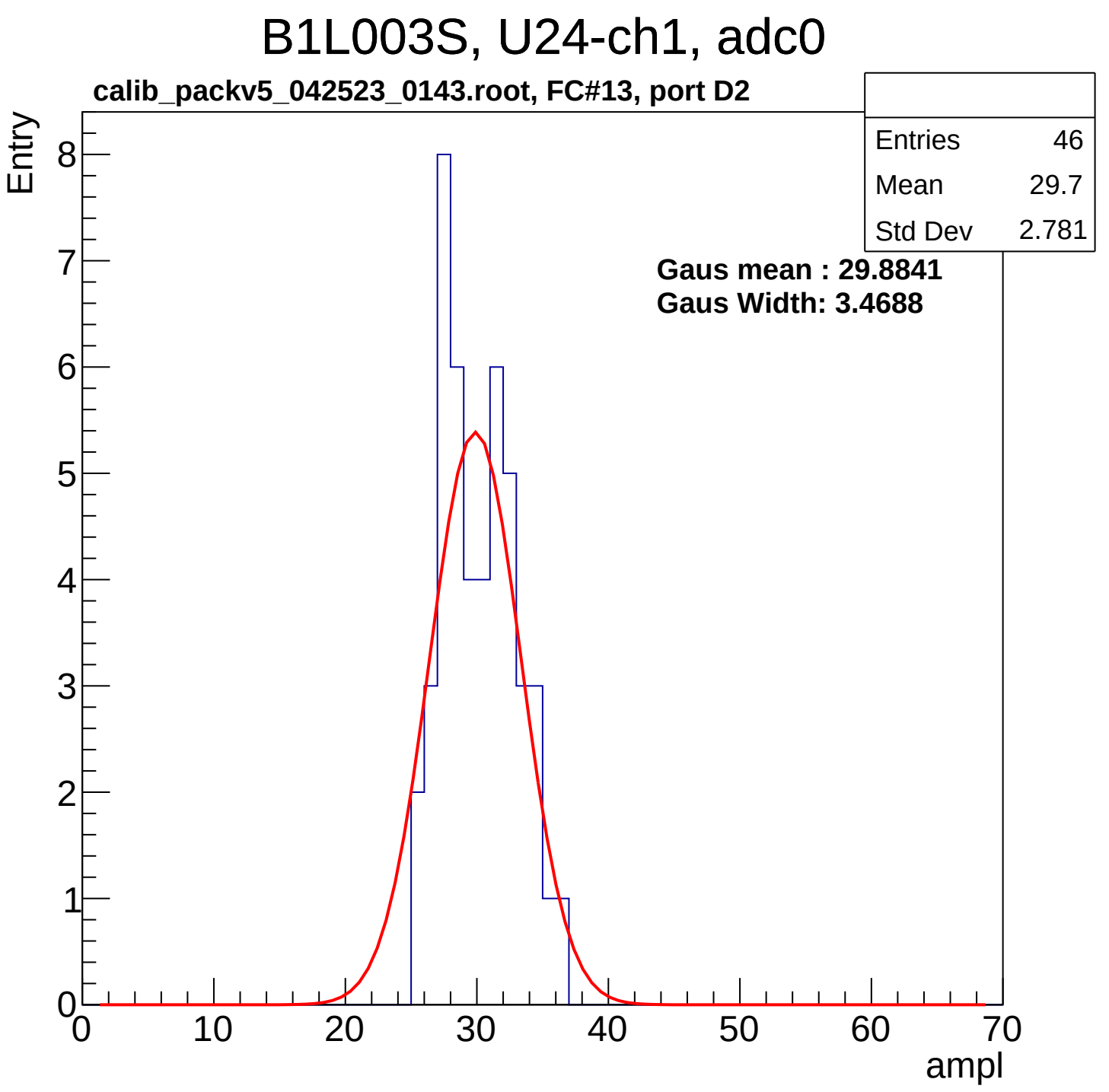

B1L003S, U24-ch0, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch1, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 29.7
Std Dev 2.781
Gaus mean : 29.8841
Gaus Width: 3.4688
Entry
ampl
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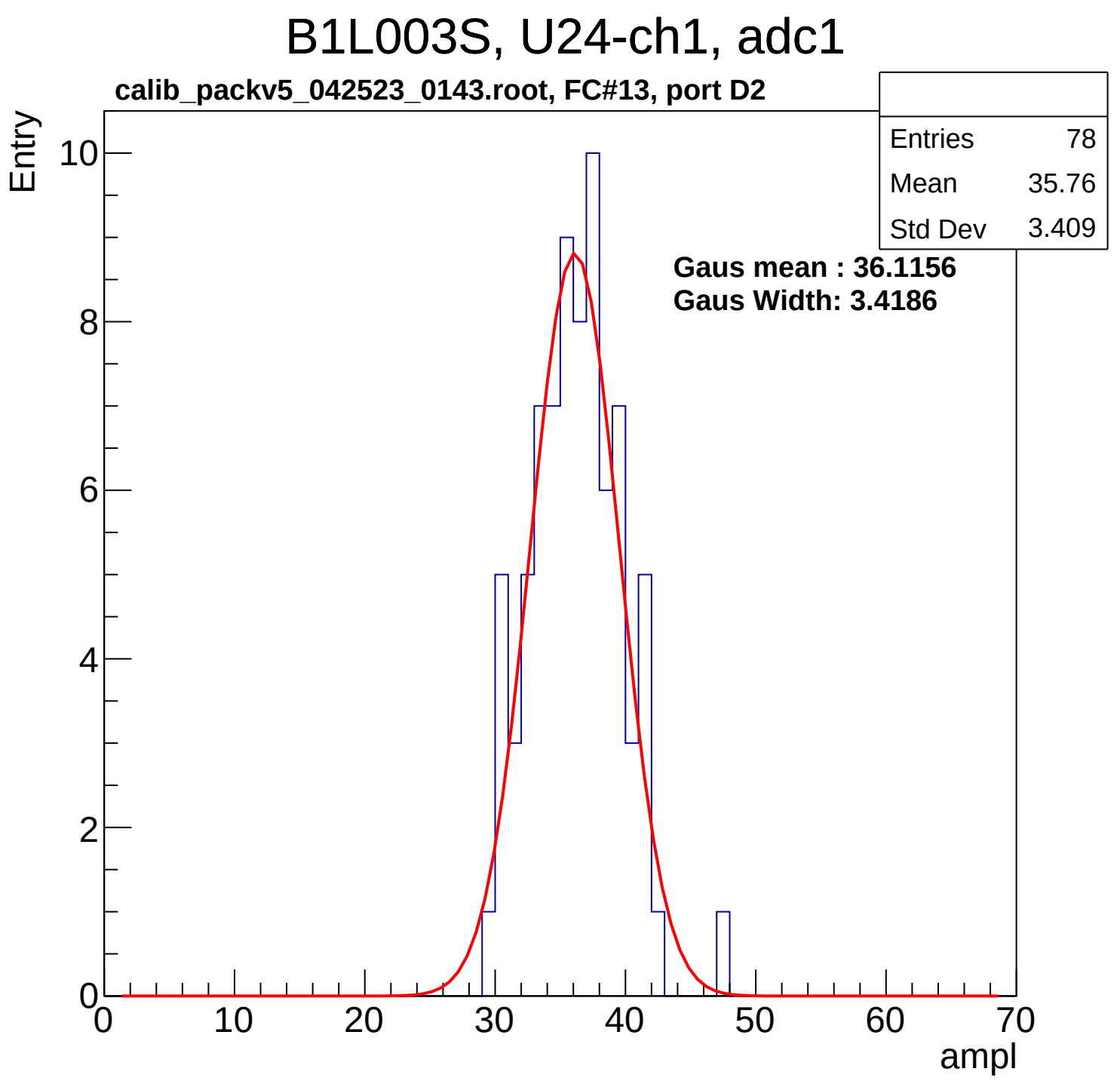
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch1, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 35.76
Std Dev 3.409
Gaus mean : 36.1156
Gaus Width: 3.4186
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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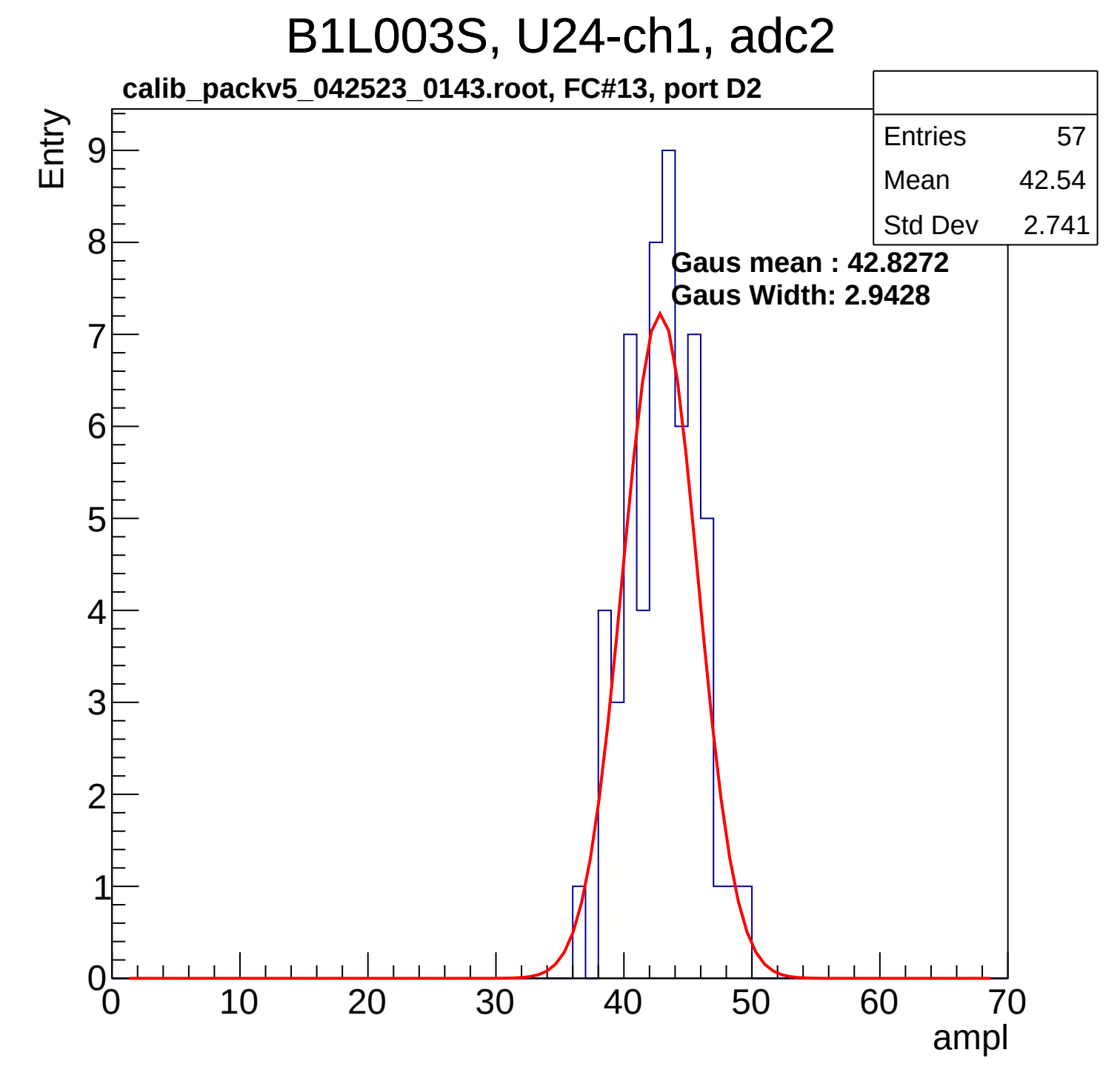
B1L003S, U24-ch1, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 42.54
Std Dev 2.741
Gaus mean : 42.8272
Gaus Width: 2.9428
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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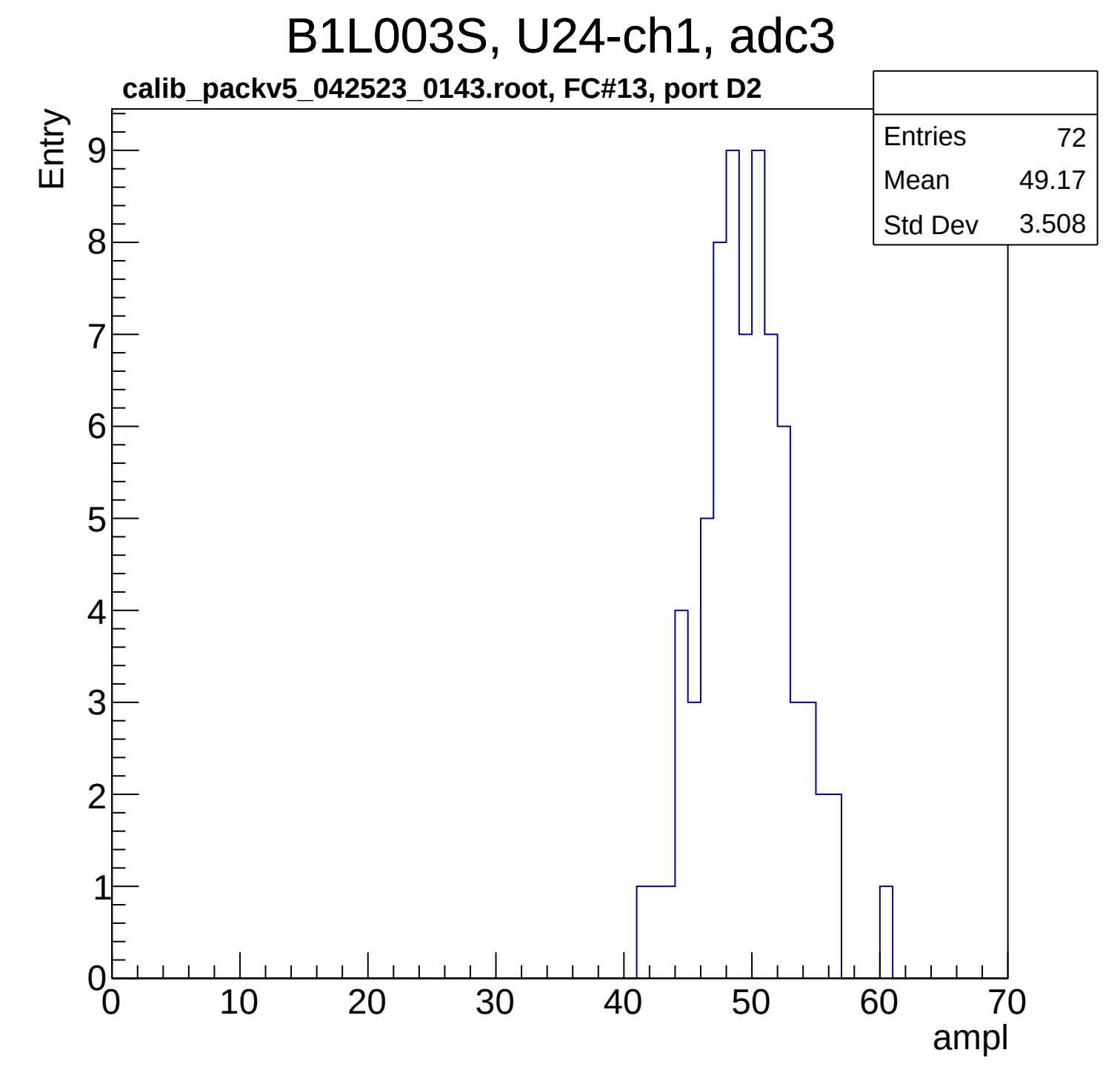
B1L003S, U24-ch1, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.17
Std Dev 3.508
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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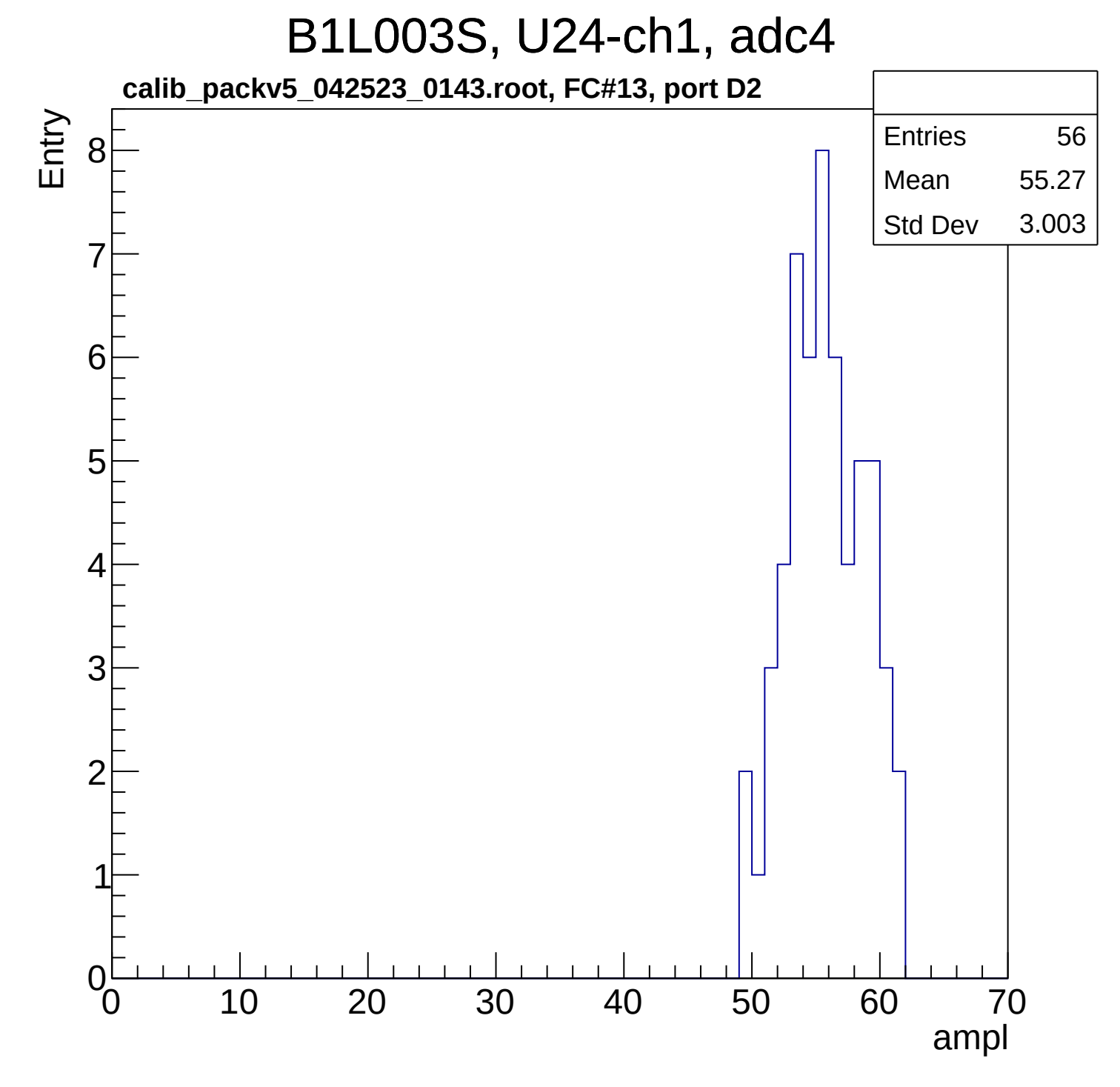
B1L003S, U24-ch1, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.27
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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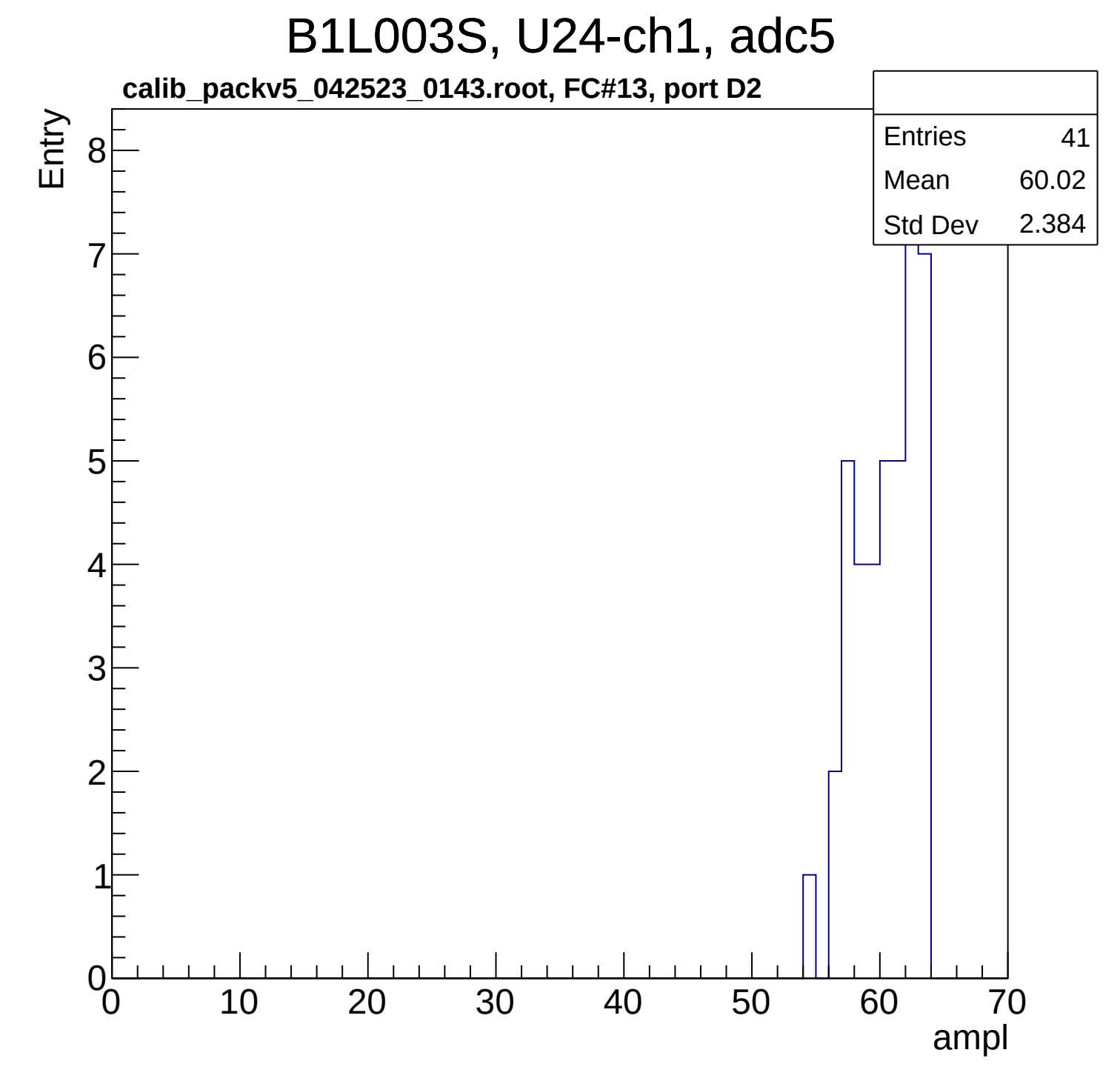
B1L003S, U24-ch1, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 60.02
Std Dev 2.384
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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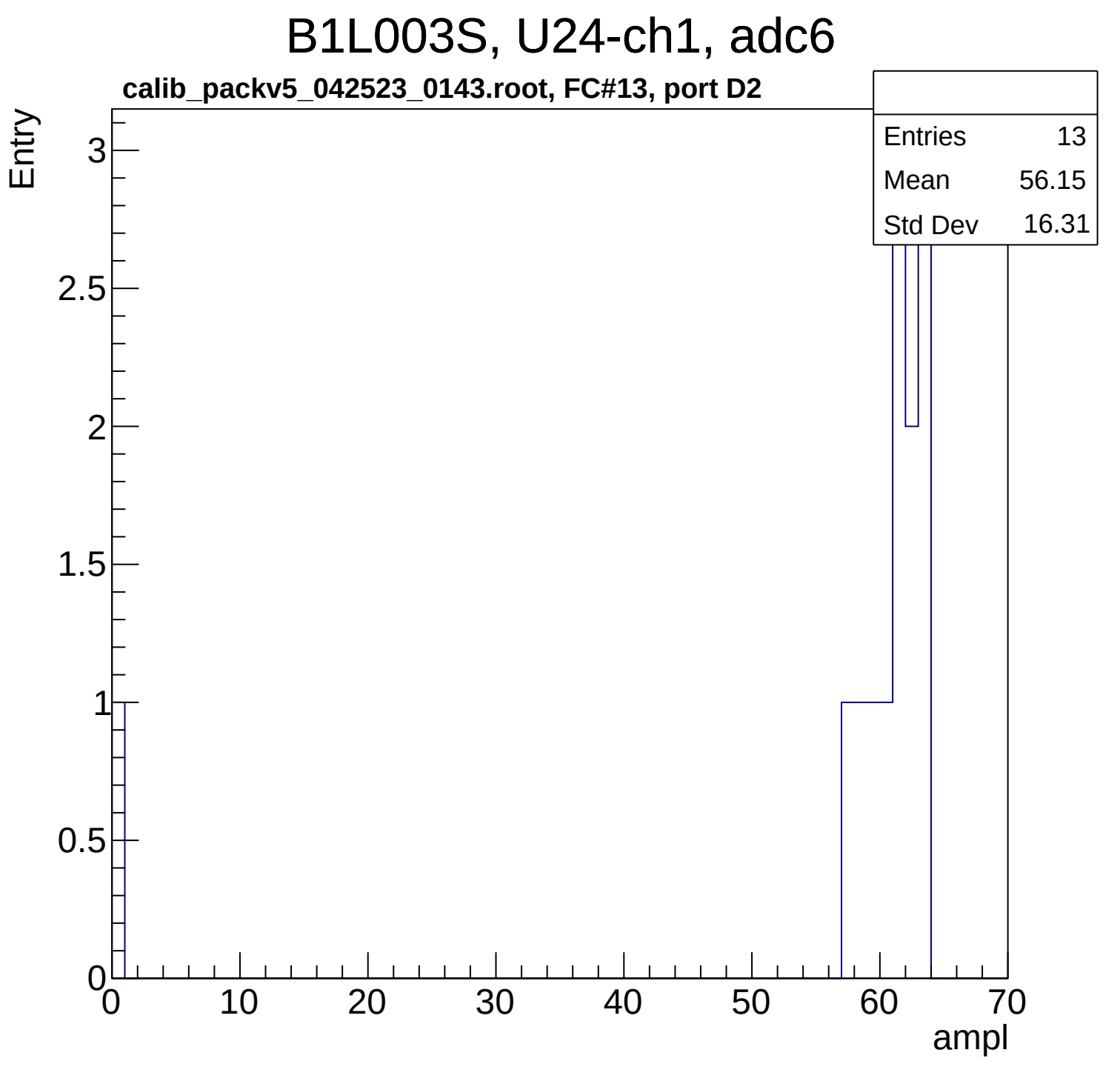
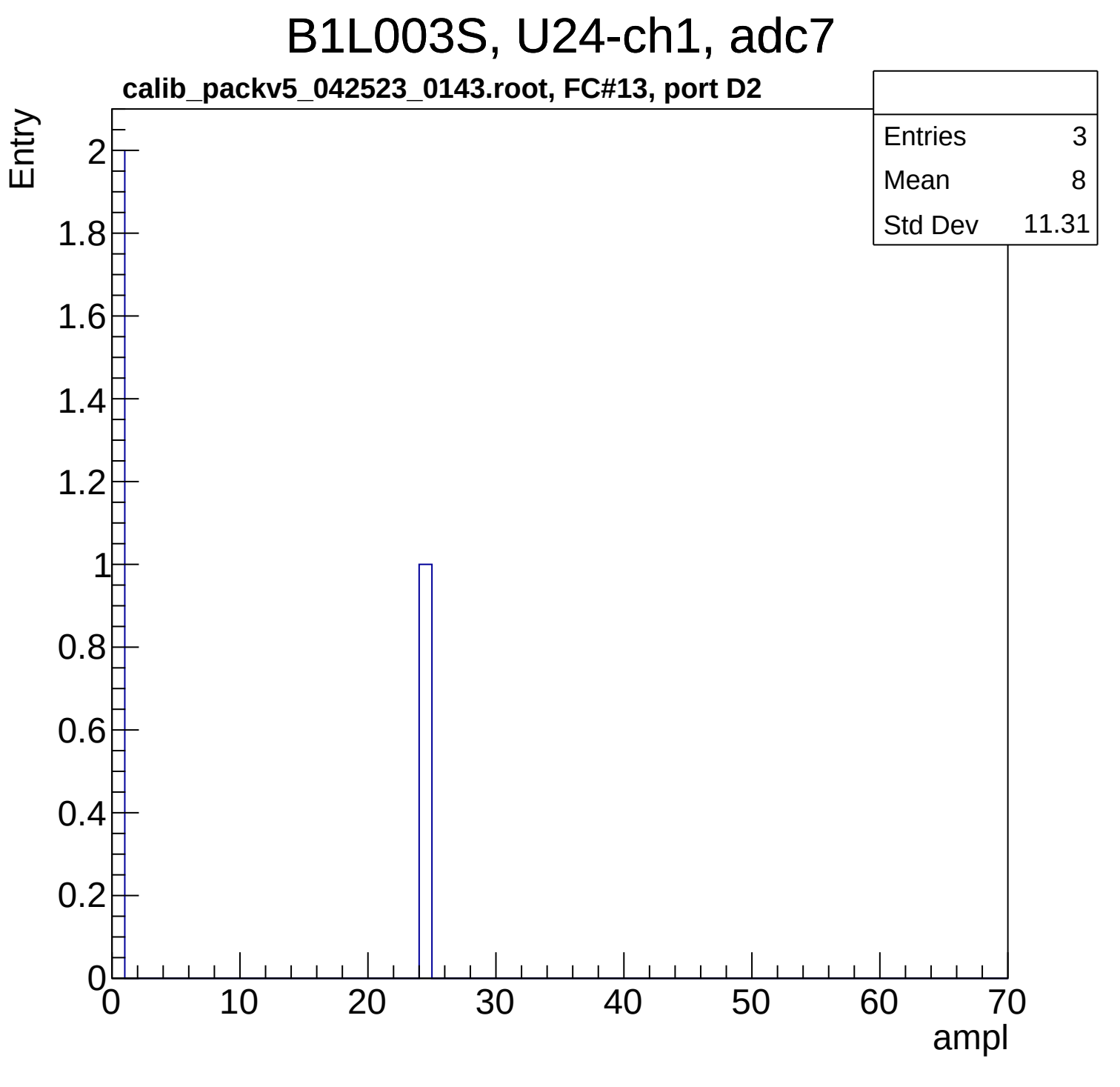

B1L003S, U24-ch1, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.15
Std Dev 16.31
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch1, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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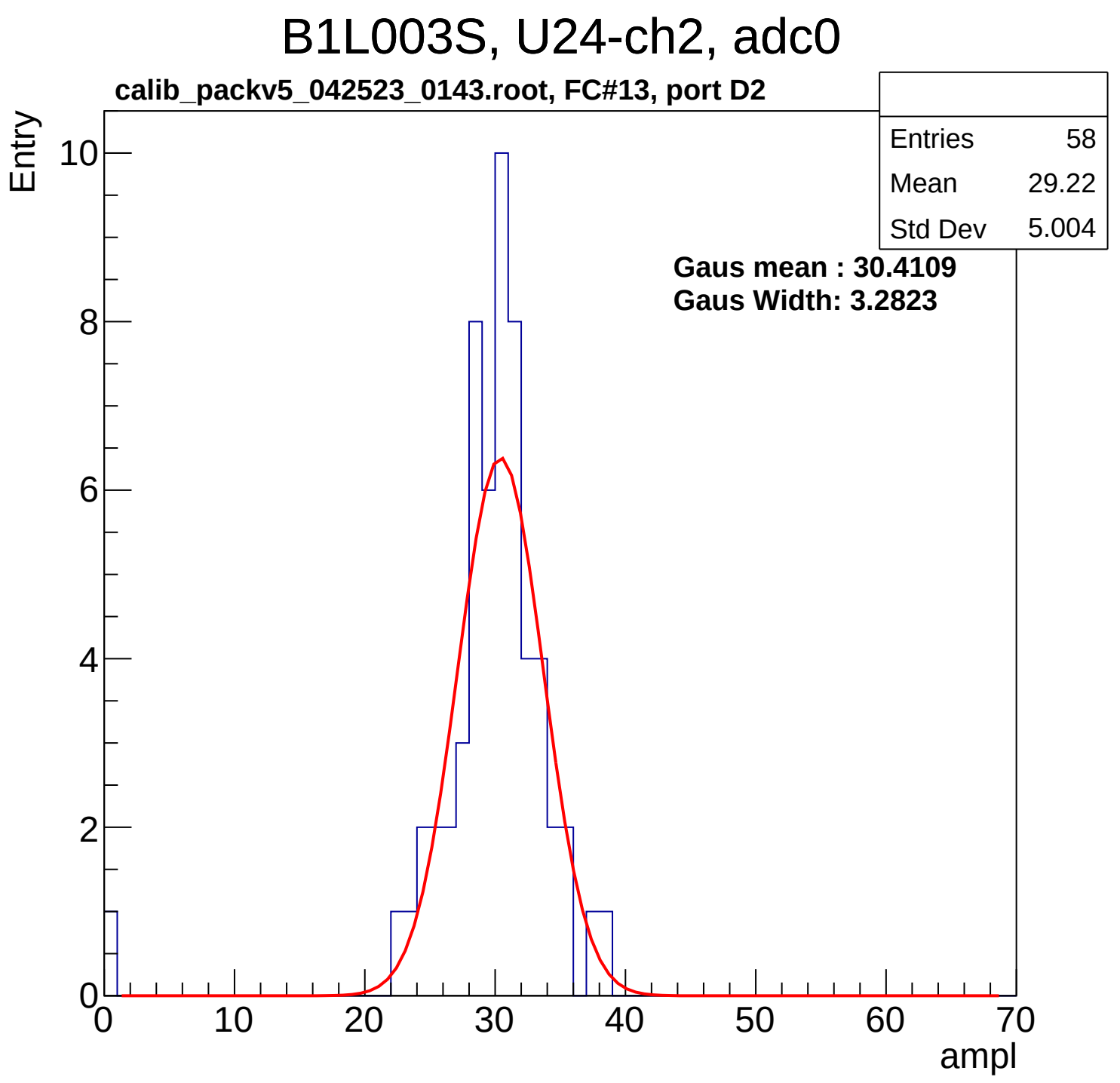
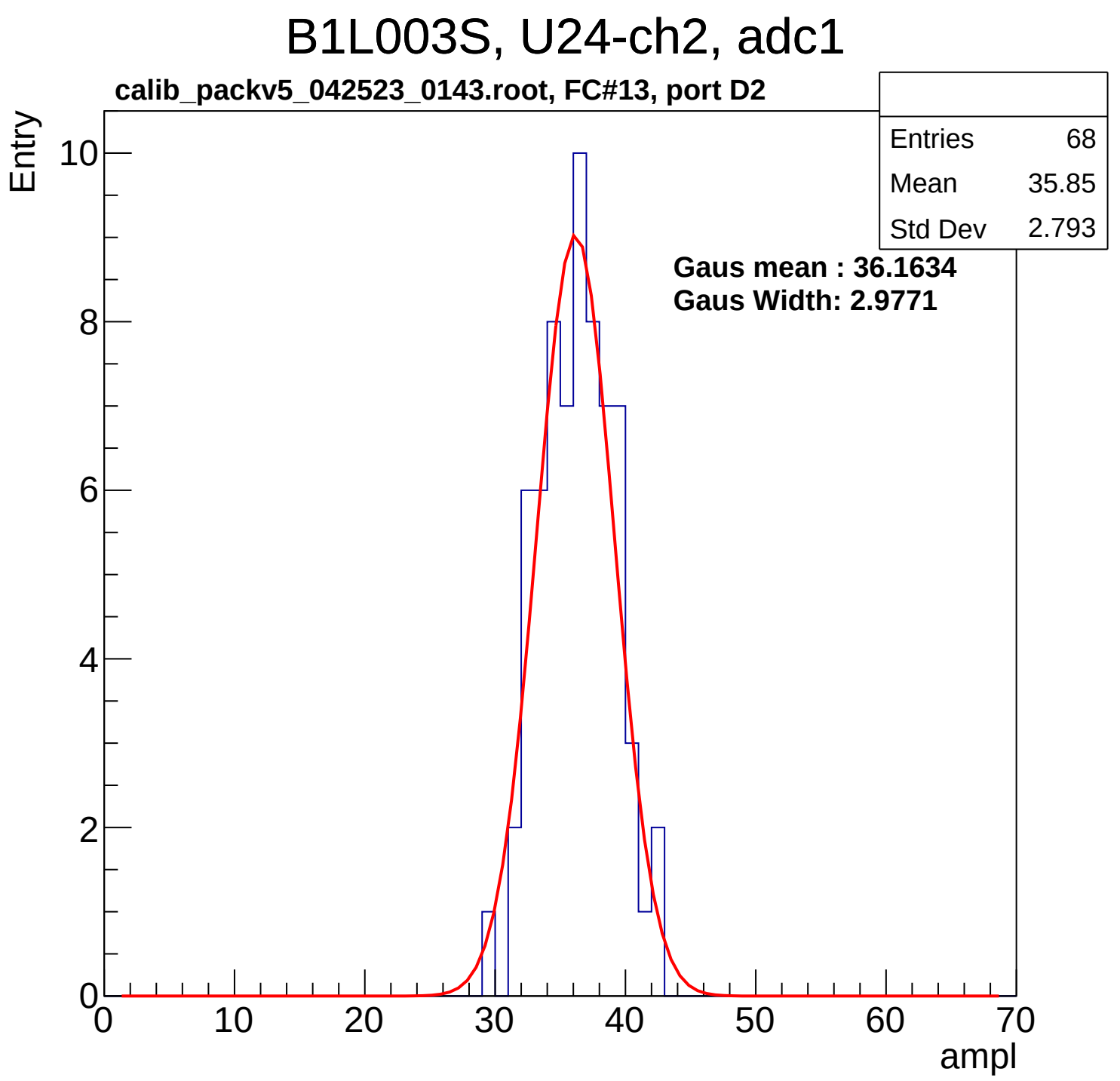
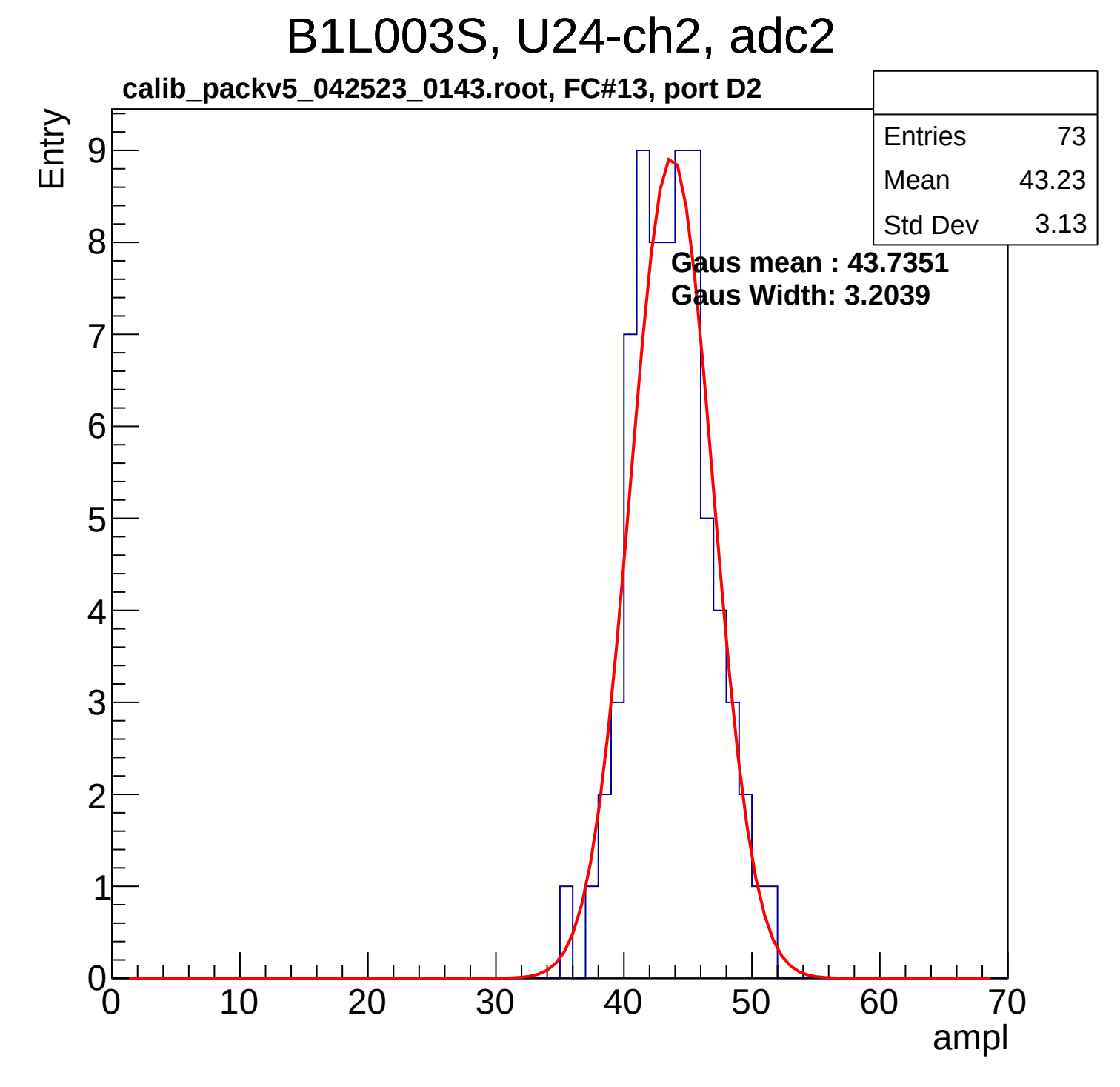

B1L003S, U24-ch2, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 29.22
Std Dev 5.004
Gaus mean : 30.4109
Gaus Width: 3.2823
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch2, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.85
Std Dev 2.793
Gaus mean : 36.1634
Gaus Width: 2.9771
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch2, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 43.23
Std Dev 3.13
Gaus mean : 43.7351
Gaus Width: 3.2039
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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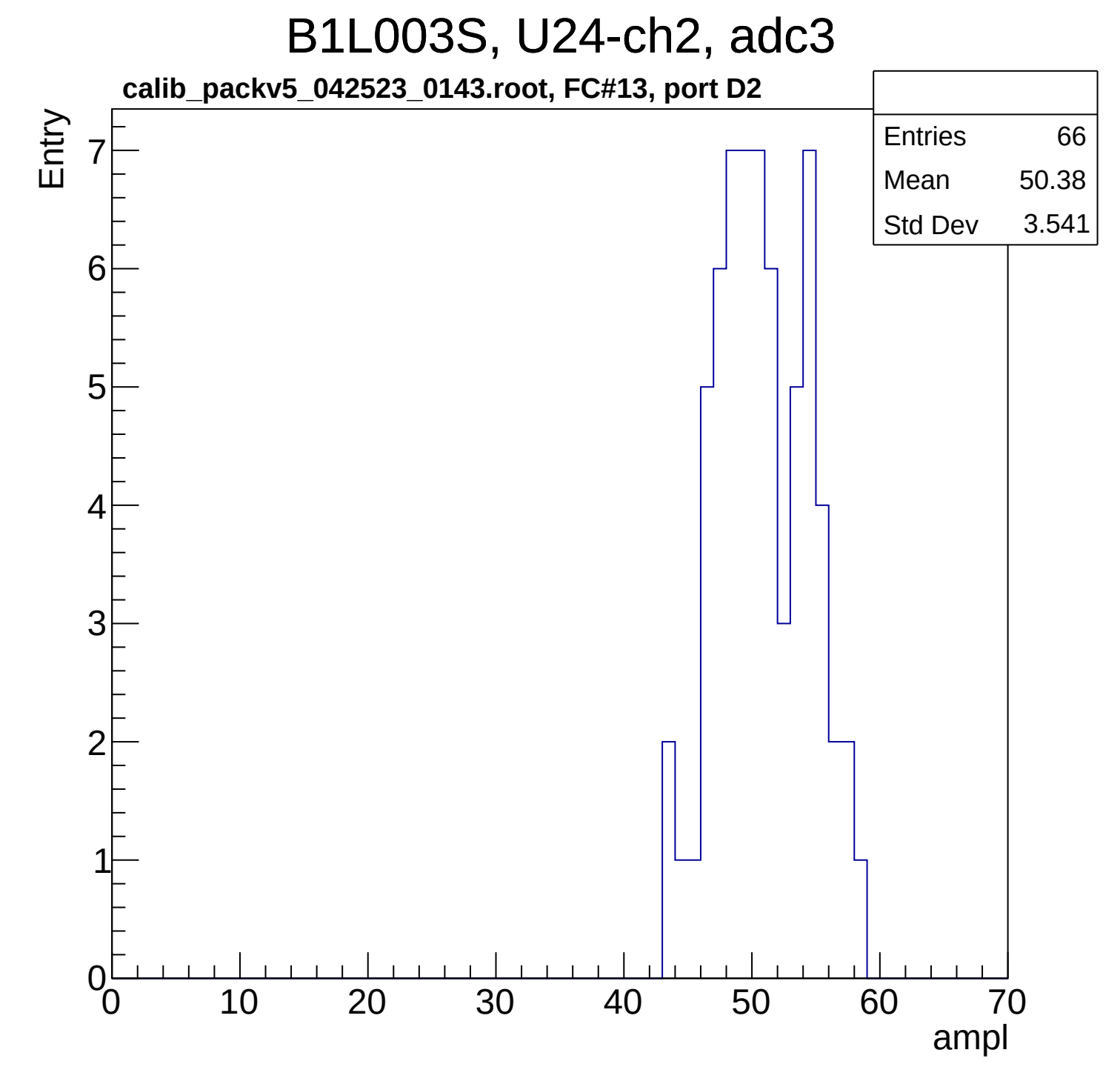
B1L003S, U24-ch2, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 50.38
Std Dev 3.541
Entry
ampl
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1
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4
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6
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0
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40
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60
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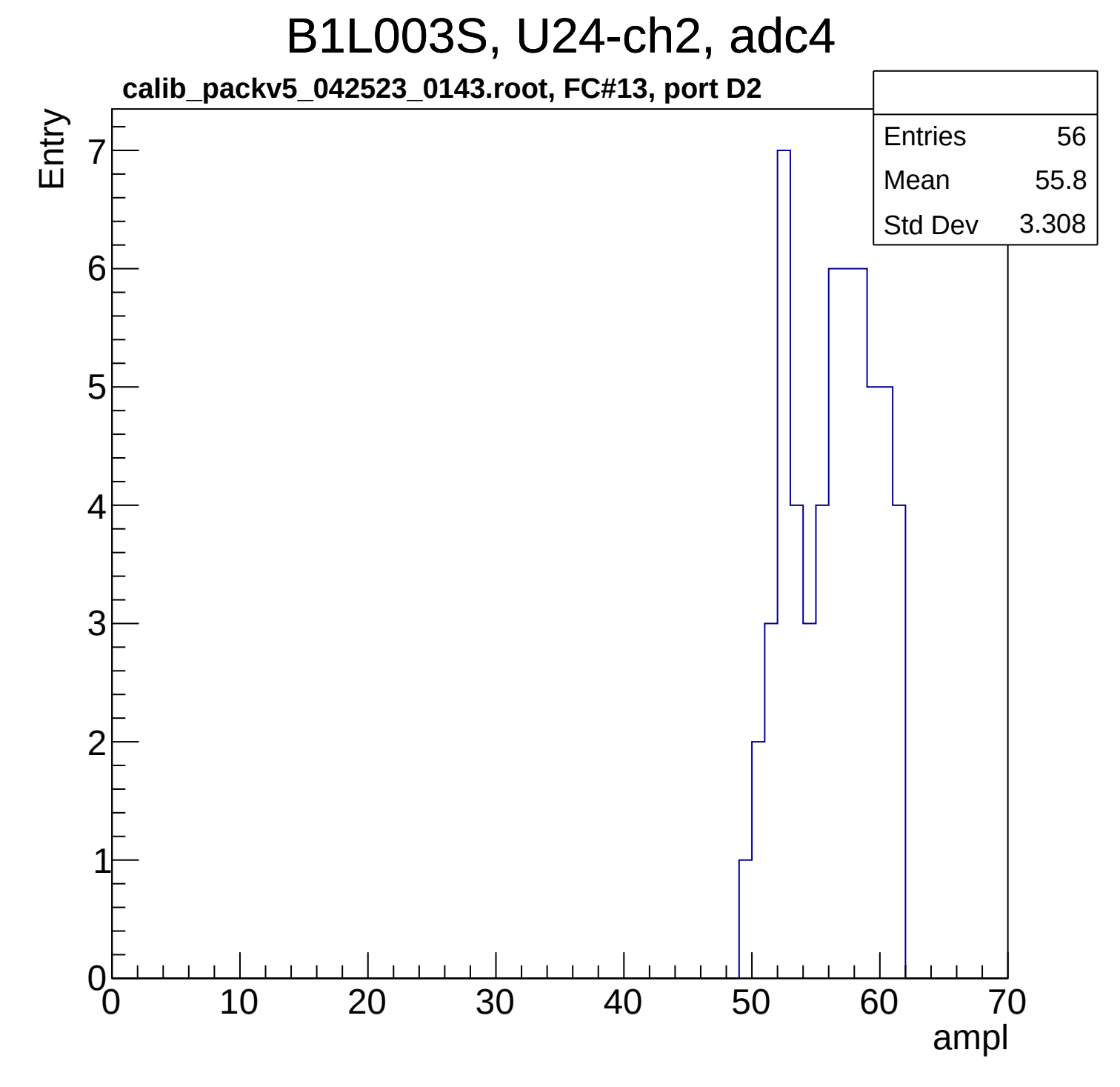
B1L003S, U24-ch2, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.8
Std Dev 3.308
Entry
ampl
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1
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3
4
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6
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0
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20
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50
60
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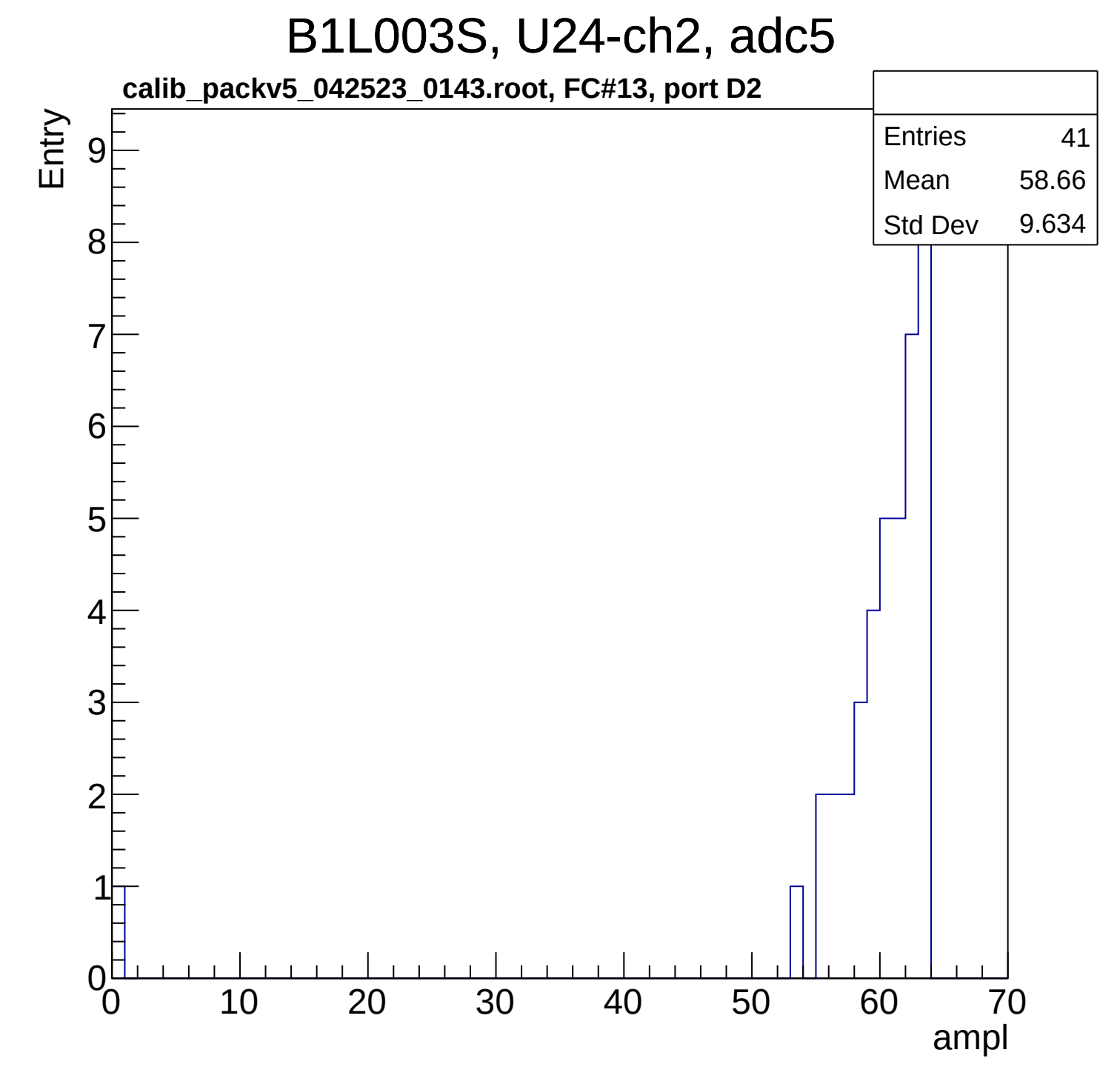
B1L003S, U24-ch2, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.66
Std Dev 9.634
Entry
ampl
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3
4
5
6
7
8
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0
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60
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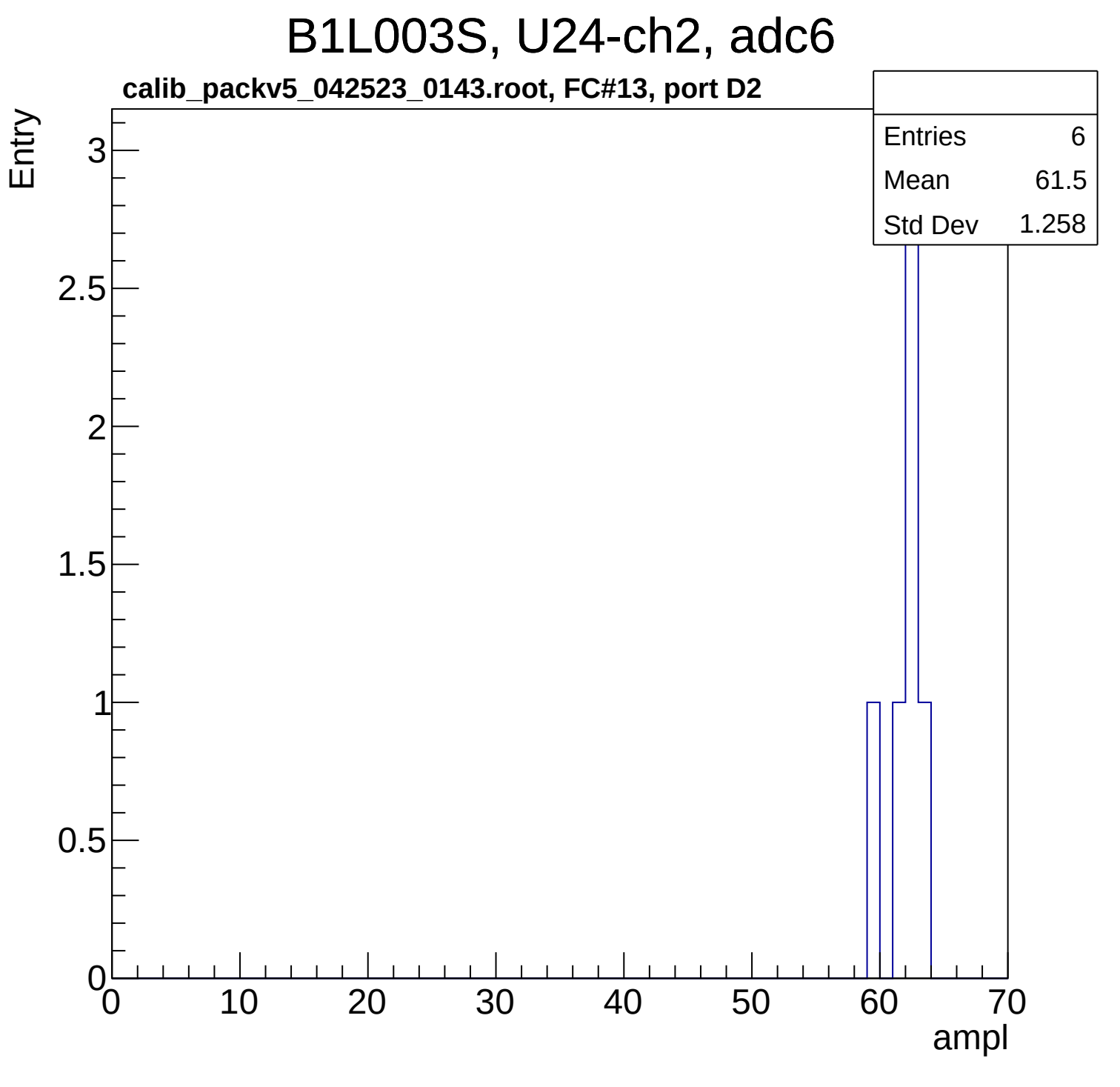
B1L003S, U24-ch2, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.5
Std Dev 1.258
Entry
ampl
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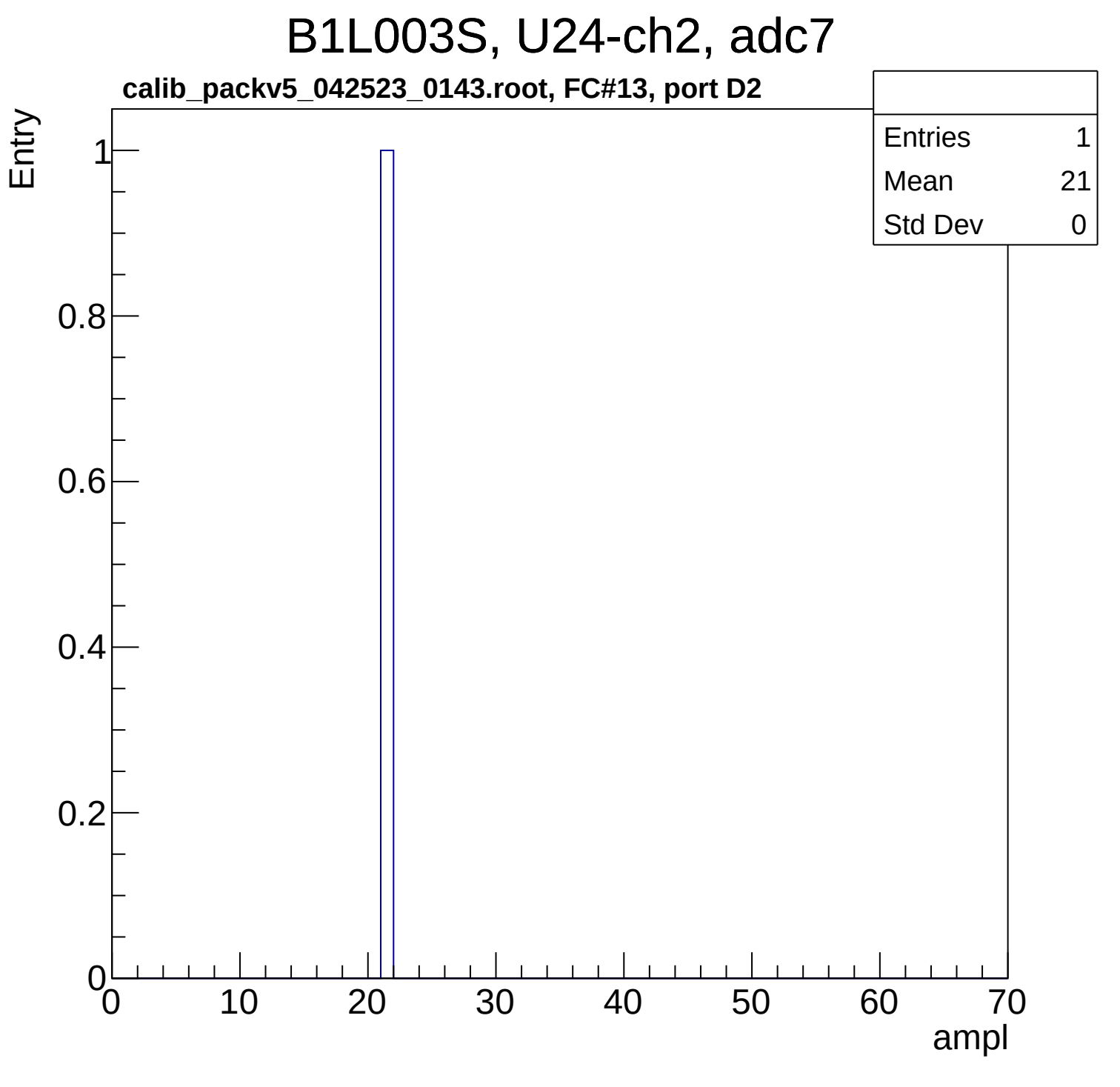
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch2, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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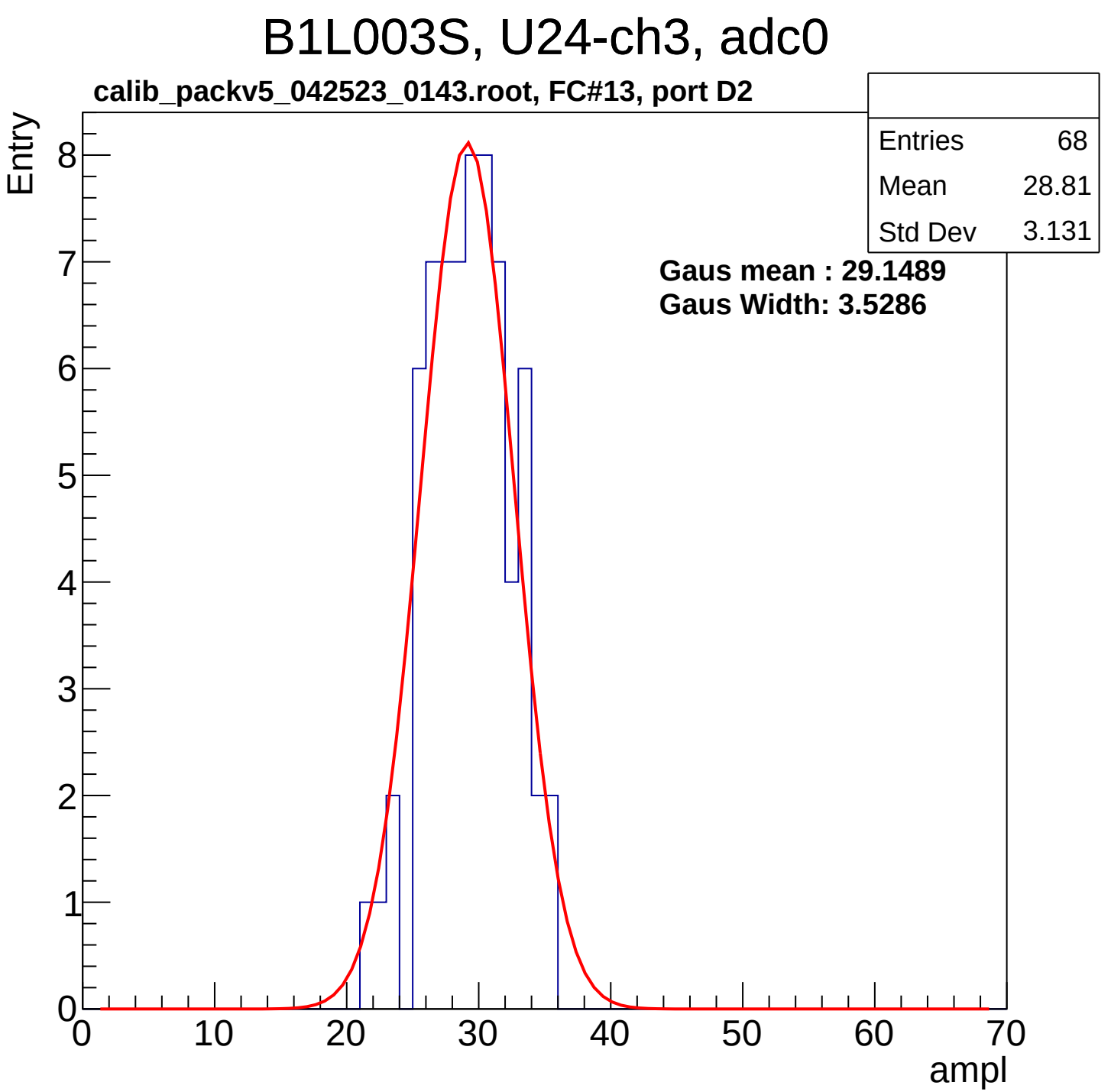
50
60
70
ampl


B1L003S, U24-ch3, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.81
Std Dev 3.131
Gaus mean : 29.1489
Gaus Width: 3.5286
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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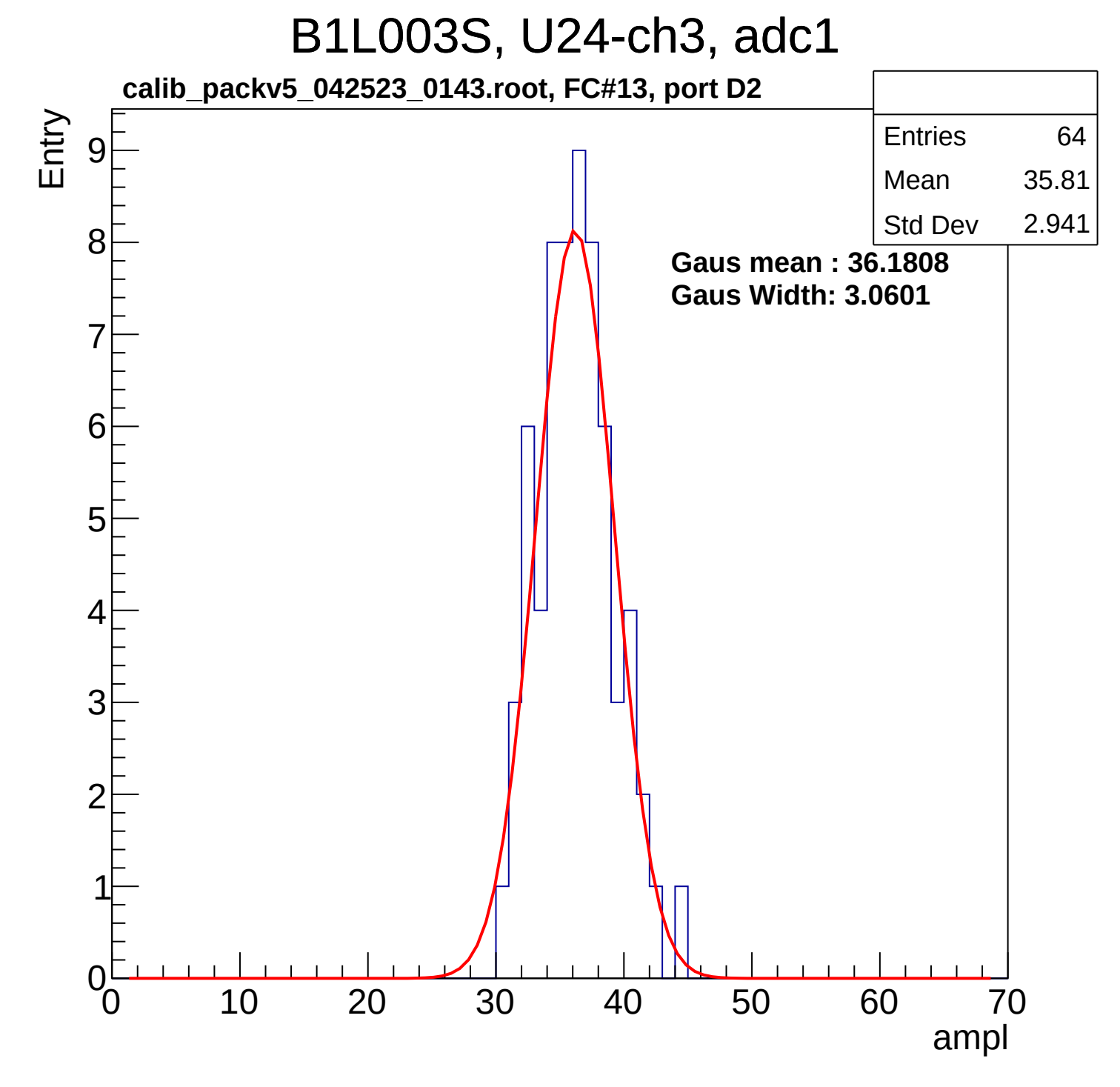
B1L003S, U24-ch3, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 35.81
Std Dev 2.941
Gaus mean : 36.1808
Gaus Width: 3.0601
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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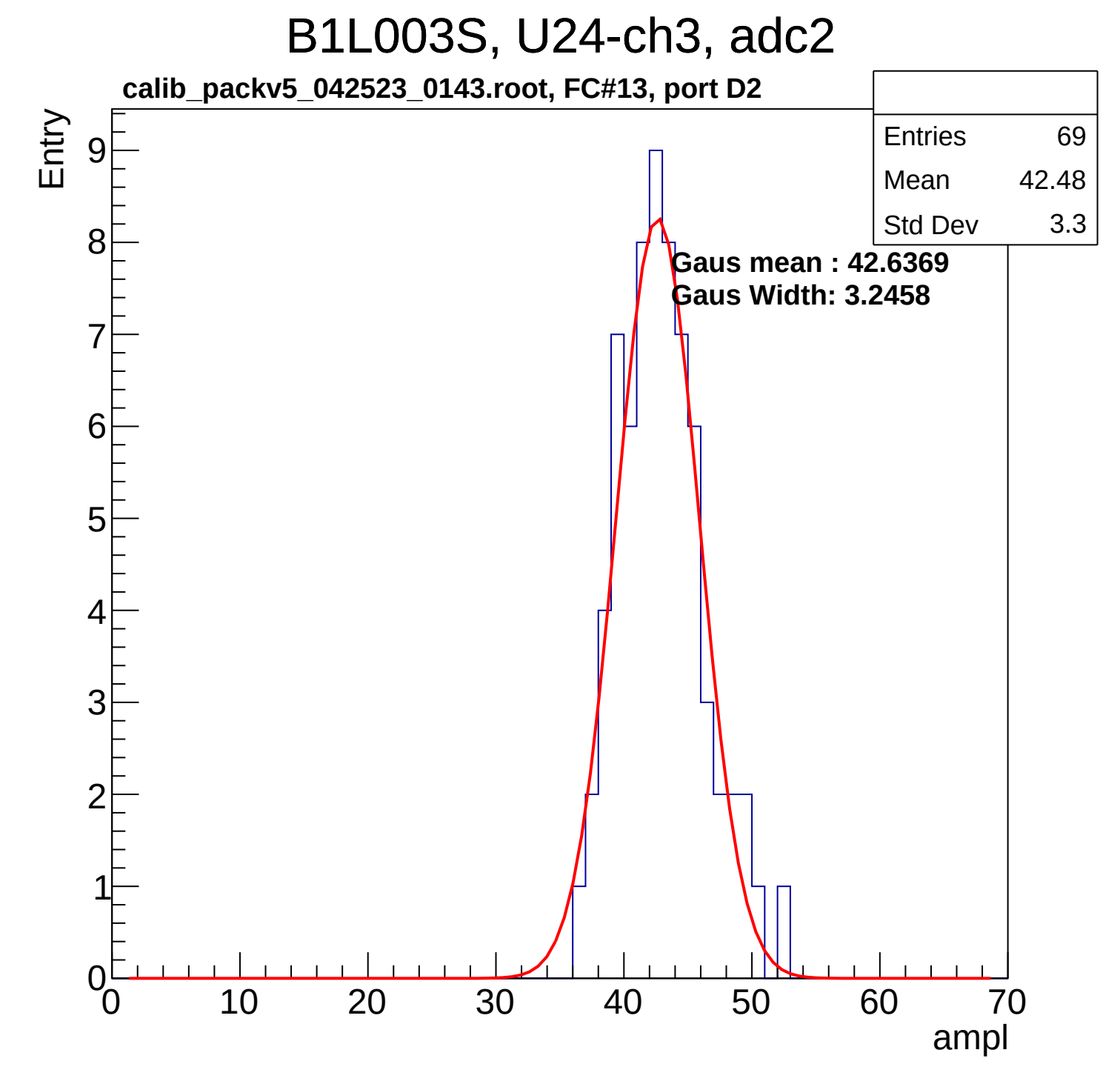
B1L003S, U24-ch3, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 42.48
Std Dev 3.3
Gaus mean : 42.6369
Gaus Width: 3.2458
Entry
ampl
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2
3
4
5
6
7
8
9
0
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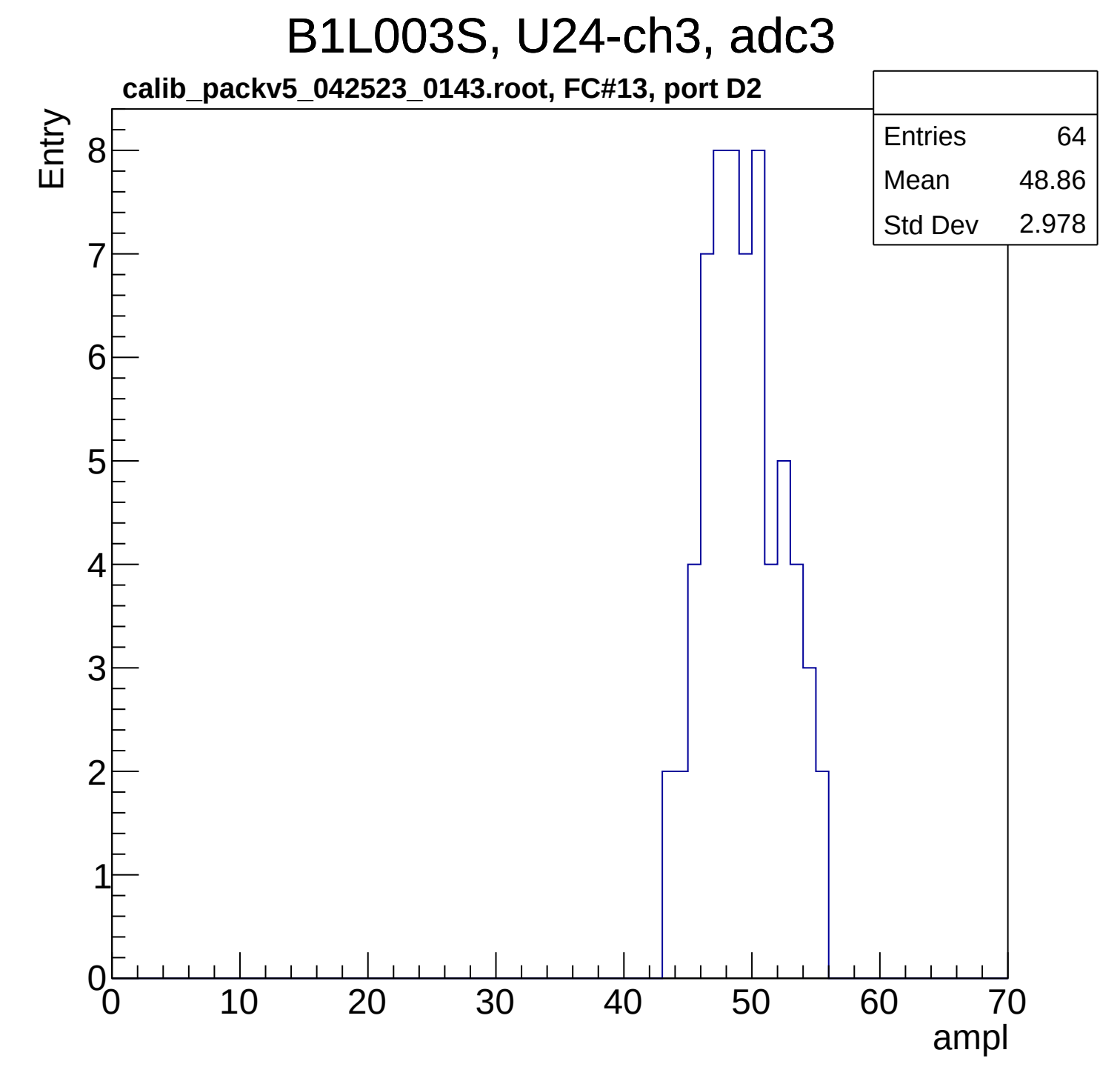
B1L003S, U24-ch3, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 48.86
Std Dev 2.978
Entry
ampl
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4
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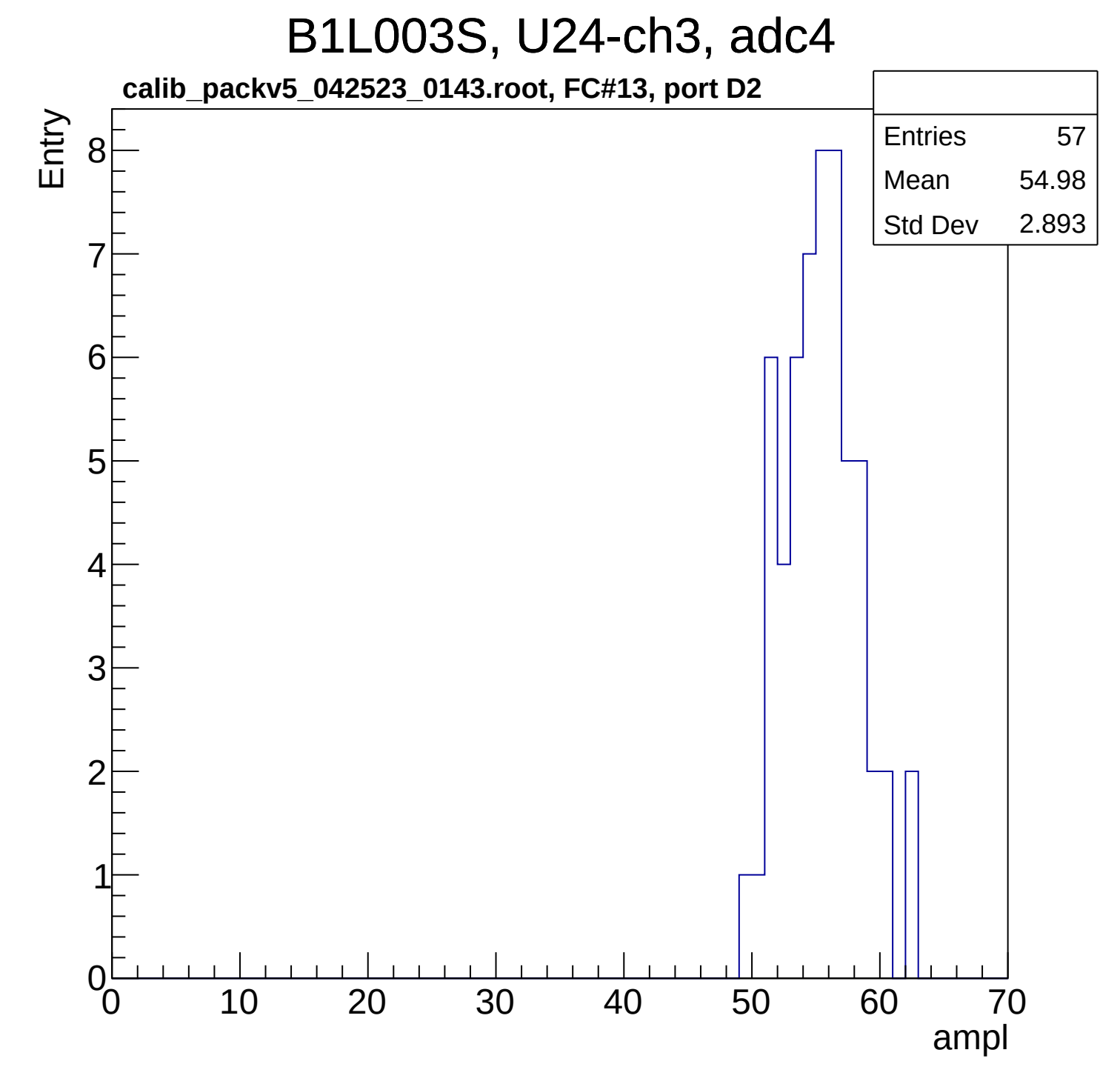
B1L003S, U24-ch3, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.98
Std Dev 2.893
Entry
ampl
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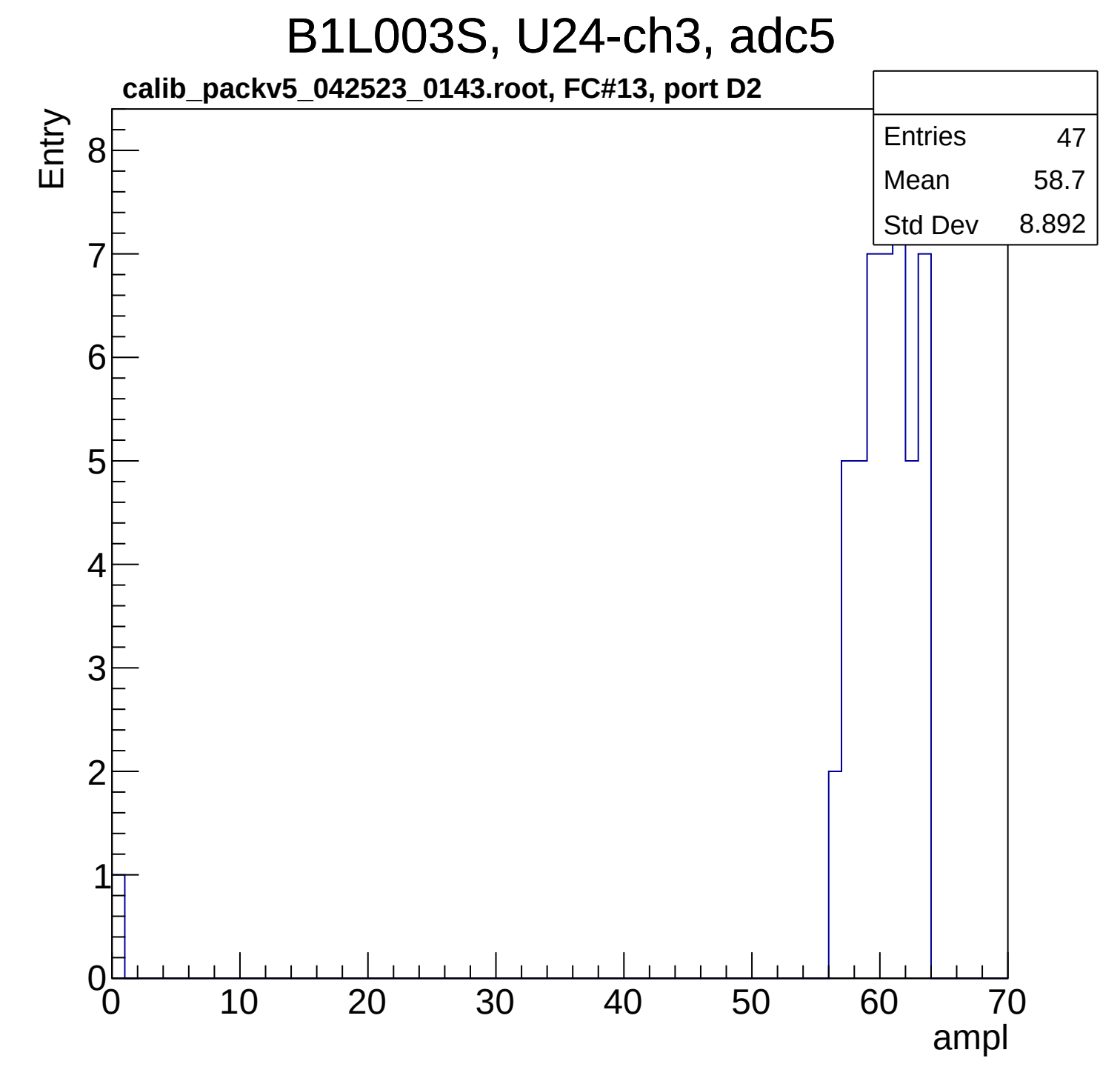
B1L003S, U24-ch3, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.7
Std Dev 8.892
Entry
ampl
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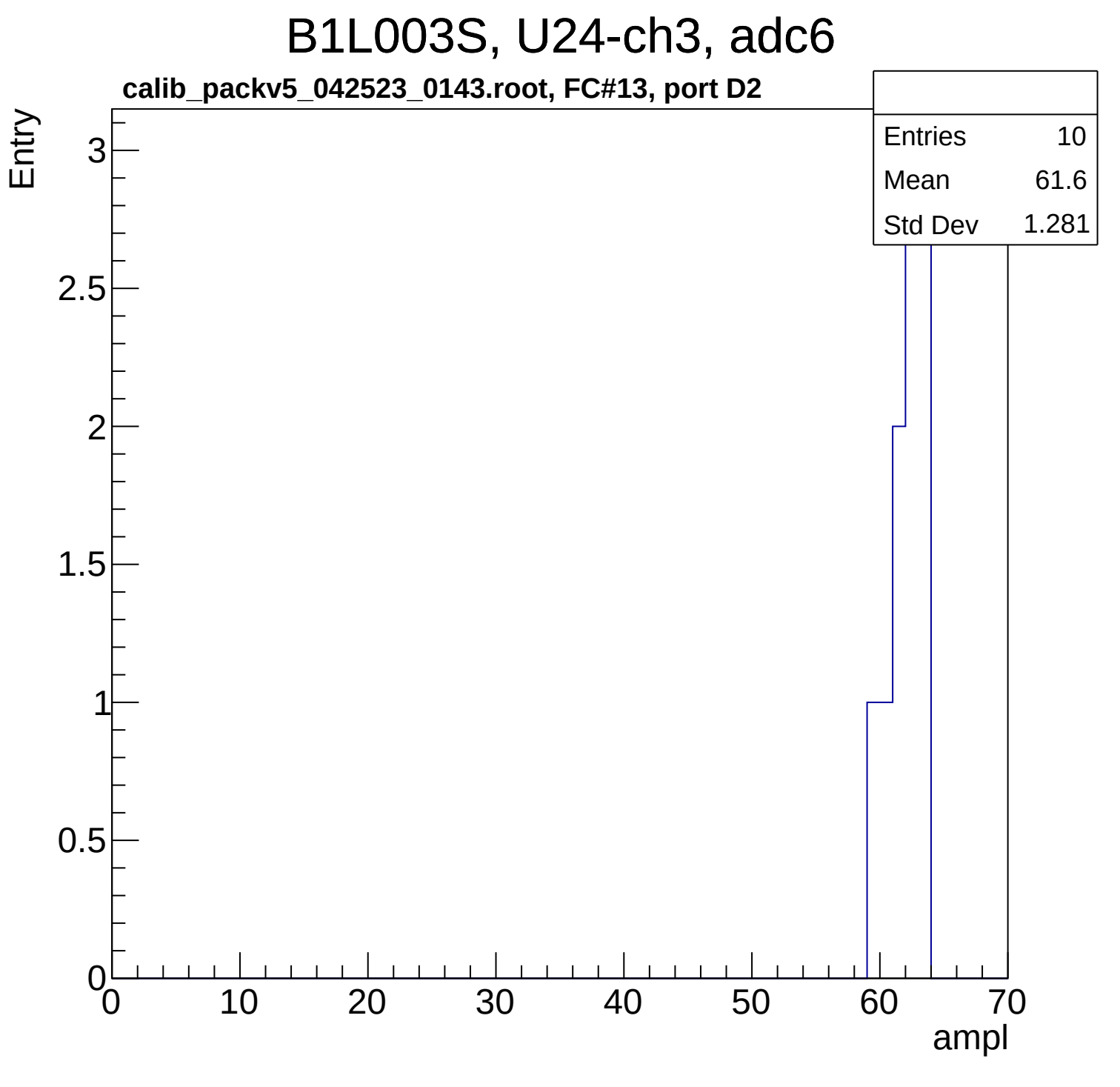
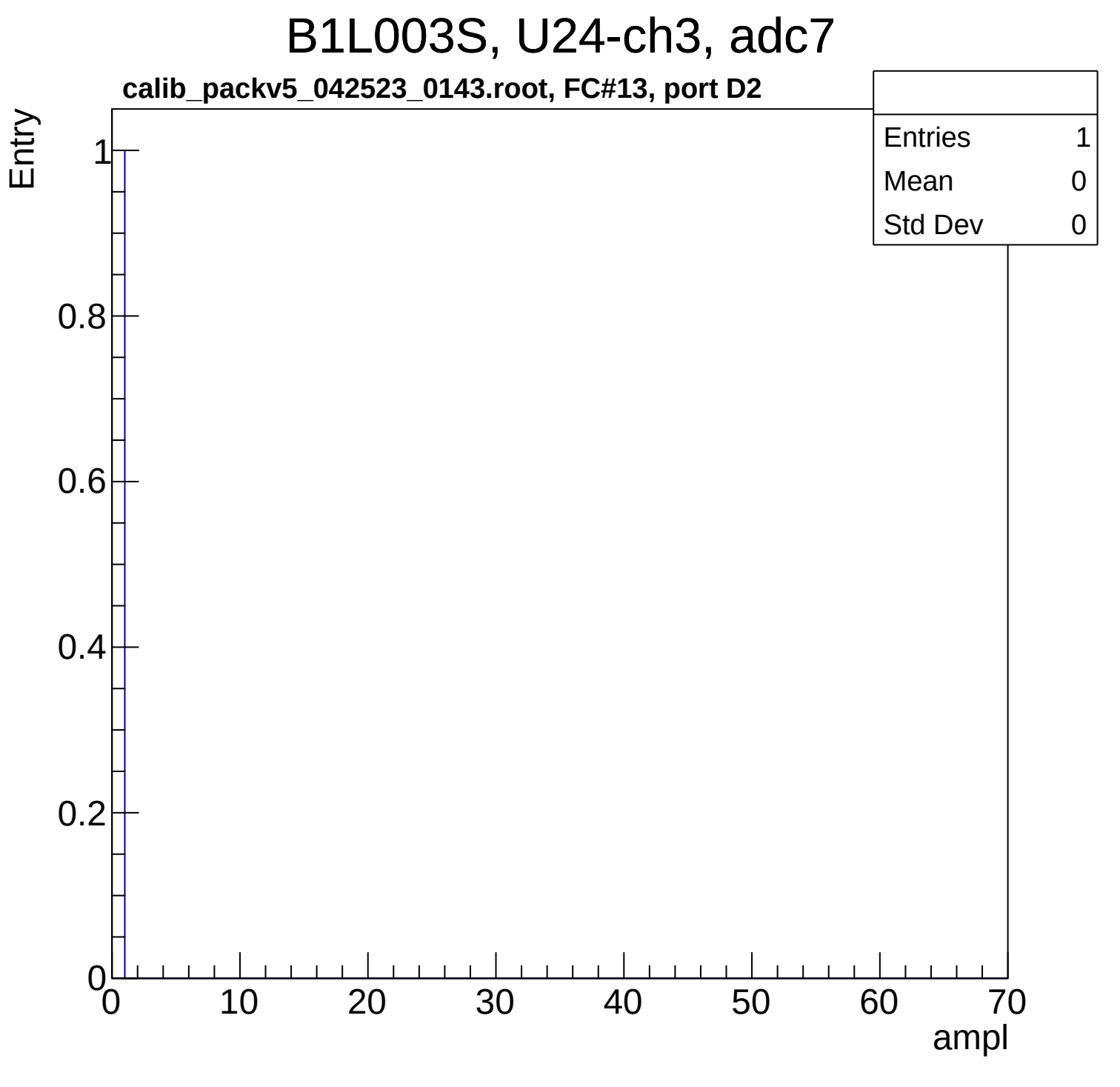

B1L003S, U24-ch3, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.6
Std Dev 1.281
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch3, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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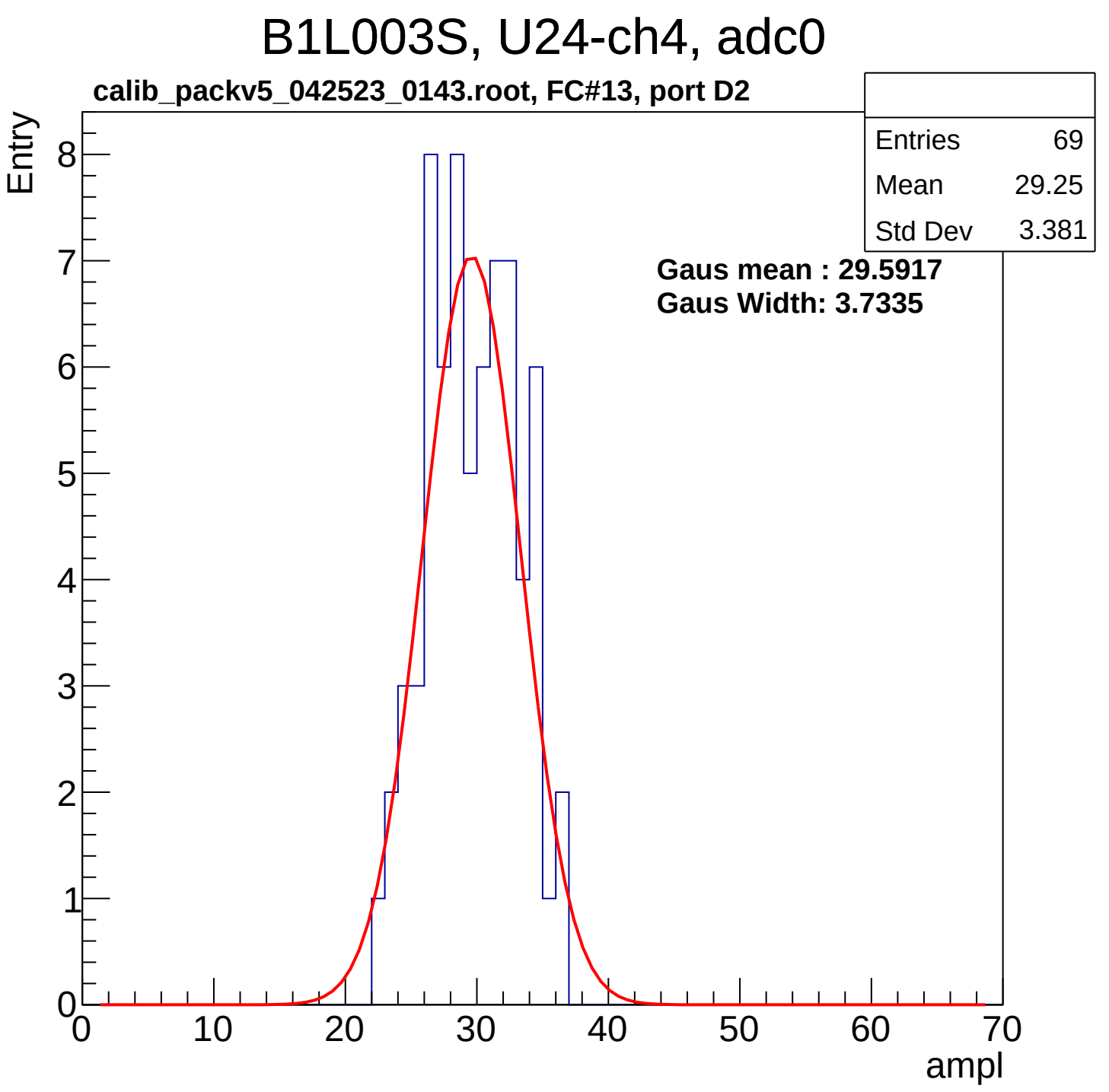
40
50
60
70
ampl


B1L003S, U24-ch4, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 29.25
Std Dev 3.381
Gaus mean : 29.5917
Gaus Width: 3.7335
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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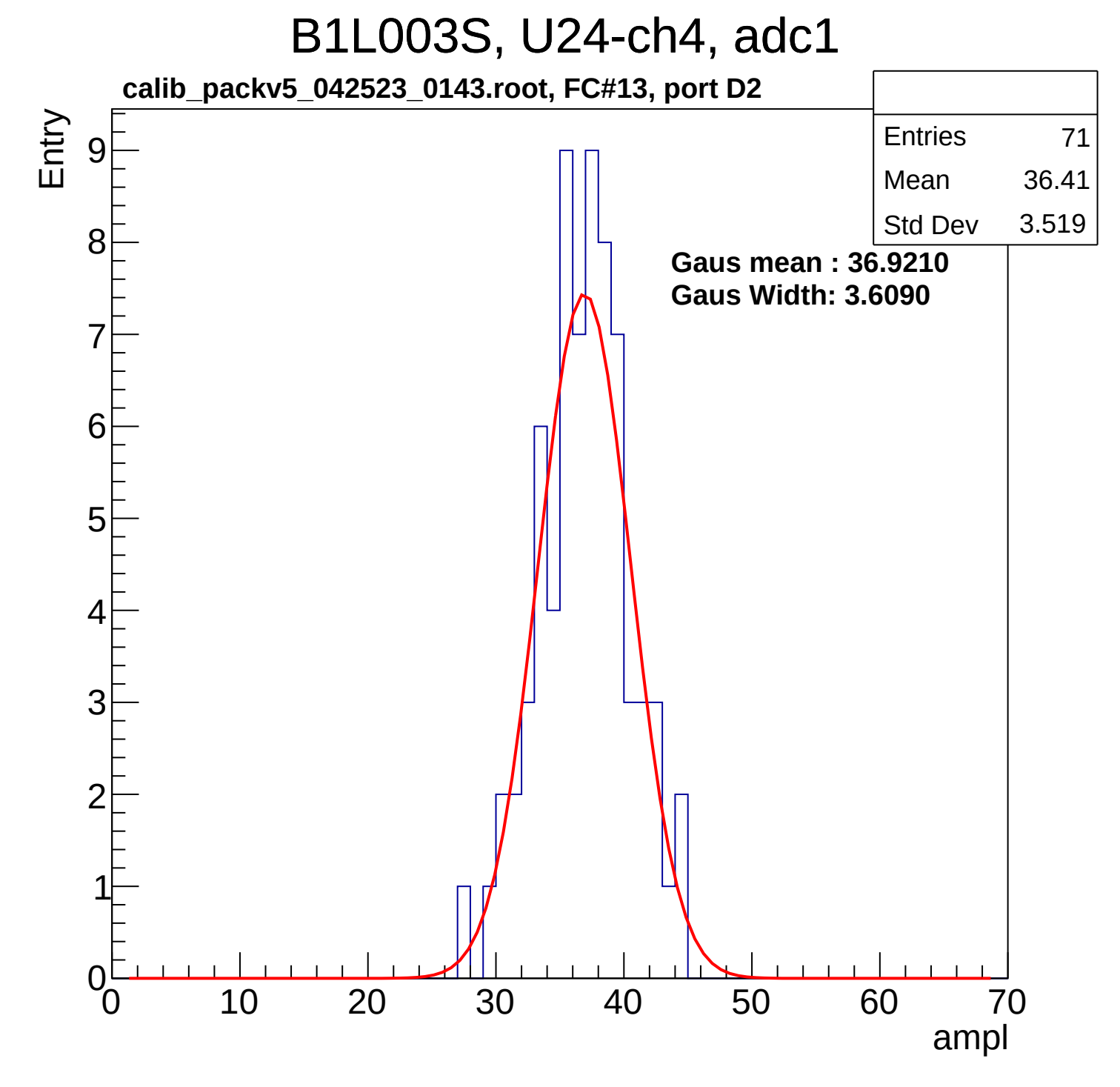
B1L003S, U24-ch4, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.41
Std Dev 3.519
Gaus mean : 36.9210
Gaus Width: 3.6090
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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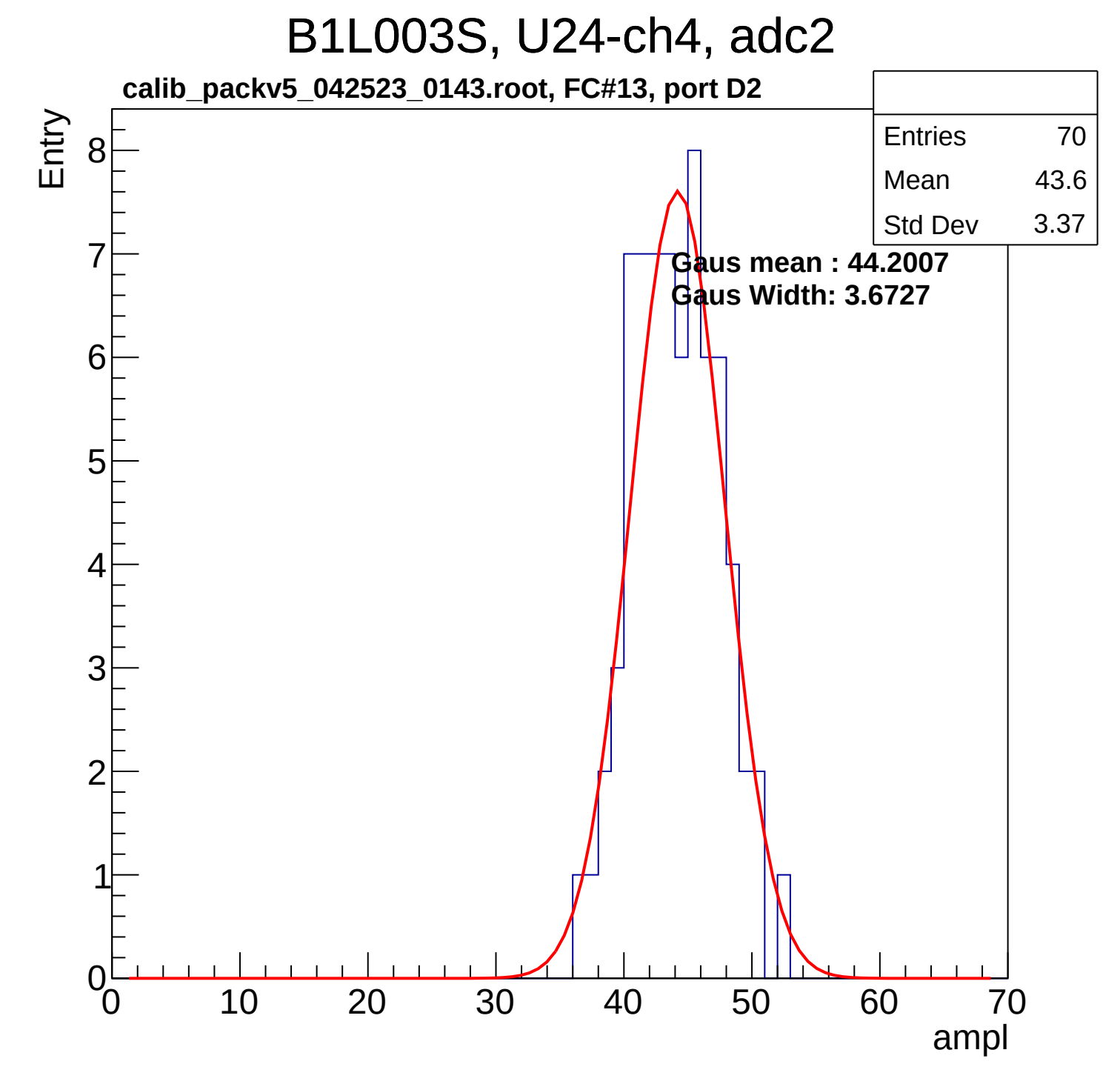
B1L003S, U24-ch4, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 43.6
Std Dev 3.37
Gaus mean : 44.2007
Gaus Width: 3.6727
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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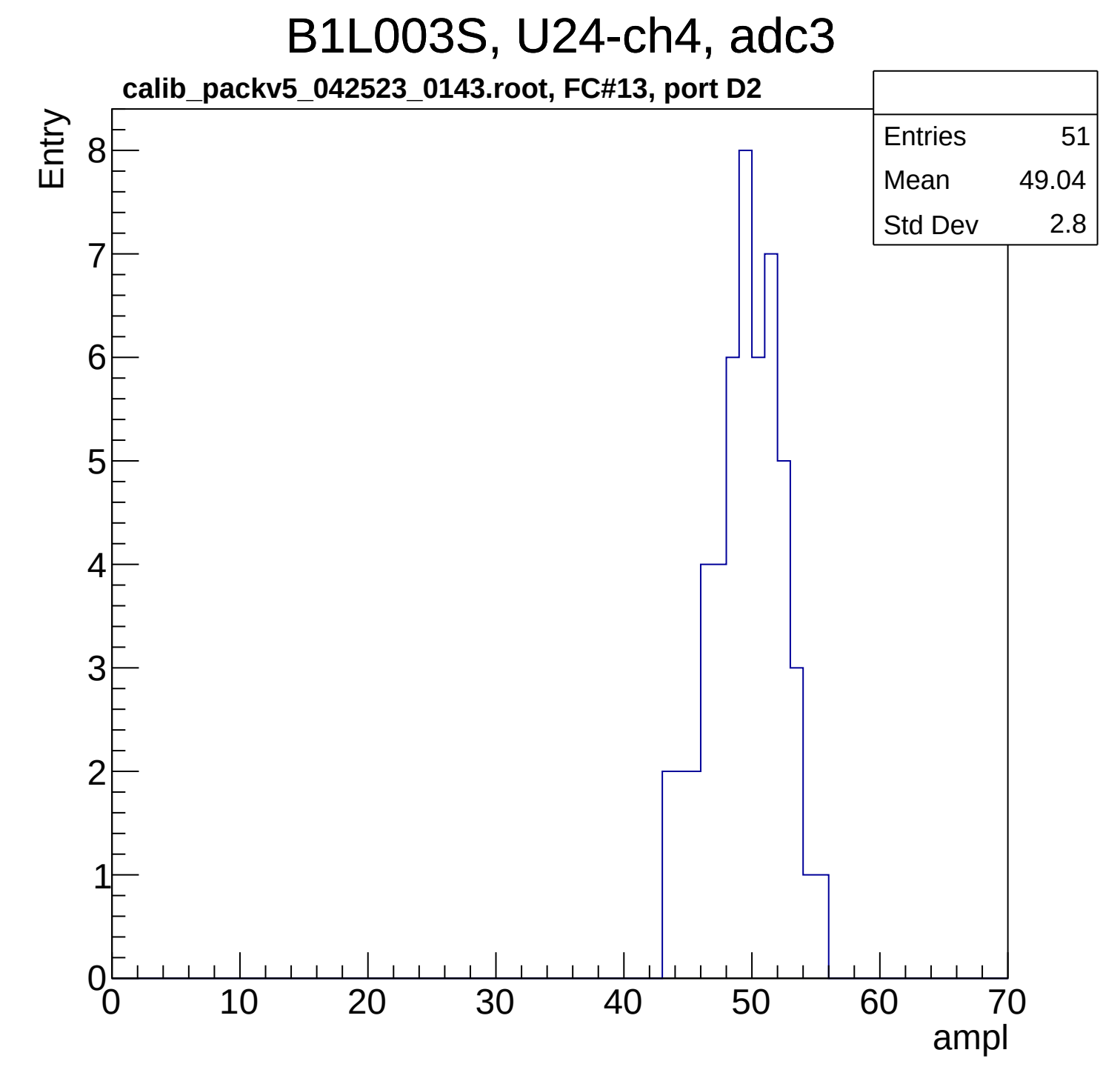
B1L003S, U24-ch4, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 49.04
Std Dev 2.8
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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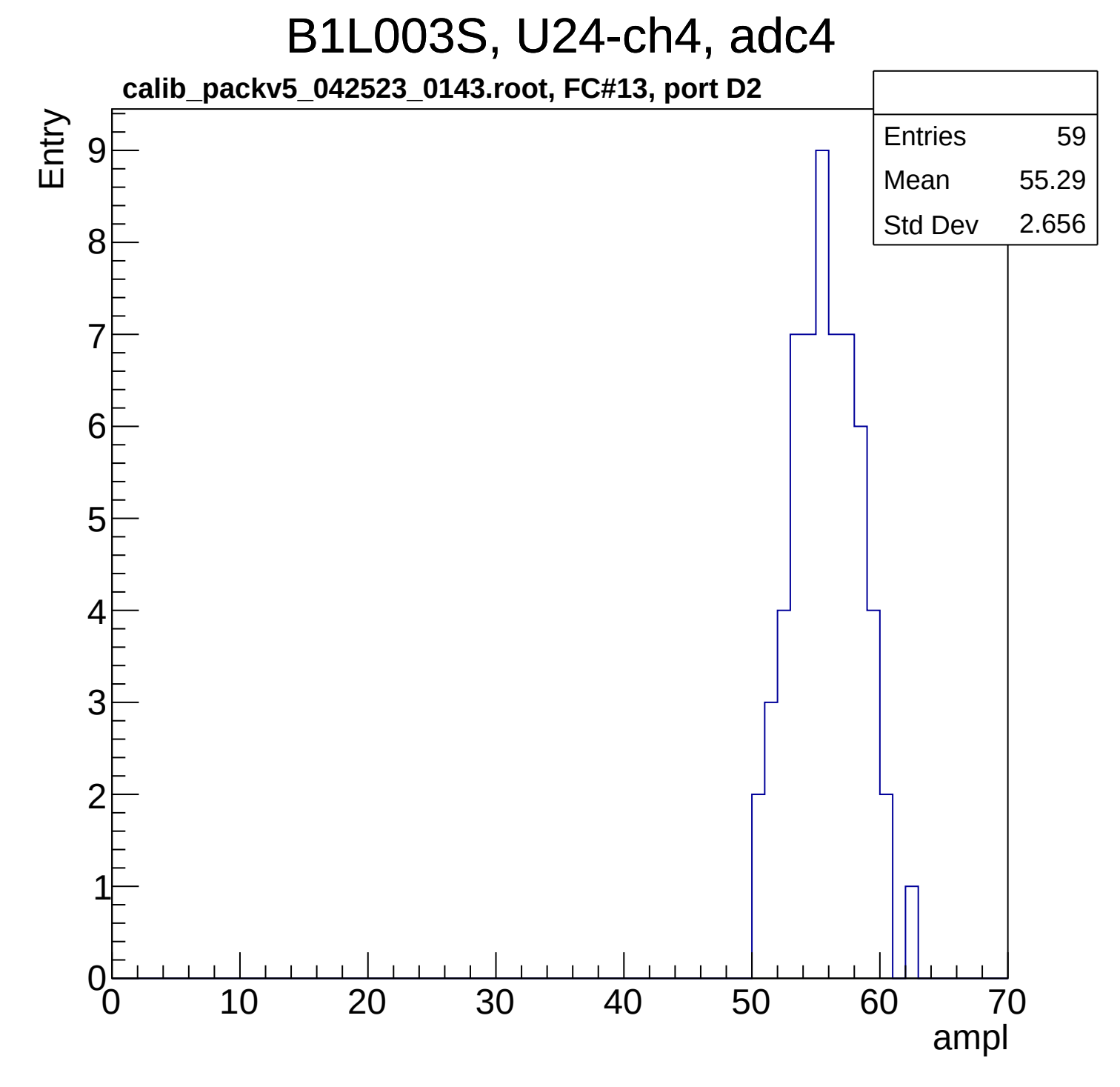
B1L003S, U24-ch4, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.29
Std Dev 2.656
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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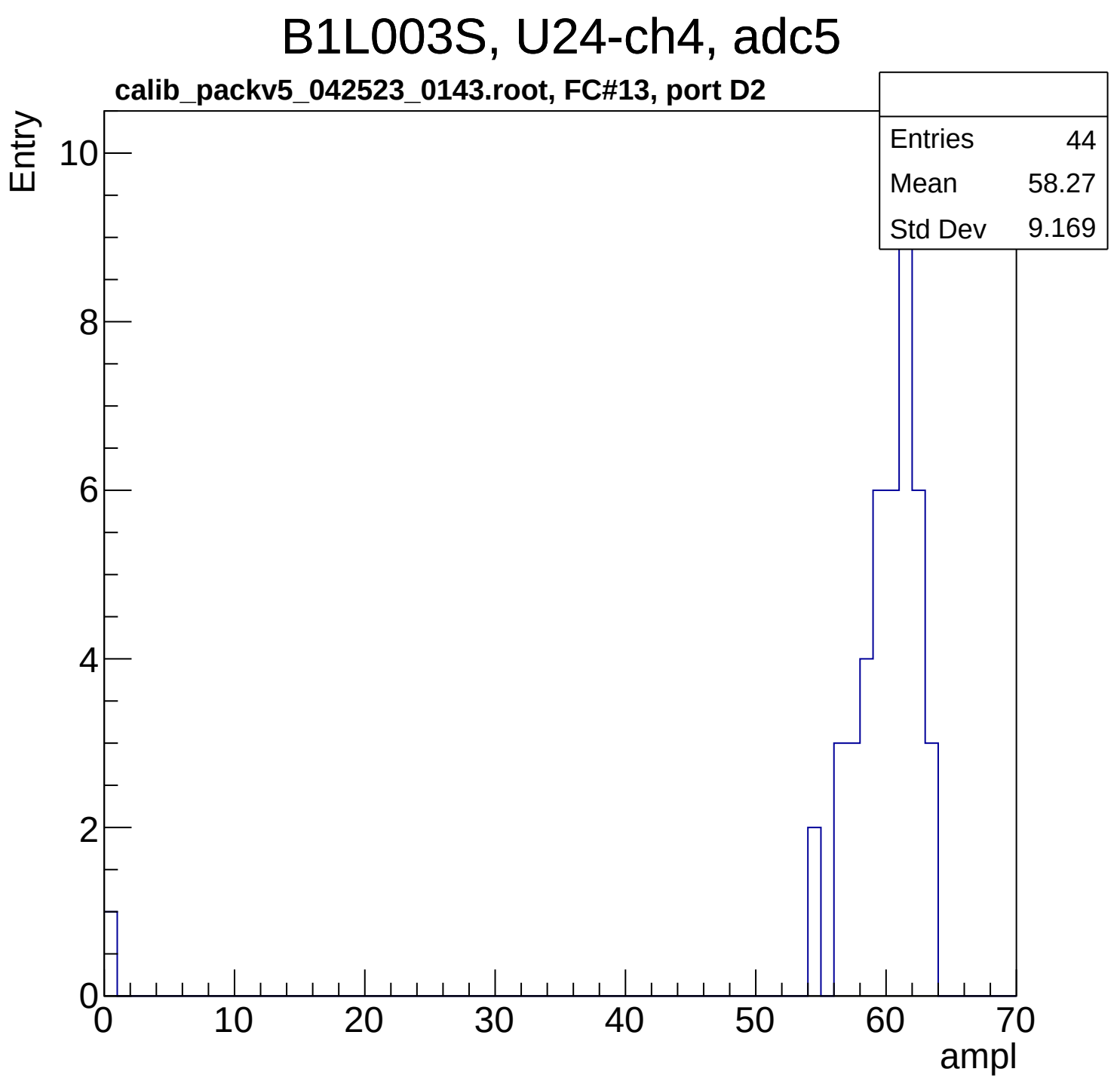
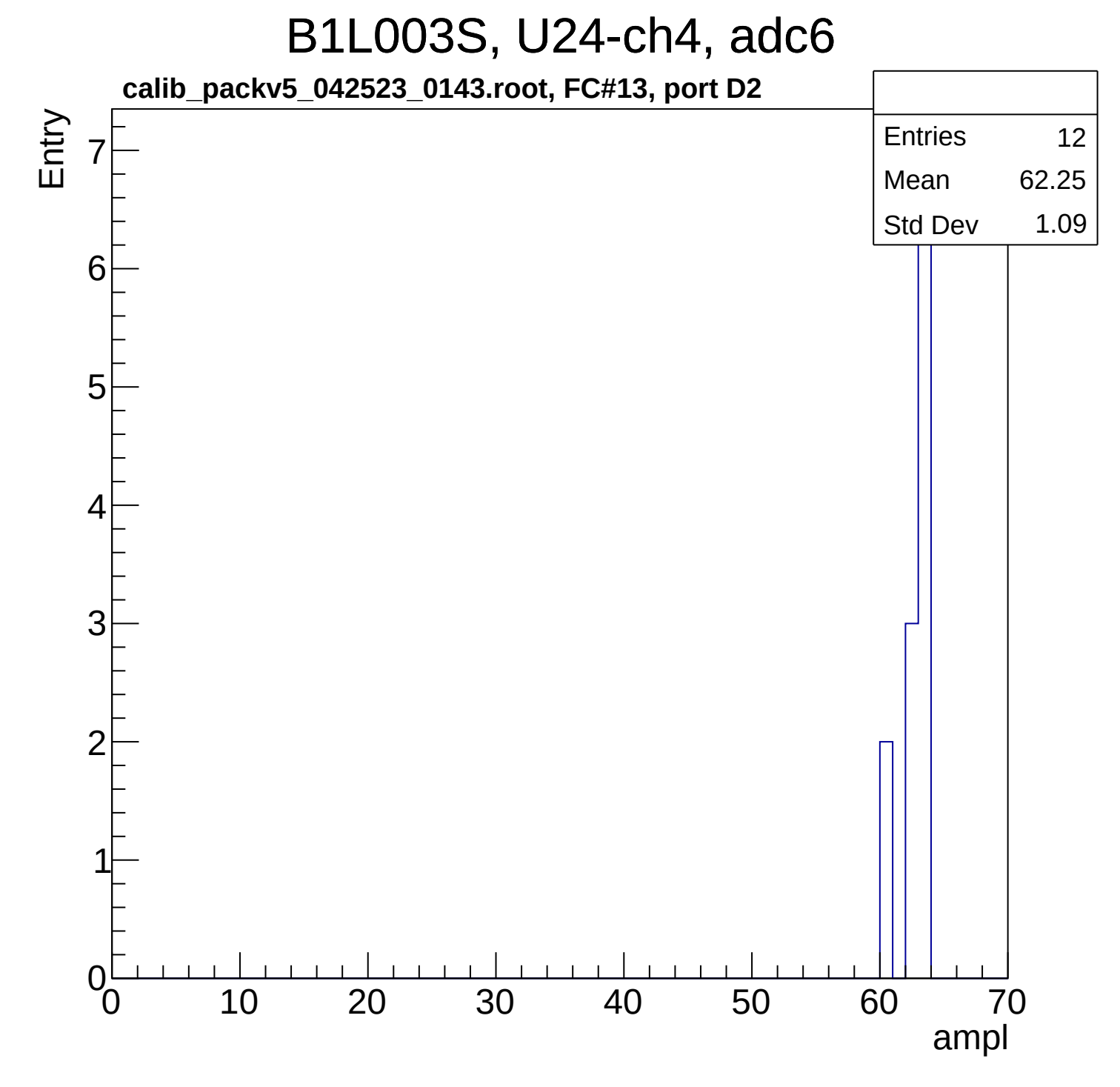
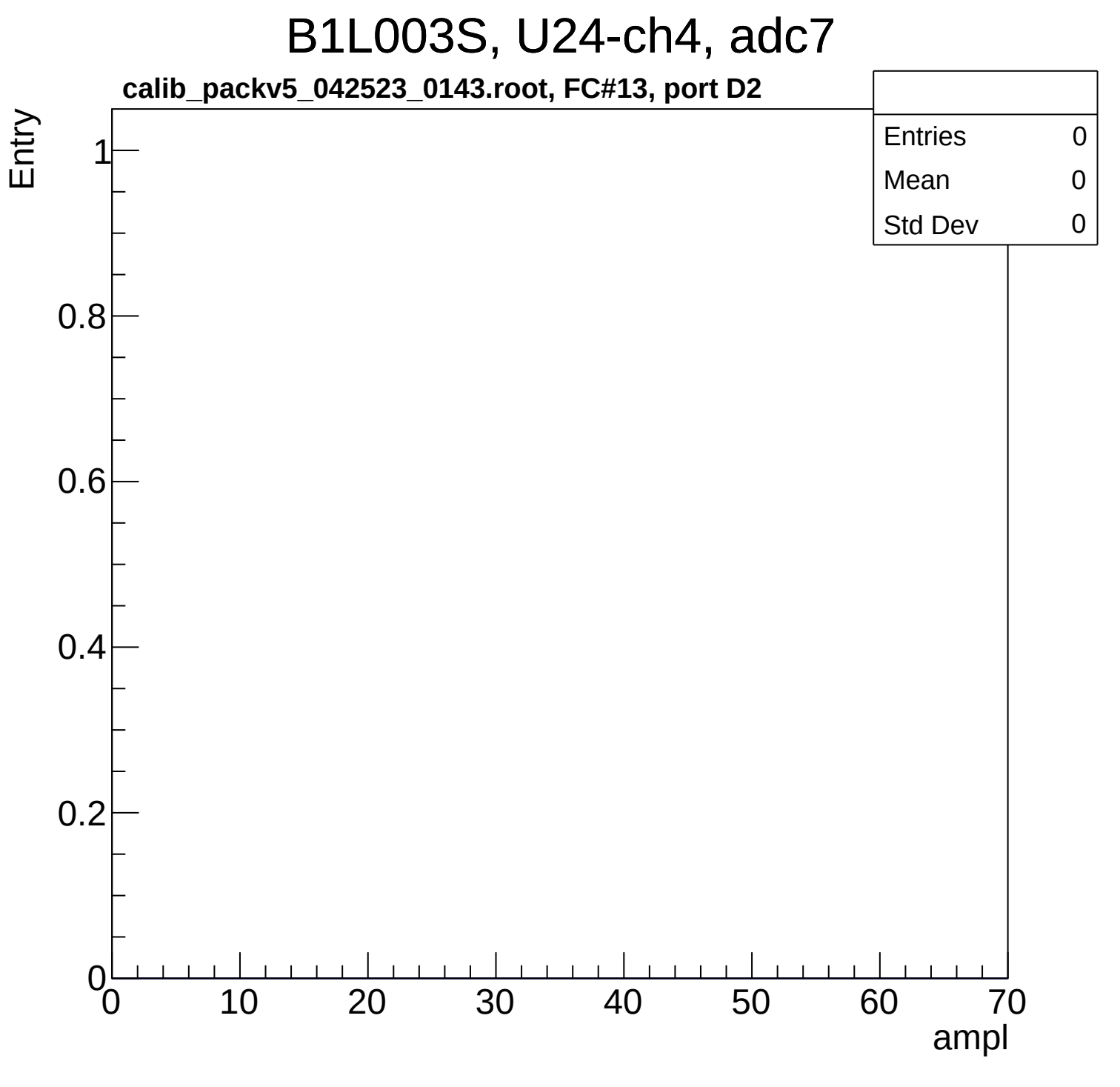
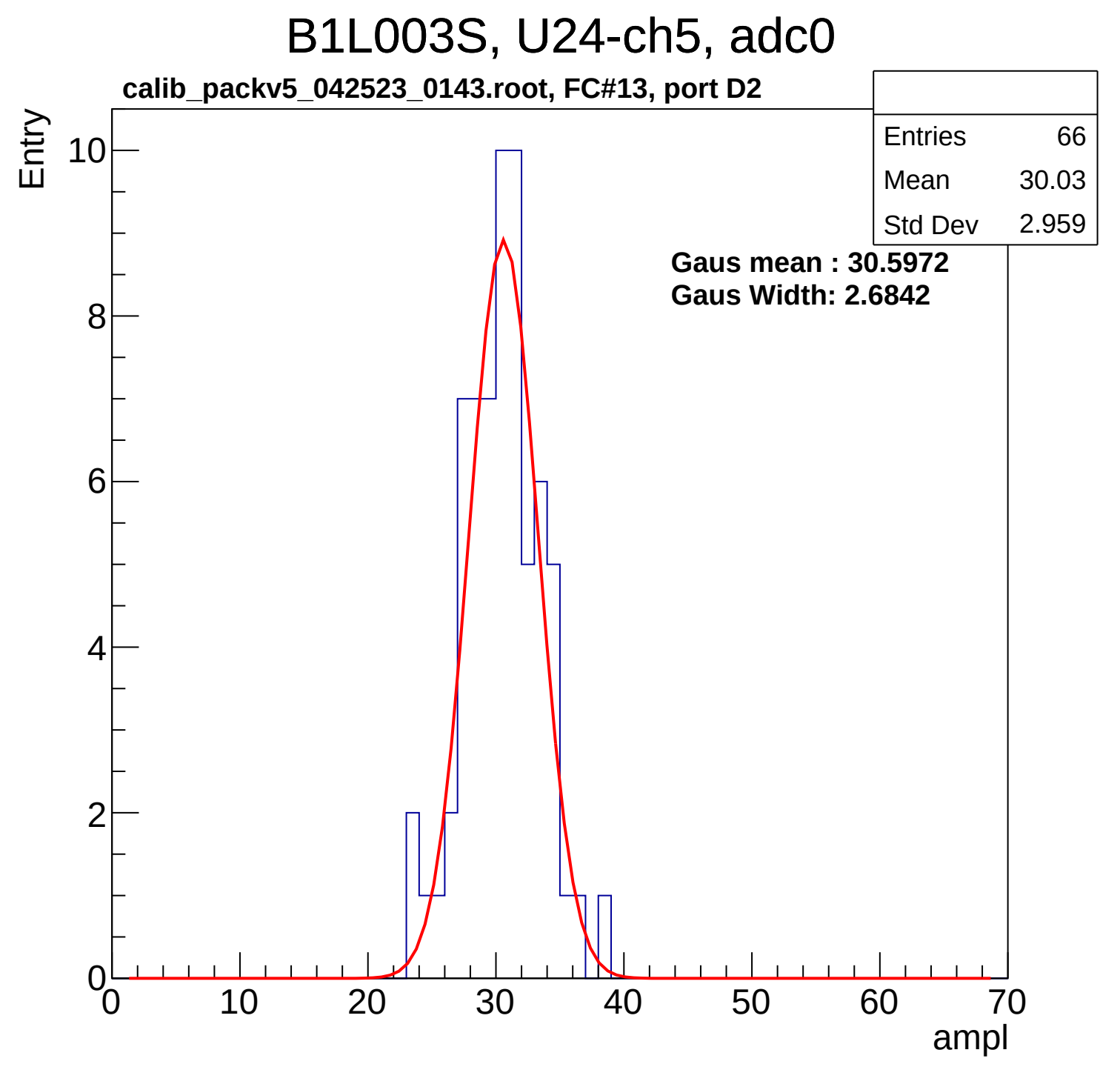
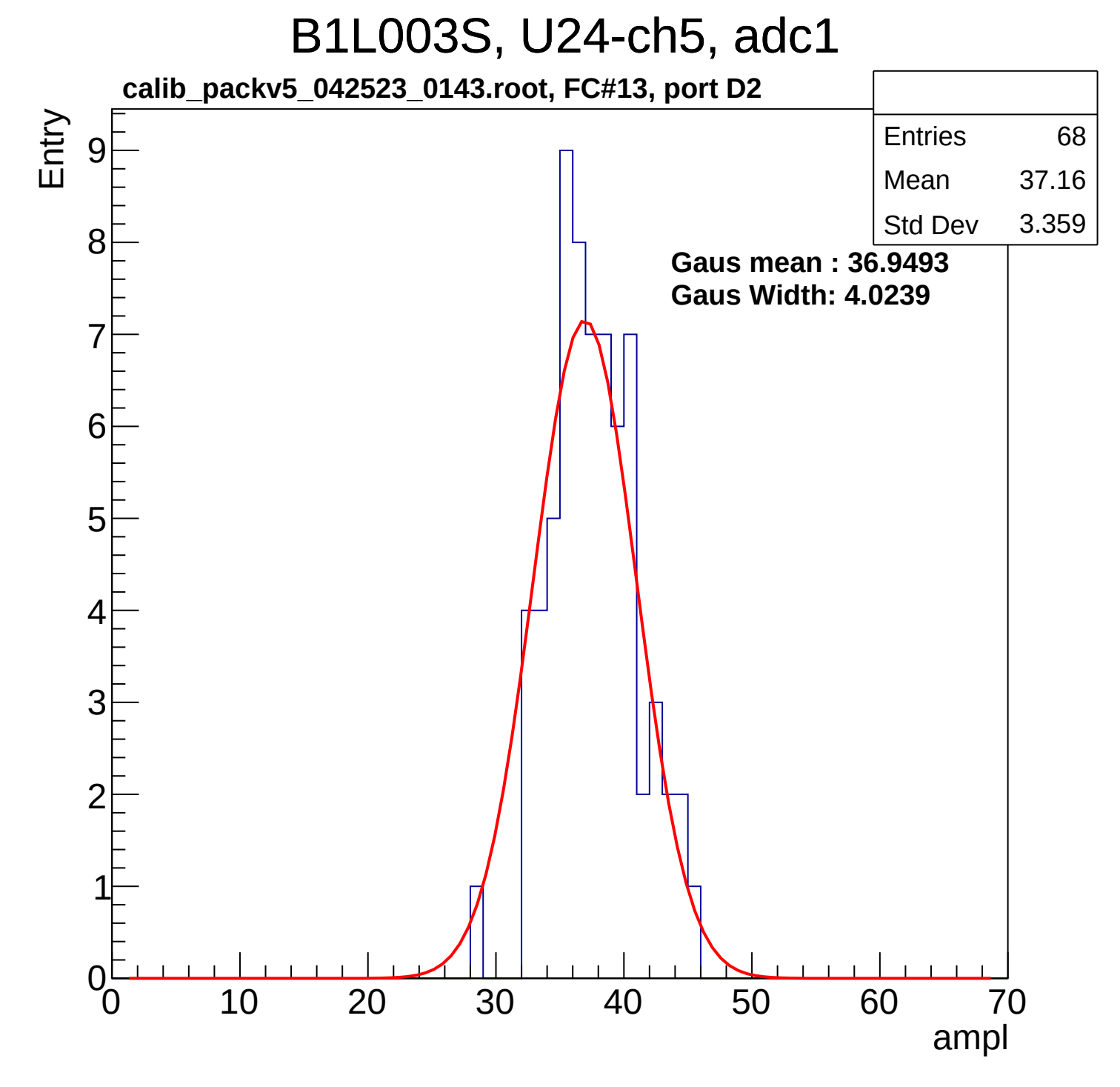
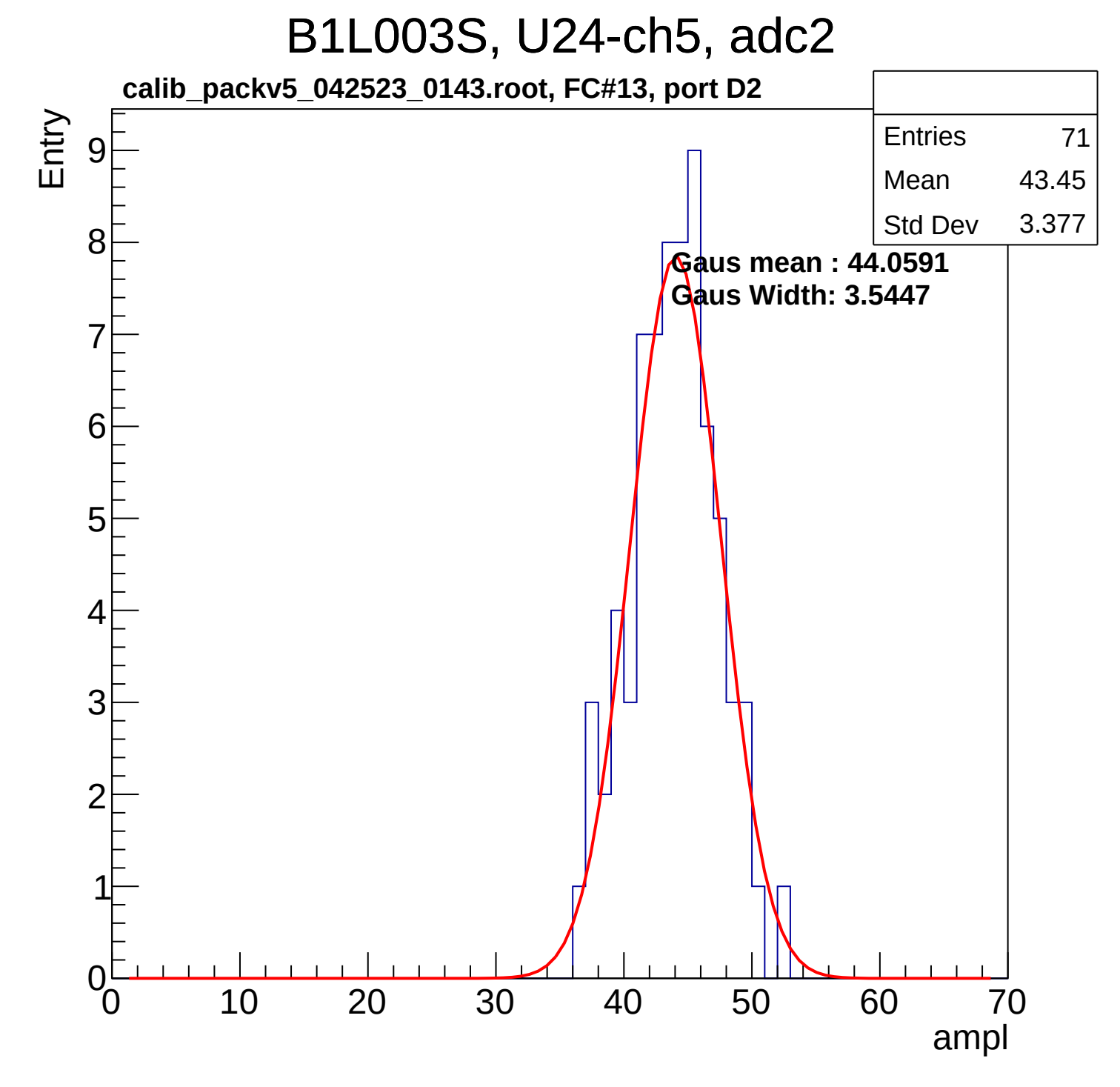

B1L003S, U24-ch4, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.27
Std Dev 9.169
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch4, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 62.25
Std Dev 1.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch4, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch5, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.03
Std Dev 2.959
Gaus mean : 30.5972
Gaus Width: 2.6842
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch5, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 37.16
Std Dev 3.359
Gaus mean : 36.9493
Gaus Width: 4.0239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch5, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 43.45
Std Dev 3.377
Gaus mean : 44.0591
Gaus Width: 3.5447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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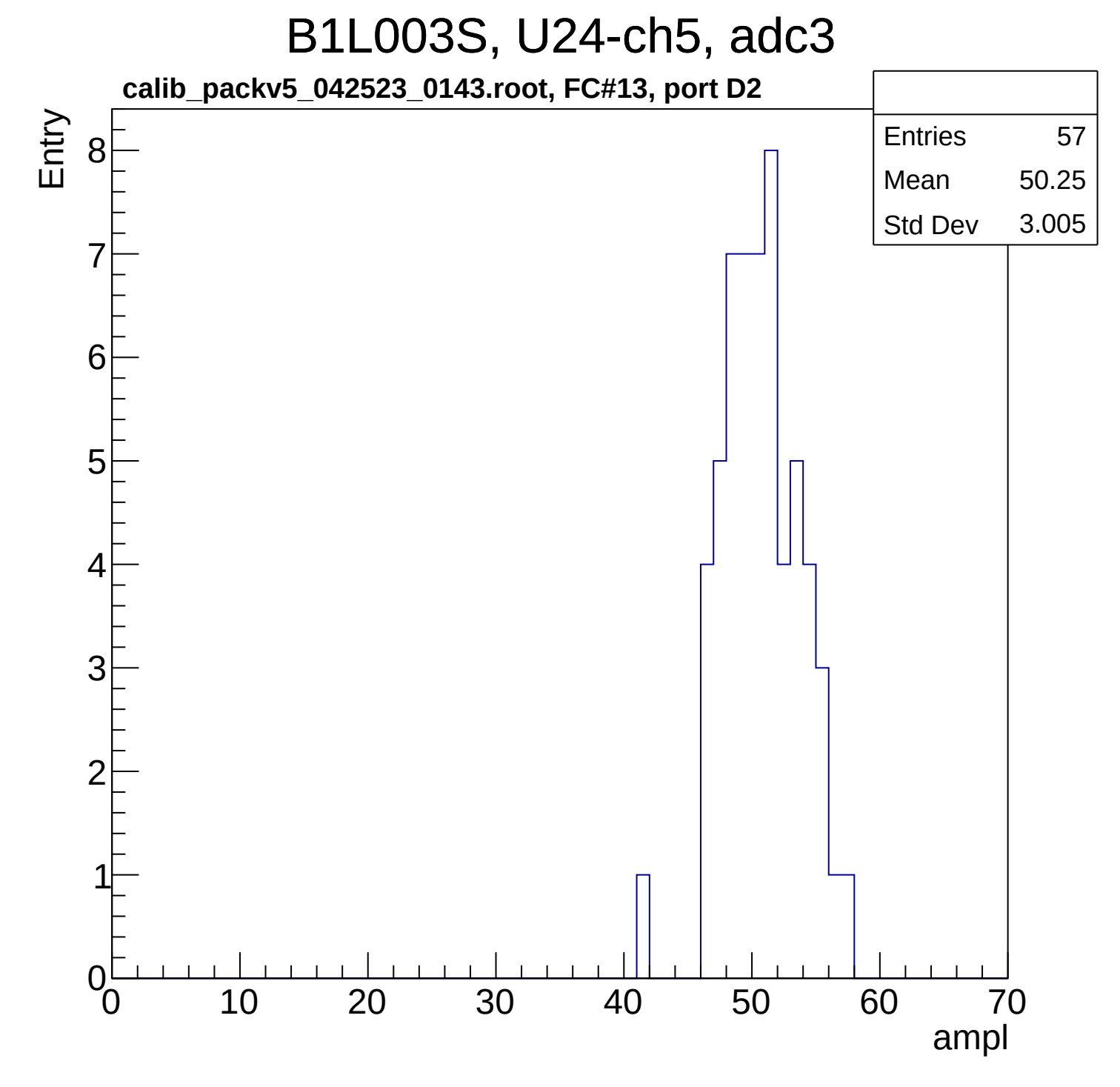
B1L003S, U24-ch5, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 50.25
Std Dev 3.005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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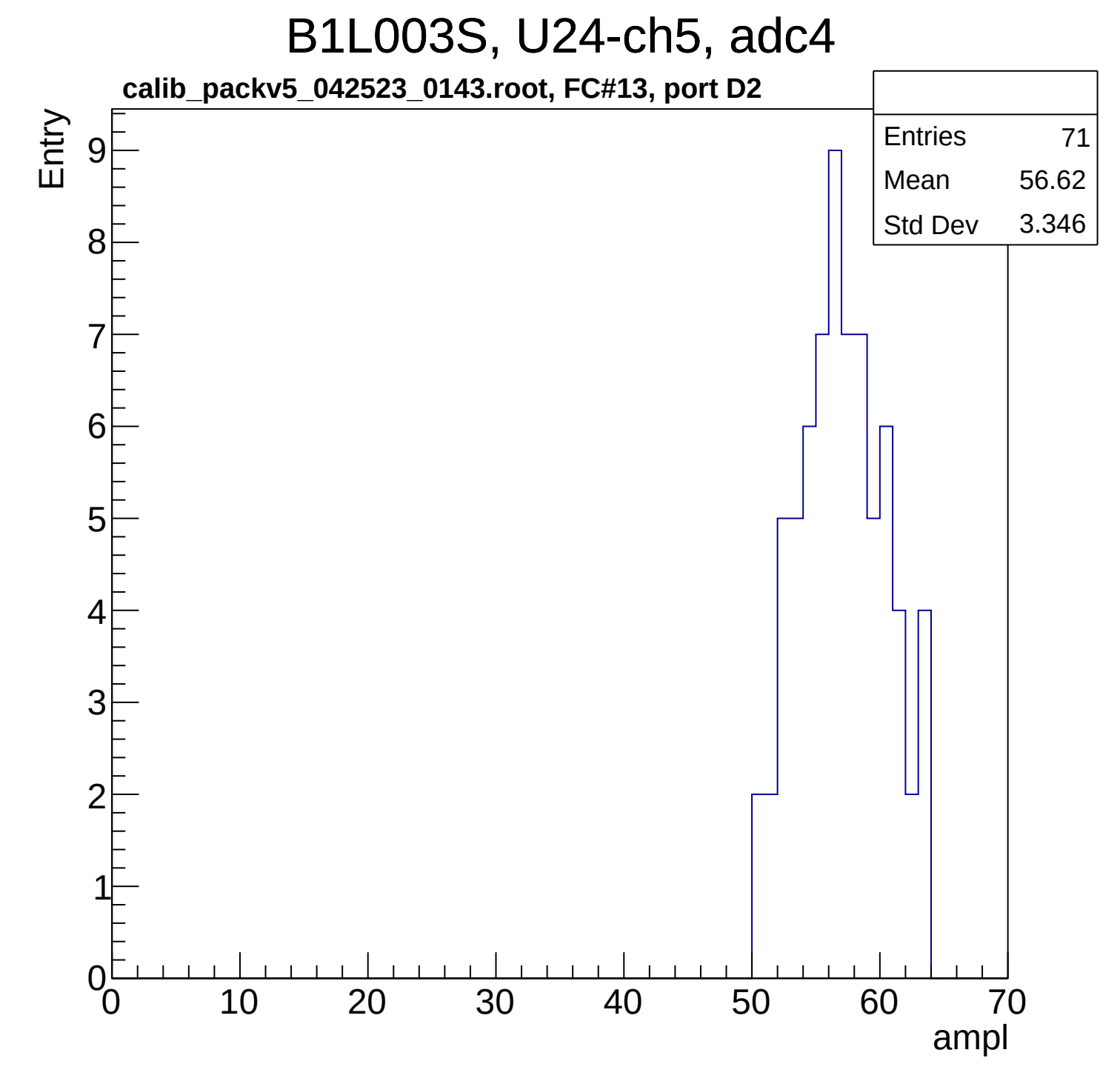
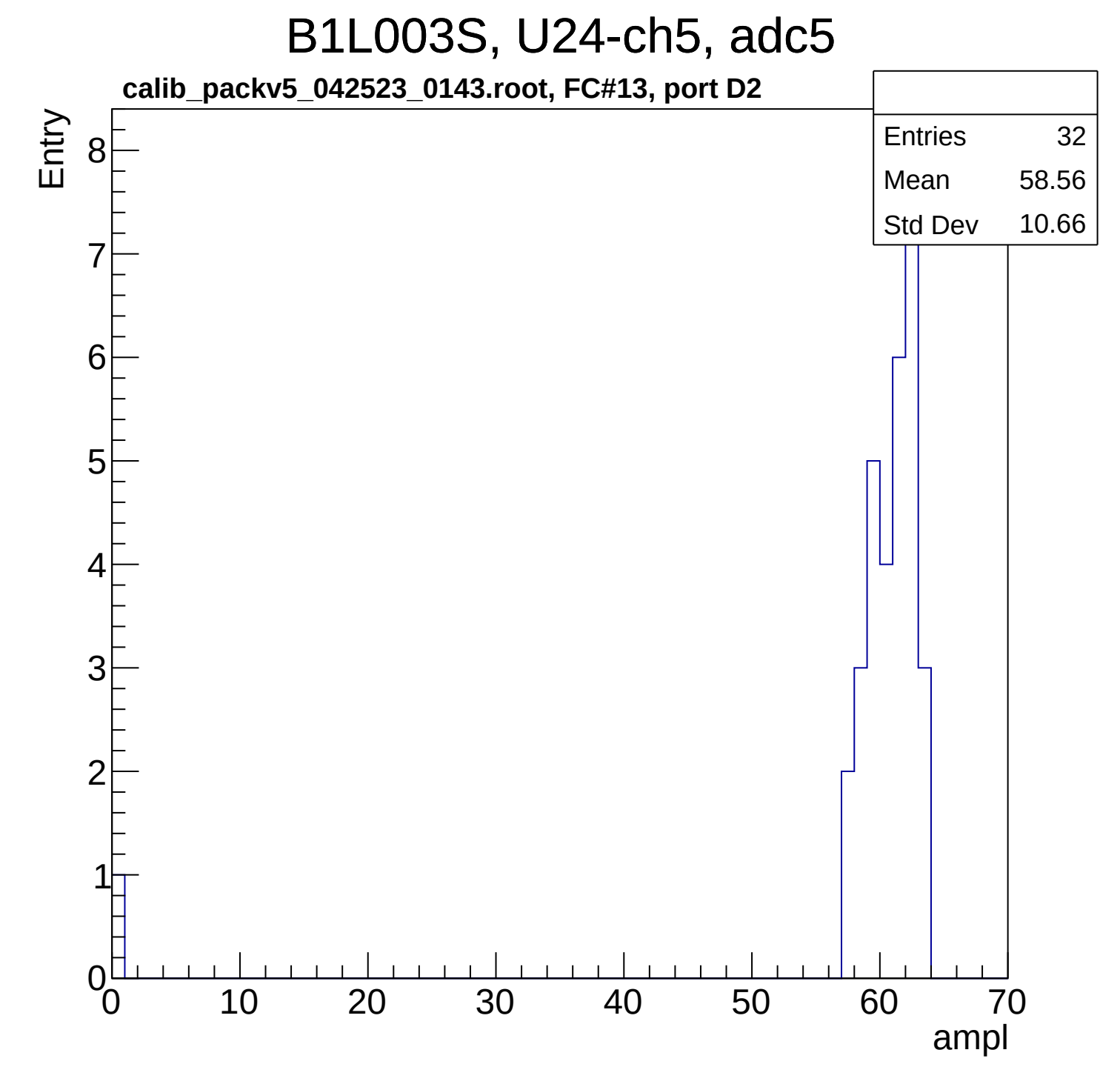
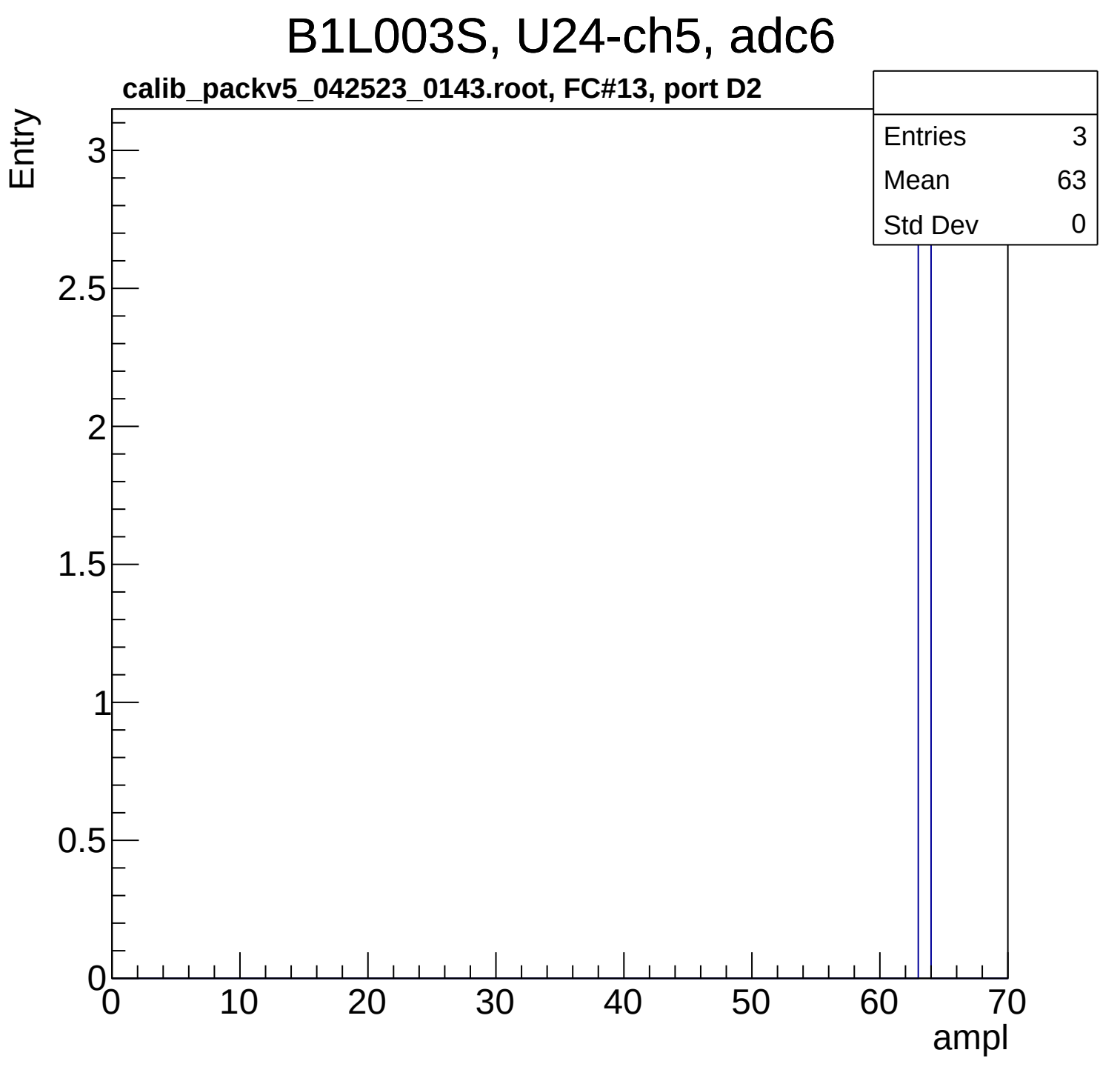
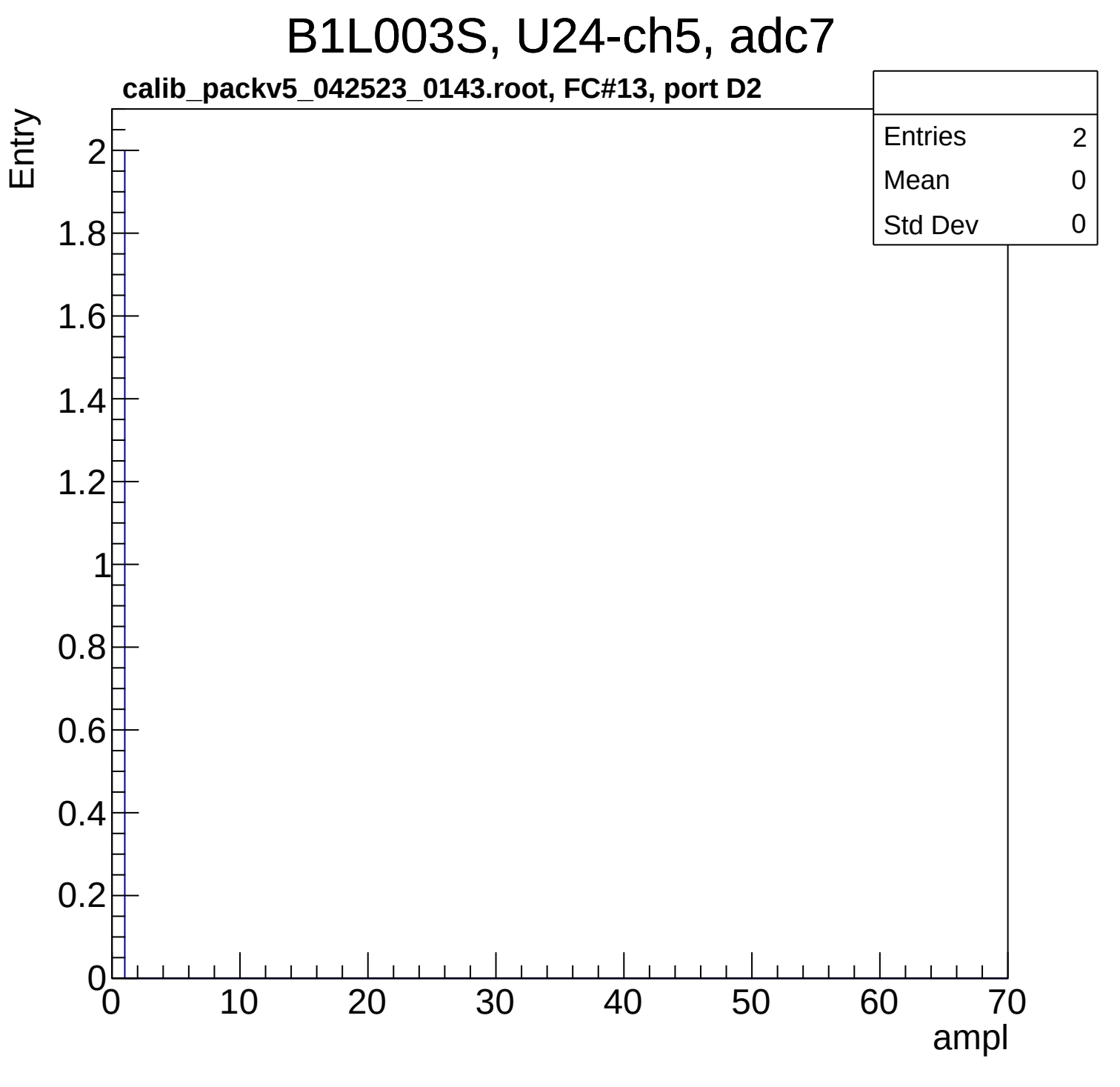
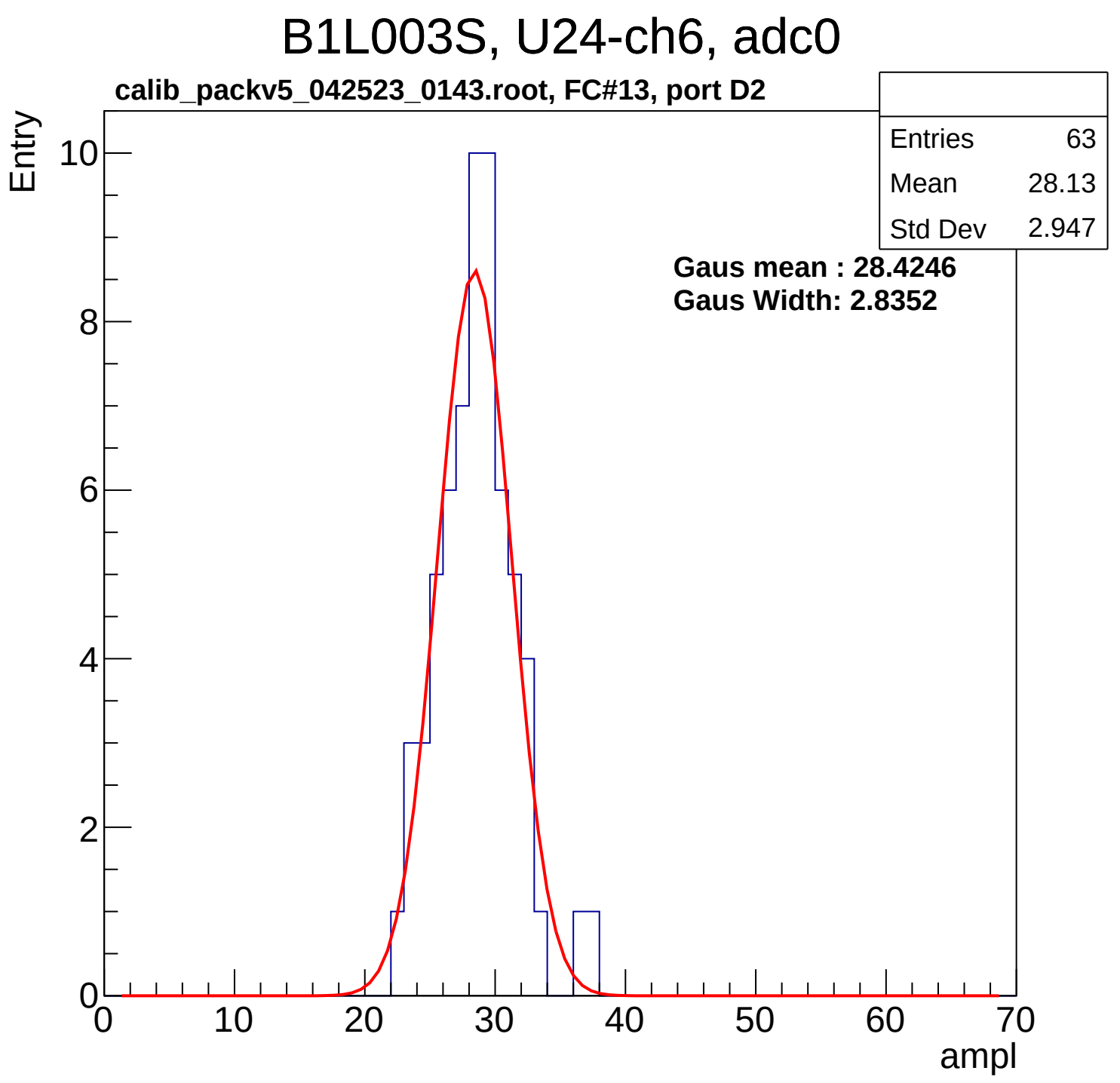
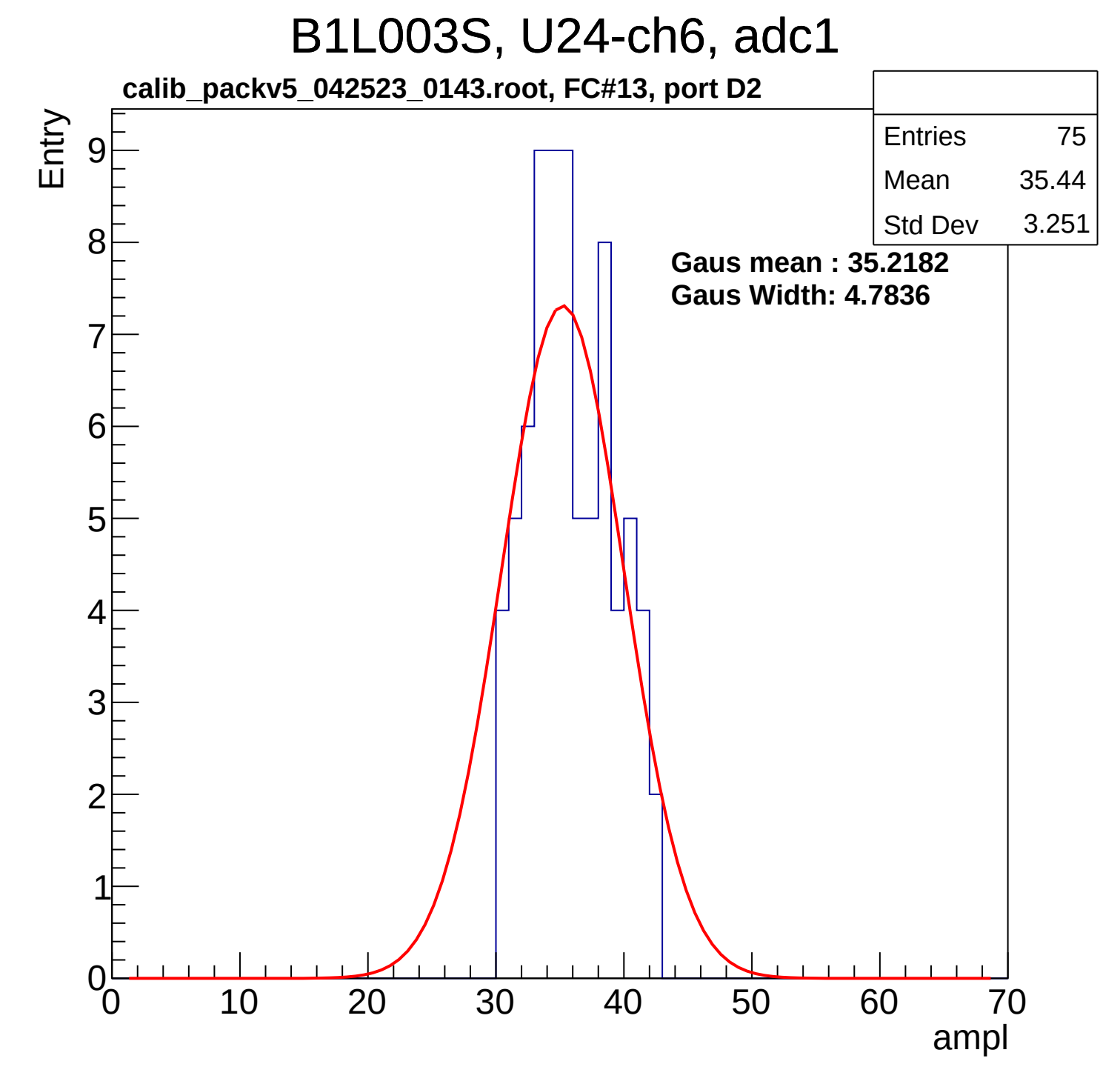

B1L003S, U24-ch5, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 56.62
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch5, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 58.56
Std Dev 10.66
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch5, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch5, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch6, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.13
Std Dev 2.947
Gaus mean : 28.4246
Gaus Width: 2.8352
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch6, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 35.44
Std Dev 3.251
Gaus mean : 35.2182
Gaus Width: 4.7836
Entry
ampl
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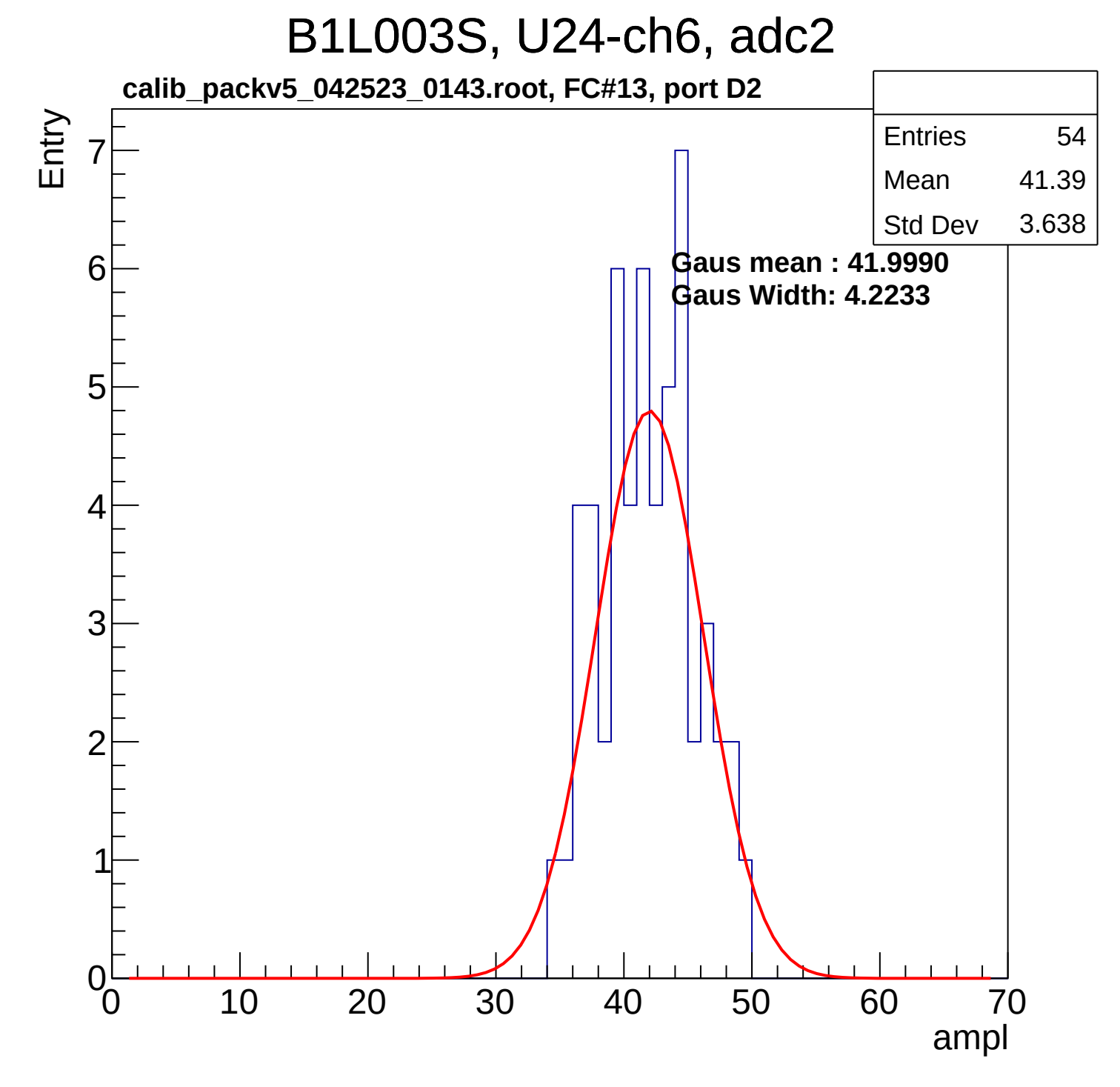
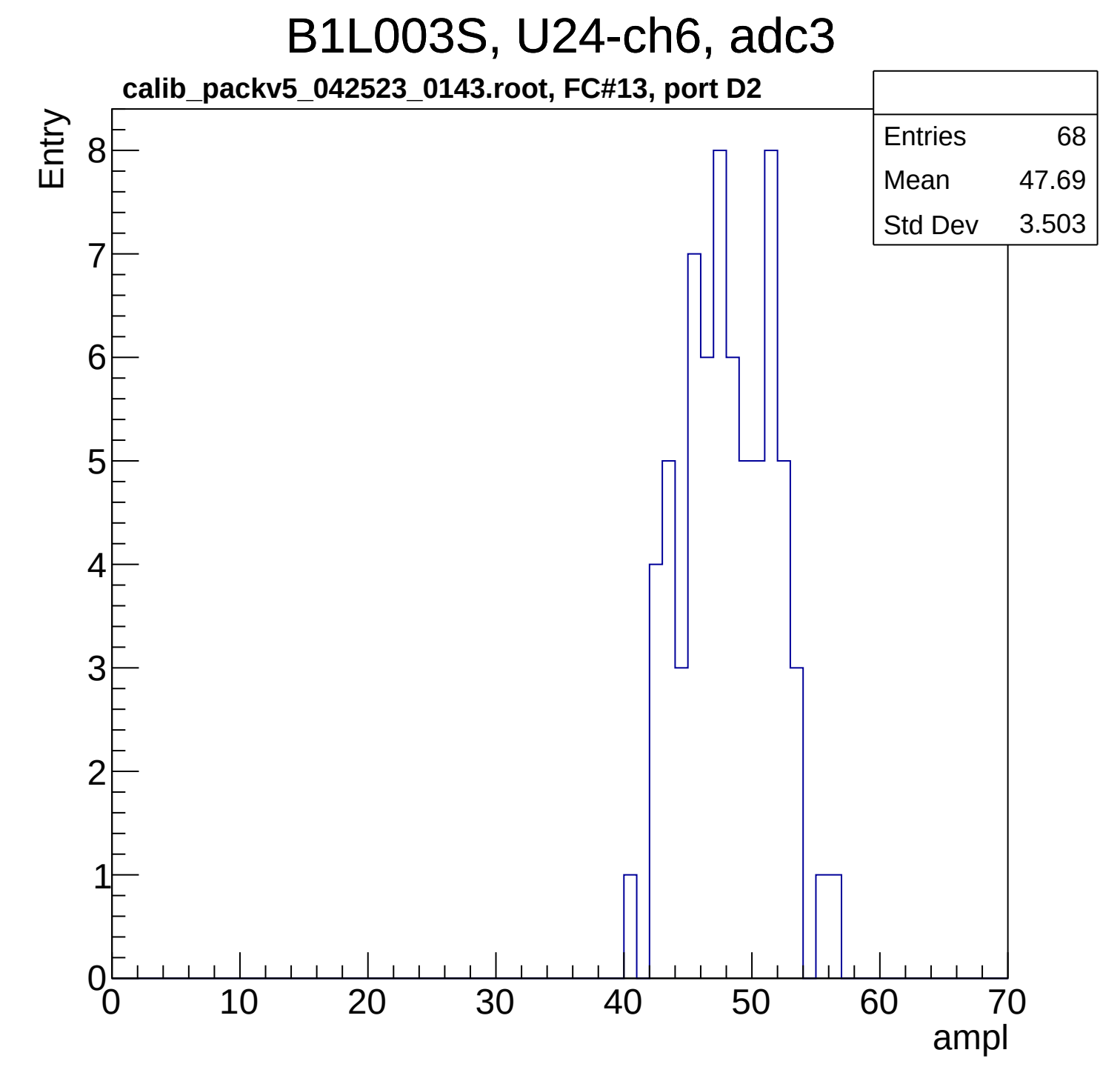
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch6, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41.39
Std Dev 3.638
Gaus mean : 41.9990
Gaus Width: 4.2233
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch6, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 47.69
Std Dev 3.503
Entry
ampl
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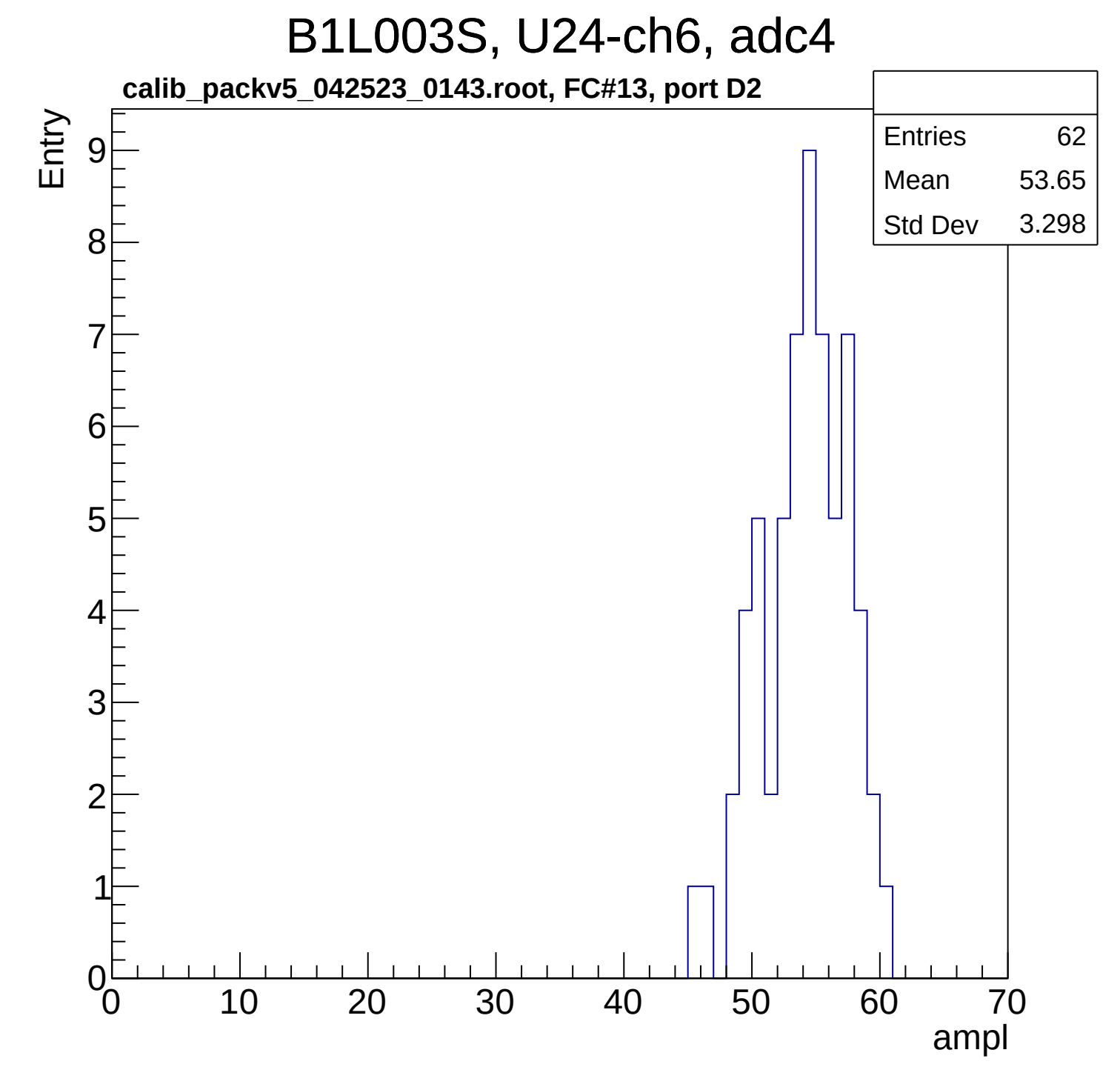
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch6, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 53.65
Std Dev 3.298
Entry
ampl
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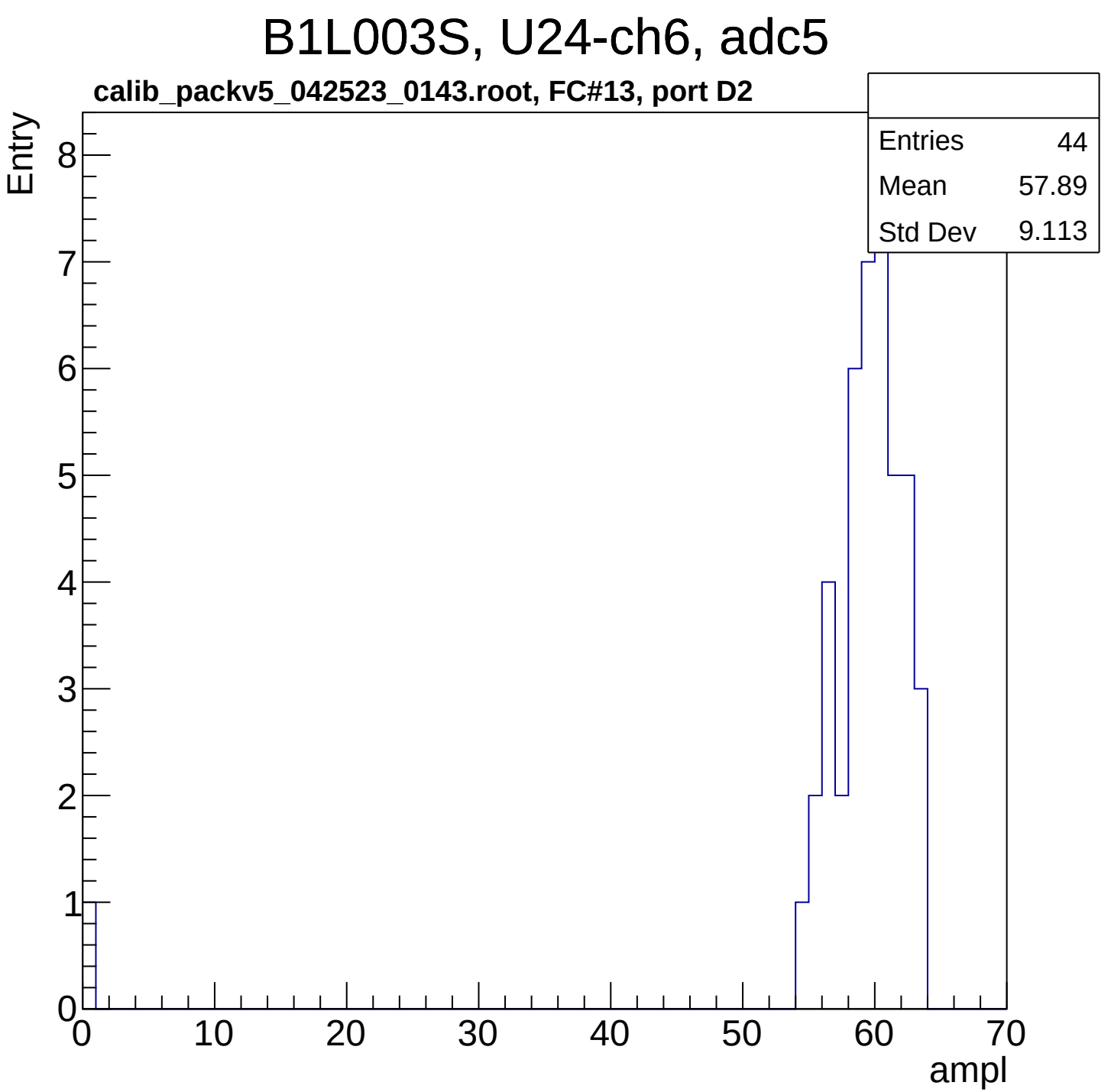
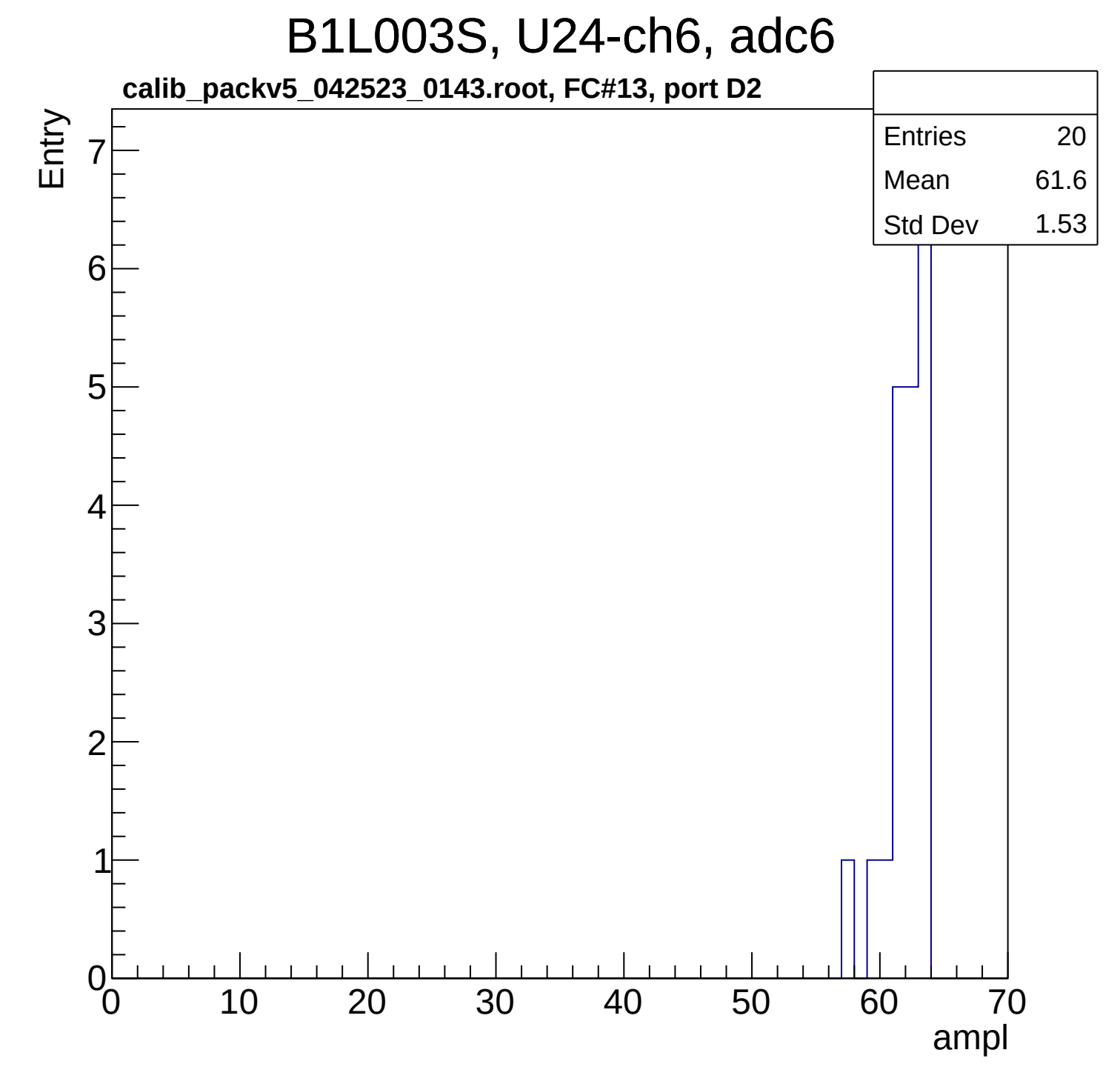
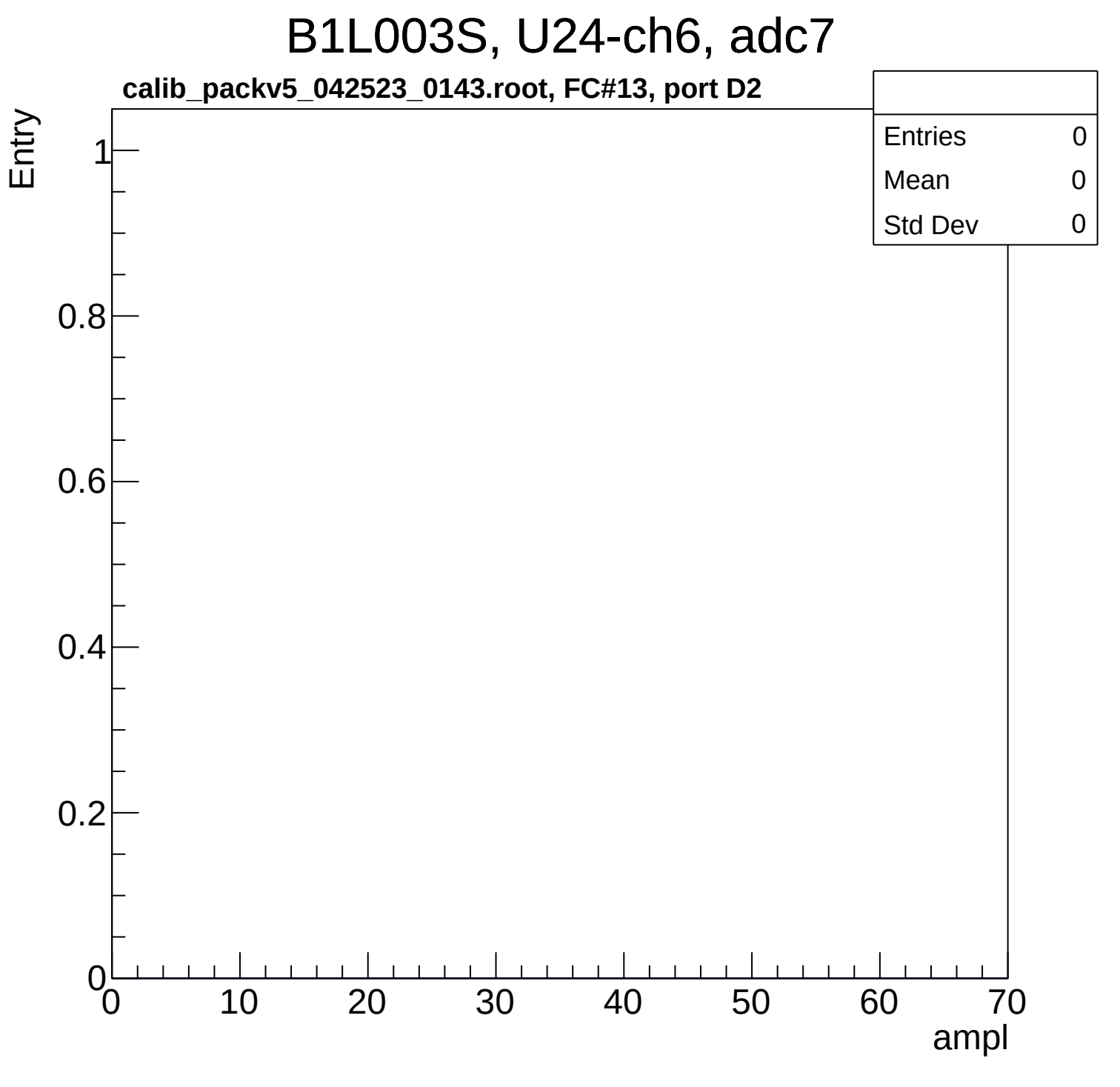
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch6, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 57.89
Std Dev 9.113
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch6, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 61.6
Std Dev 1.53
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch6, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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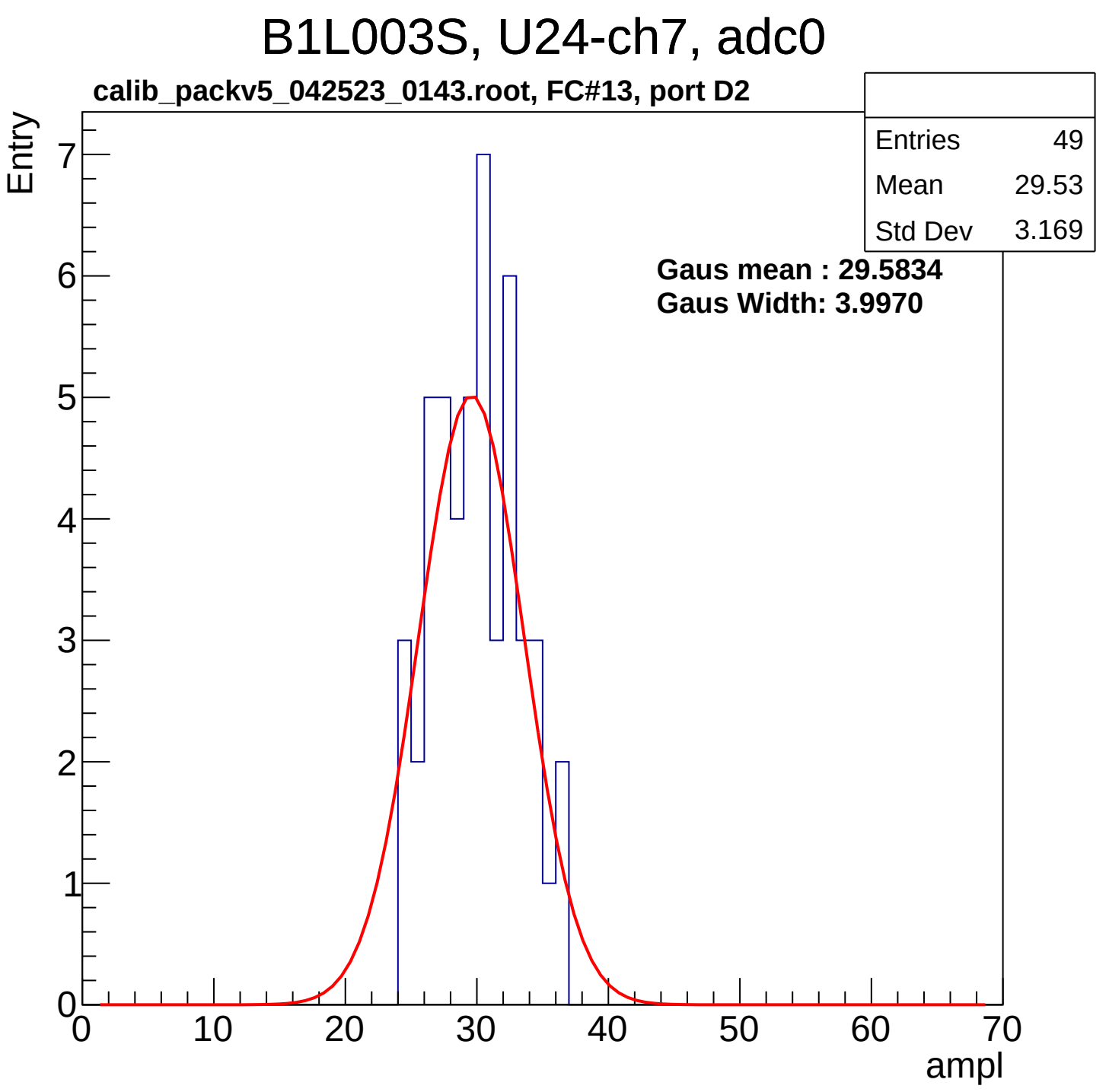
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch7, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 29.53
Std Dev 3.169
Gaus mean : 29.5834
Gaus Width: 3.9970
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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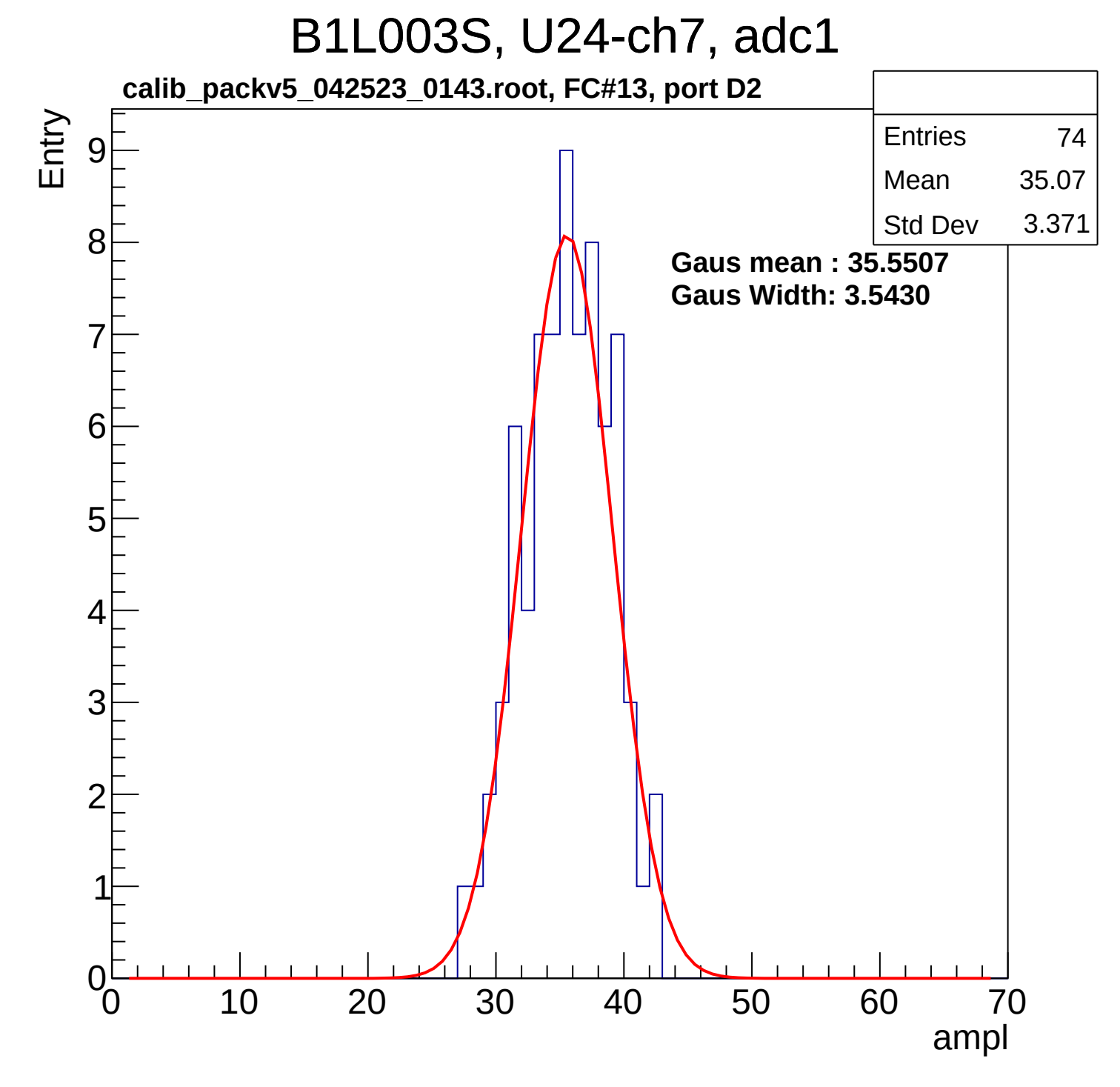
B1L003S, U24-ch7, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 35.07
Std Dev 3.371
Gaus mean : 35.5507
Gaus Width: 3.5430
Entry
ampl
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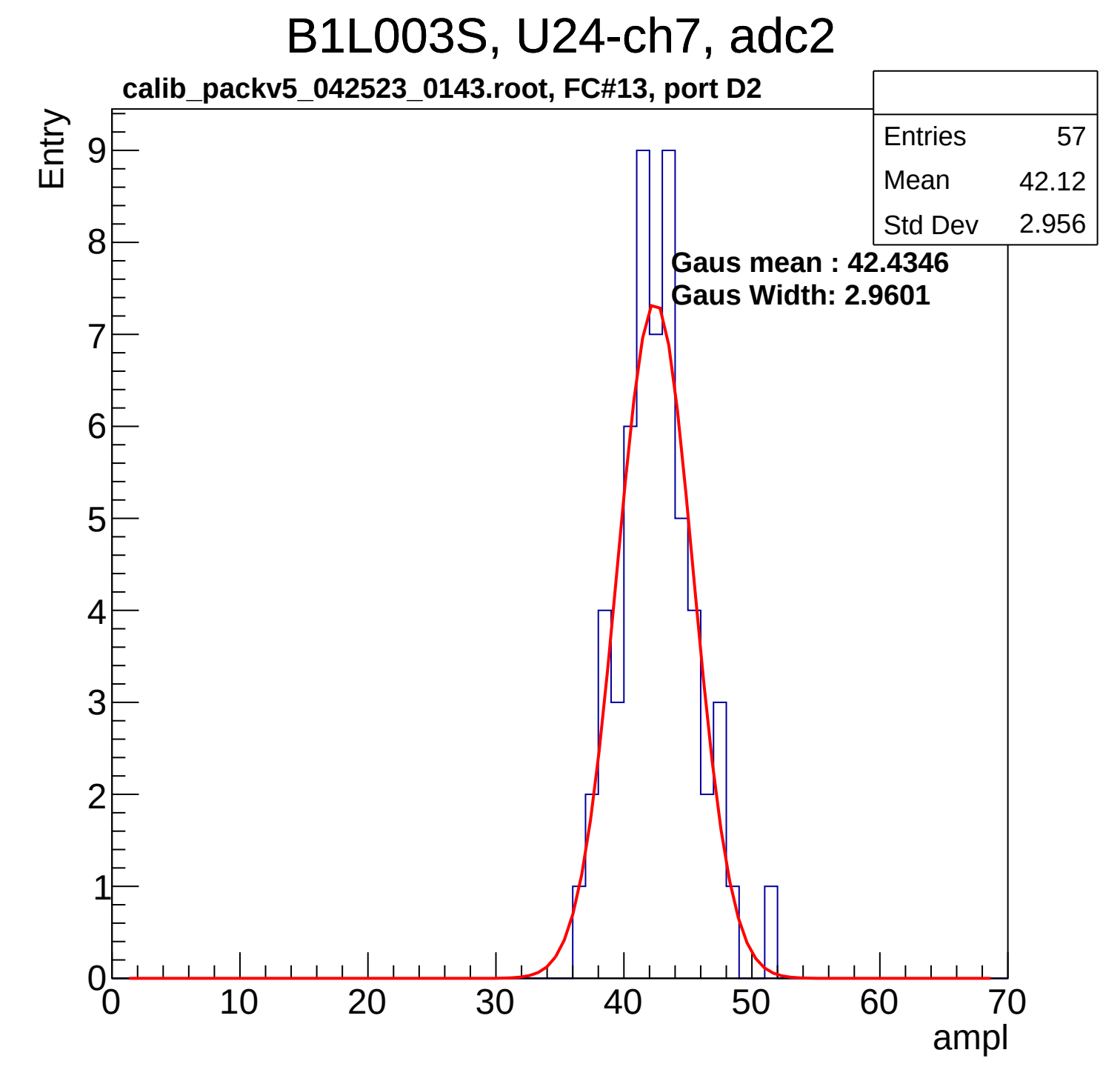
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch7, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 42.12
Std Dev 2.956
Gaus mean : 42.4346
Gaus Width: 2.9601
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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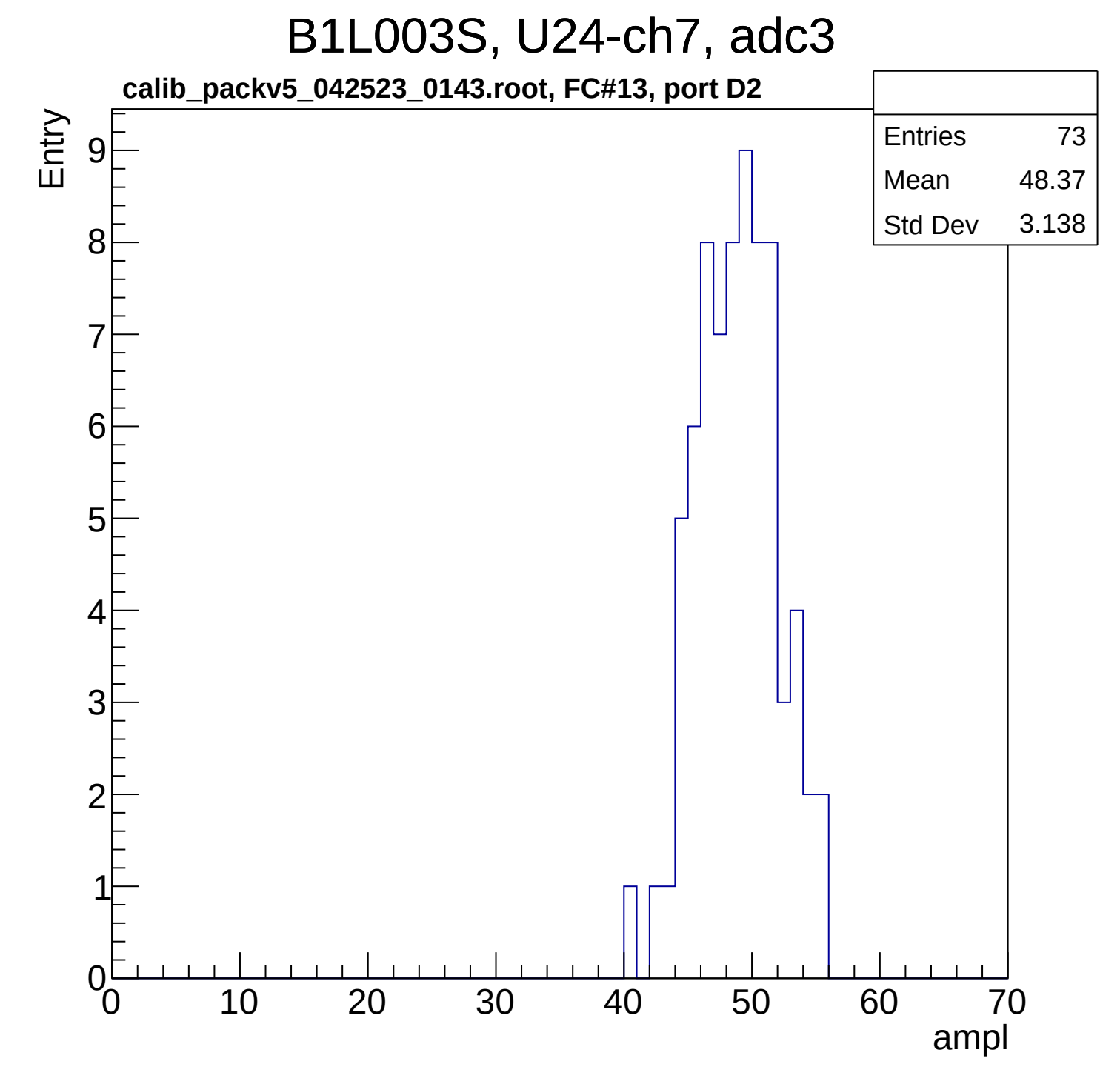
B1L003S, U24-ch7, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 48.37
Std Dev 3.138
Entry
ampl
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2
3
4
5
6
7
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9
0
10
20
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60
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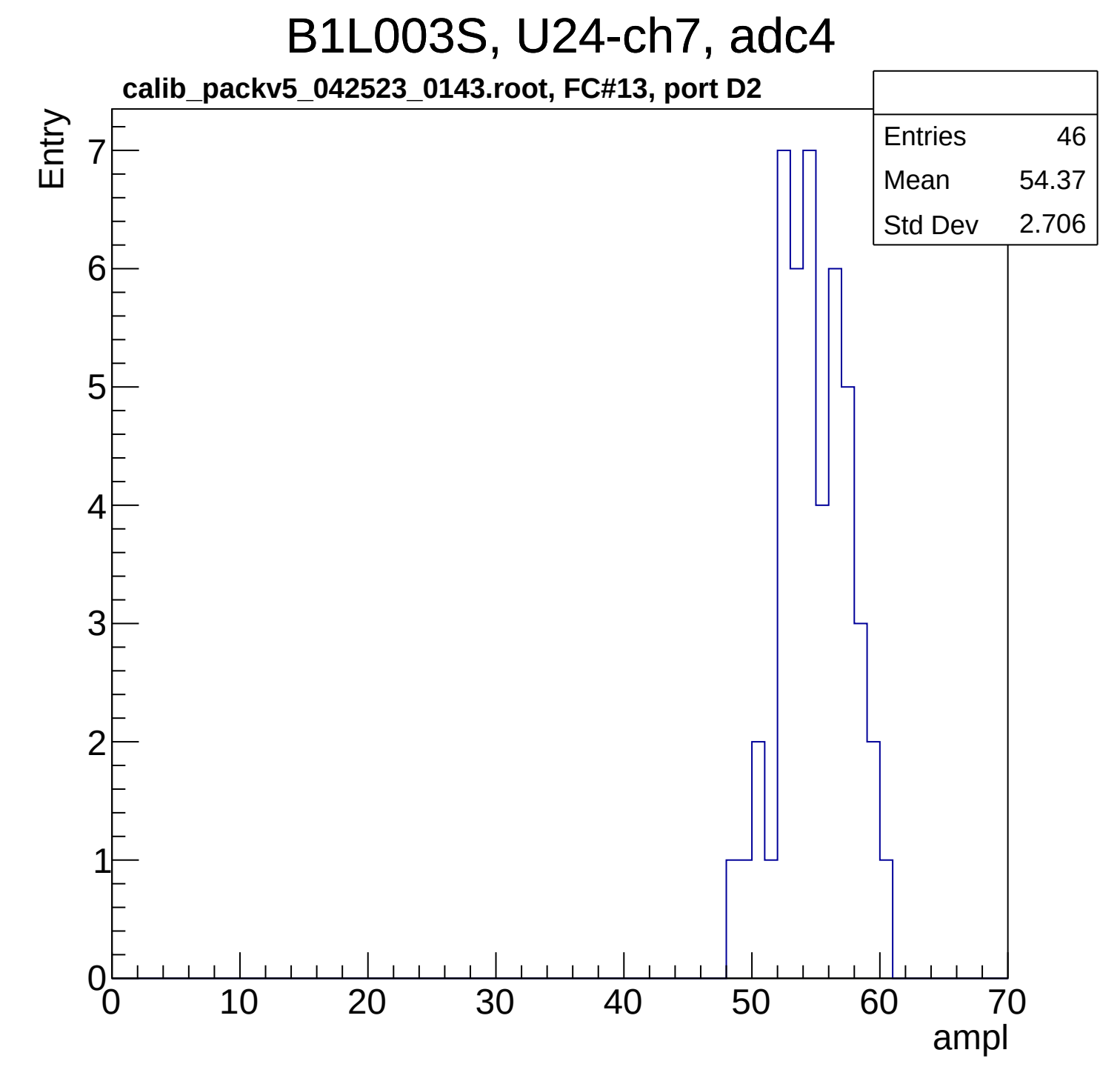
B1L003S, U24-ch7, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 54.37
Std Dev 2.706
Entry
ampl
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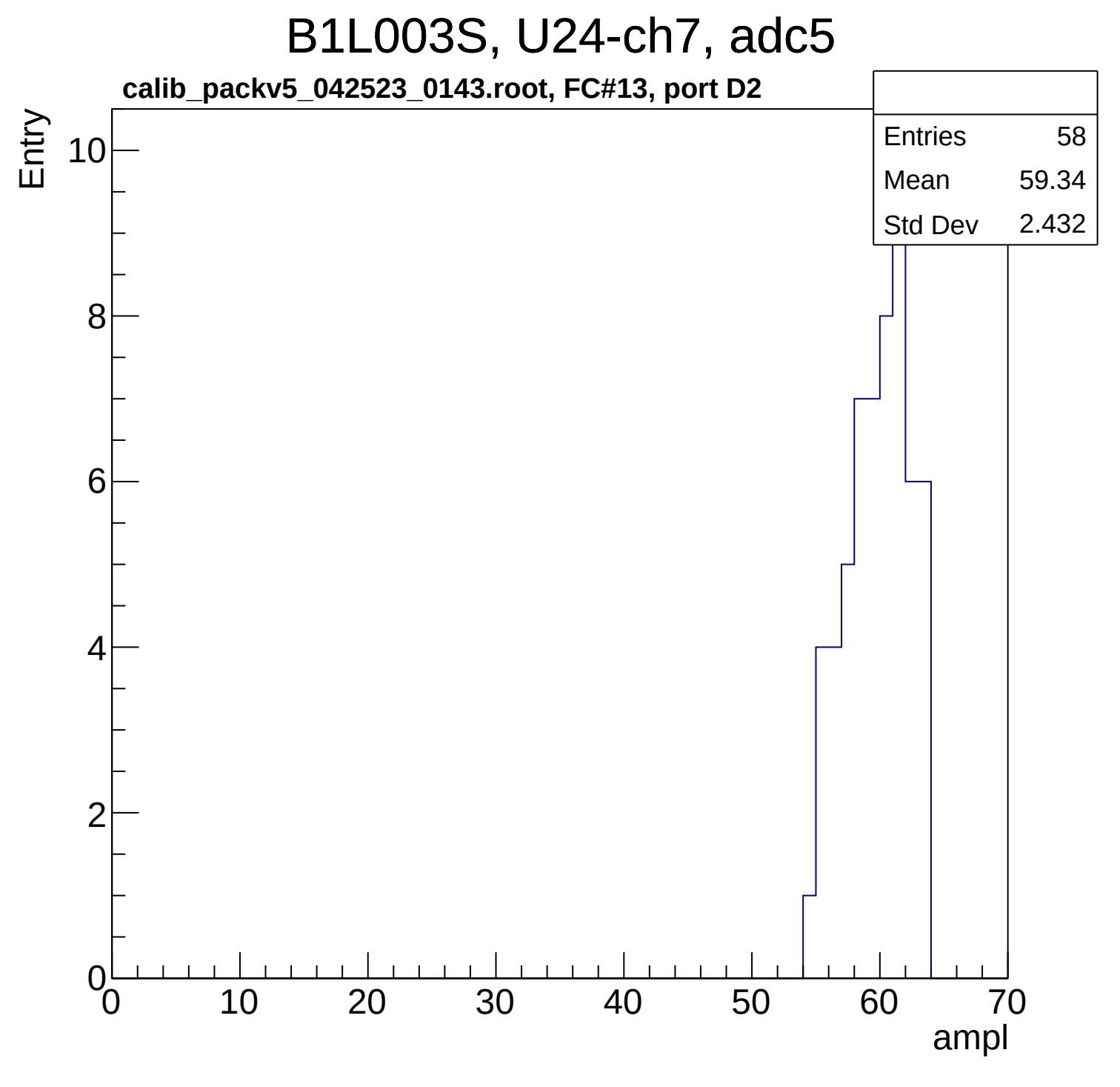
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch7, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 59.34
Std Dev 2.432
Entry
0
2
4
6
8
10
0
10
20
30
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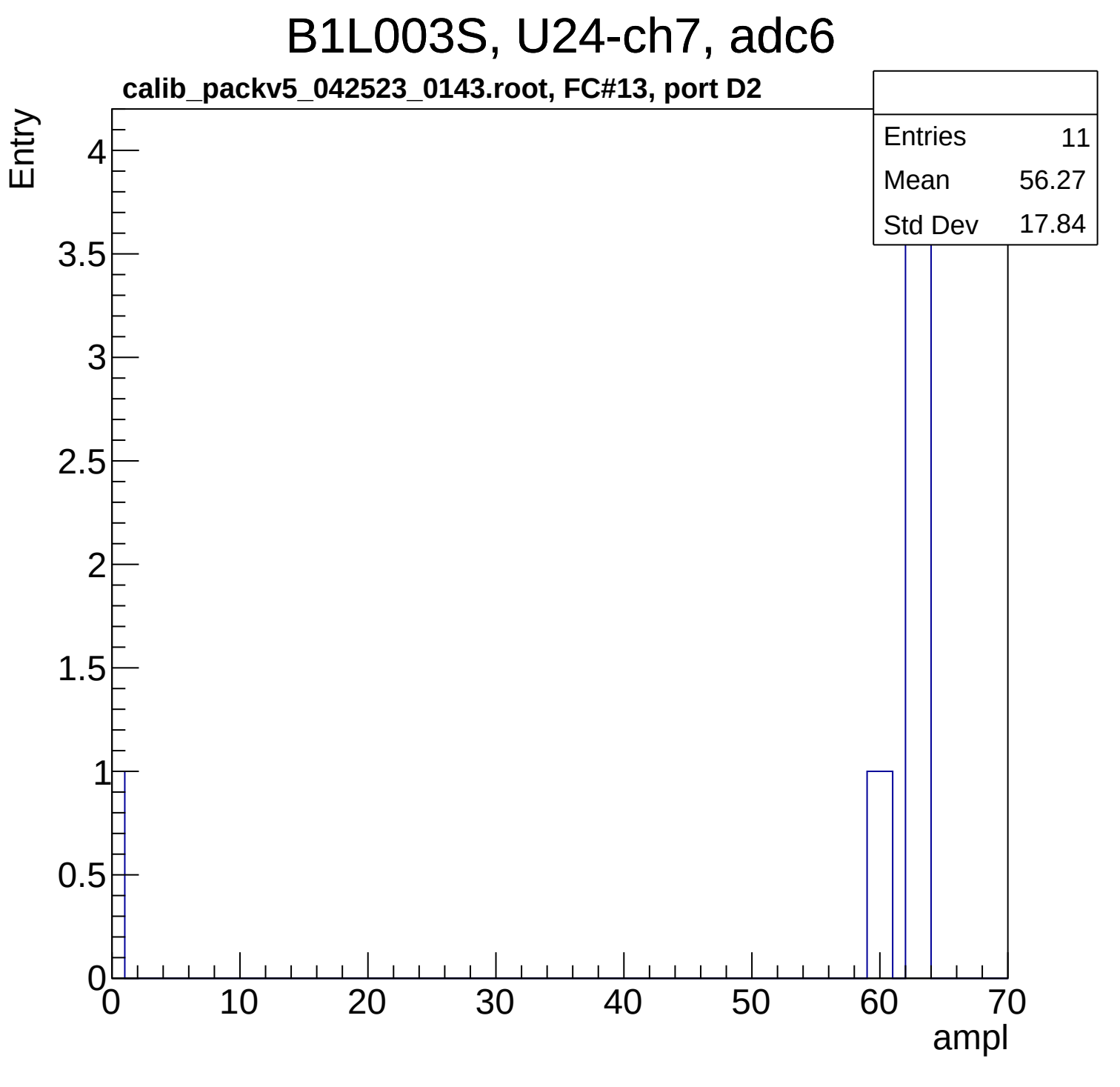
50
60
70
ampl


B1L003S, U24-ch7, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.27
Std Dev 17.84
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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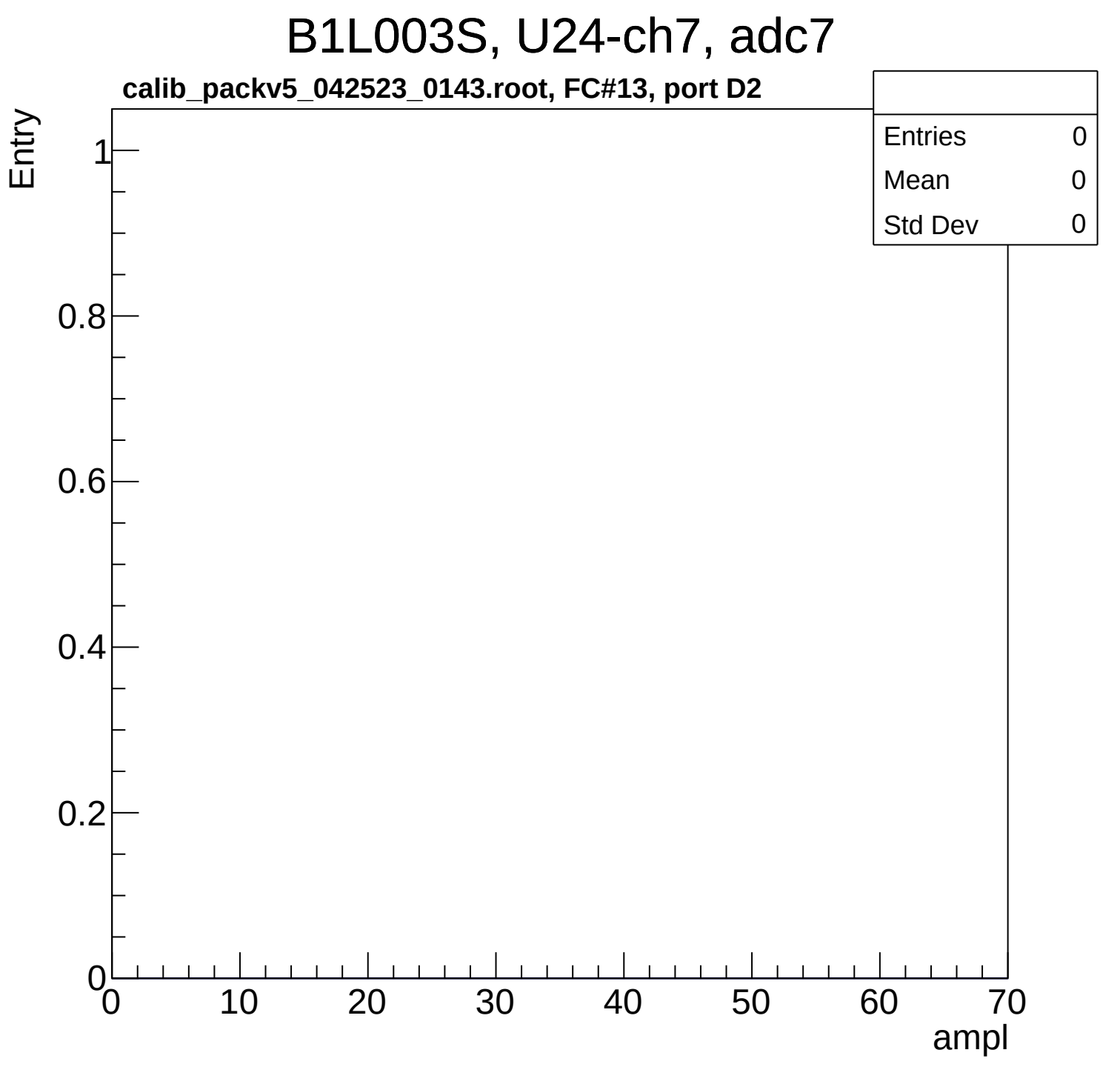
B1L003S, U24-ch7, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
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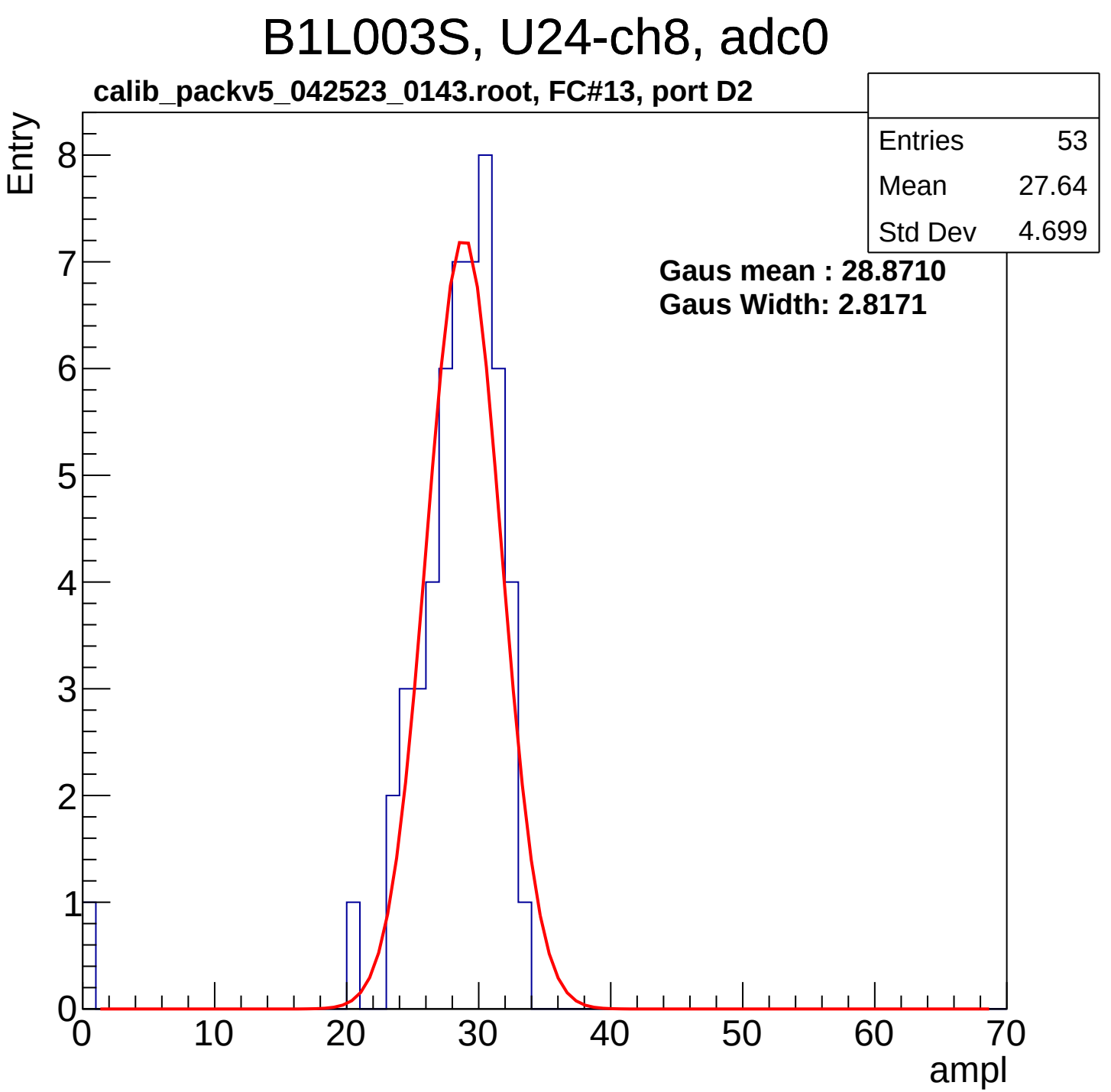
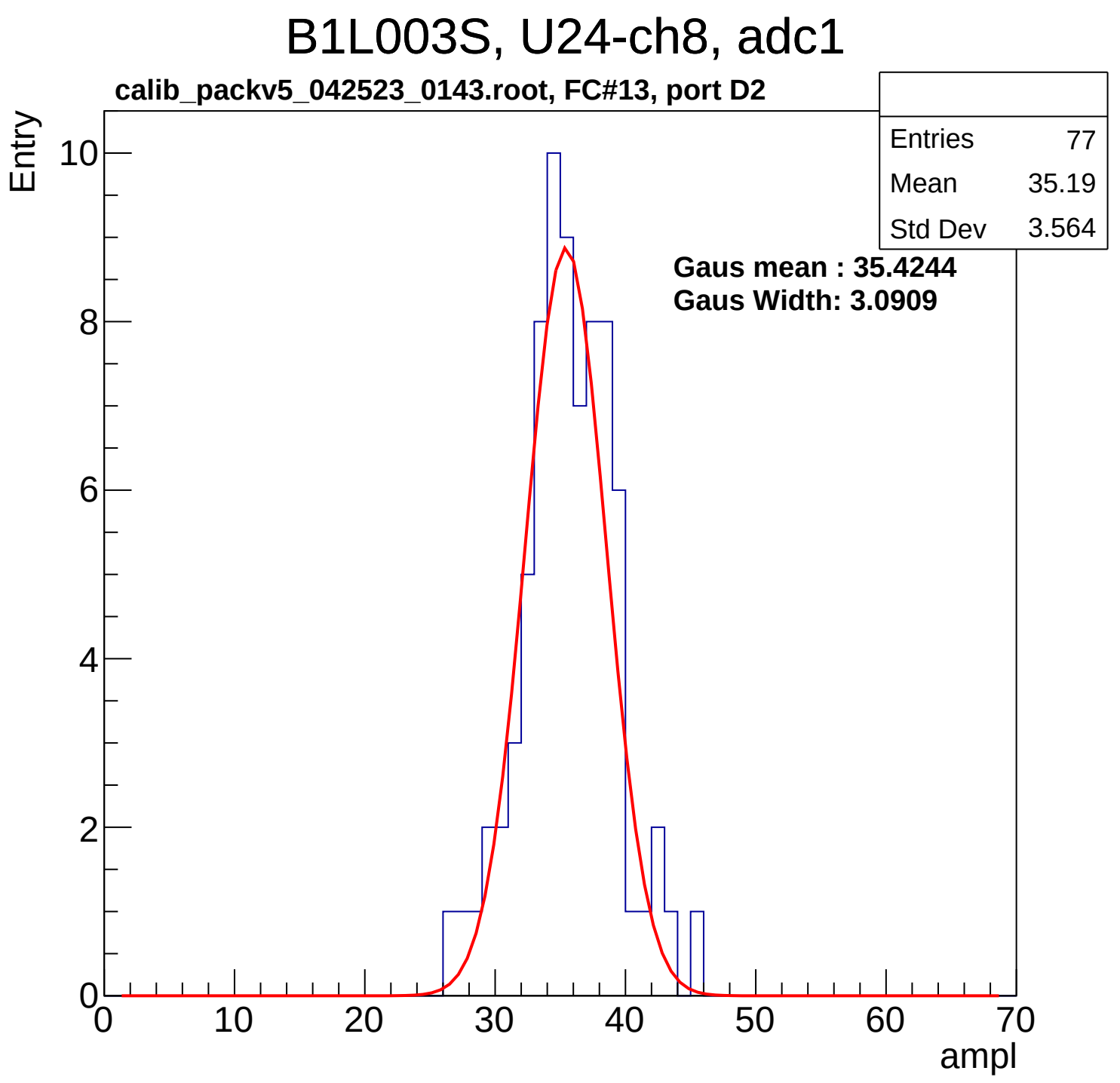
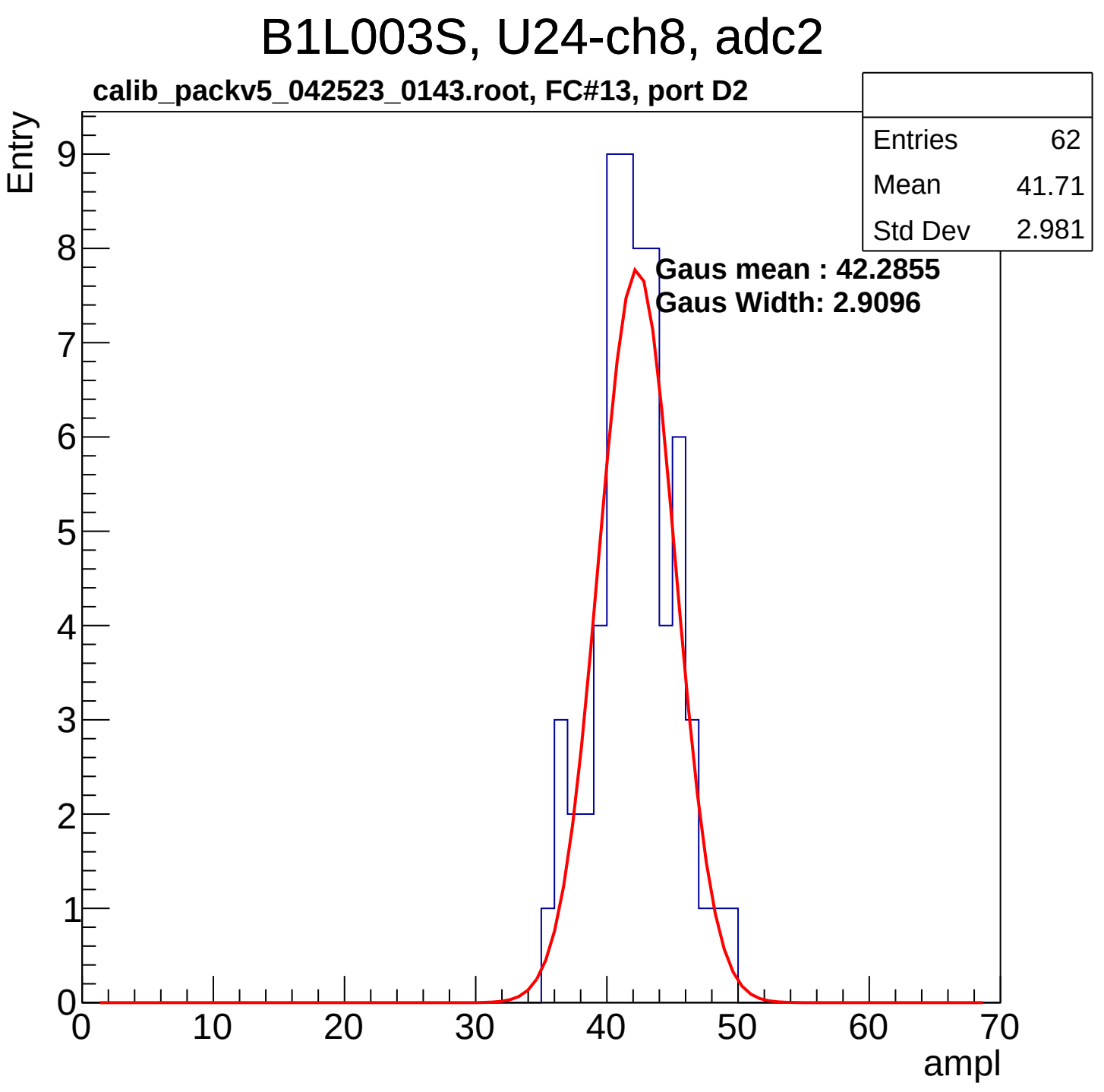
40
50
60
70
ampl


B1L003S, U24-ch8, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 27.64
Std Dev 4.699
Gaus mean : 28.8710
Gaus Width: 2.8171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch8, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 35.19
Std Dev 3.564
Gaus mean : 35.4244
Gaus Width: 3.0909
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch8, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 41.71
Std Dev 2.981
Gaus mean : 42.2855
Gaus Width: 2.9096
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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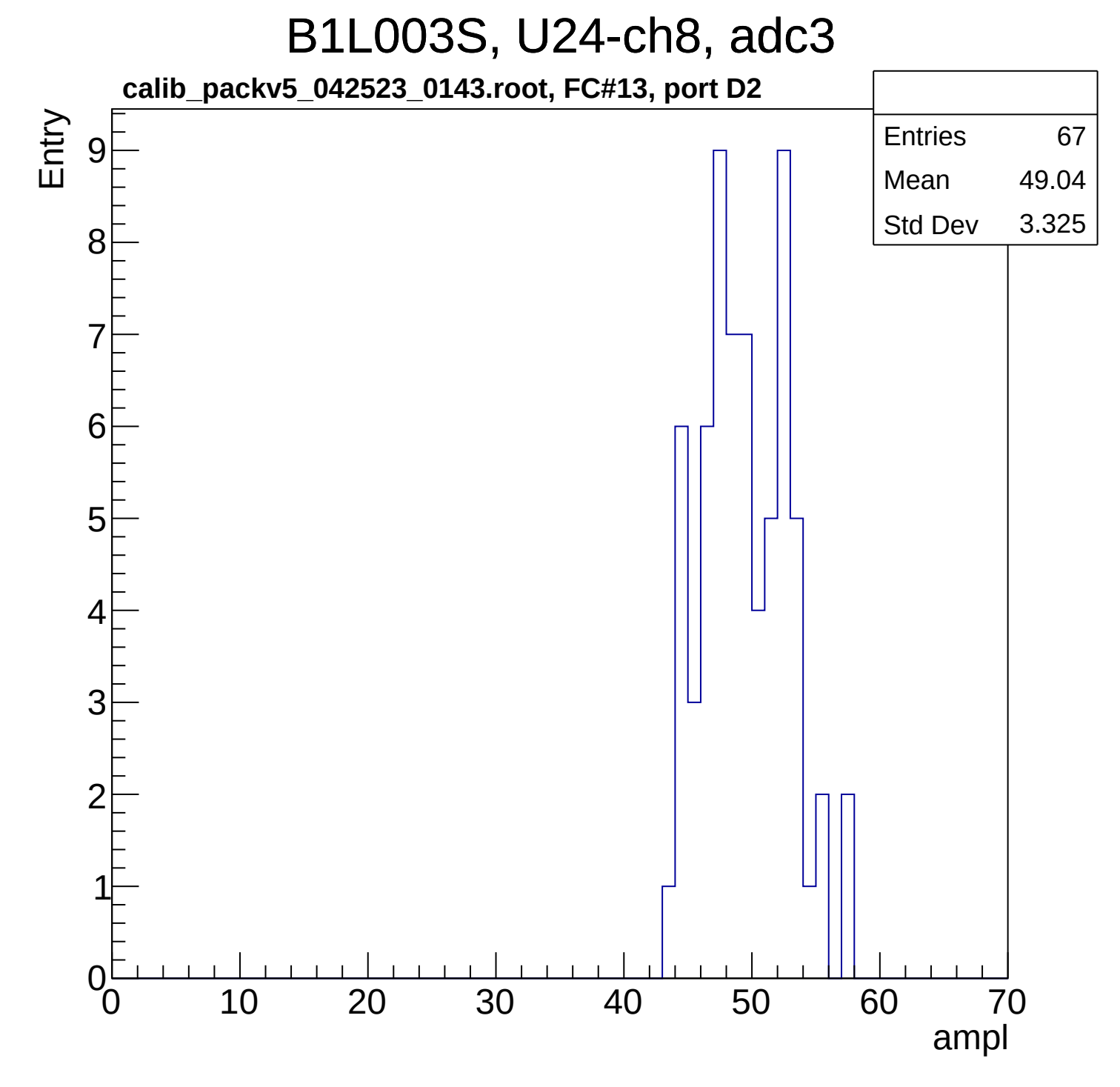
B1L003S, U24-ch8, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.04
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
50
60
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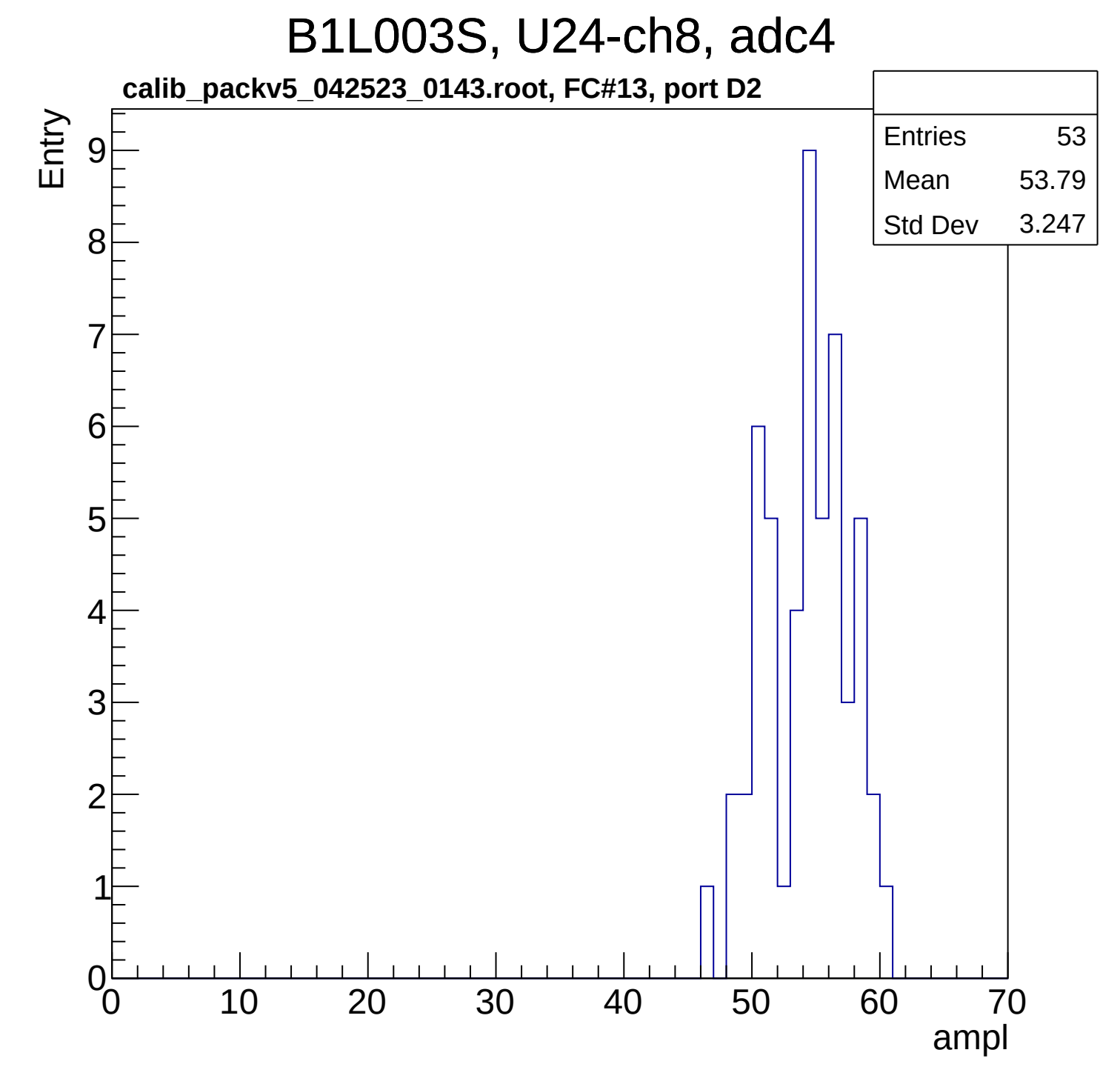
B1L003S, U24-ch8, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 53.79
Std Dev 3.247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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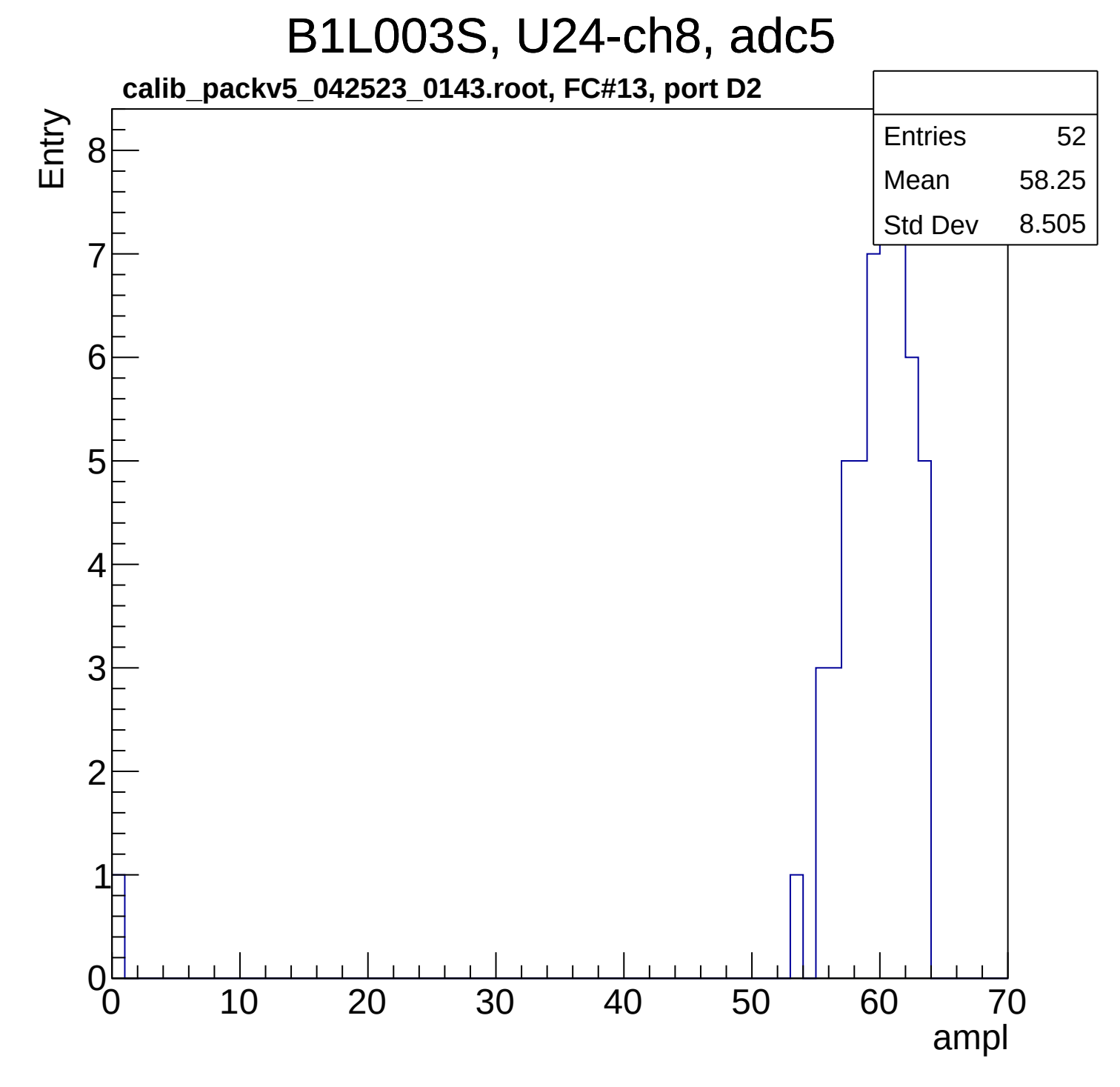
B1L003S, U24-ch8, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.25
Std Dev 8.505
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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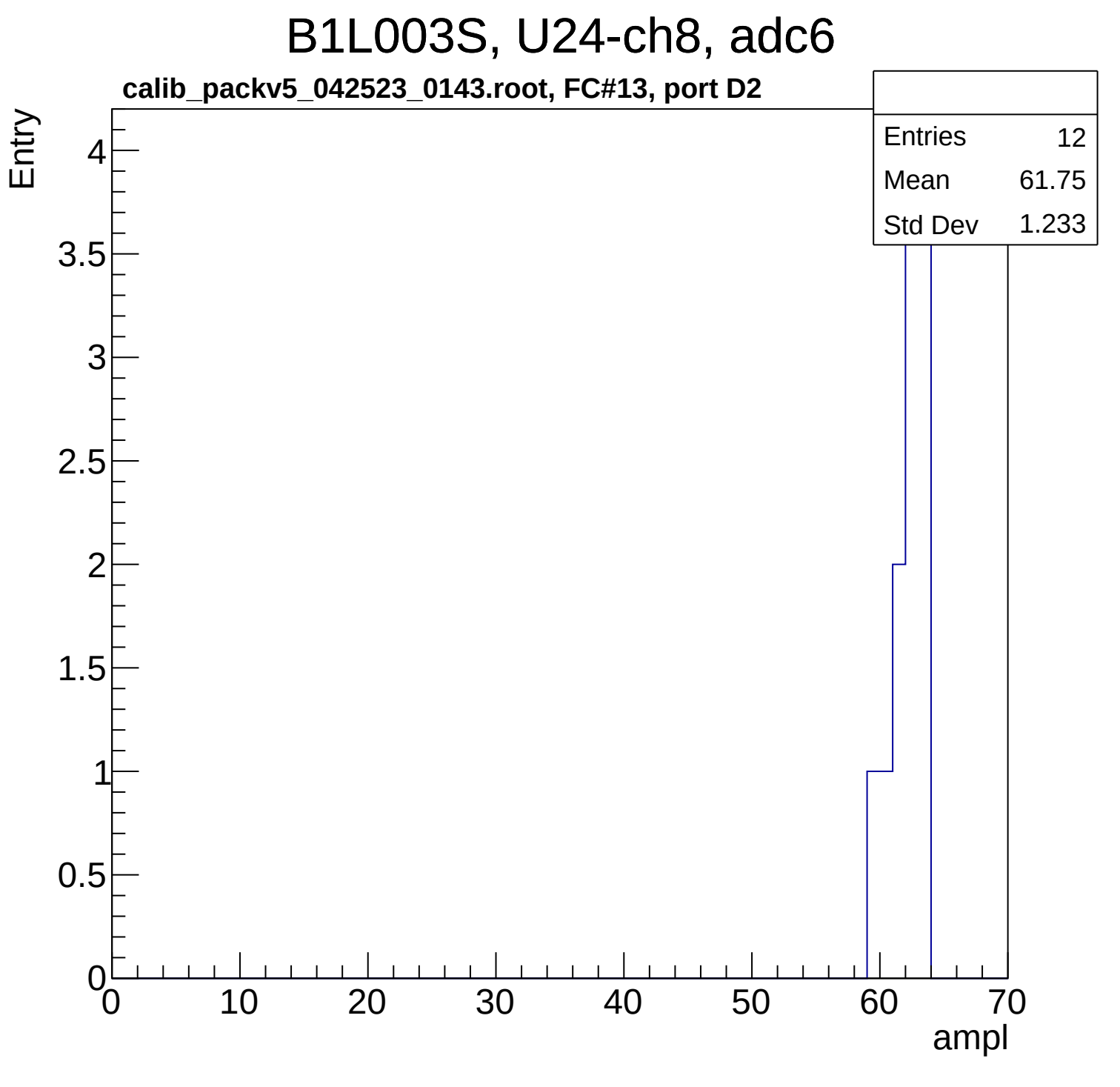
B1L003S, U24-ch8, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.75
Std Dev 1.233
Entry
ampl
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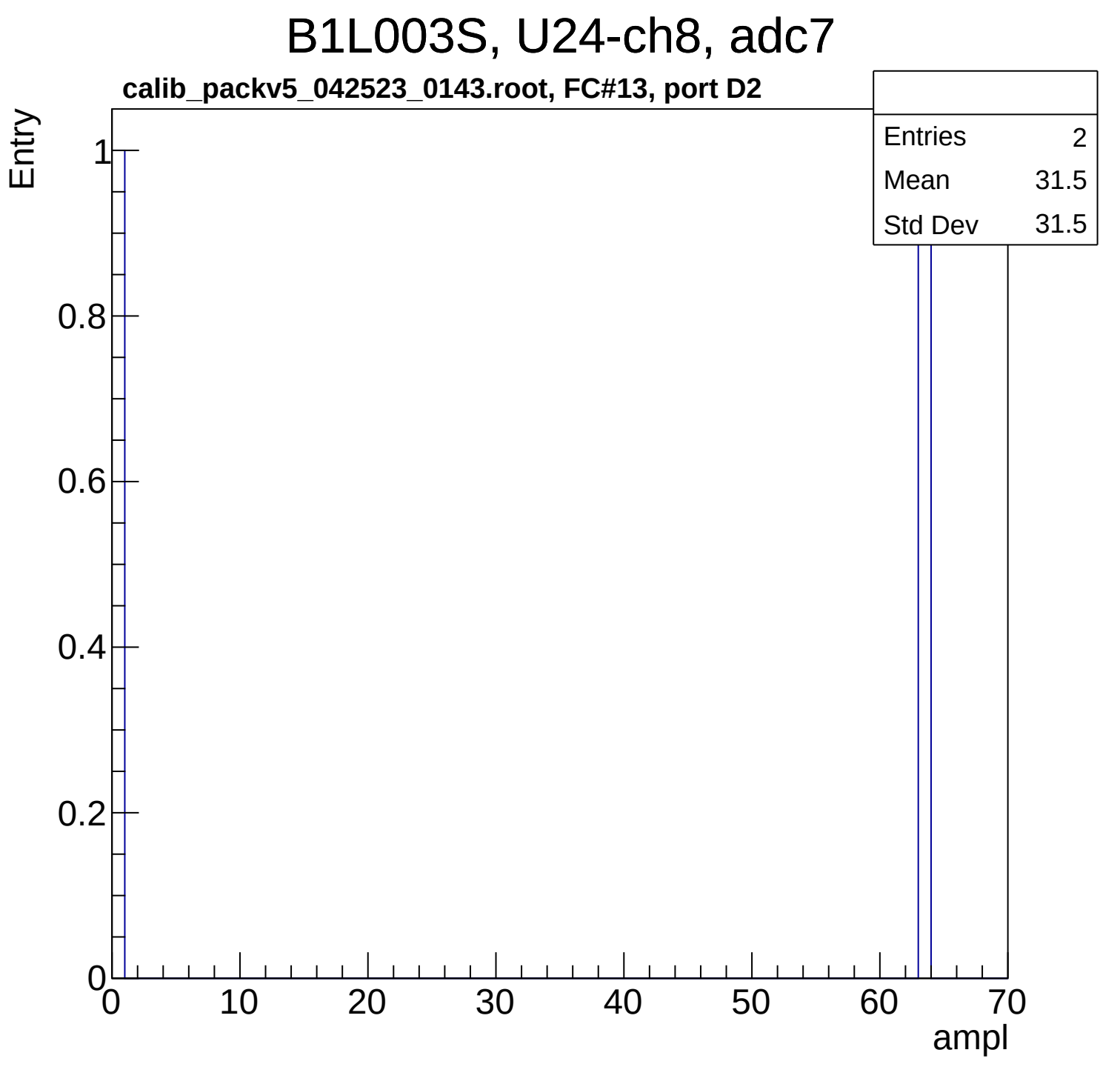
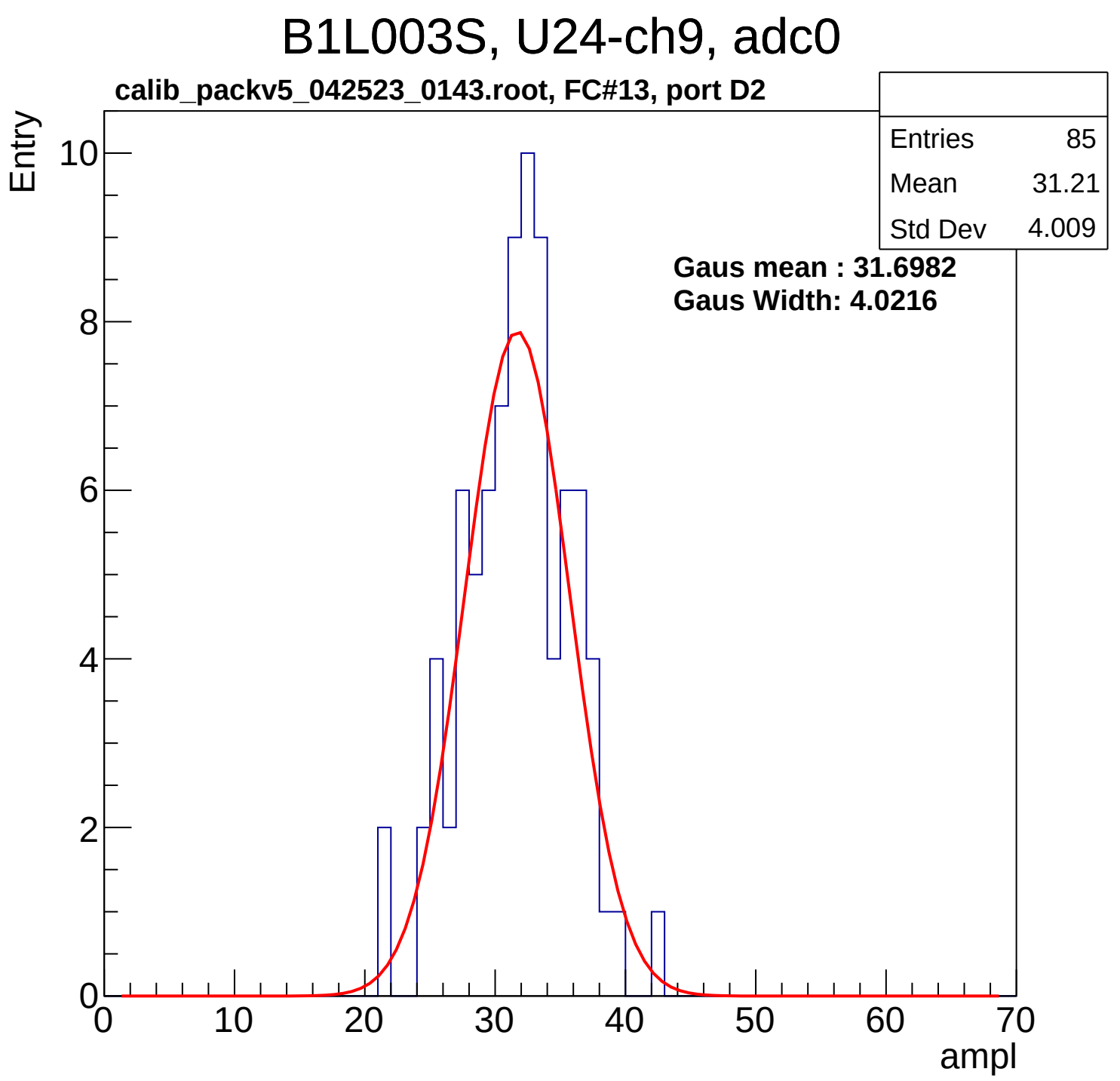
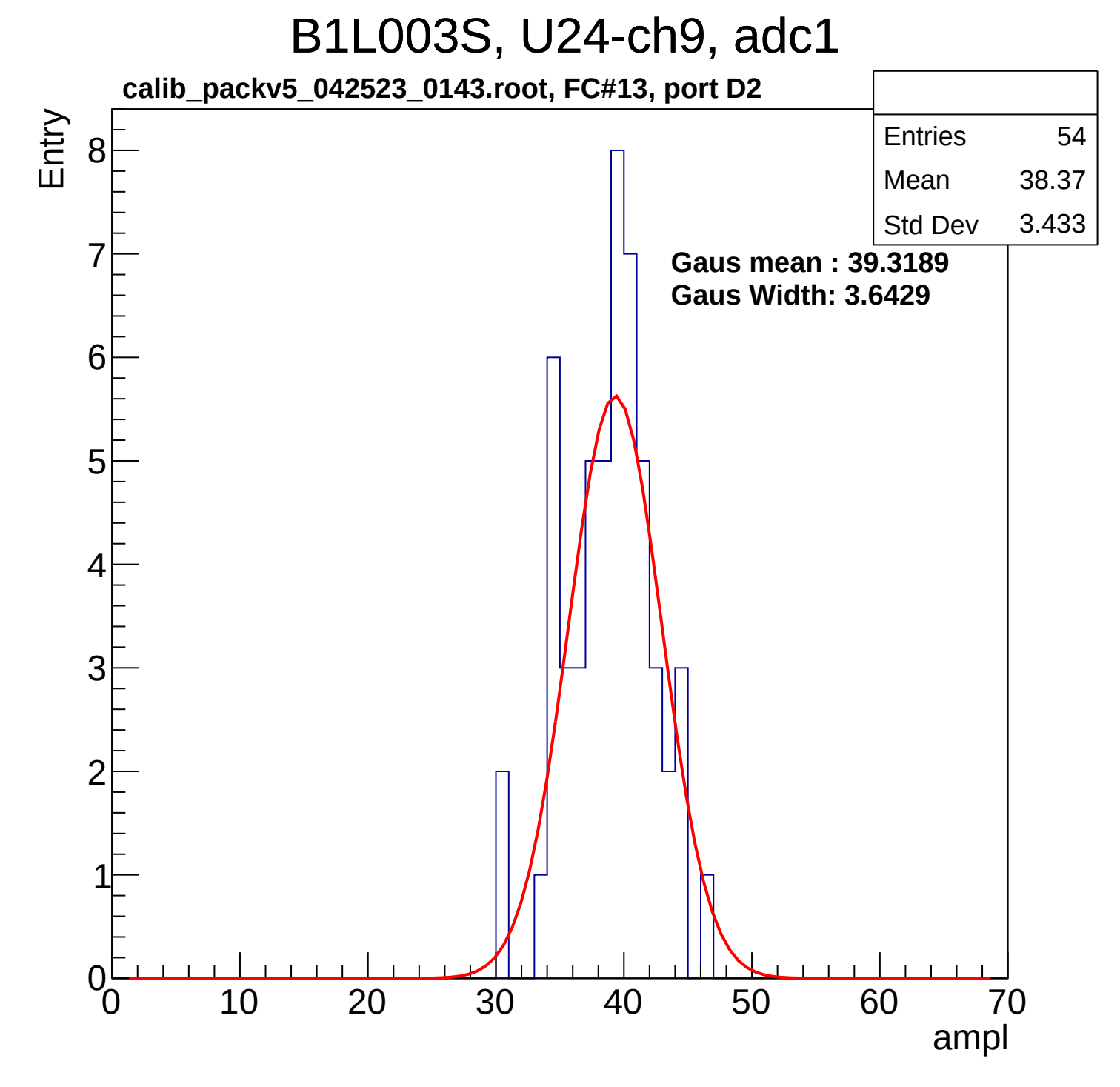
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U24-ch8, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch9, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 31.21
Std Dev 4.009
Gaus mean : 31.6982
Gaus Width: 4.0216
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch9, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 38.37
Std Dev 3.433
Gaus mean : 39.3189
Gaus Width: 3.6429
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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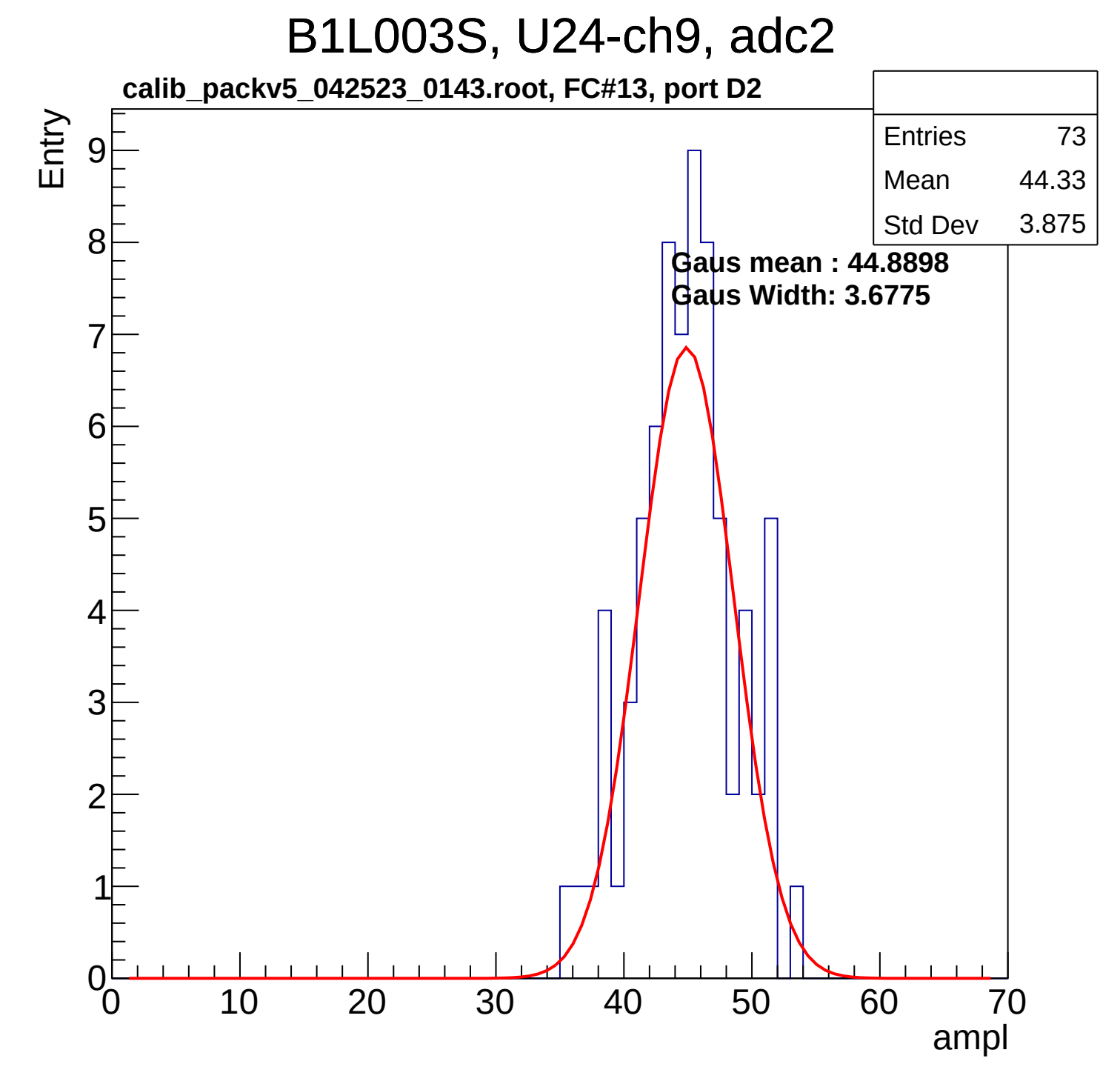
B1L003S, U24-ch9, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 44.33
Std Dev 3.875
Gaus mean : 44.8898
Gaus Width: 3.6775
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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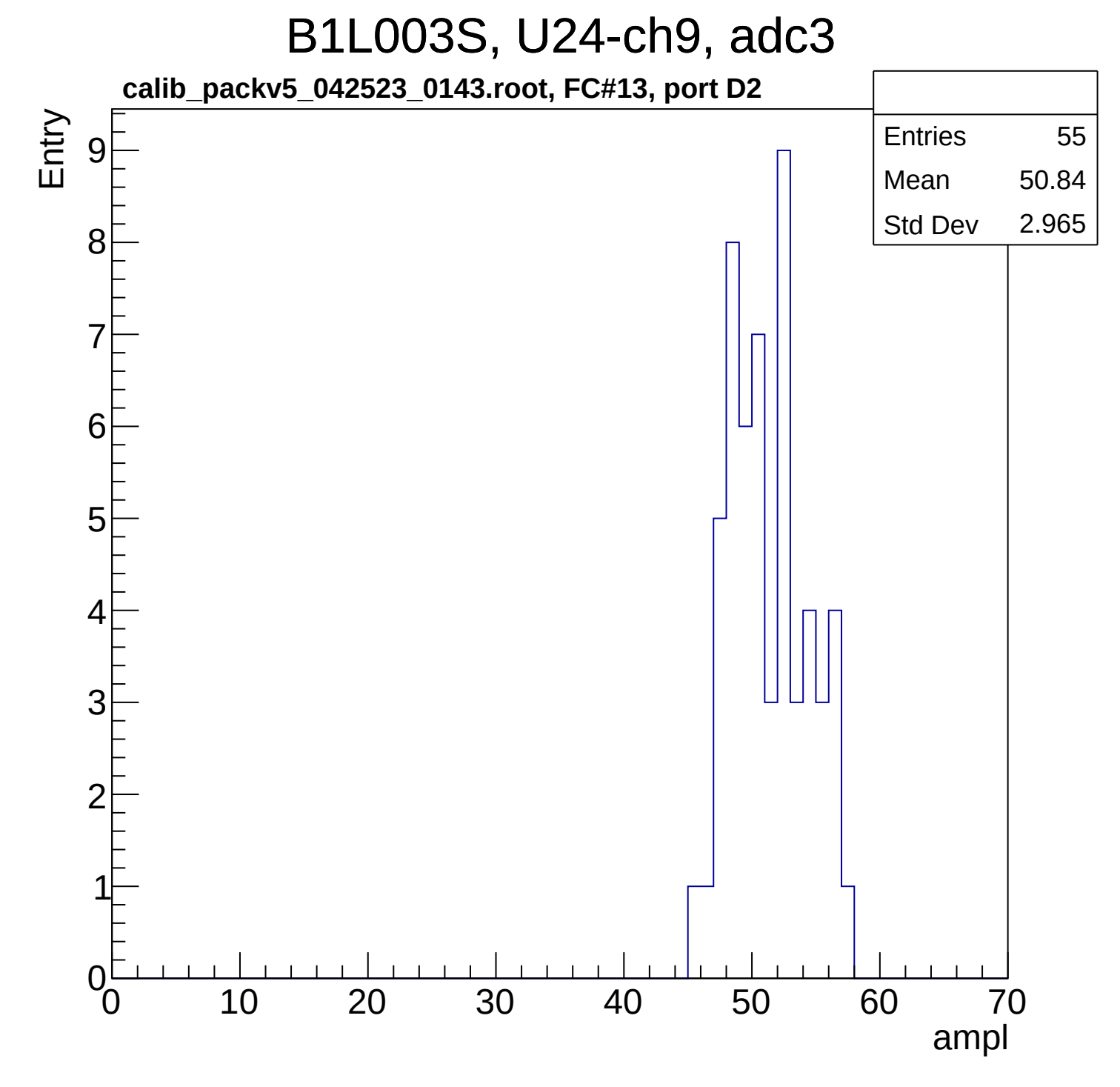
B1L003S, U24-ch9, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 50.84
Std Dev 2.965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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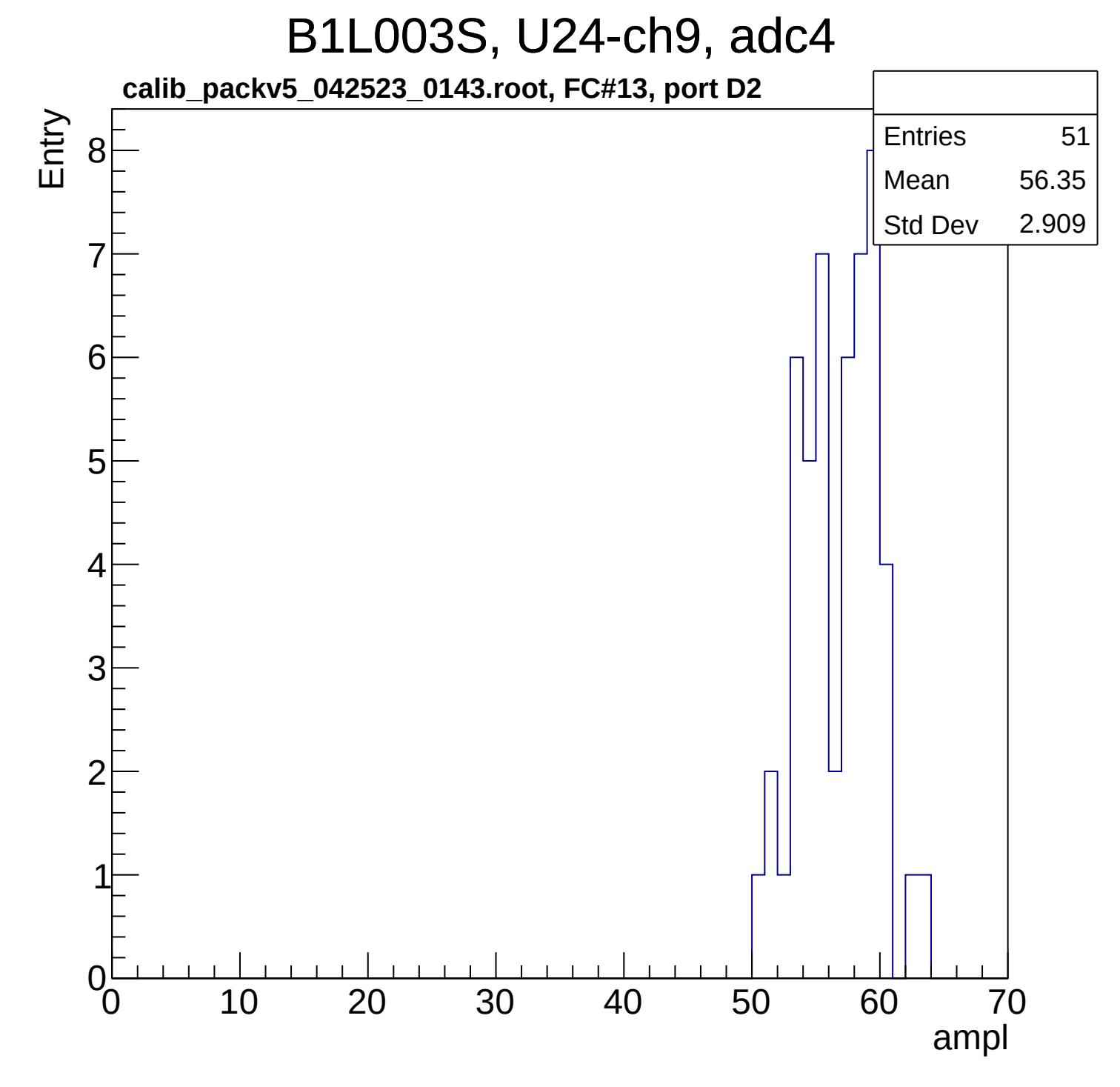
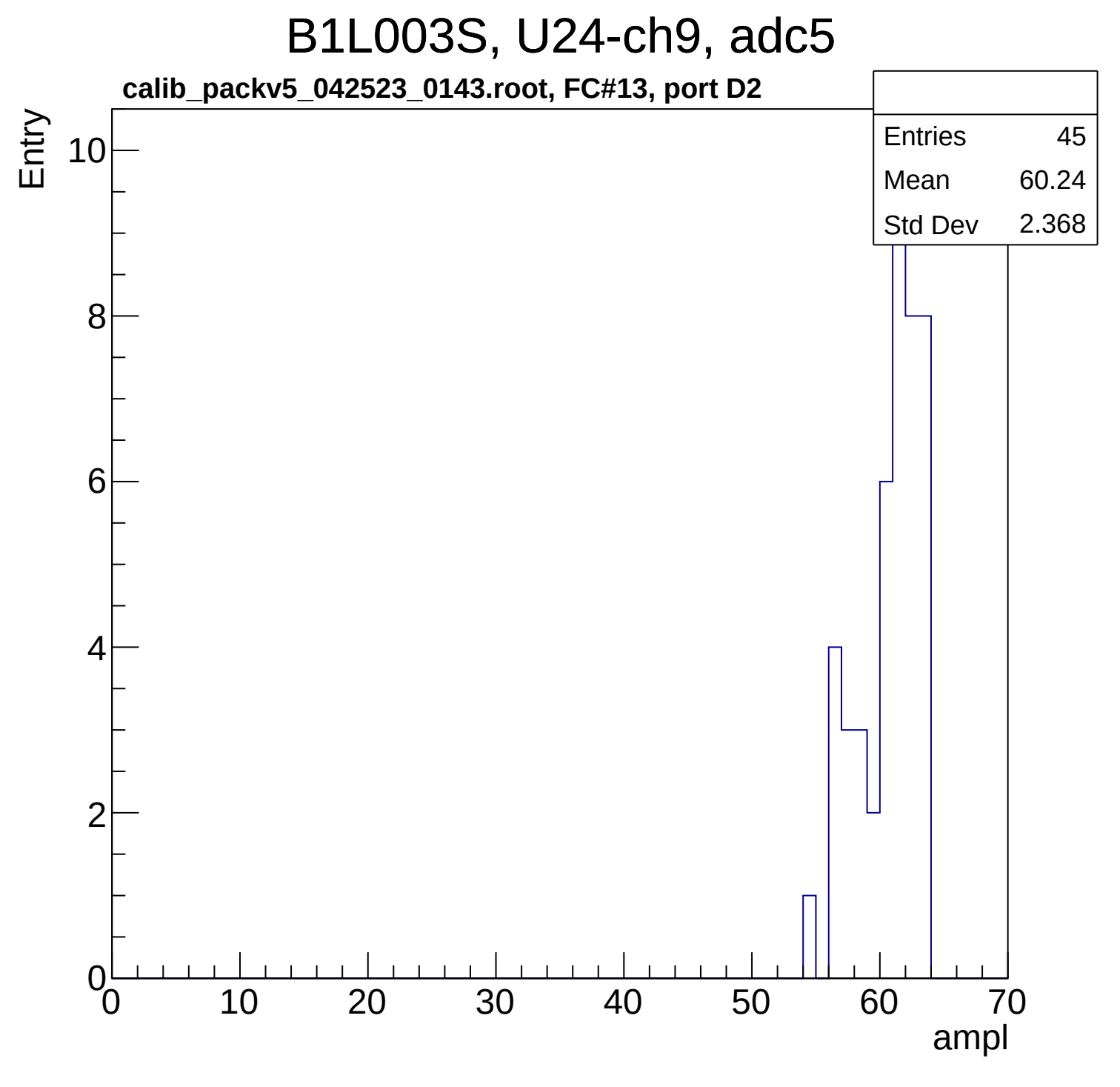
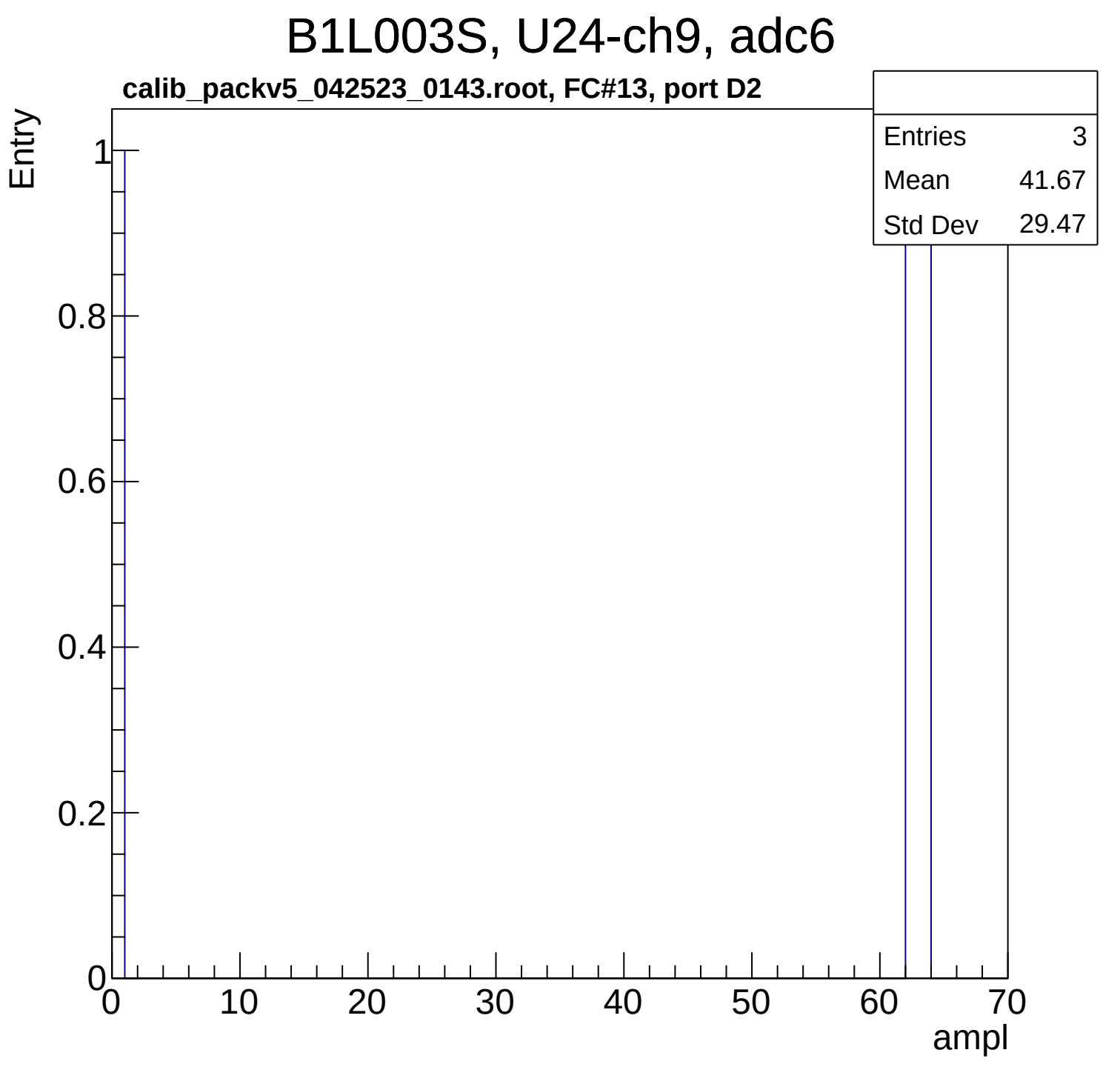
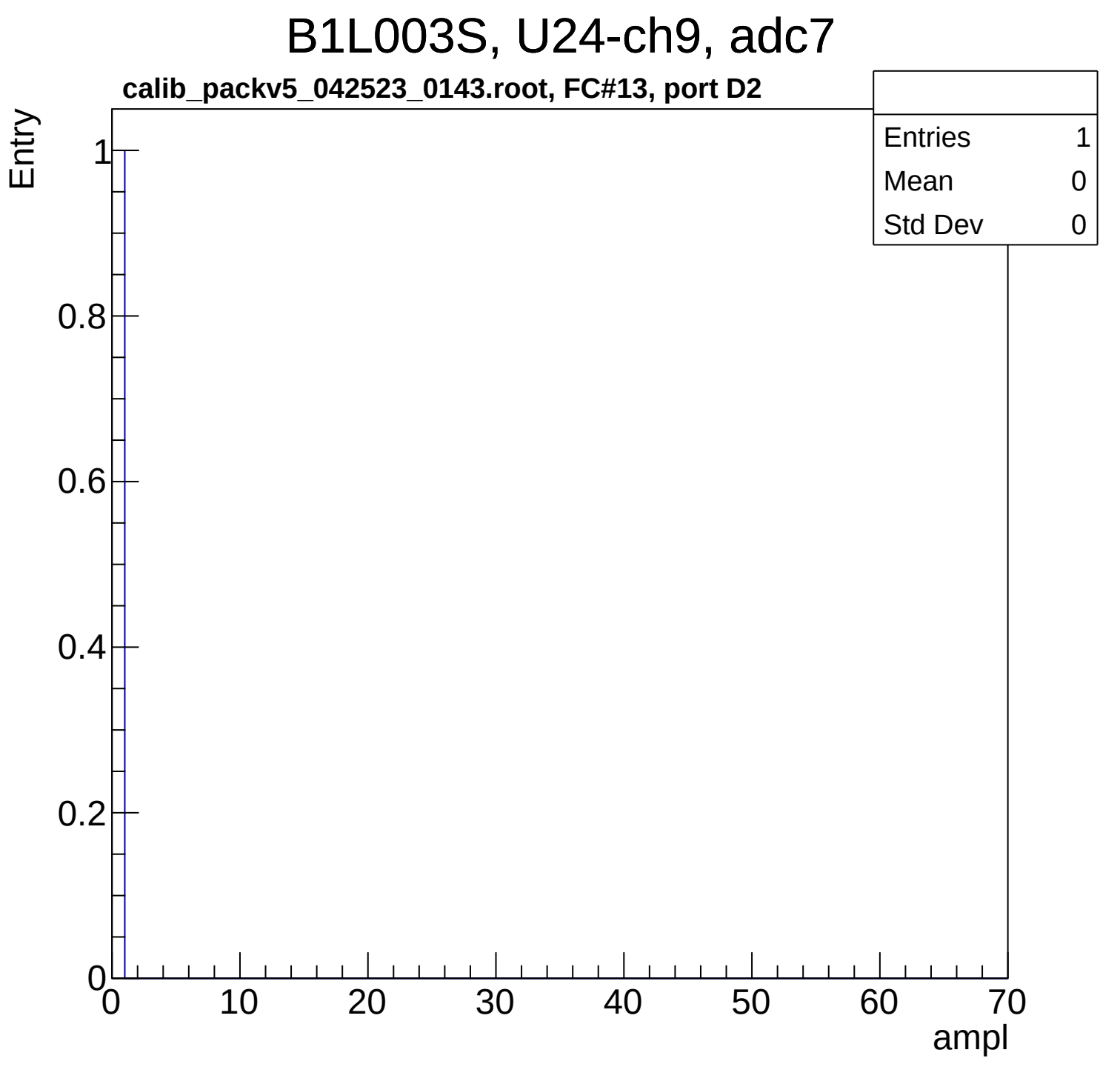
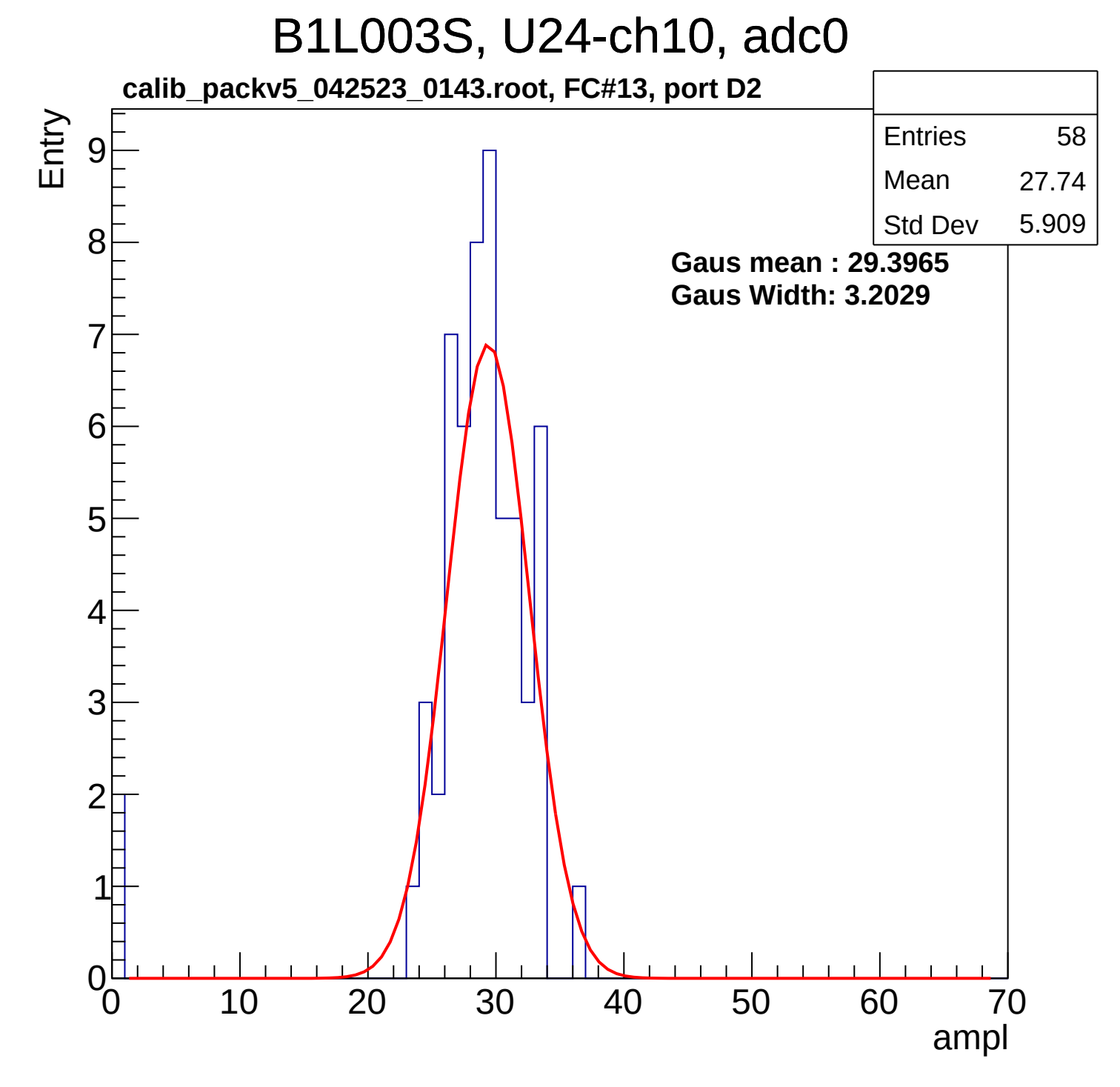

B1L003S, U24-ch9, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 56.35
Std Dev 2.909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch9, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 60.24
Std Dev 2.368
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch9, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch9, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch10, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 27.74
Std Dev 5.909
Gaus mean : 29.3965
Gaus Width: 3.2029
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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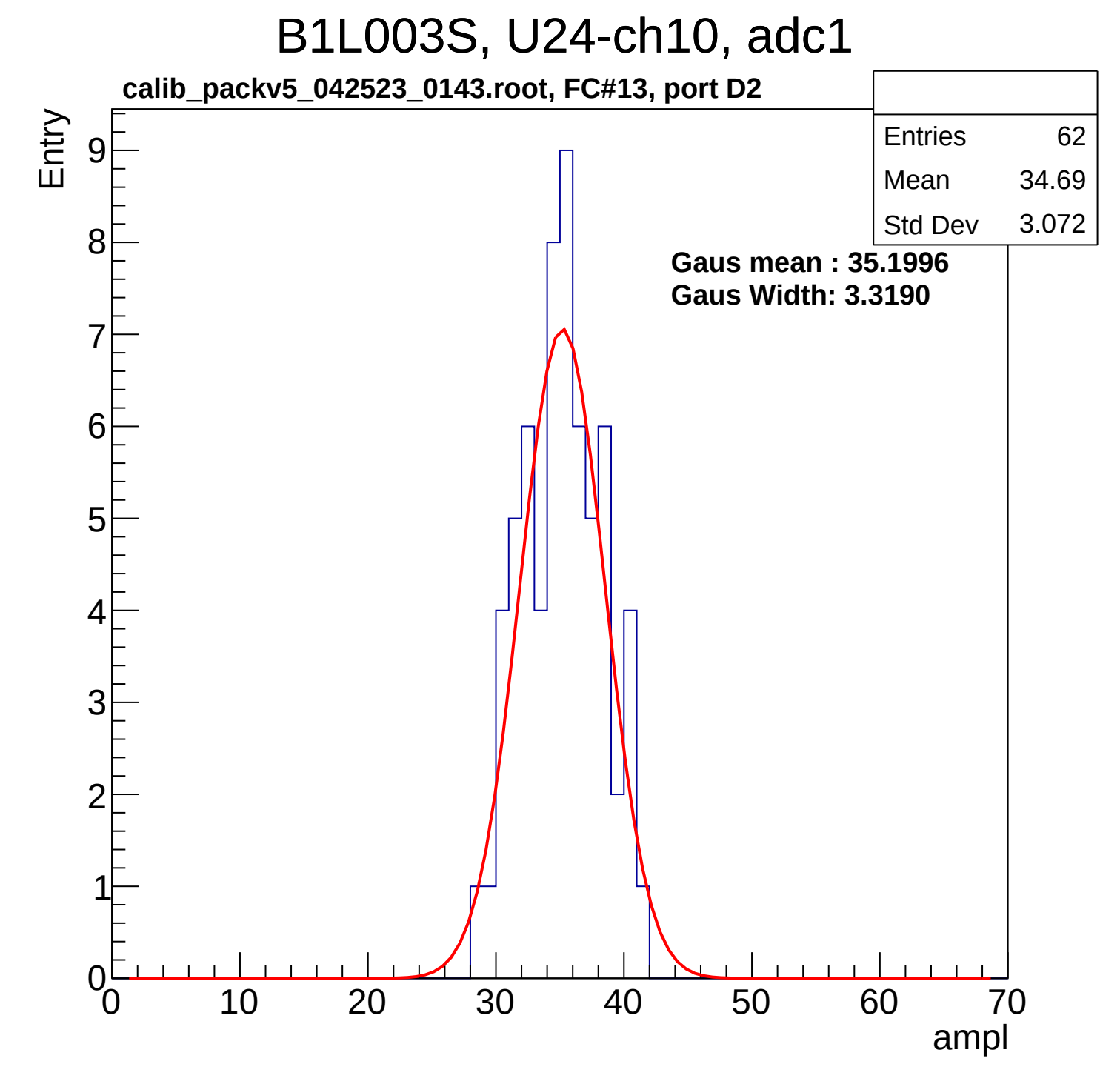
60
70


B1L003S, U24-ch10, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 34.69
Std Dev 3.072
Gaus mean : 35.1996
Gaus Width: 3.3190
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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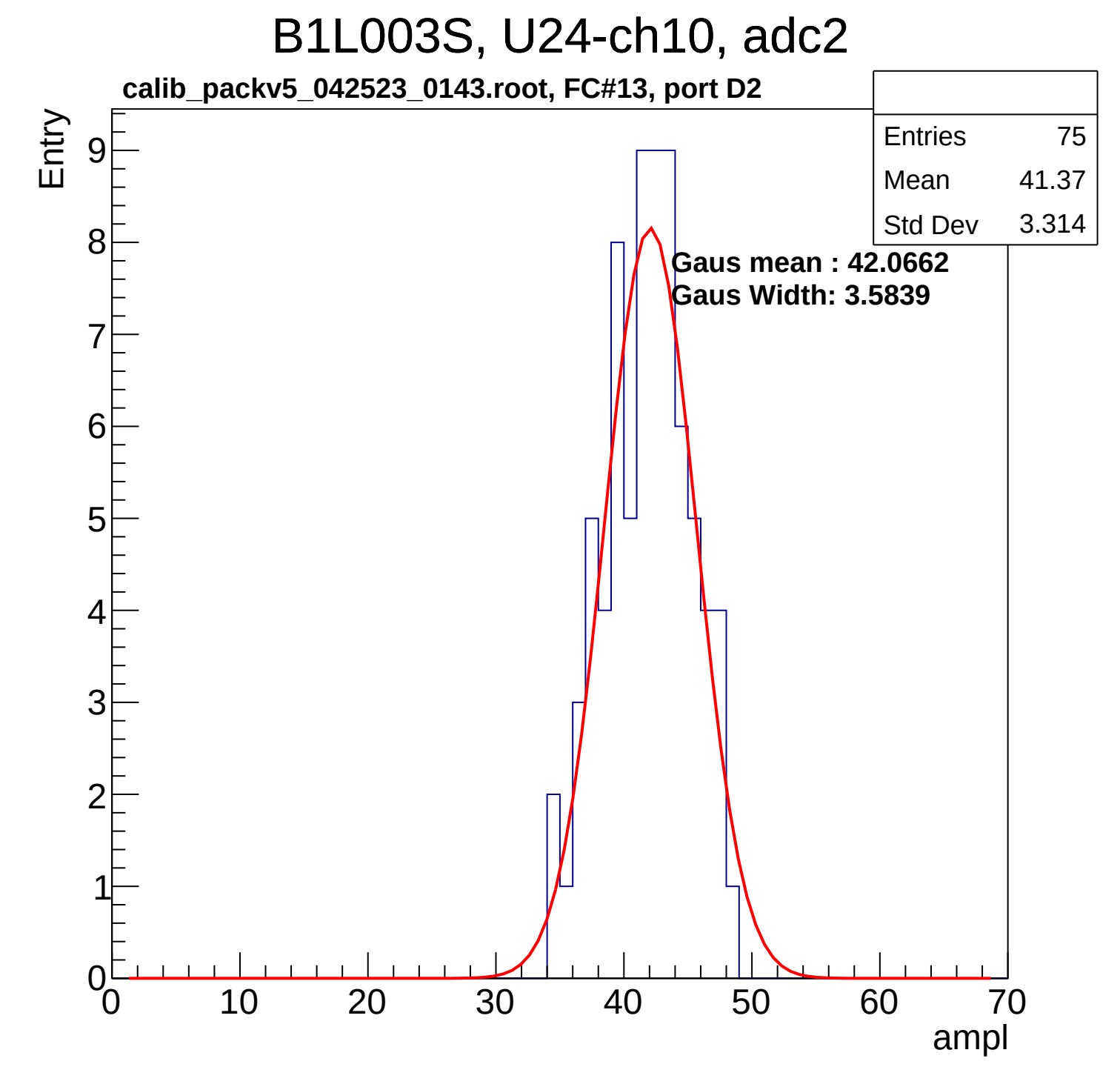
40
50
60
70


B1L003S, U24-ch10, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 41.37
Std Dev 3.314
Gaus mean : 42.0662
Gaus Width: 3.5839
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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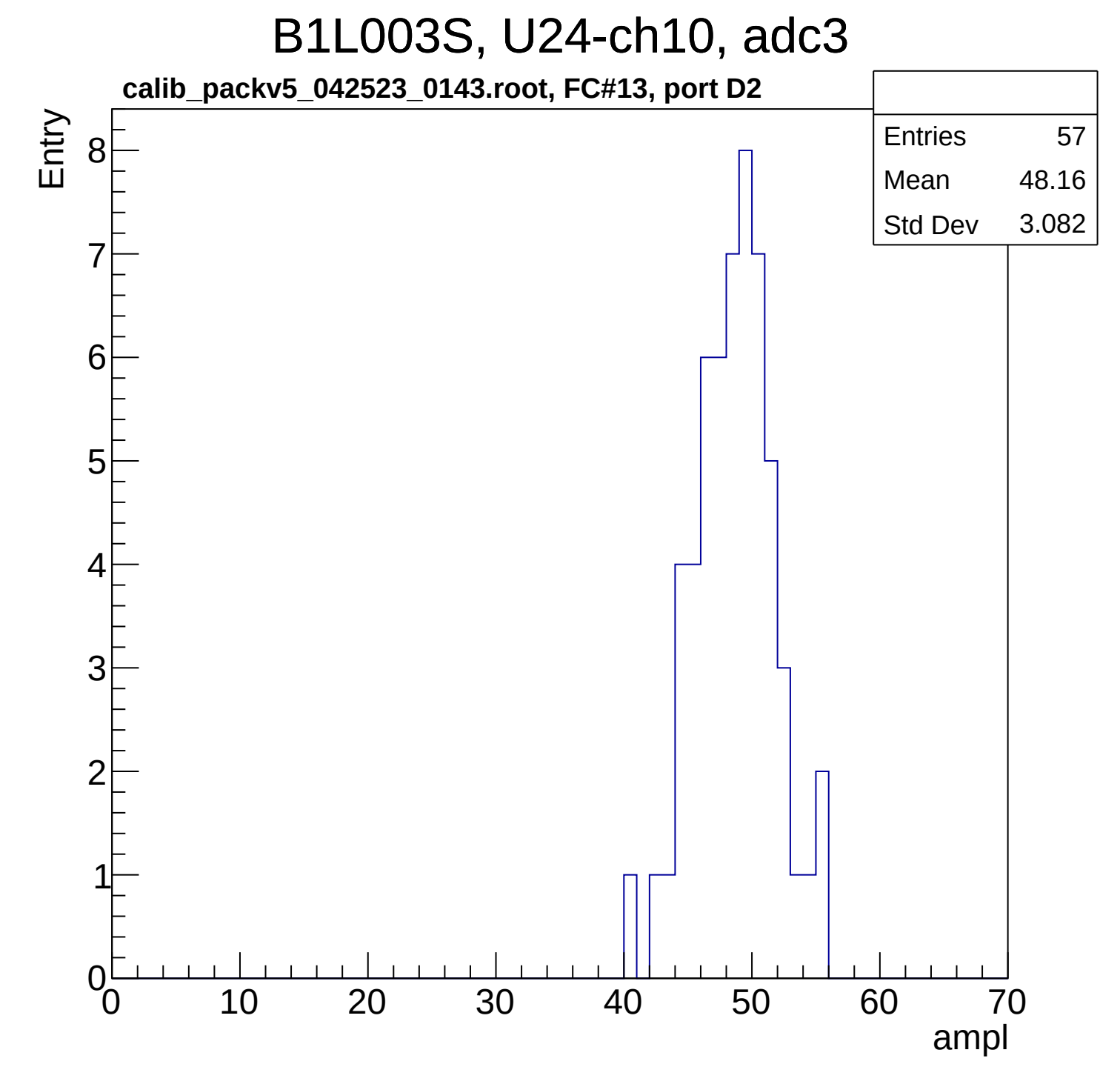
B1L003S, U24-ch10, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.16
Std Dev 3.082
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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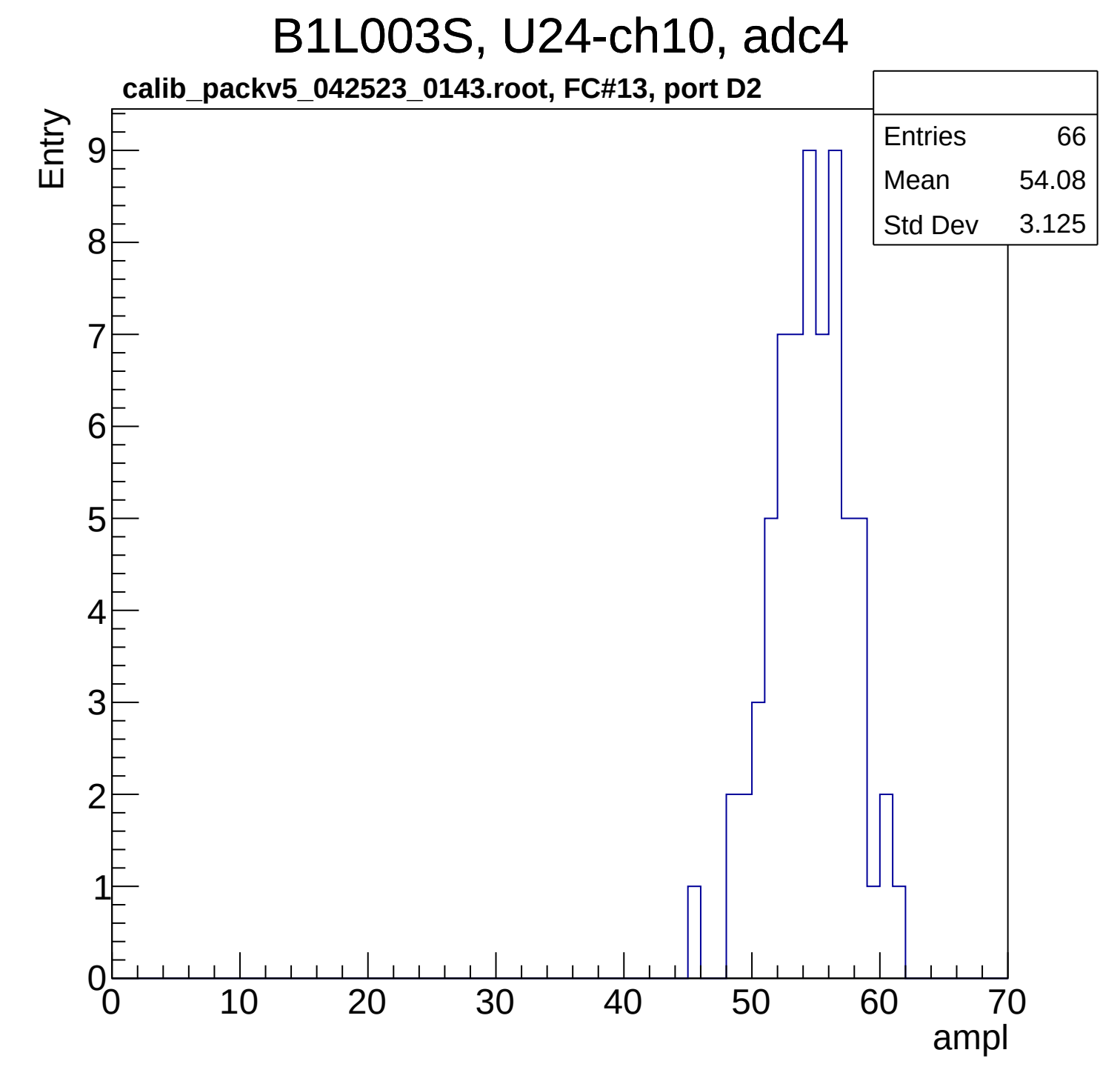
B1L003S, U24-ch10, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 54.08
Std Dev 3.125
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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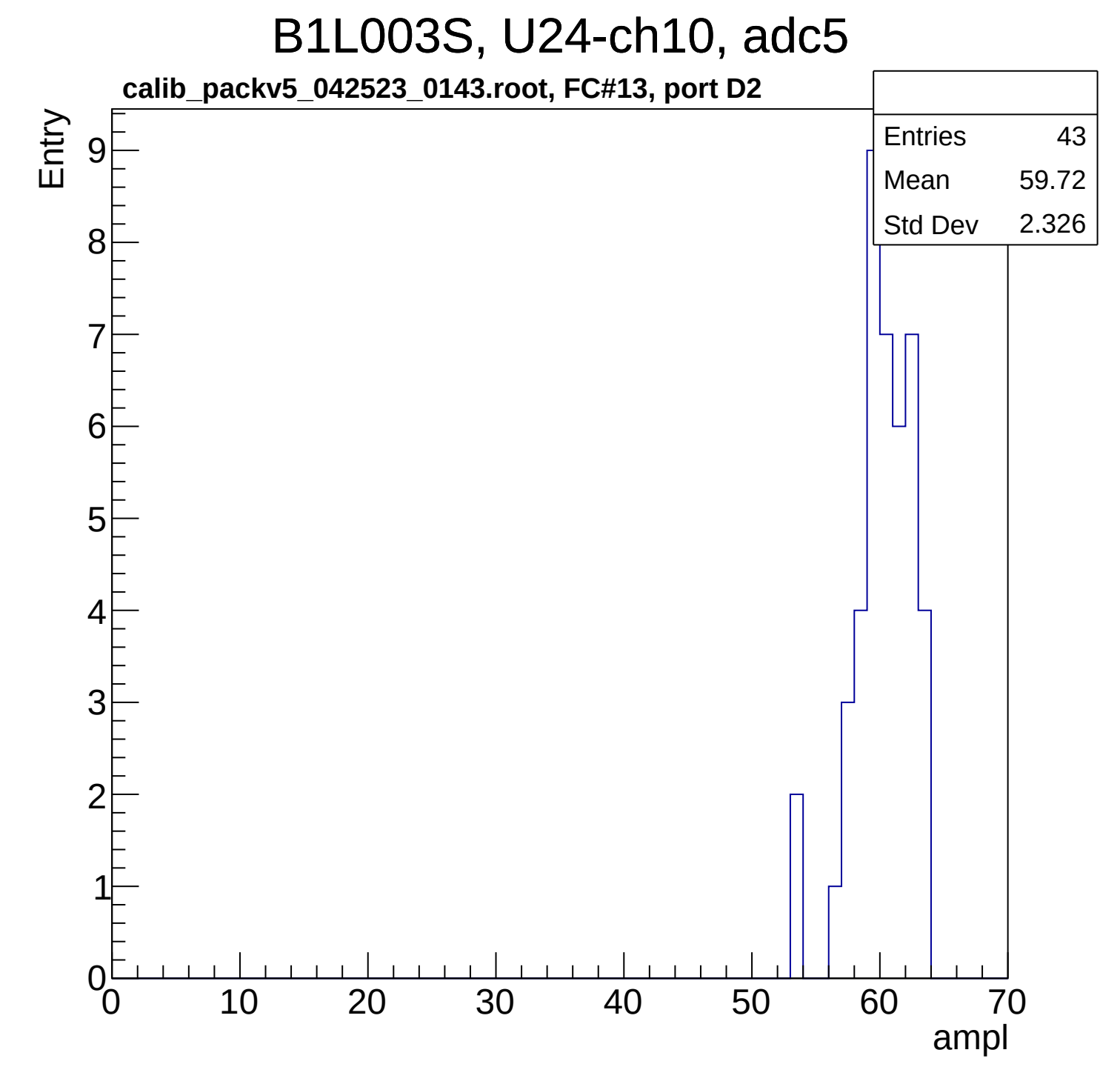
B1L003S, U24-ch10, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.72
Std Dev 2.326
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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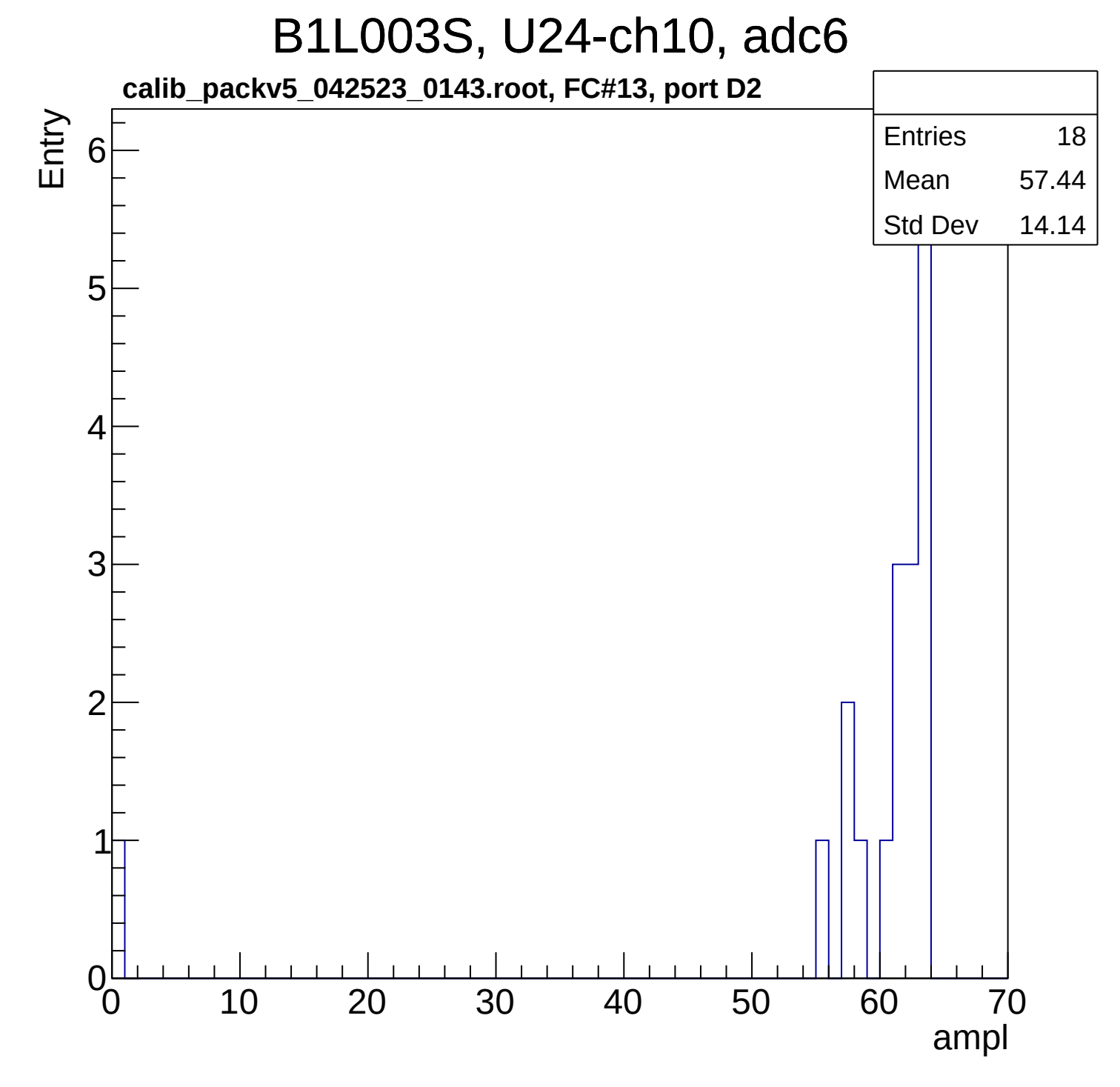
B1L003S, U24-ch10, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 57.44
Std Dev 14.14
Entry
ampl
0
1
2
3
4
5
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0
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20
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40
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60
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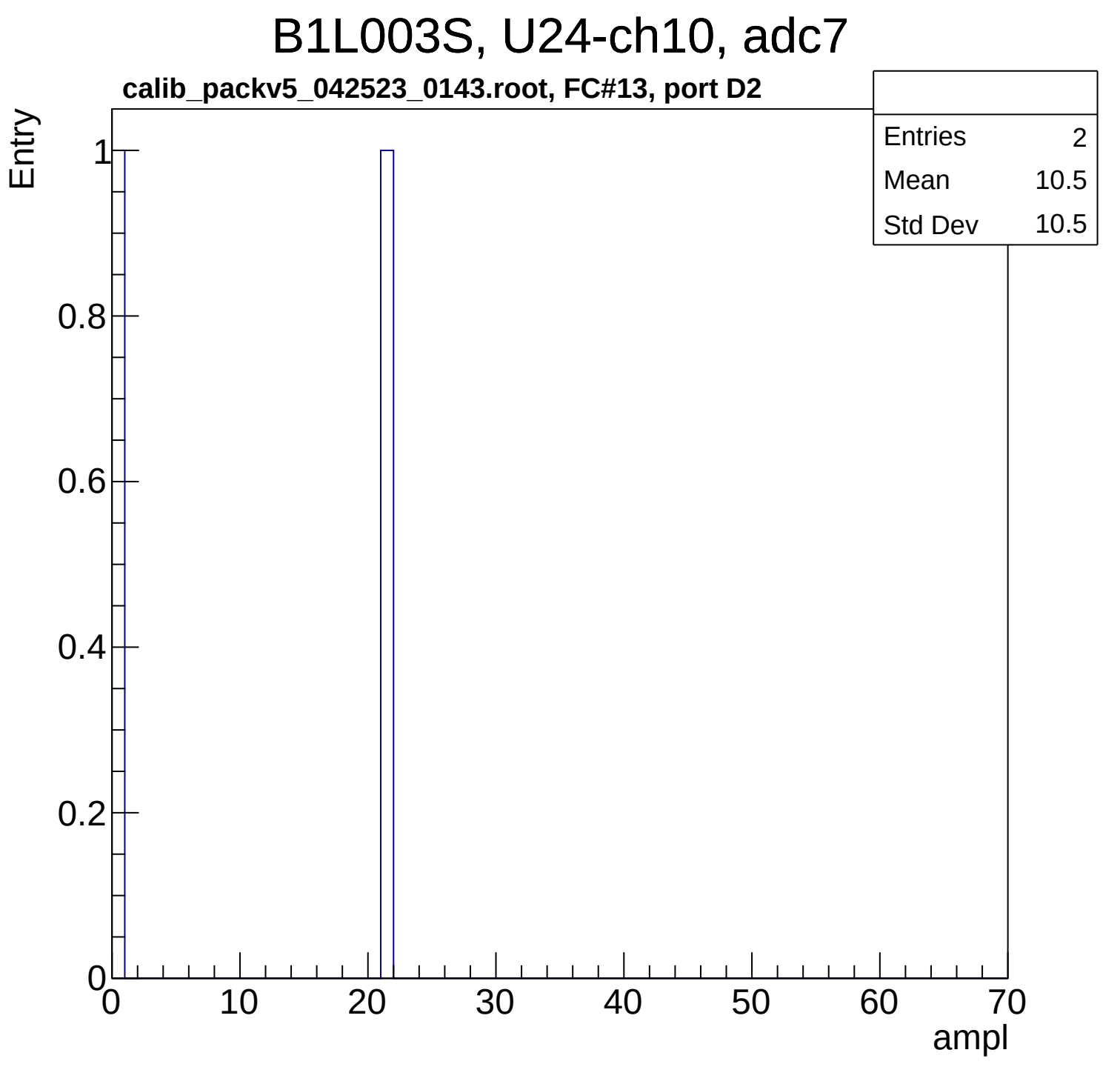
B1L003S, U24-ch10, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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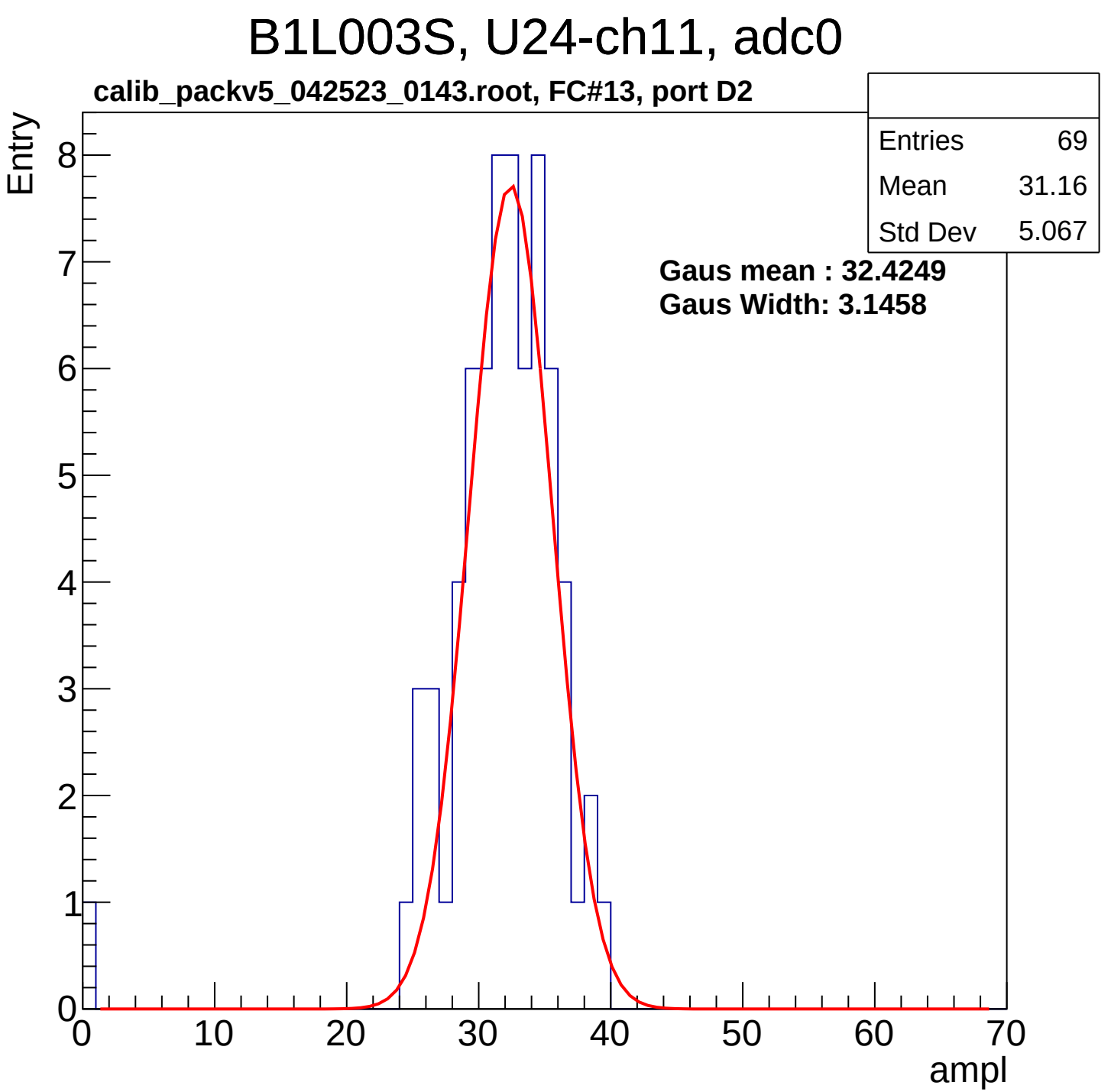
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch11, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 31.16
Std Dev 5.067
Gaus mean : 32.4249
Gaus Width: 3.1458
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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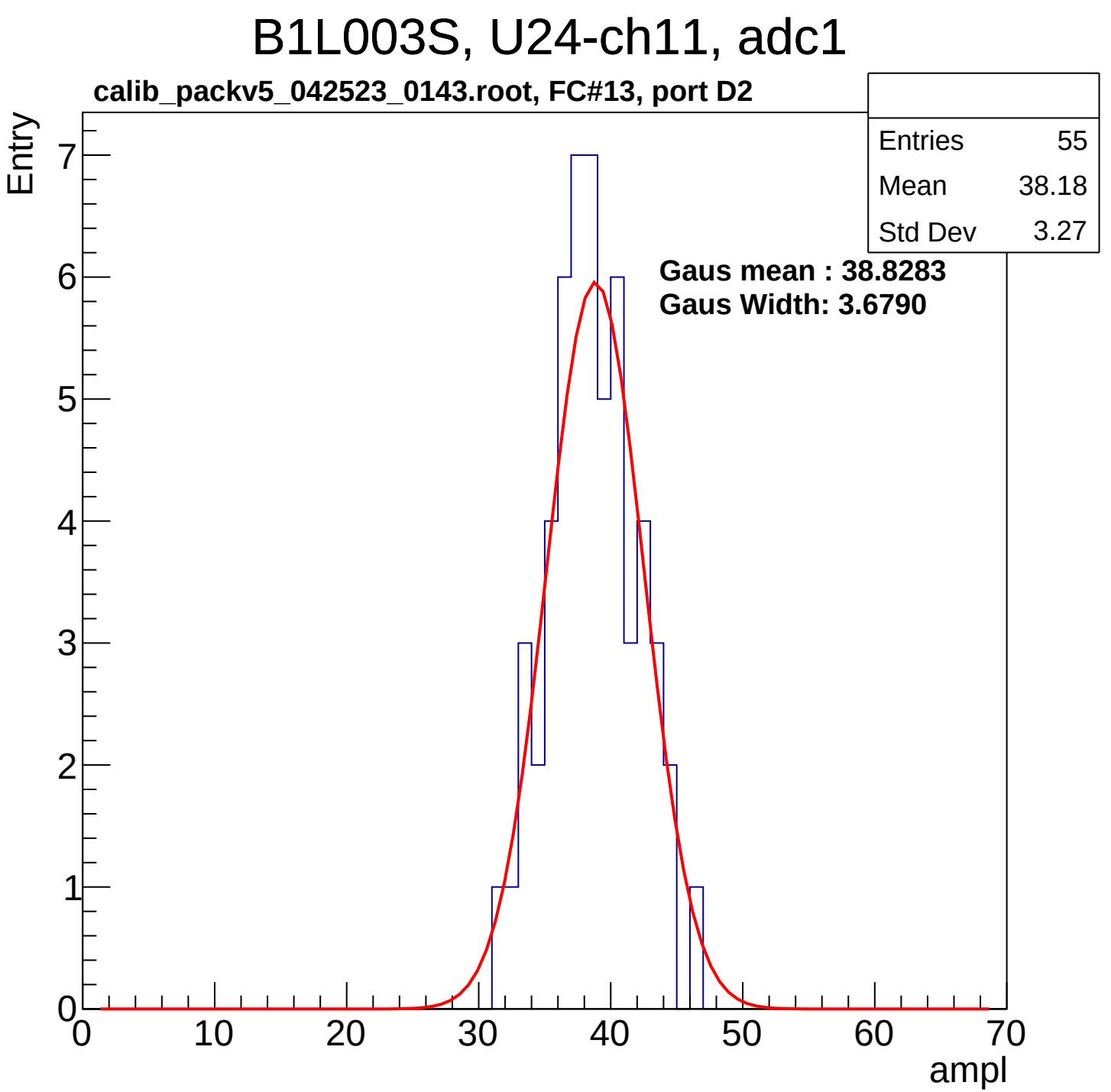
B1L003S, U24-ch11, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 38.18
Std Dev 3.27
Gaus mean : 38.8283
Gaus Width: 3.6790
Entry
ampl
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2
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4
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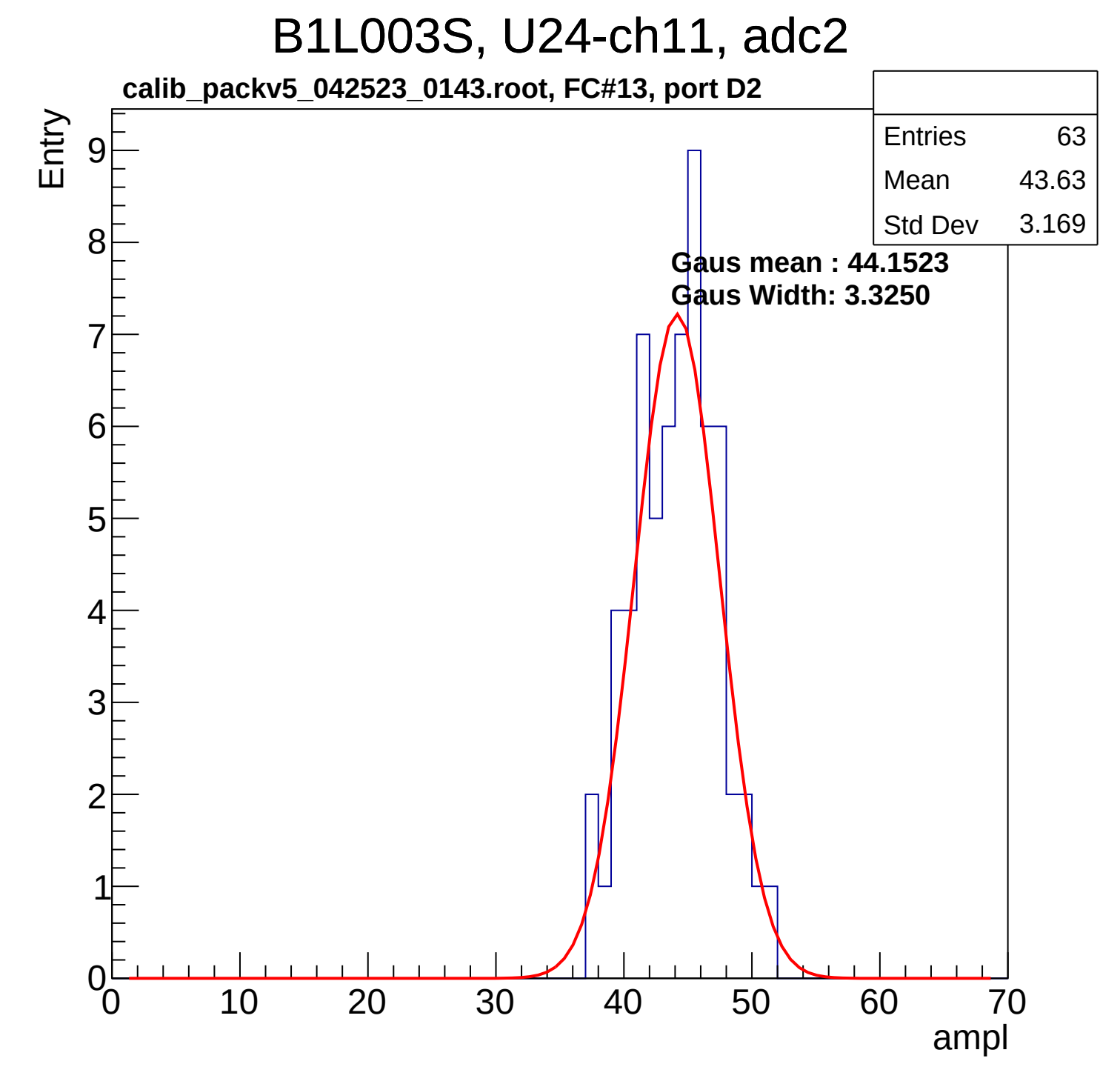
B1L003S, U24-ch11, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 43.63
Std Dev 3.169
Gaus mean : 44.1523
Gaus Width: 3.3250
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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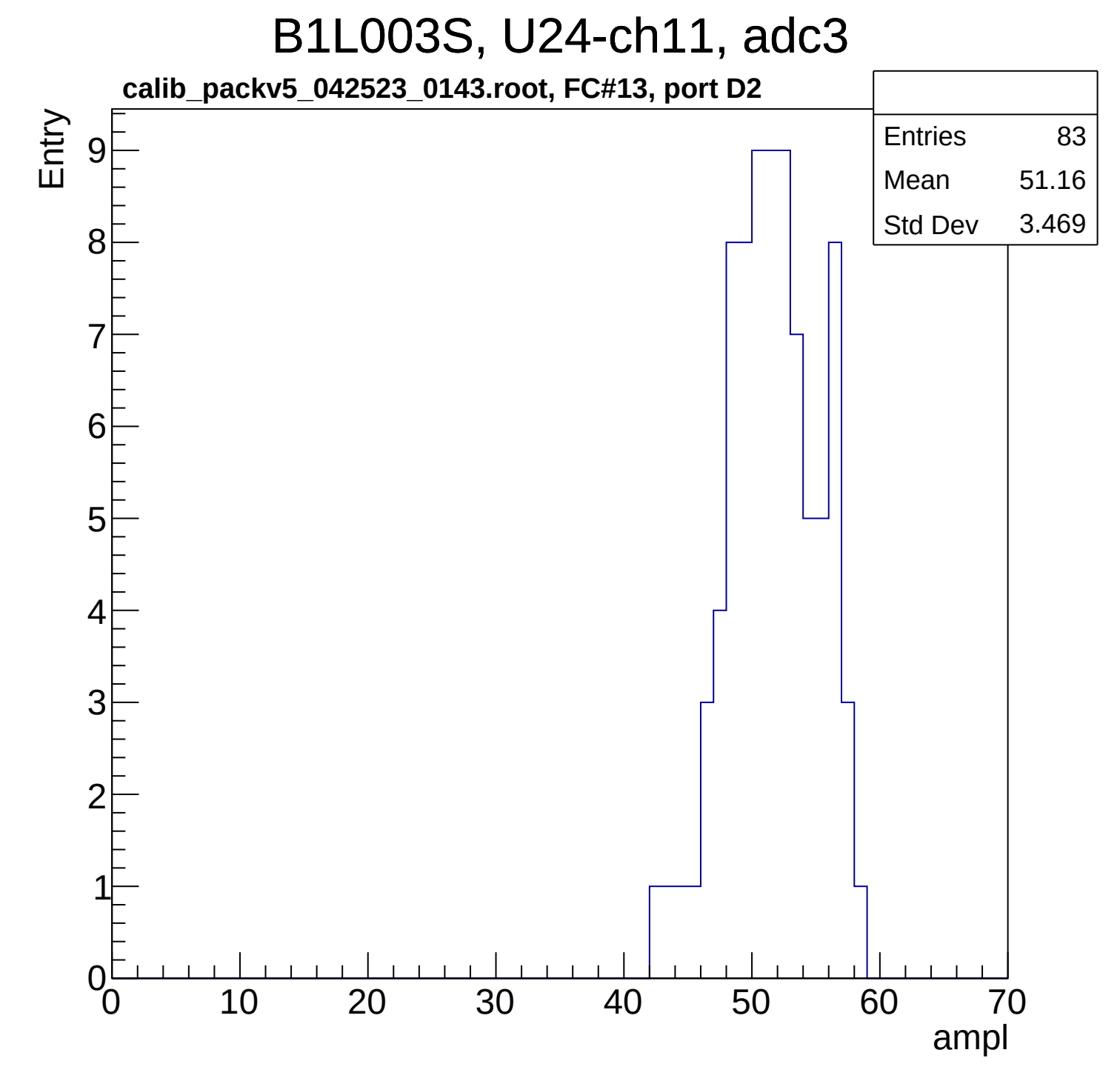
B1L003S, U24-ch11, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 51.16
Std Dev 3.469
Entry
ampl
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3
4
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6
7
8
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0
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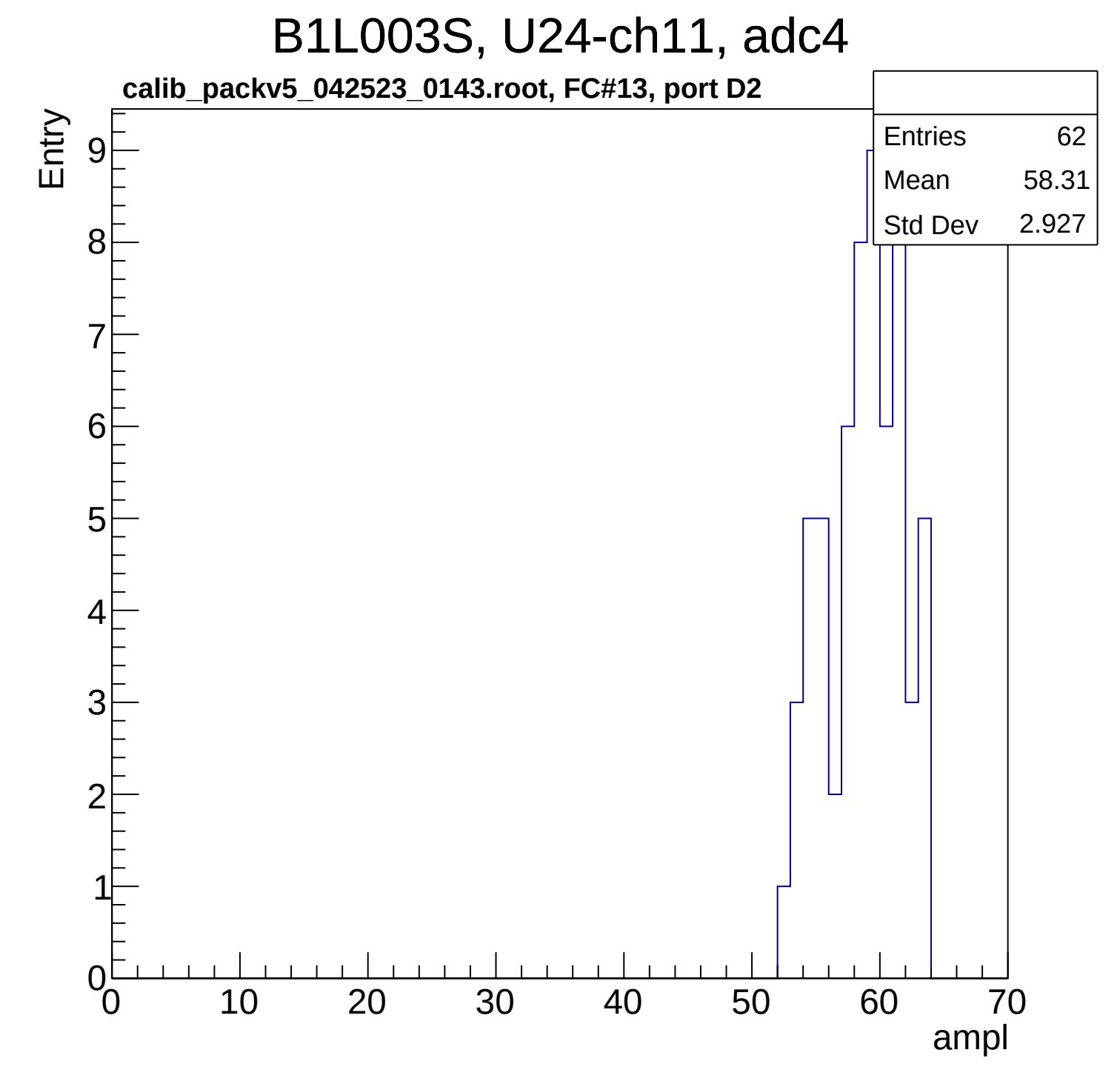
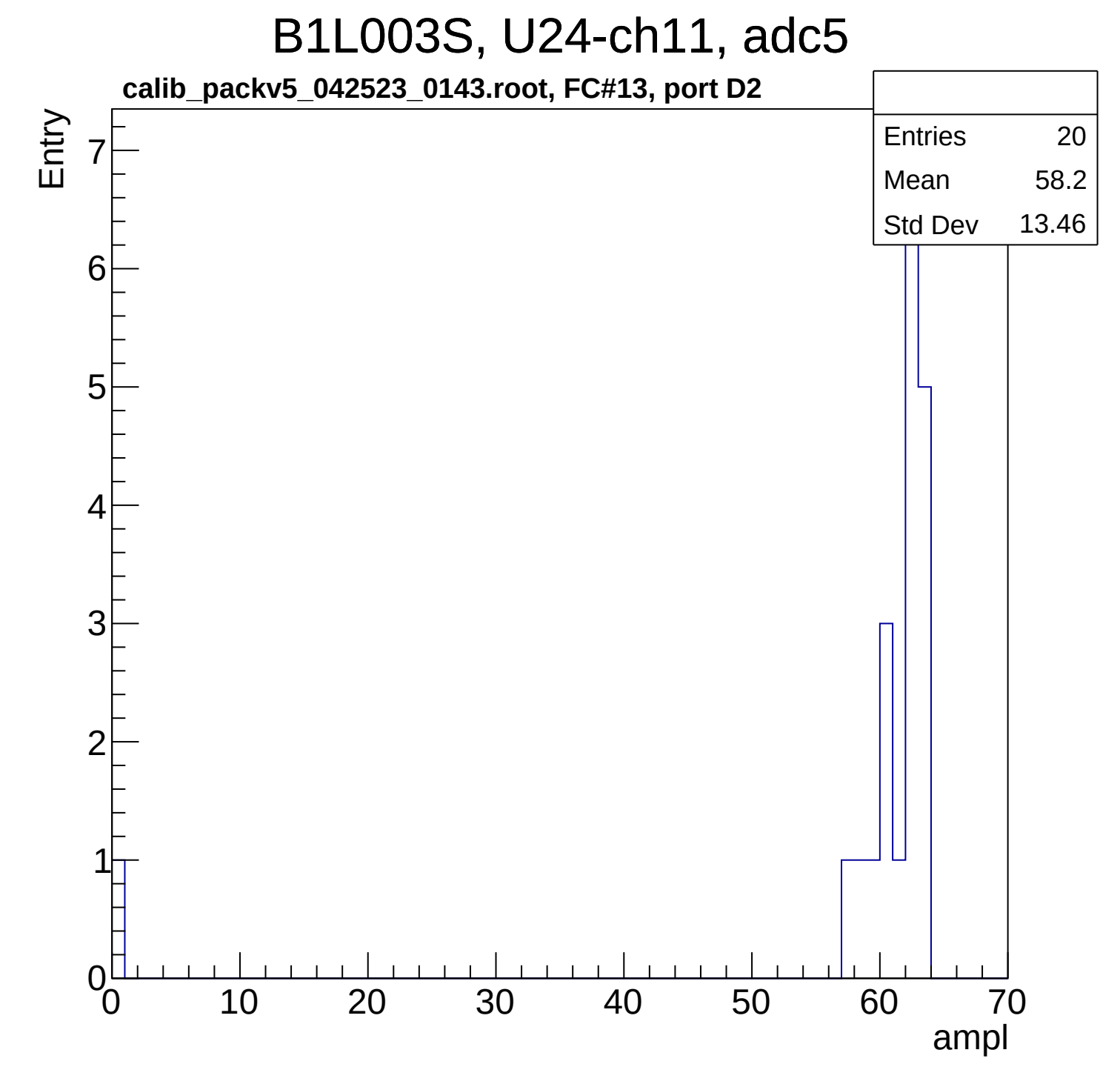

B1L003S, U24-ch11, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.31
Std Dev 2.927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch11, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 58.2
Std Dev 13.46
Entry
0
1
2
3
4
5
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7
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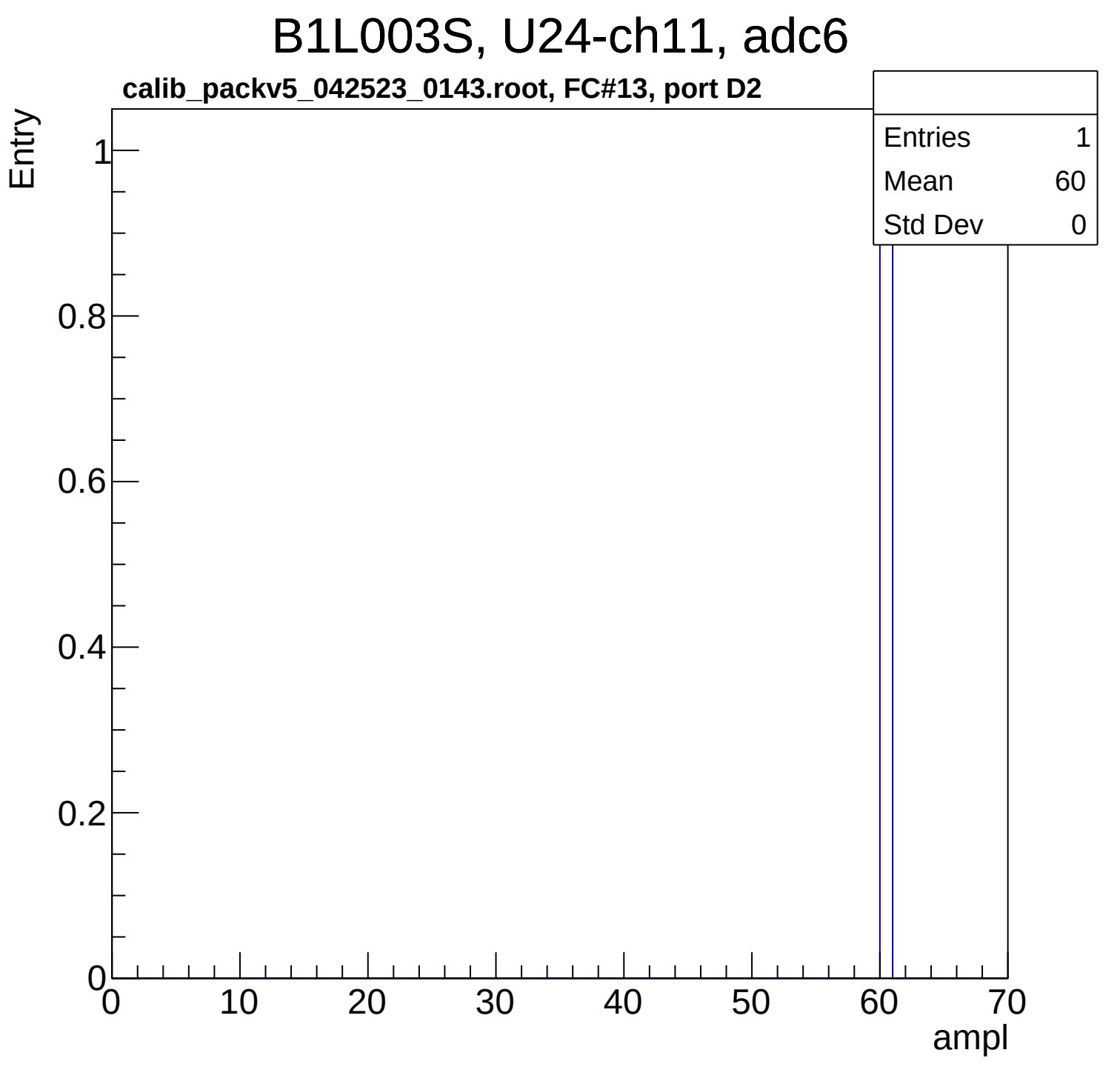
70
ampl


B1L003S, U24-ch11, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 60
Std Dev 0
Entry
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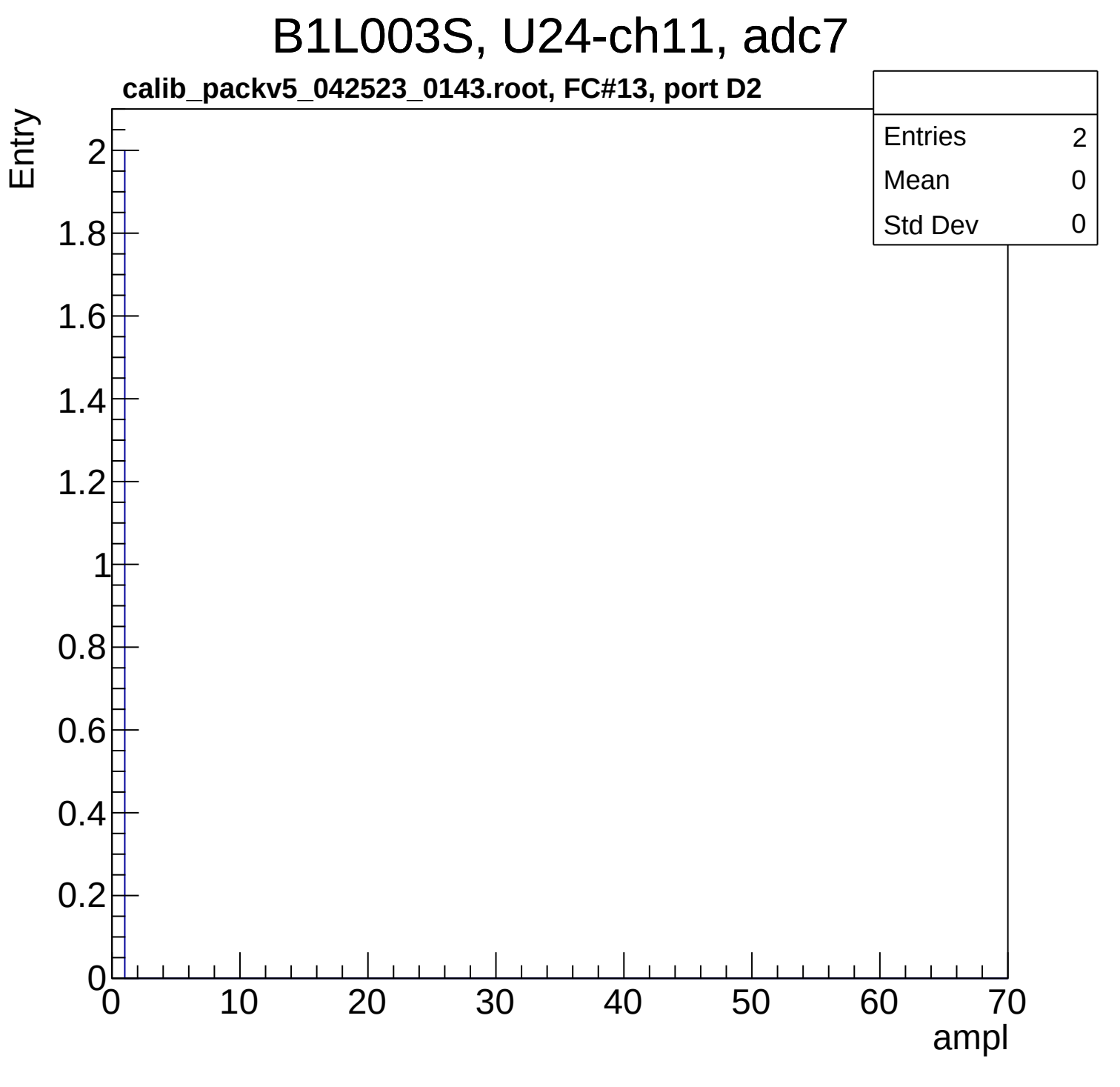
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch11, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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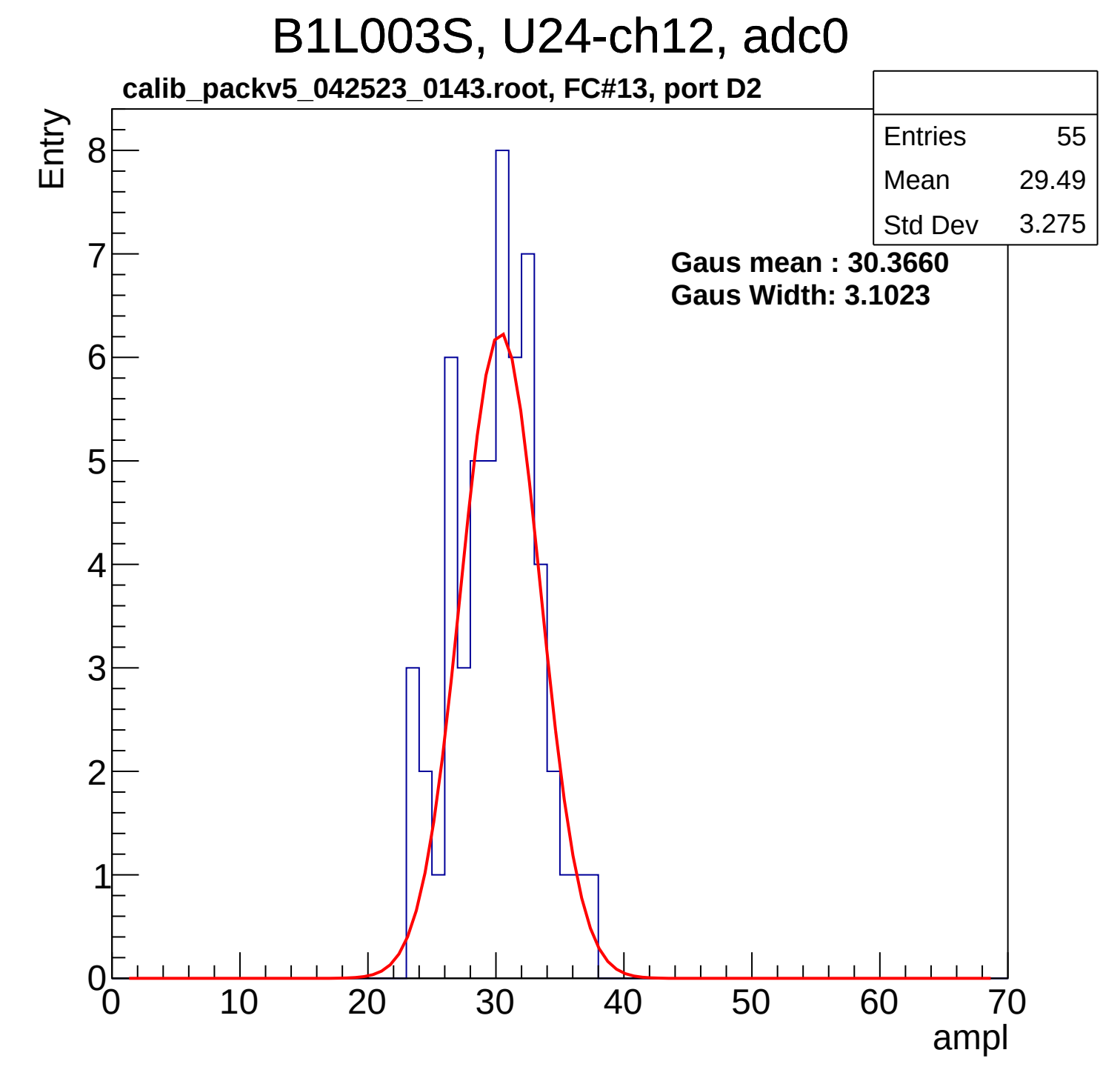
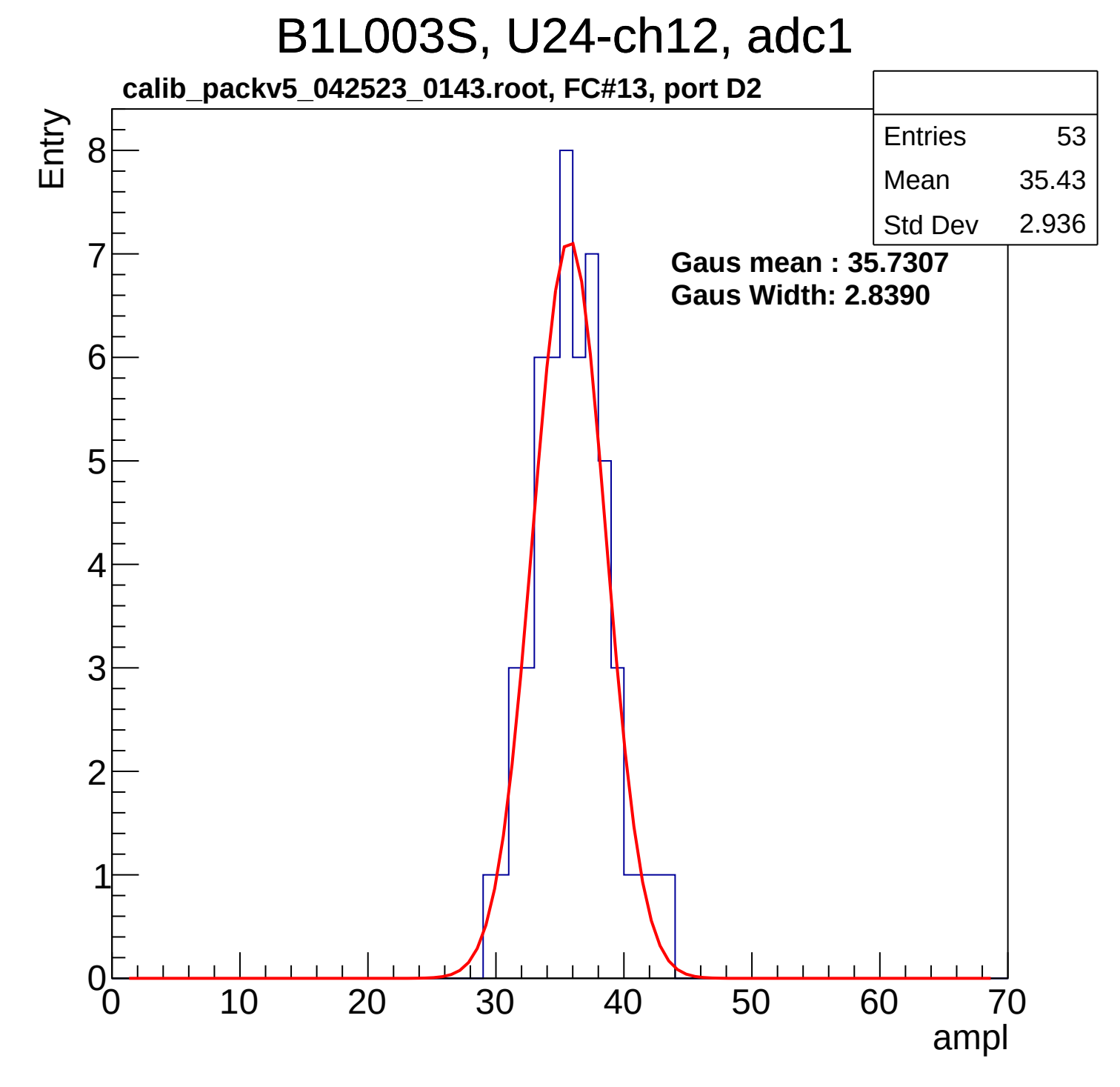

B1L003S, U24-ch12, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 29.49
Std Dev 3.275
Gaus mean : 30.3660
Gaus Width: 3.1023
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch12, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 35.43
Std Dev 2.936
Gaus mean : 35.7307
Gaus Width: 2.8390
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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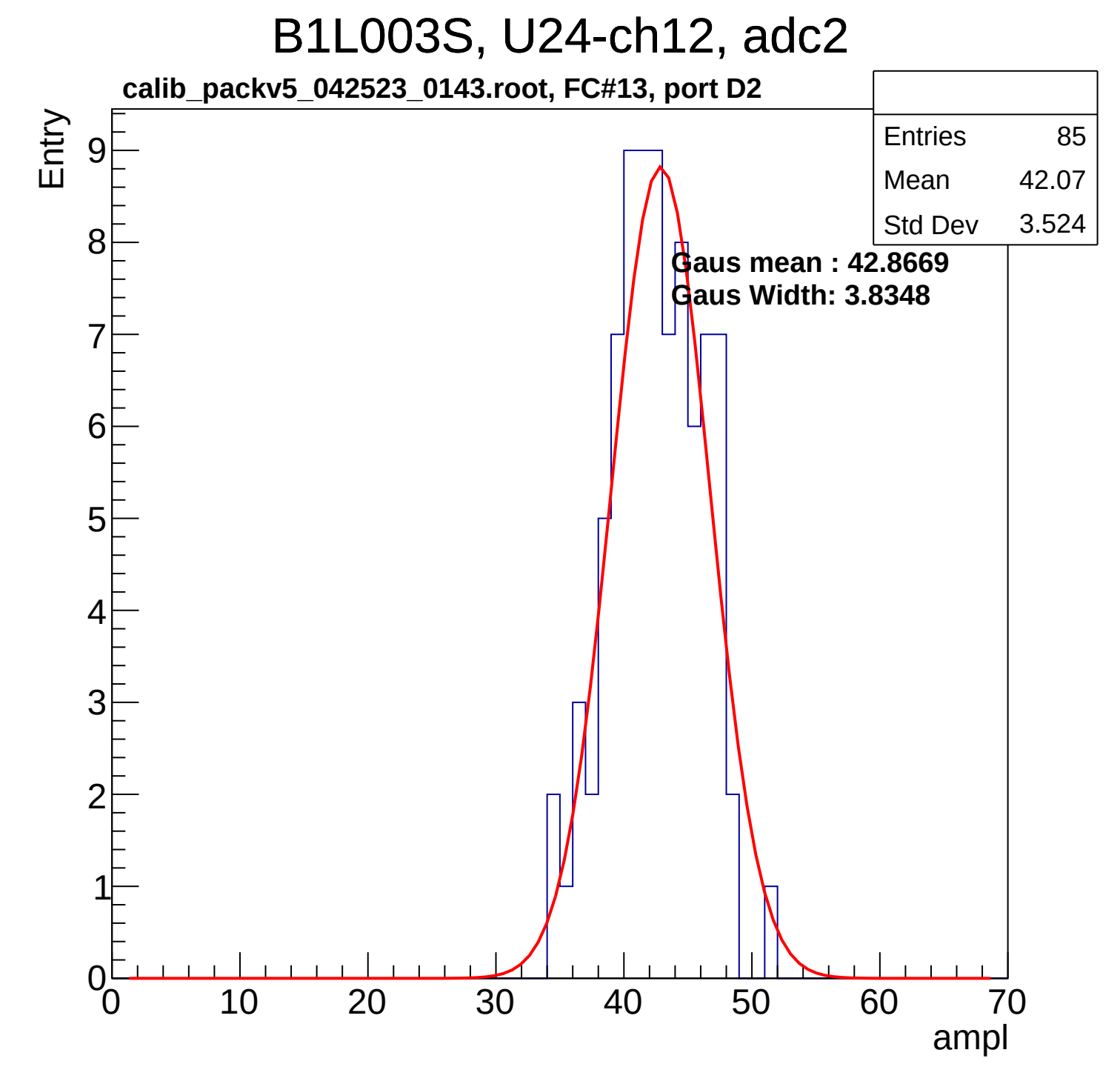
B1L003S, U24-ch12, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 42.07
Std Dev 3.524
Gaus mean : 42.8669
Gaus Width: 3.8348
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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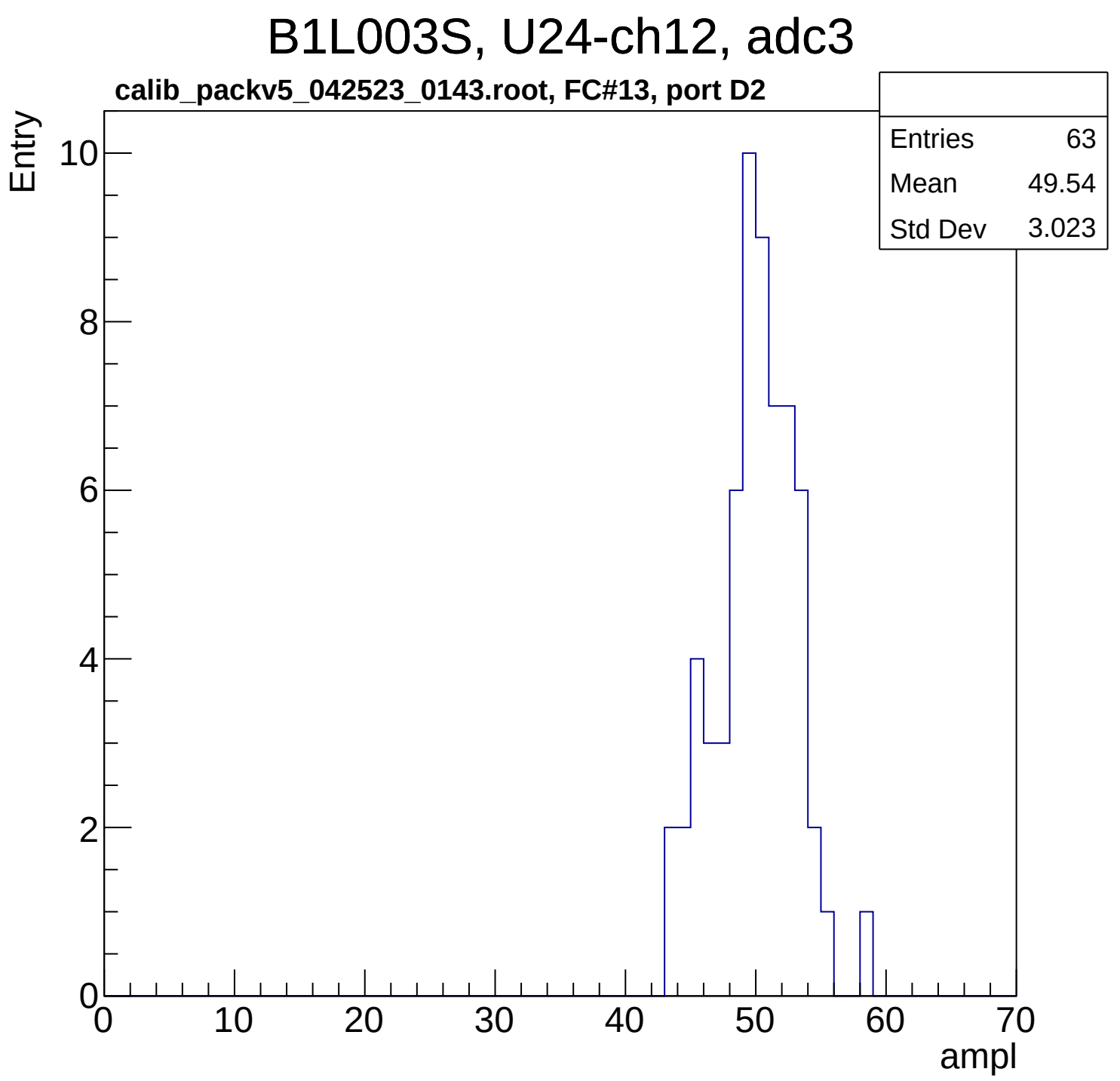
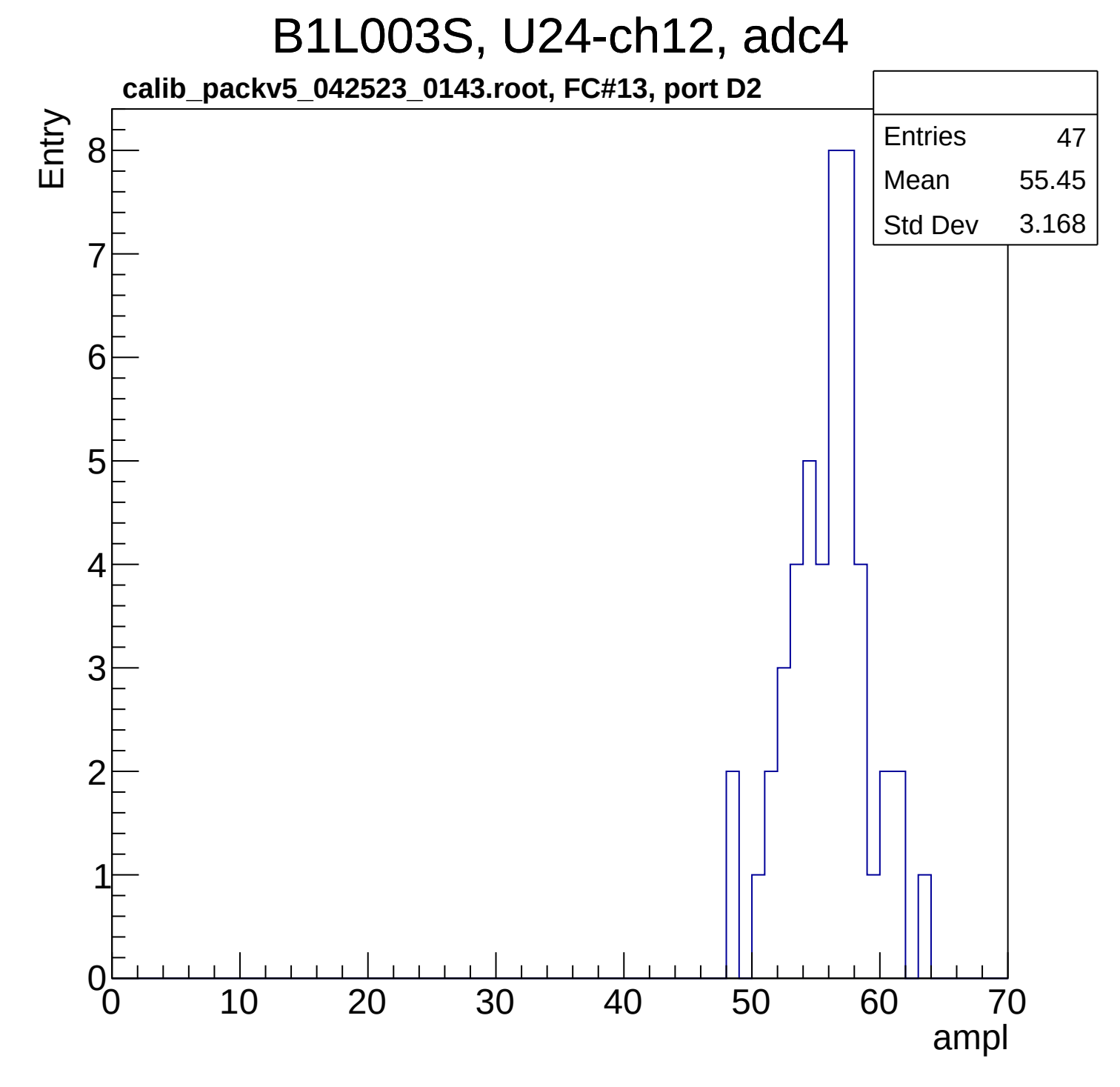

B1L003S, U24-ch12, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.54
Std Dev 3.023
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch12, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 55.45
Std Dev 3.168
Entry
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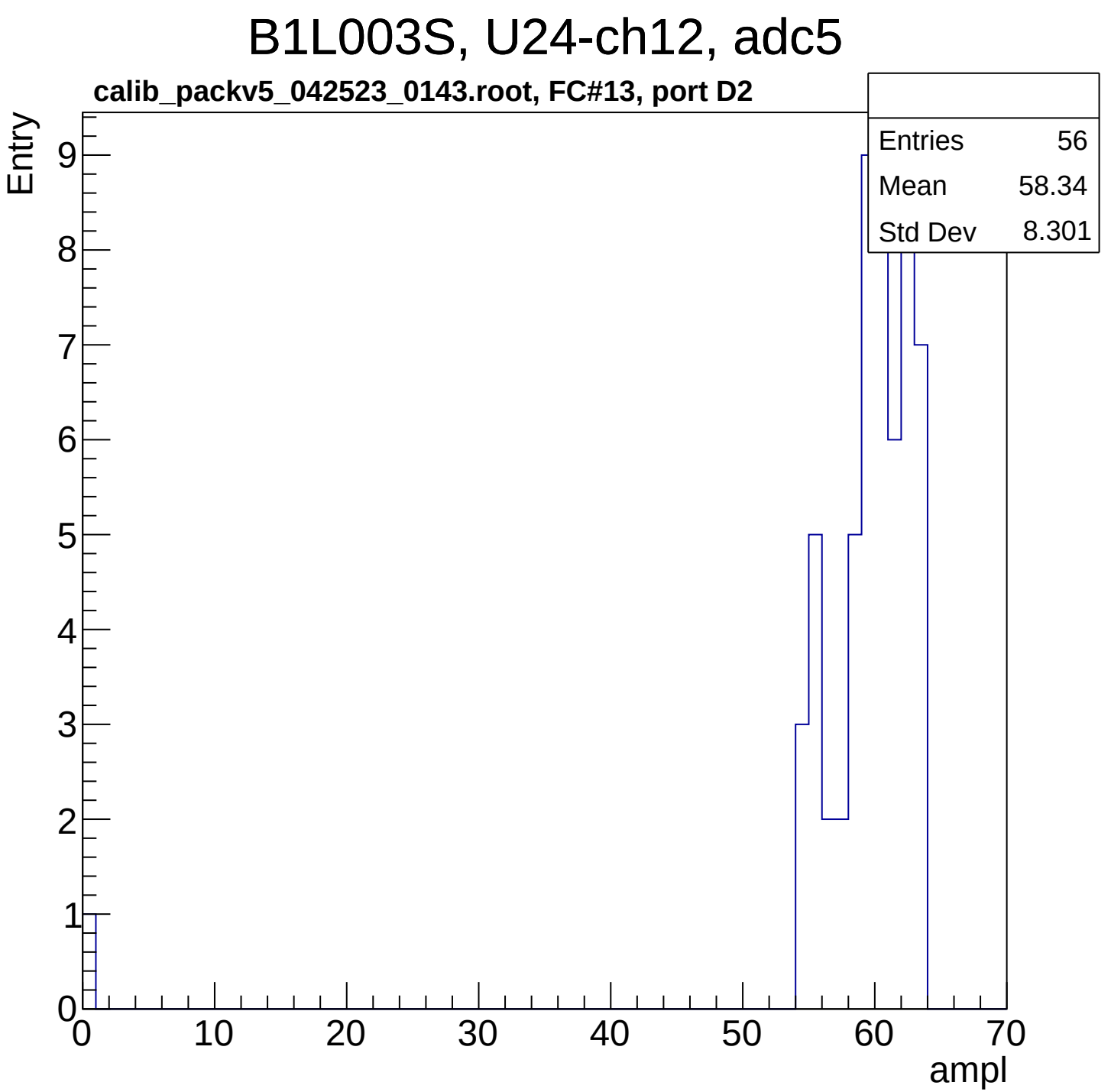
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch12, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.34
Std Dev 8.301
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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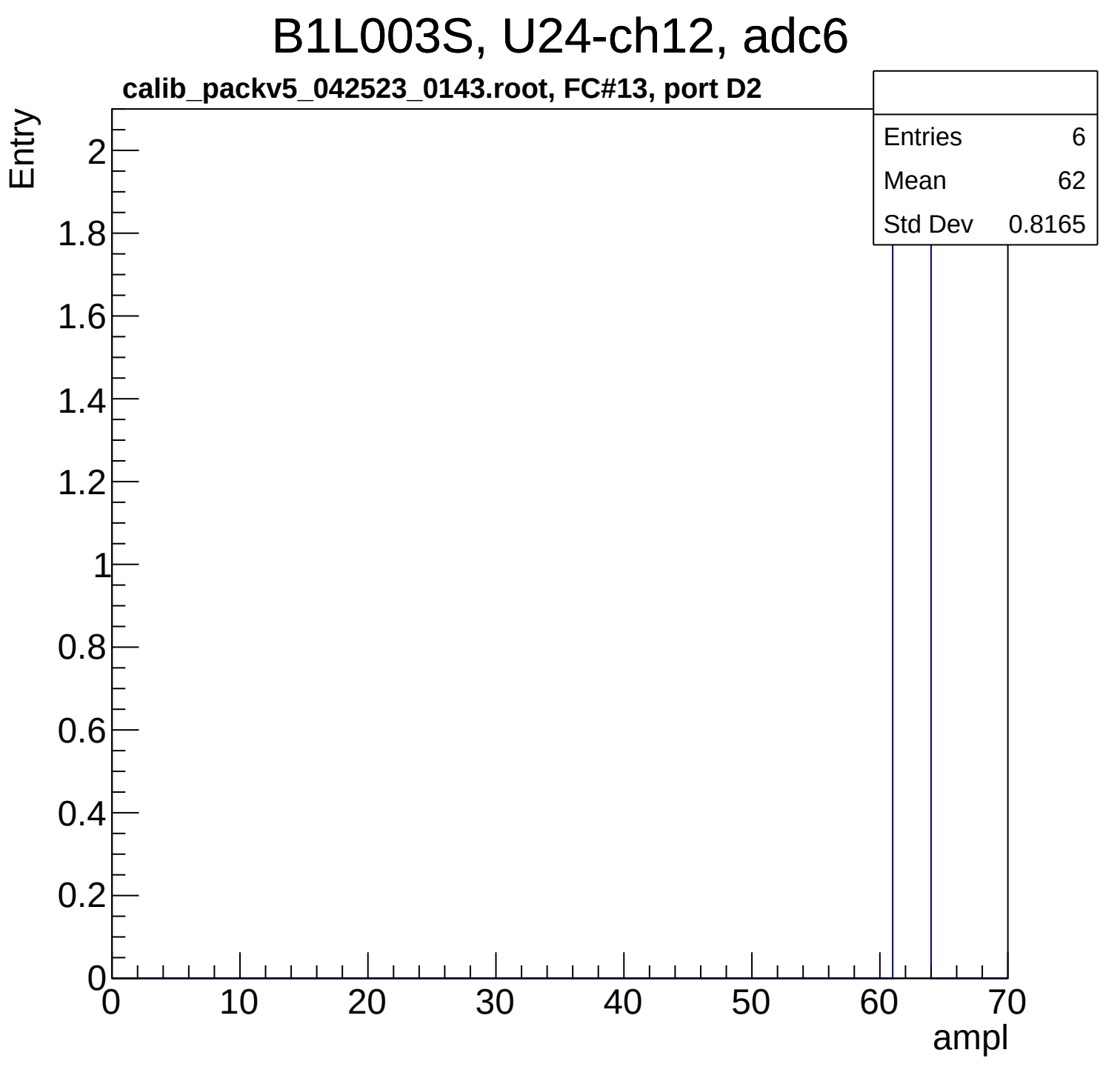
B1L003S, U24-ch12, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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30
40
50
60
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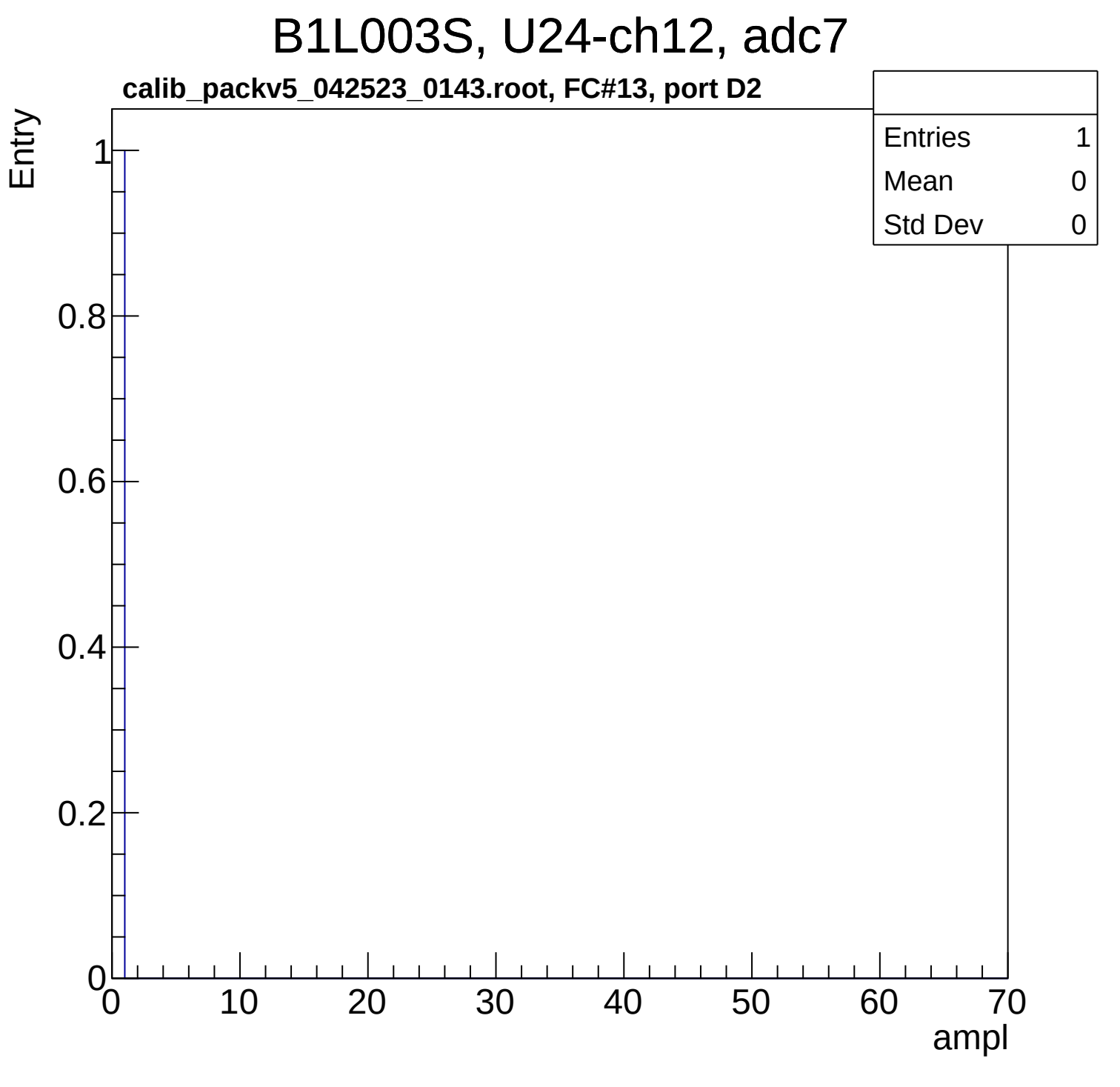
B1L003S, U24-ch12, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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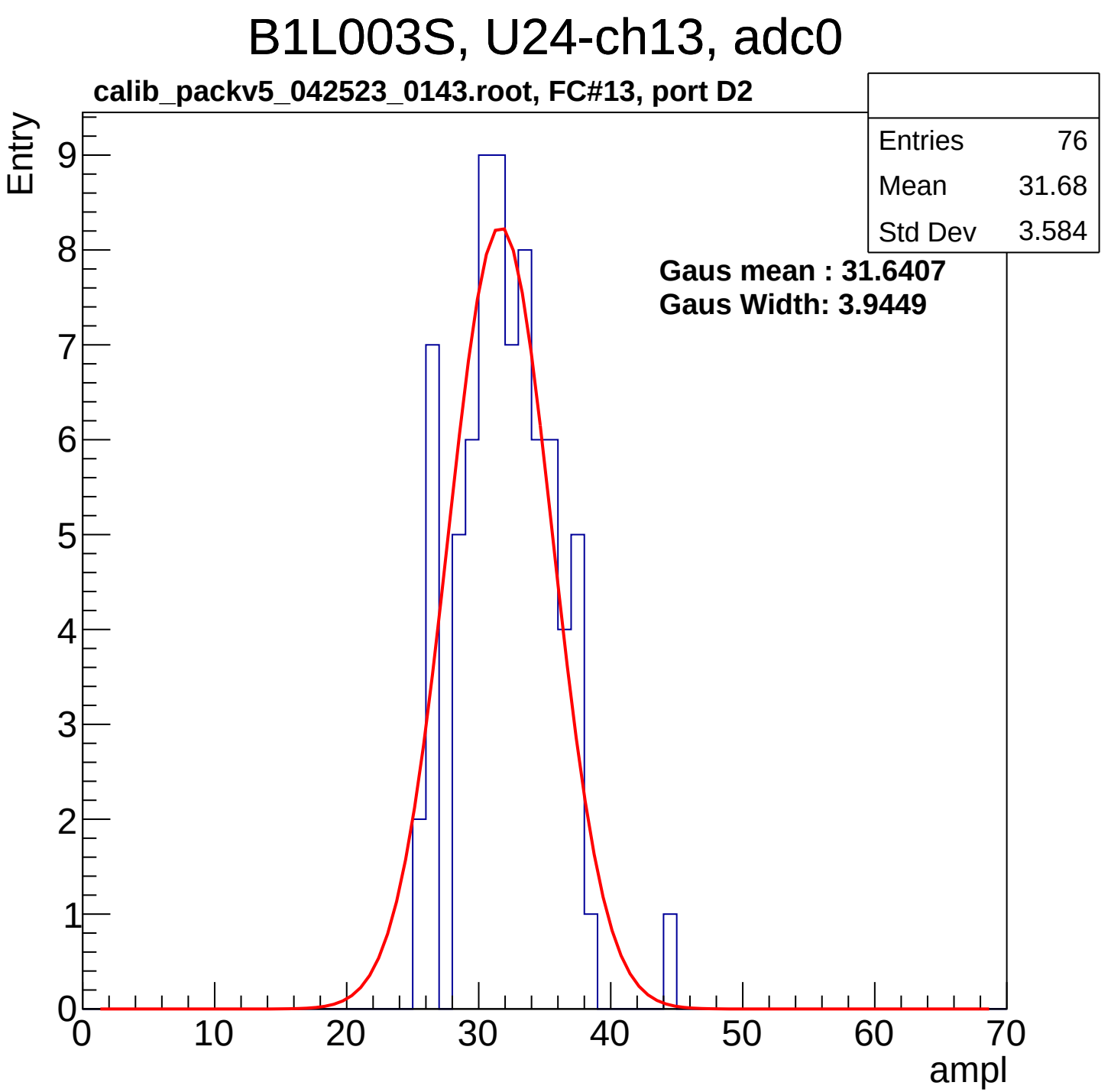
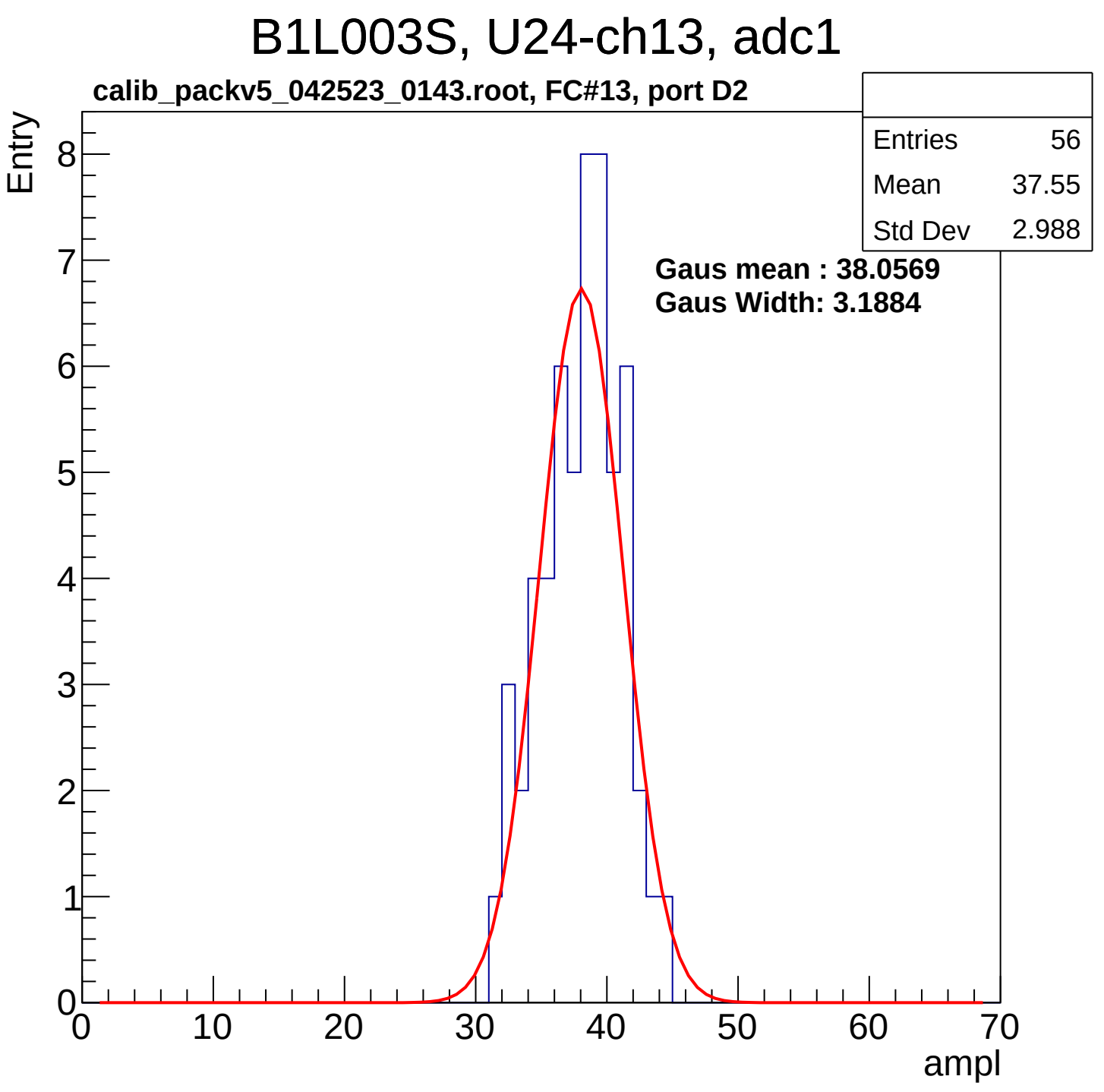
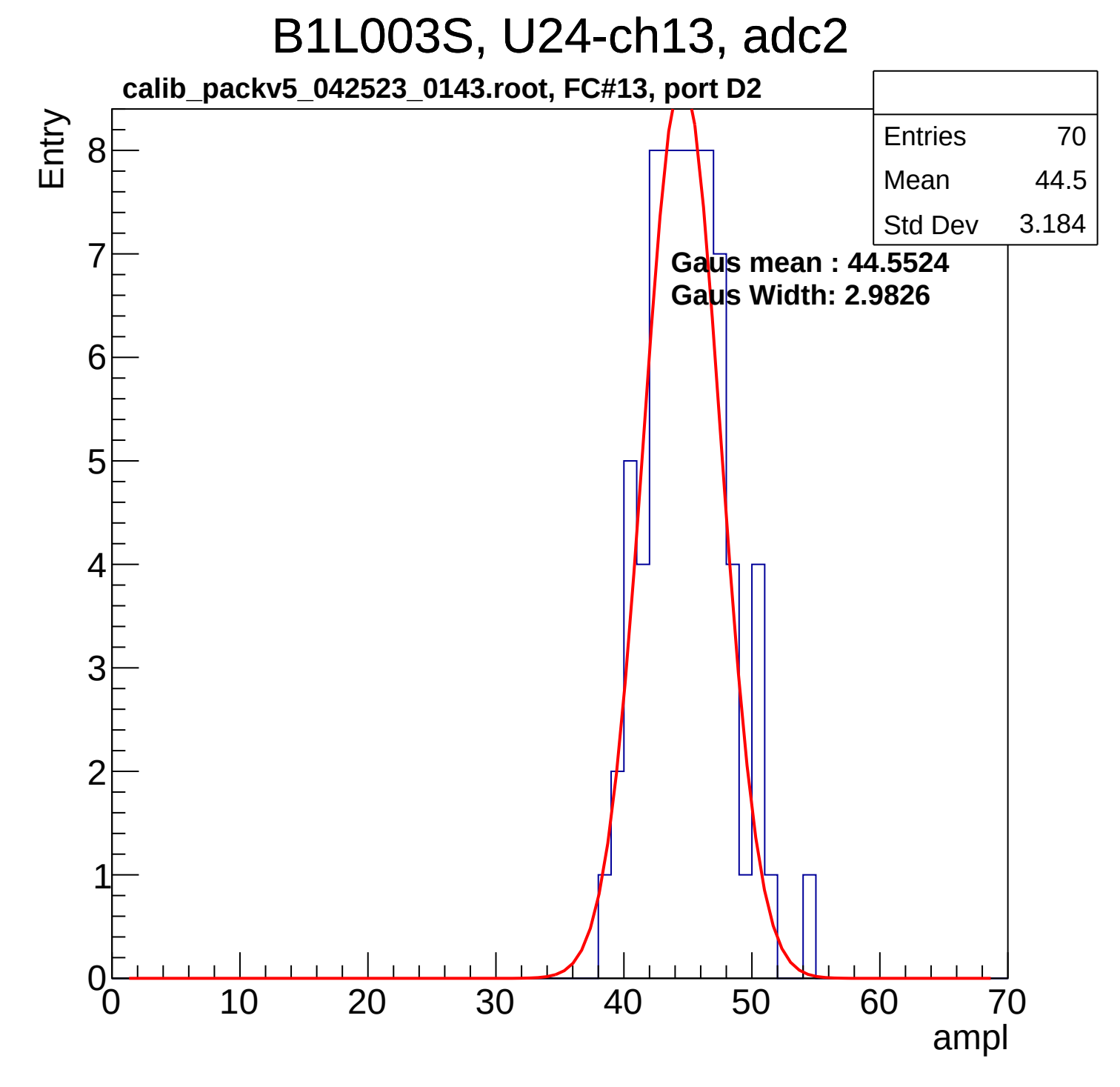
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch13, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 31.68
Std Dev 3.584
Gaus mean : 31.6407
Gaus Width: 3.9449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch13, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 37.55
Std Dev 2.988
Gaus mean : 38.0569
Gaus Width: 3.1884
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch13, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 44.5
Std Dev 3.184
Gaus mean : 44.5524
Gaus Width: 2.9826
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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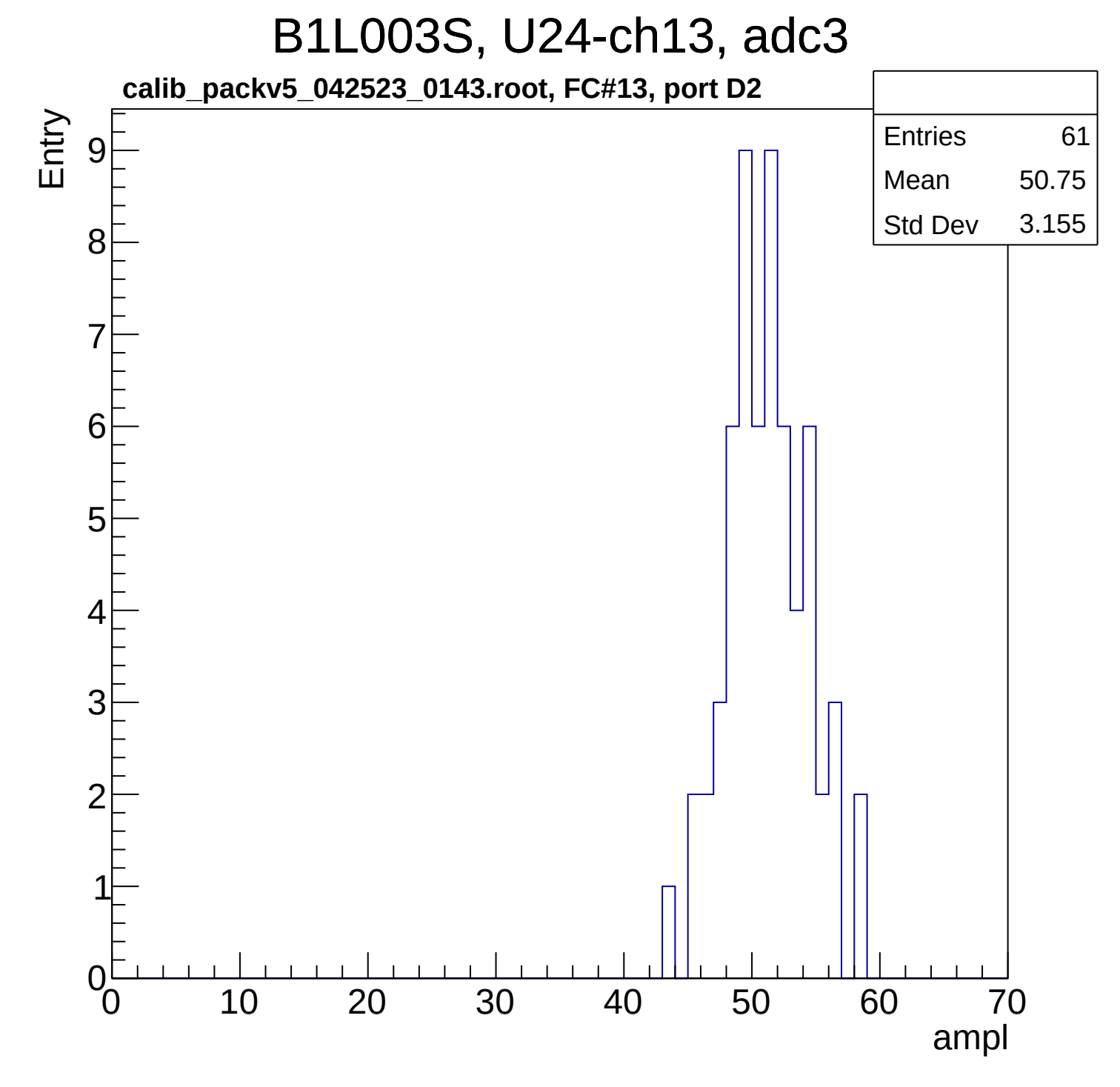
B1L003S, U24-ch13, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.75
Std Dev 3.155
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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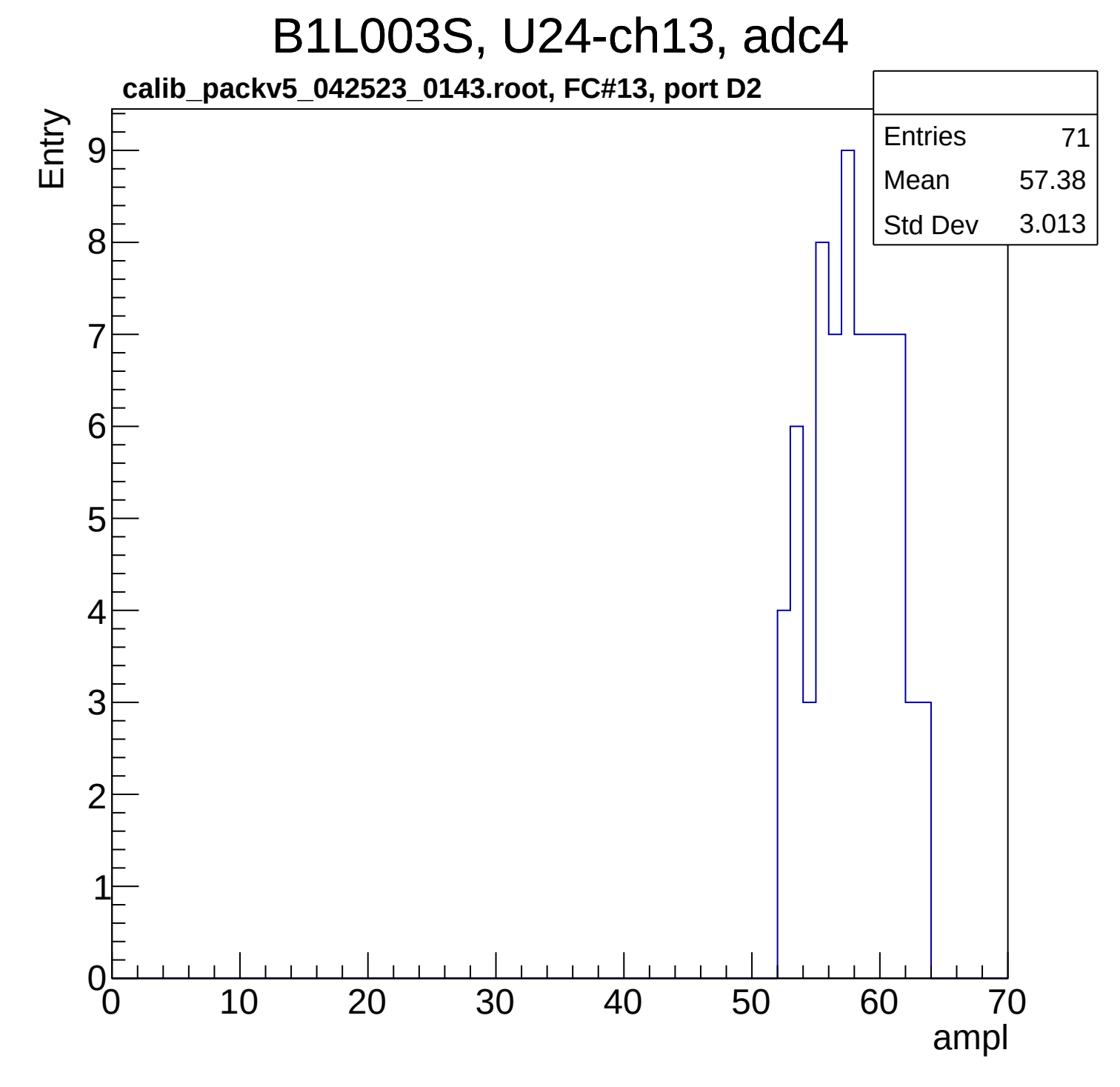
40
50
60
70


B1L003S, U24-ch13, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 57.38
Std Dev 3.013
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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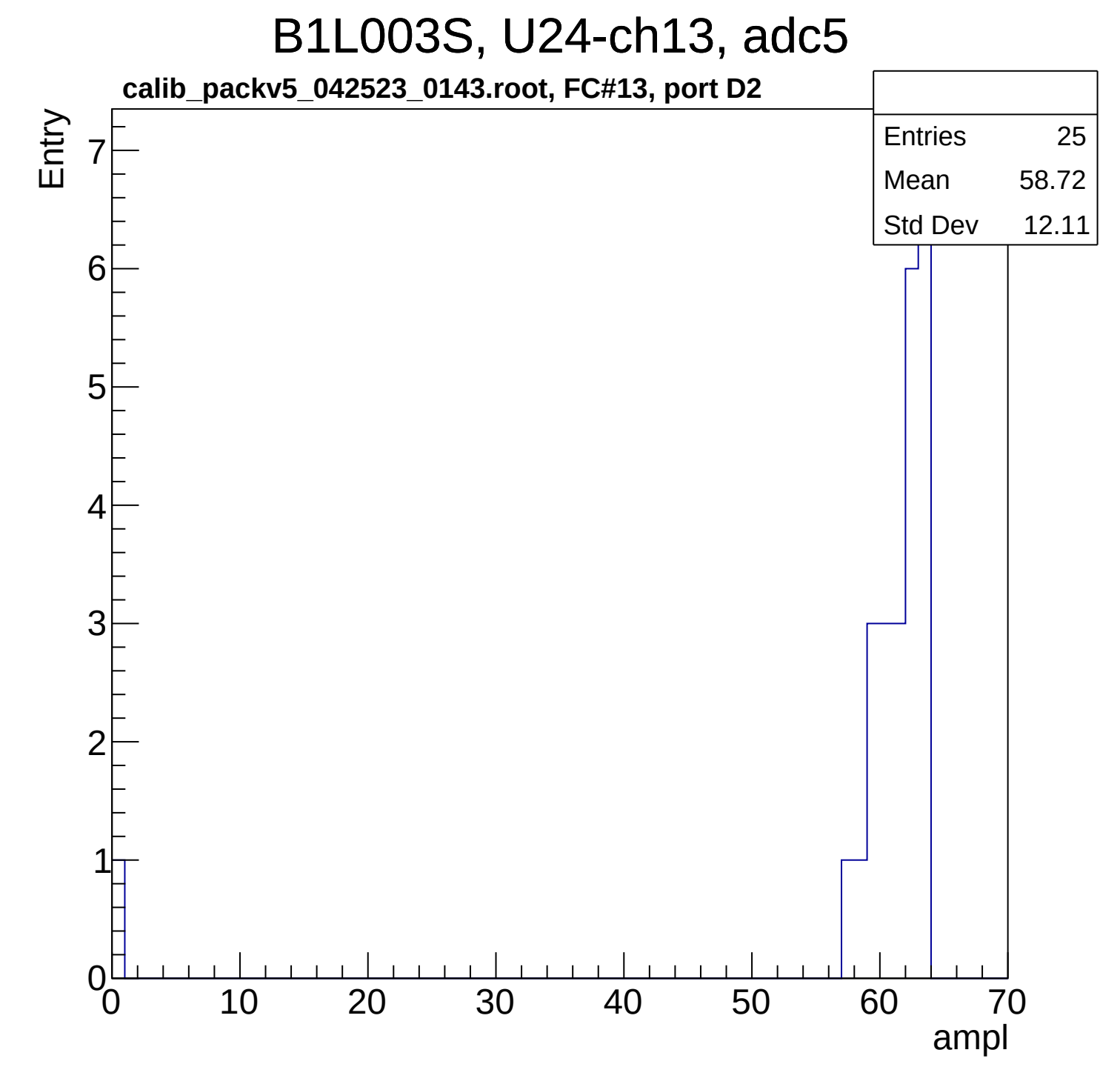
B1L003S, U24-ch13, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 25
Mean 58.72
Std Dev 12.11
Entry
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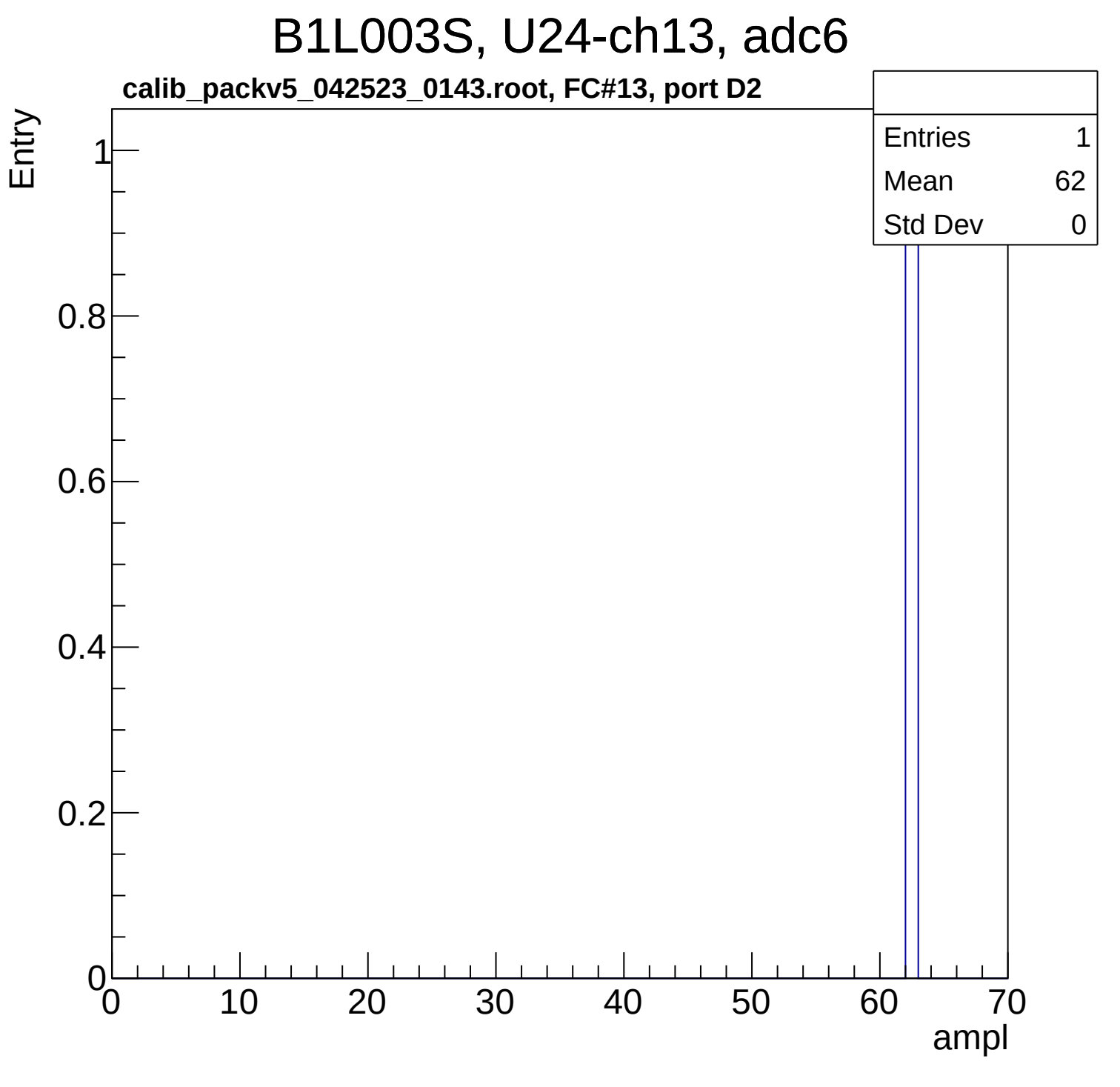
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch13, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
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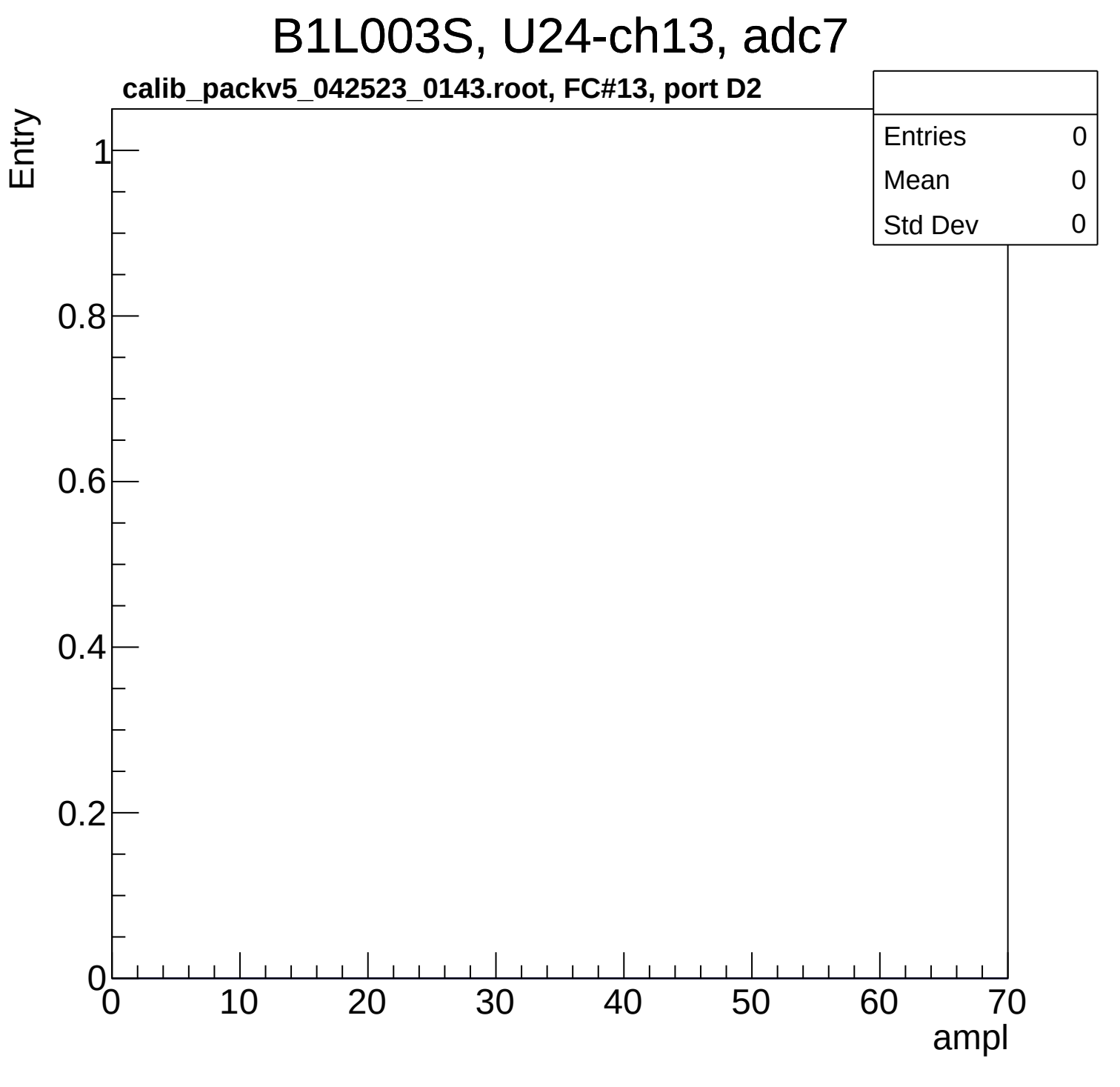
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch13, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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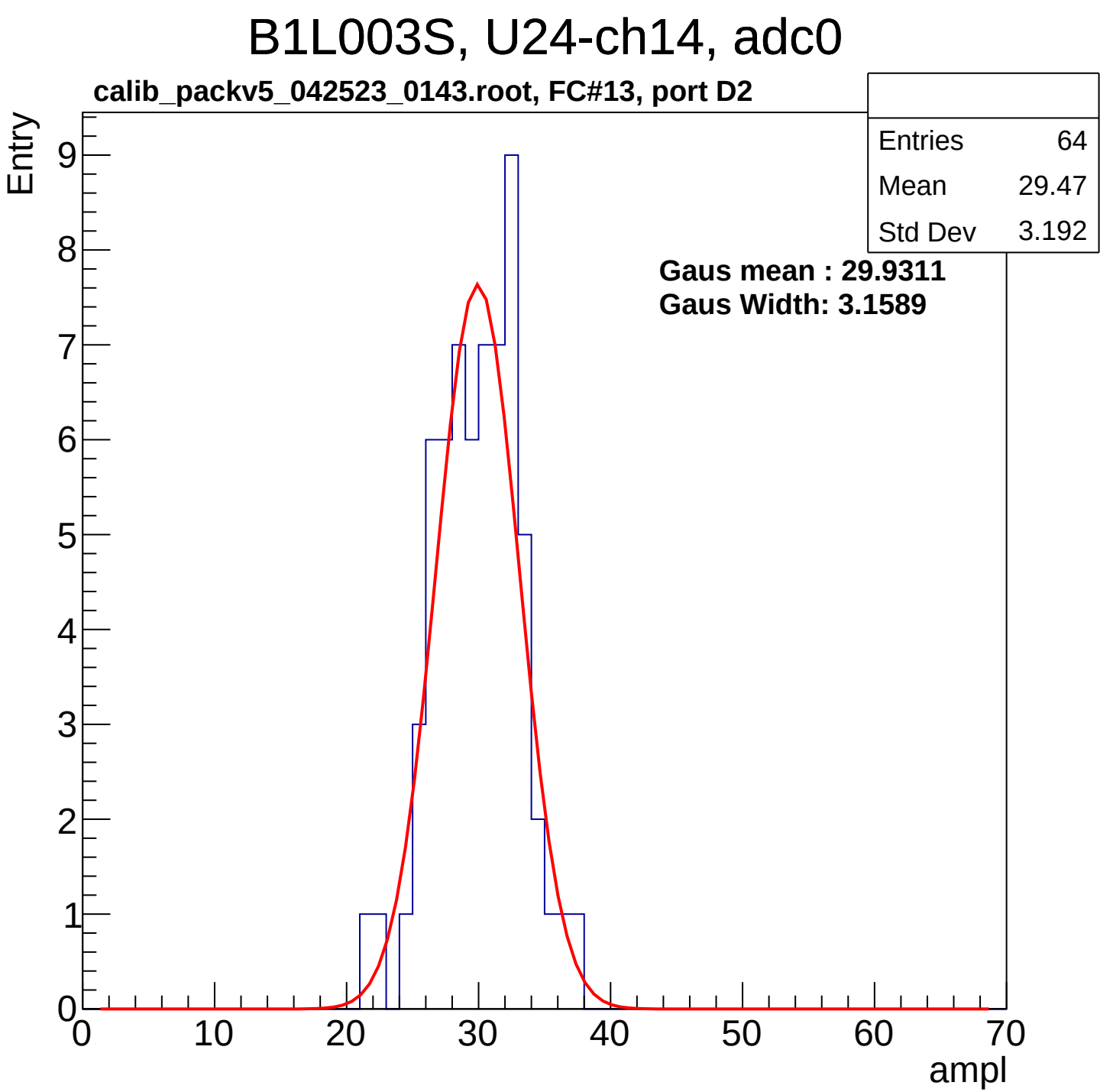
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch14, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.47
Std Dev 3.192
Gaus mean : 29.9311
Gaus Width: 3.1589
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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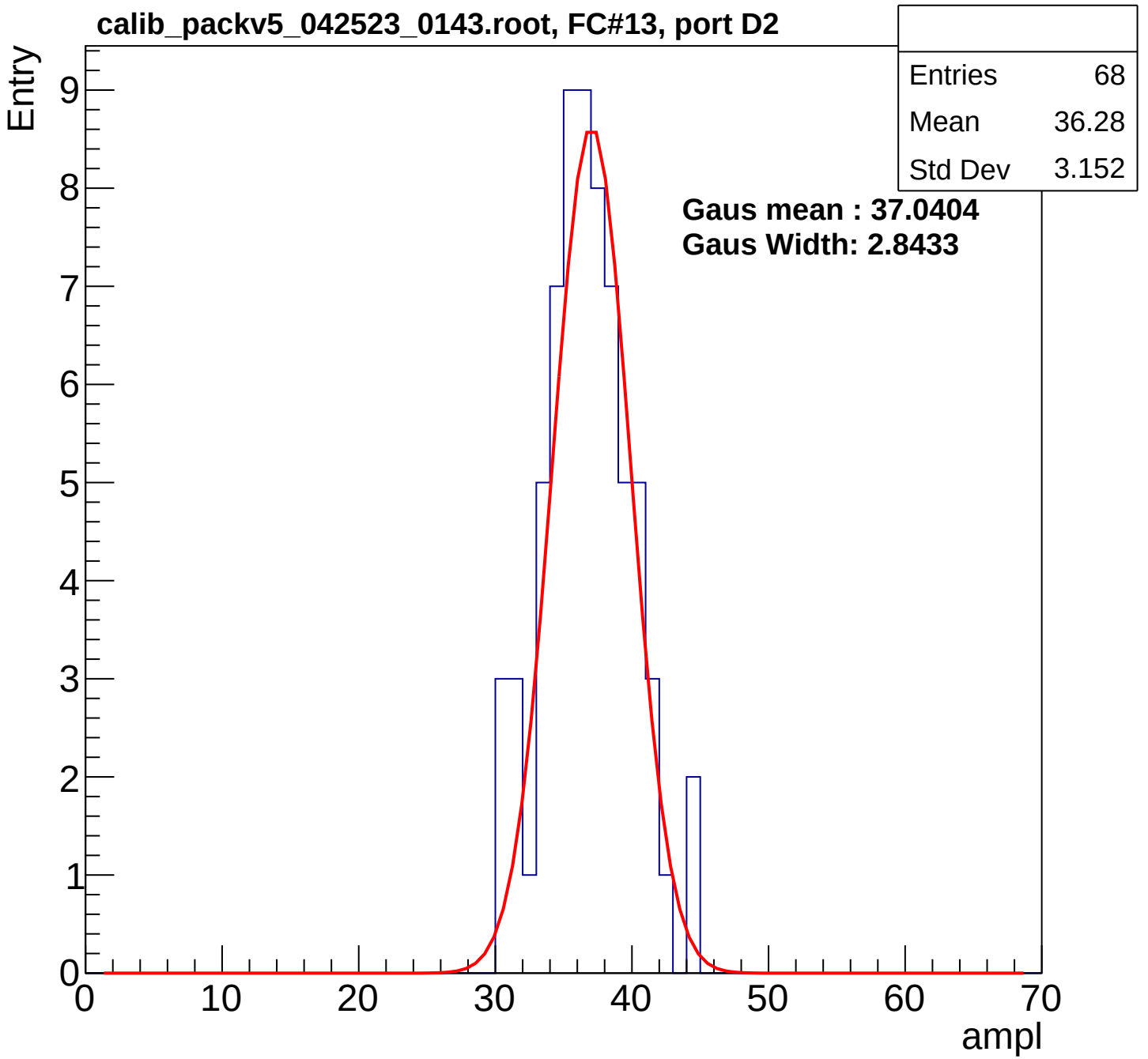
B1L003S, U24-ch14, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.28
Std Dev 3.152
Gaus mean : 37.0404
Gaus Width: 2.8433
Entry
ampl
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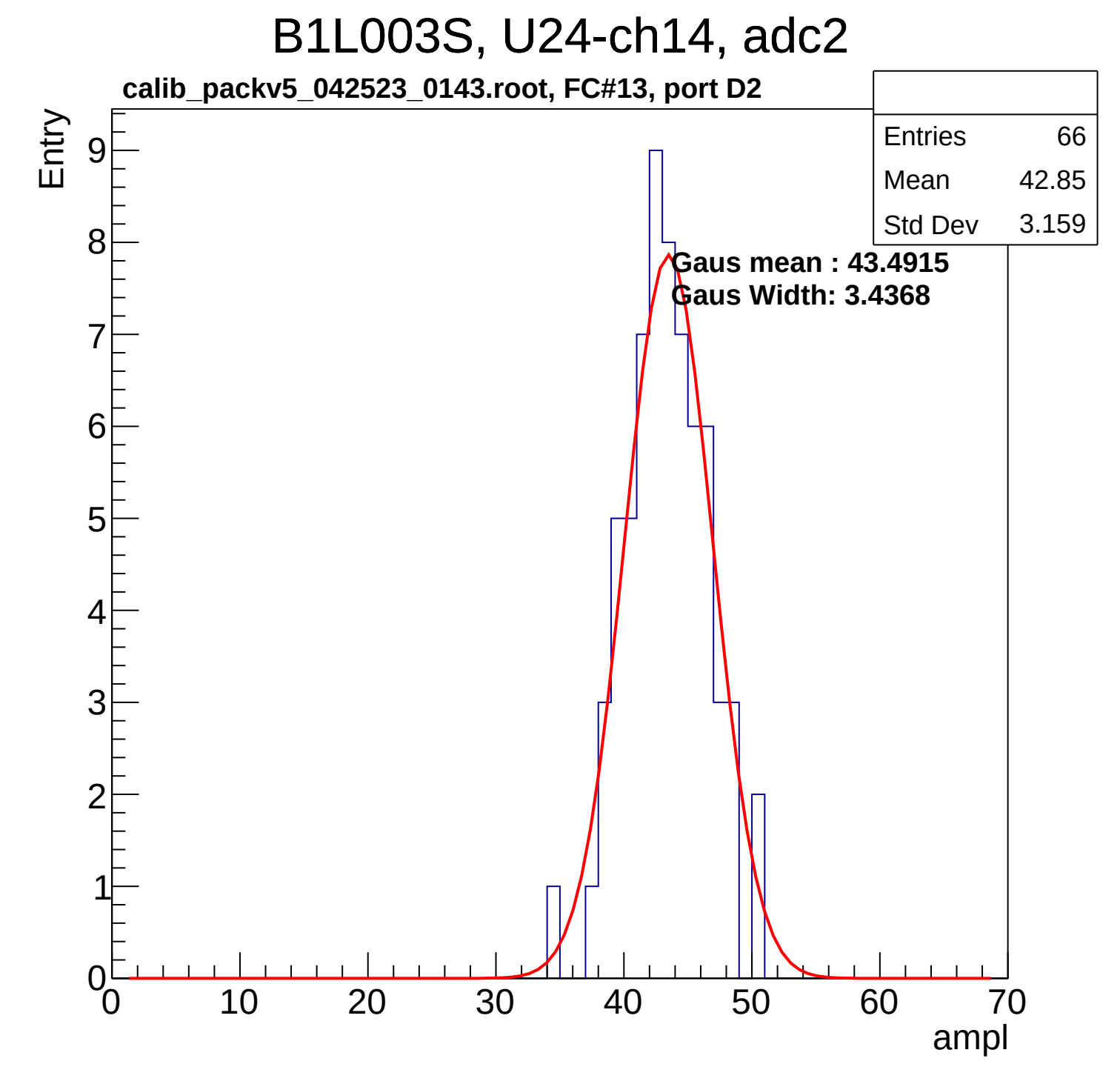
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch14, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.85
Std Dev 3.159
Gaus mean : 43.4915
Gaus Width: 3.4368
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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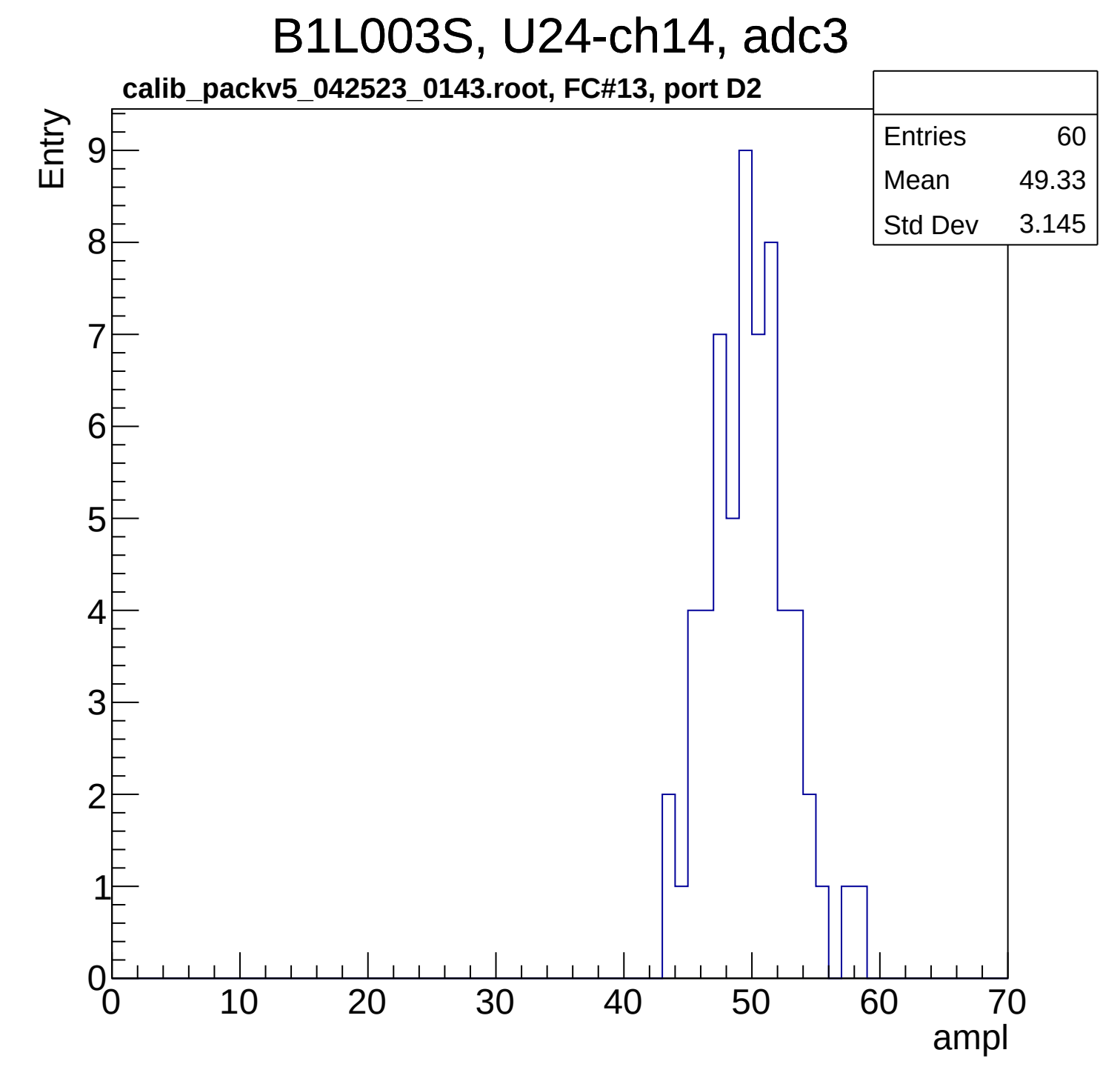
B1L003S, U24-ch14, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.33
Std Dev 3.145
Entry
ampl
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7
8
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0
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40
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60
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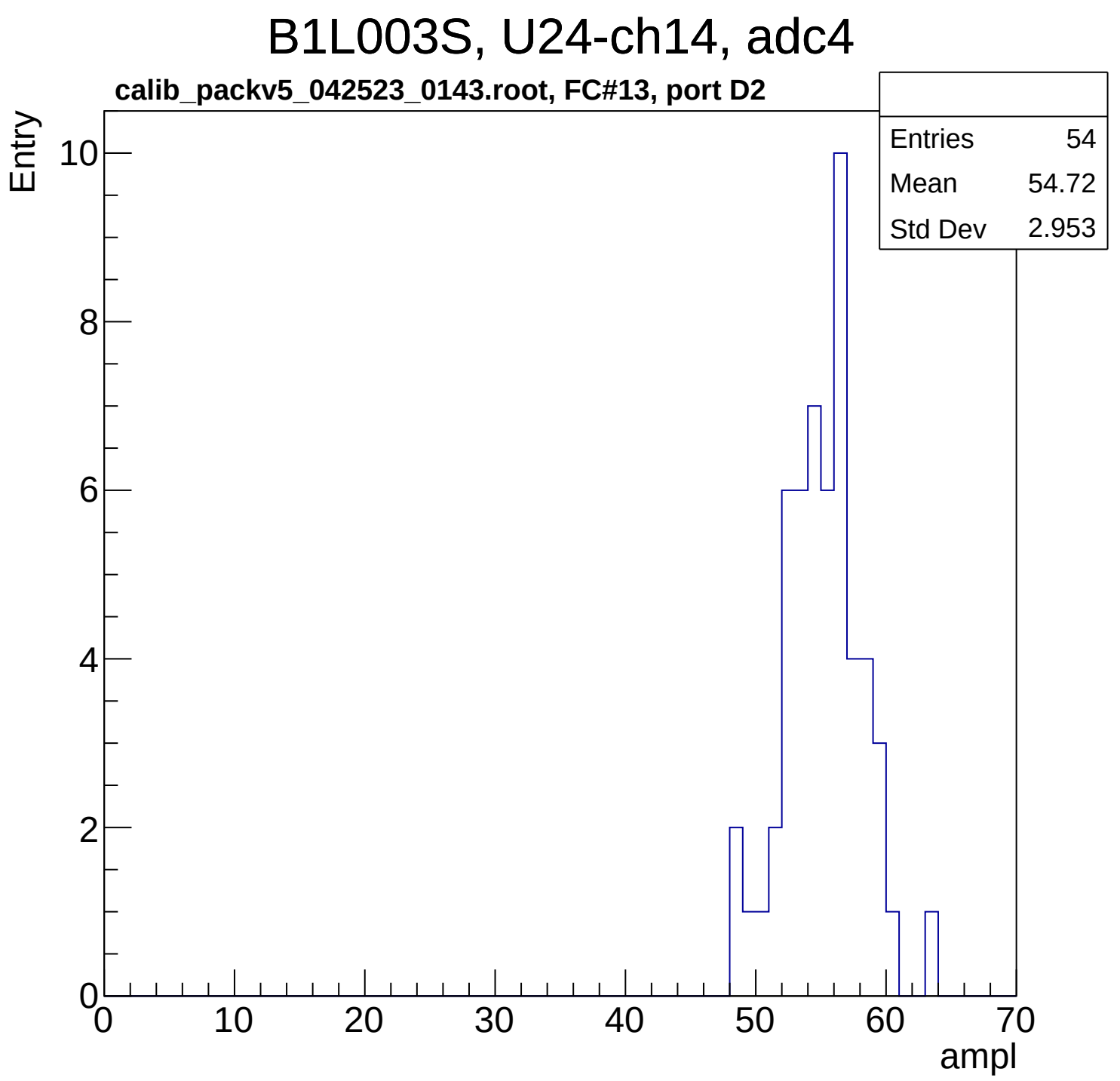
B1L003S, U24-ch14, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.72
Std Dev 2.953
Entry
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6
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10
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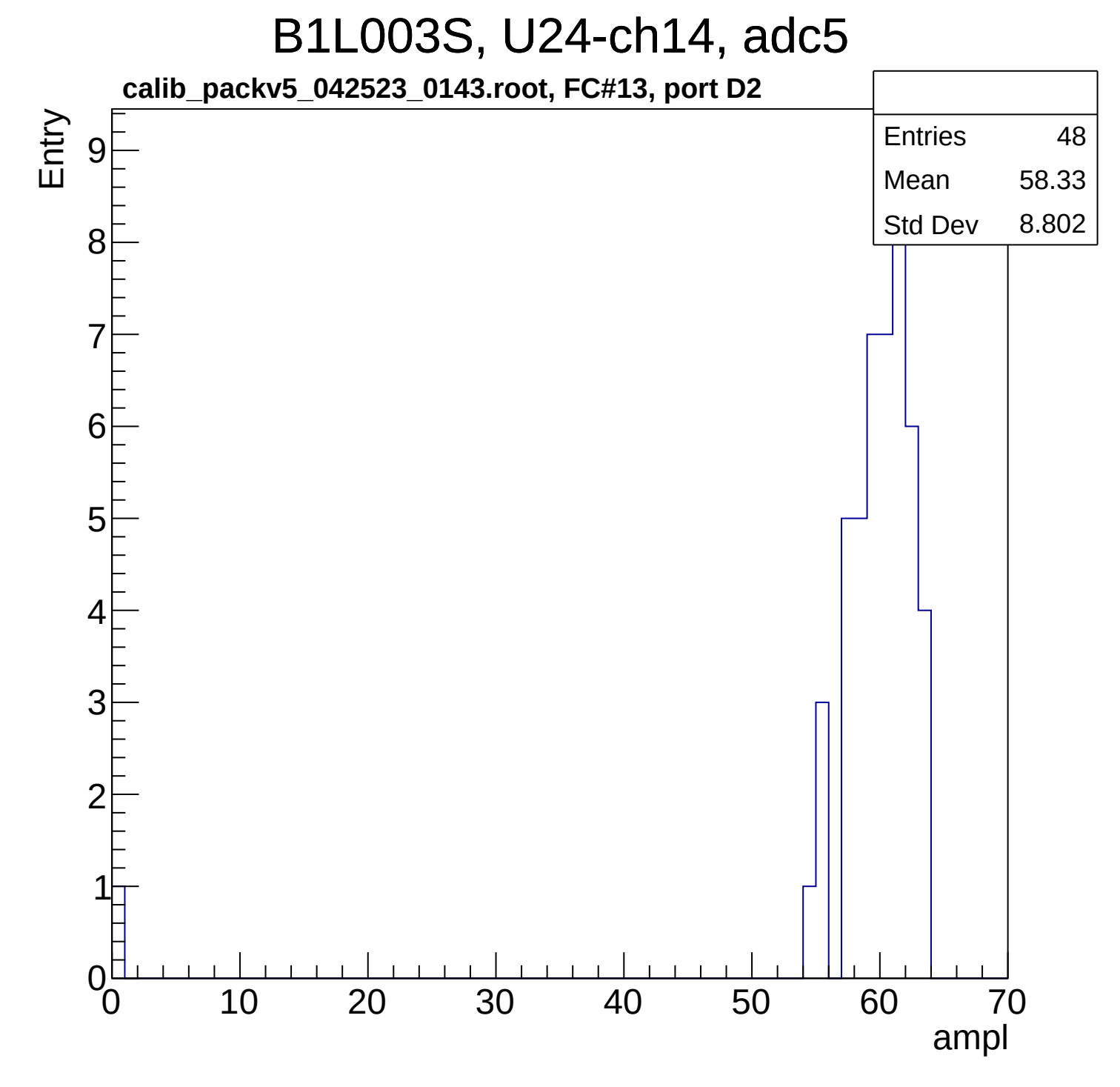
50
60
70
ampl


B1L003S, U24-ch14, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.33
Std Dev 8.802
Entry
ampl
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3
4
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6
7
8
9
0
10
20
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40
50
60
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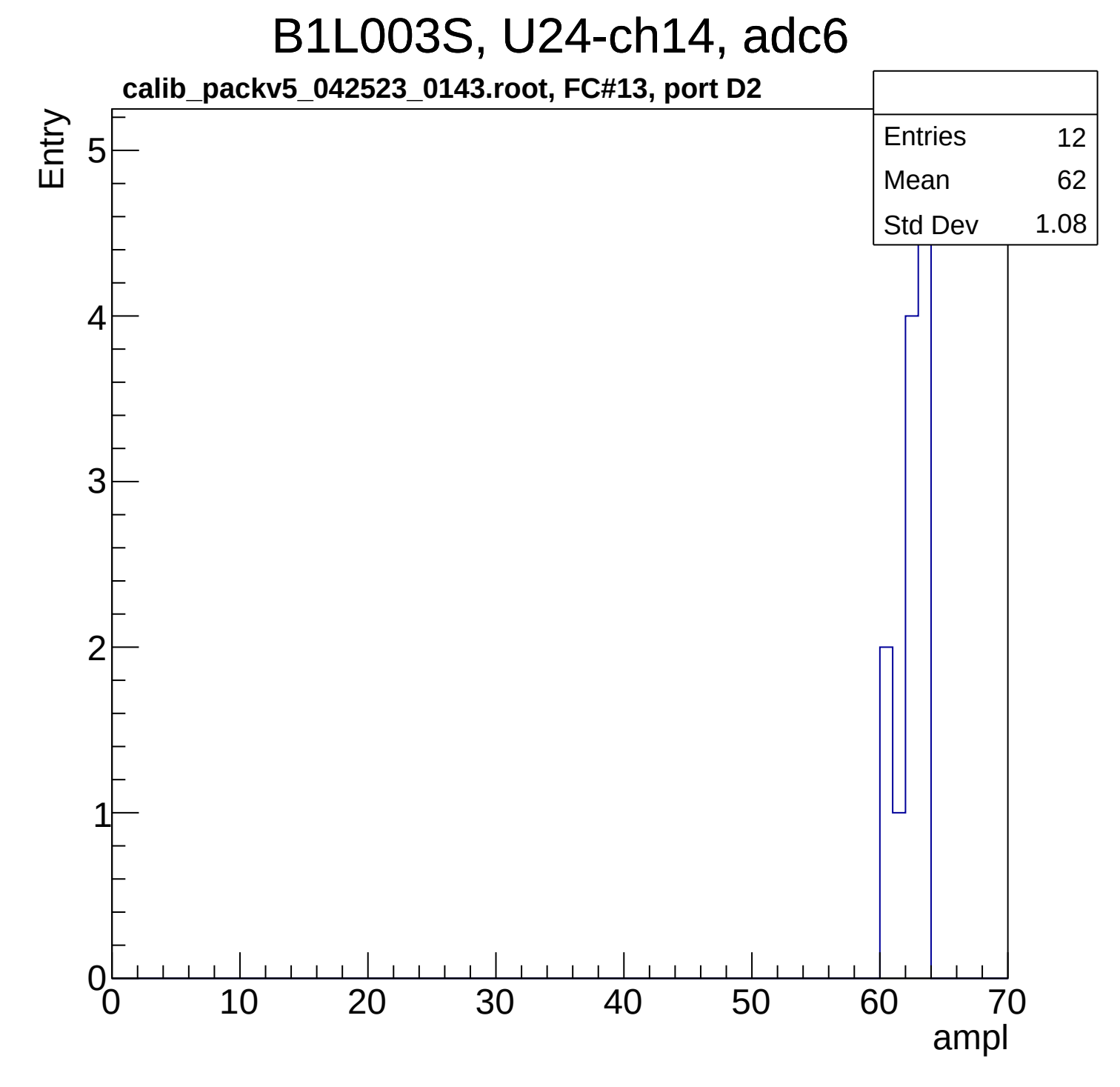
B1L003S, U24-ch14, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 62
Std Dev 1.08
Entry
0
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3
4
5
0
10
20
30
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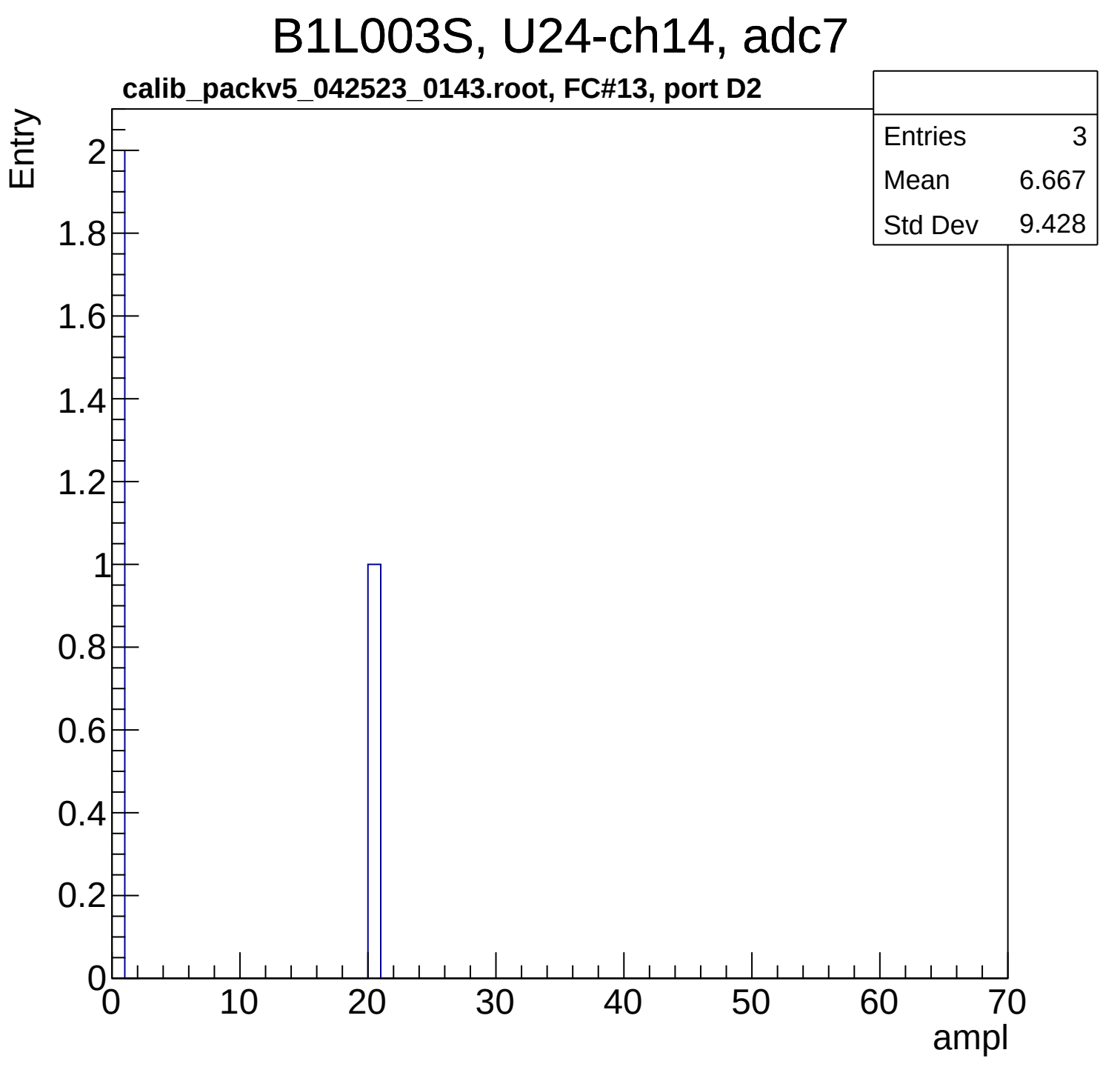
50
60
70
ampl


B1L003S, U24-ch14, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
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0.2
0.4
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1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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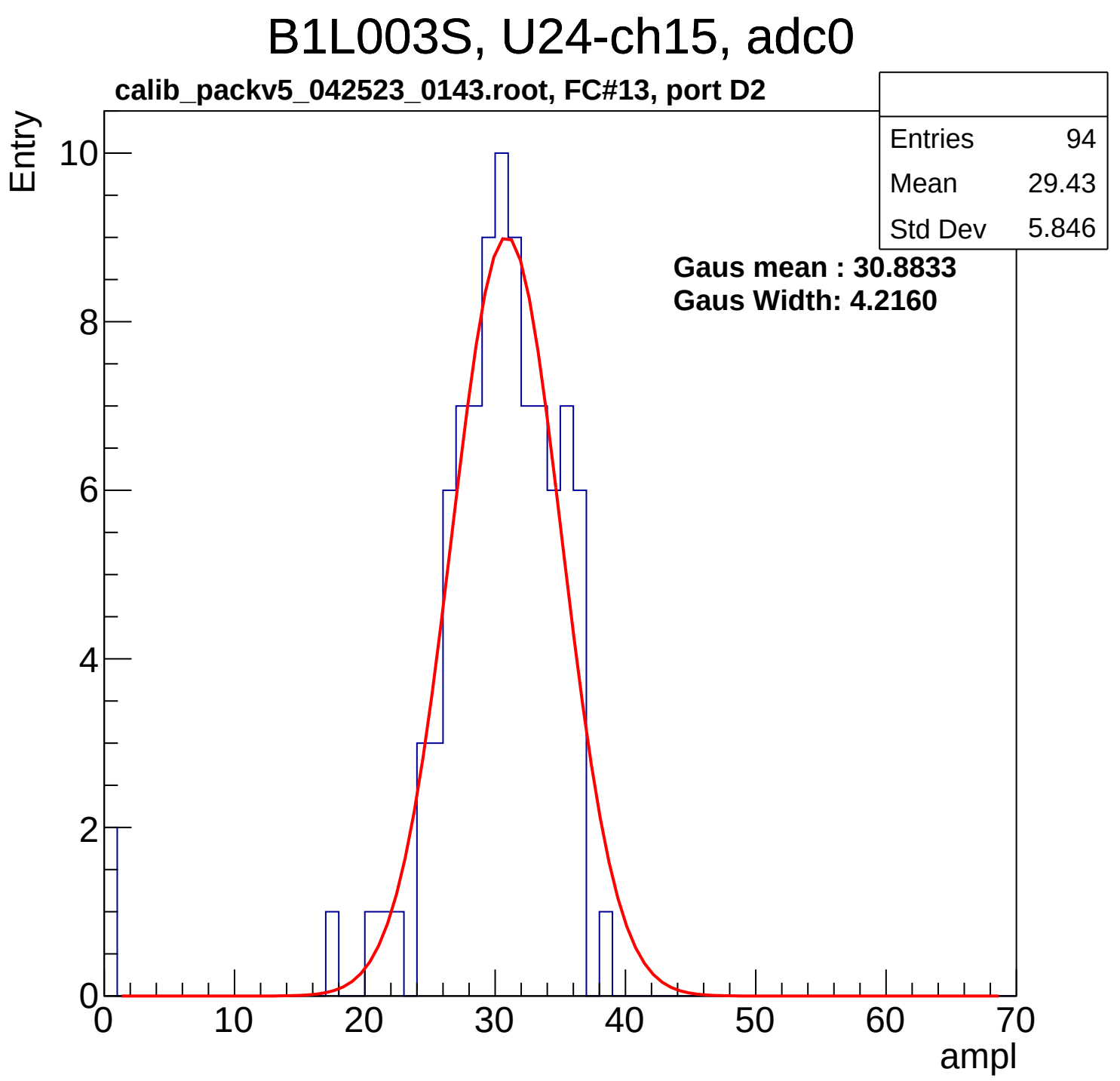
B1L003S, U24-ch15, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 94
Mean 29.43
Std Dev 5.846
Gaus mean : 30.8833
Gaus Width: 4.2160
Entry
ampl
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2
4
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40
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60
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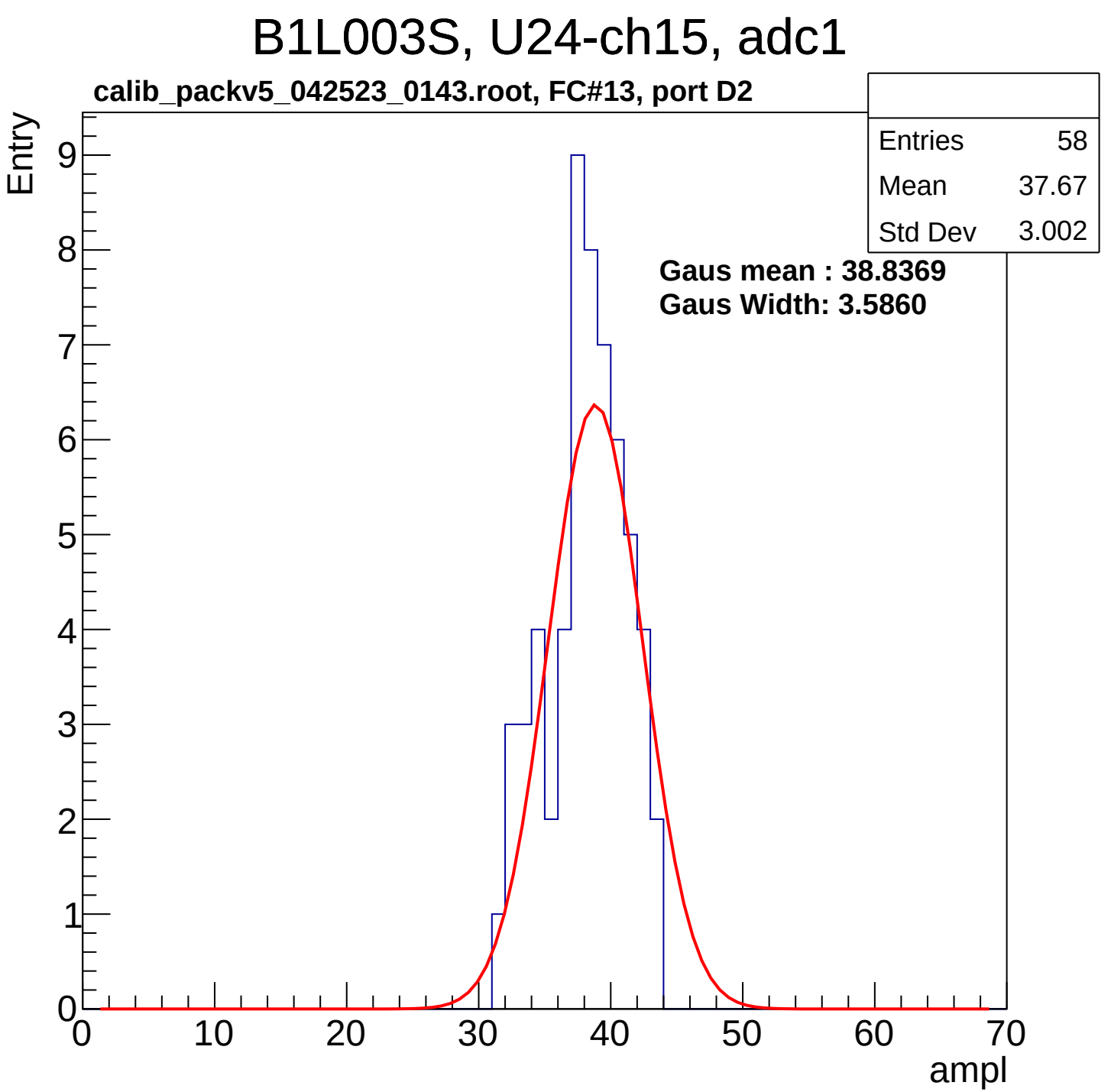
B1L003S, U24-ch15, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 37.67
Std Dev 3.002
Gaus mean : 38.8369
Gaus Width: 3.5860
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
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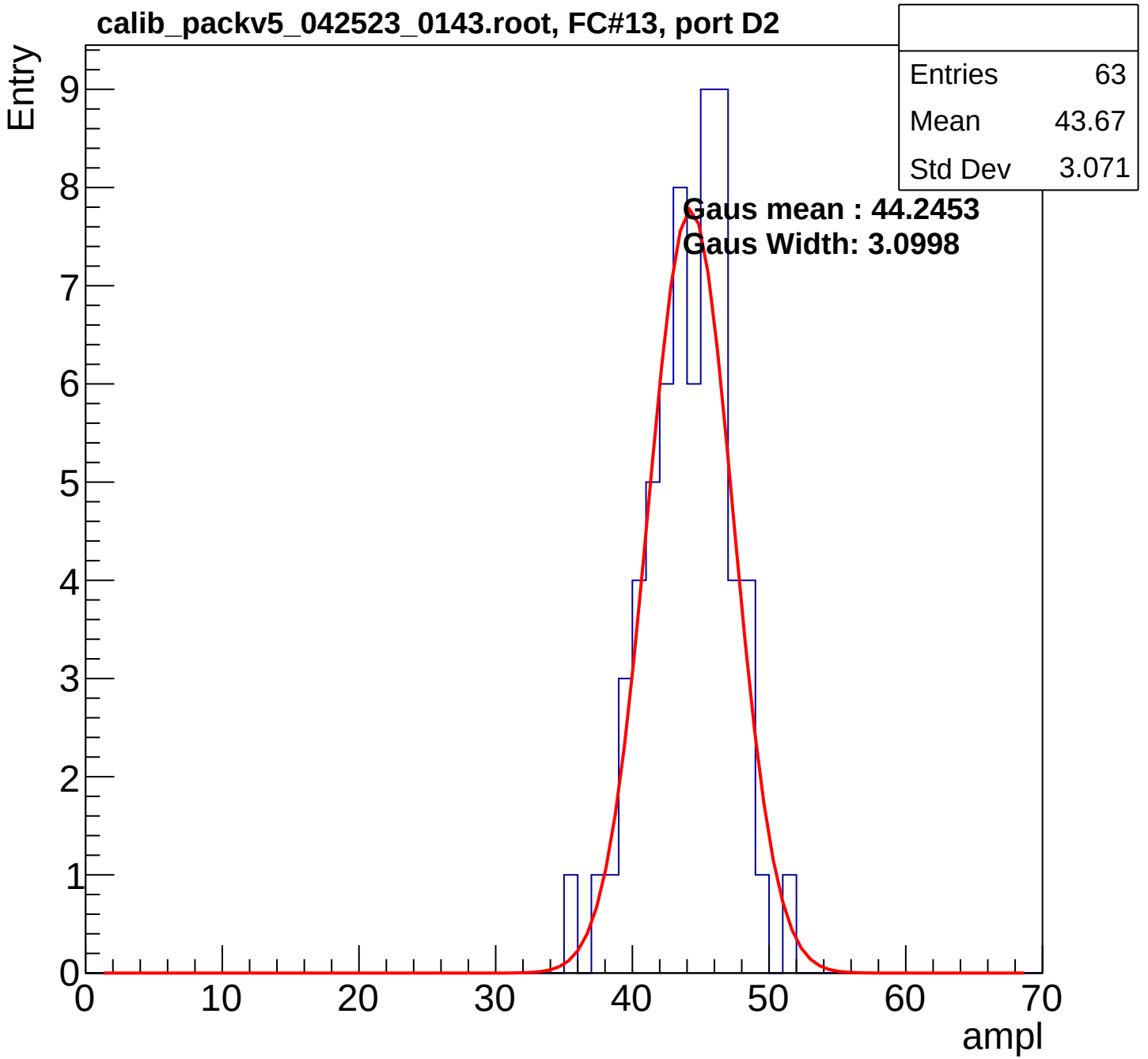
B1L003S, U24-ch15, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 43.67
Std Dev 3.071
Gaus mean : 44.2453
Gaus Width: 3.0998
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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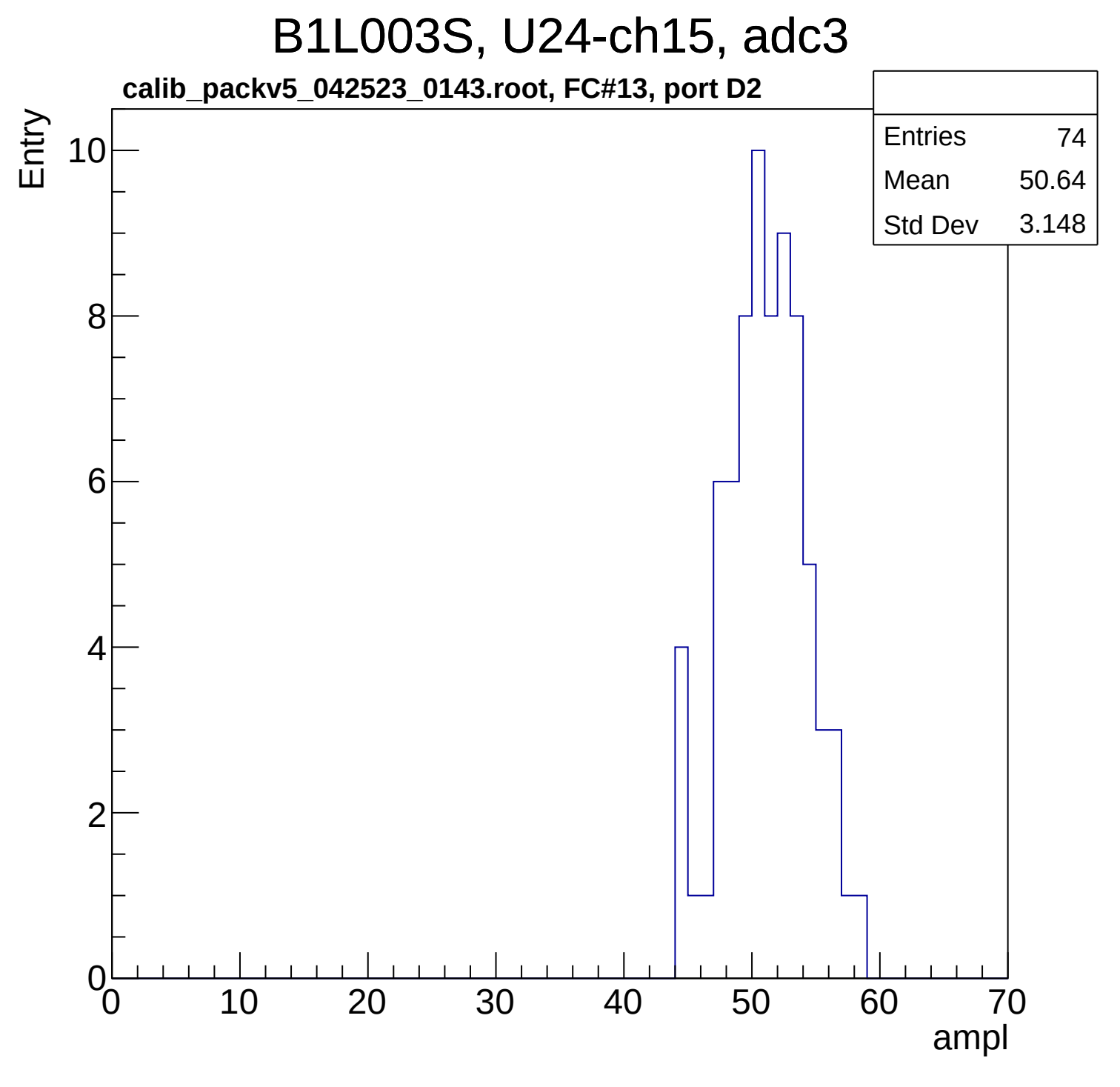
60
70


B1L003S, U24-ch15, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 50.64
Std Dev 3.148
Entry
0
2
4
6
8
10
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10
20
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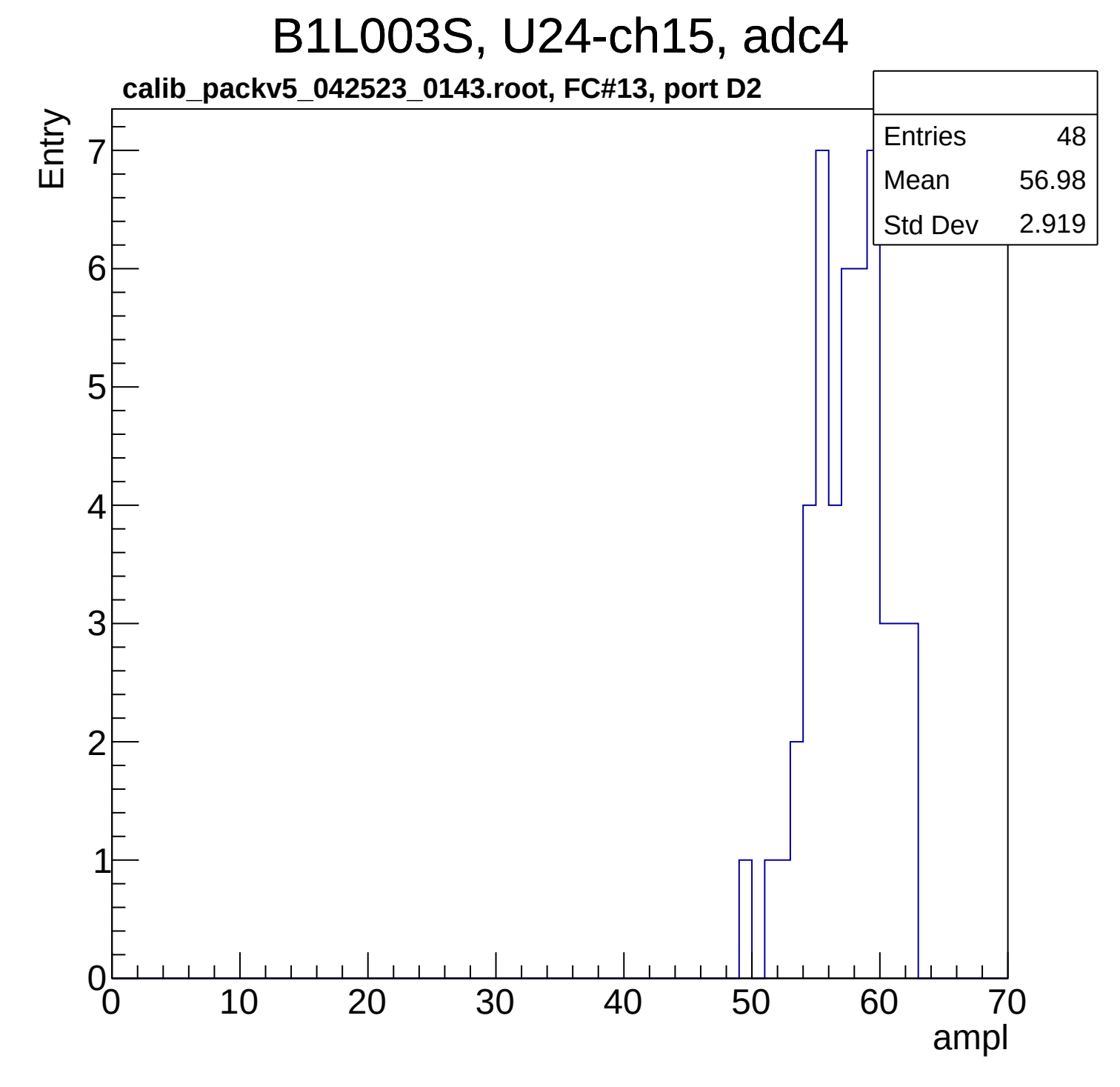
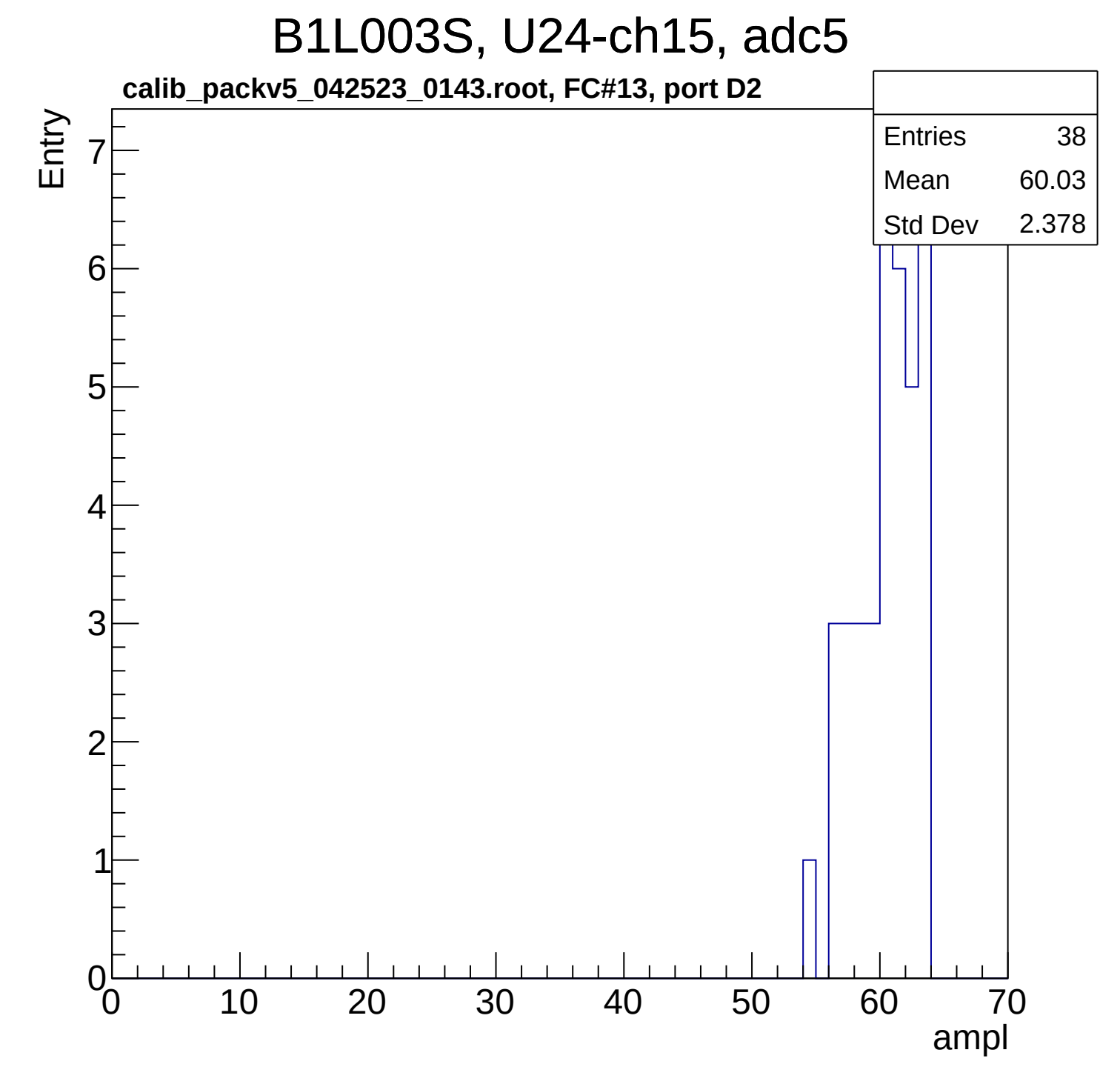
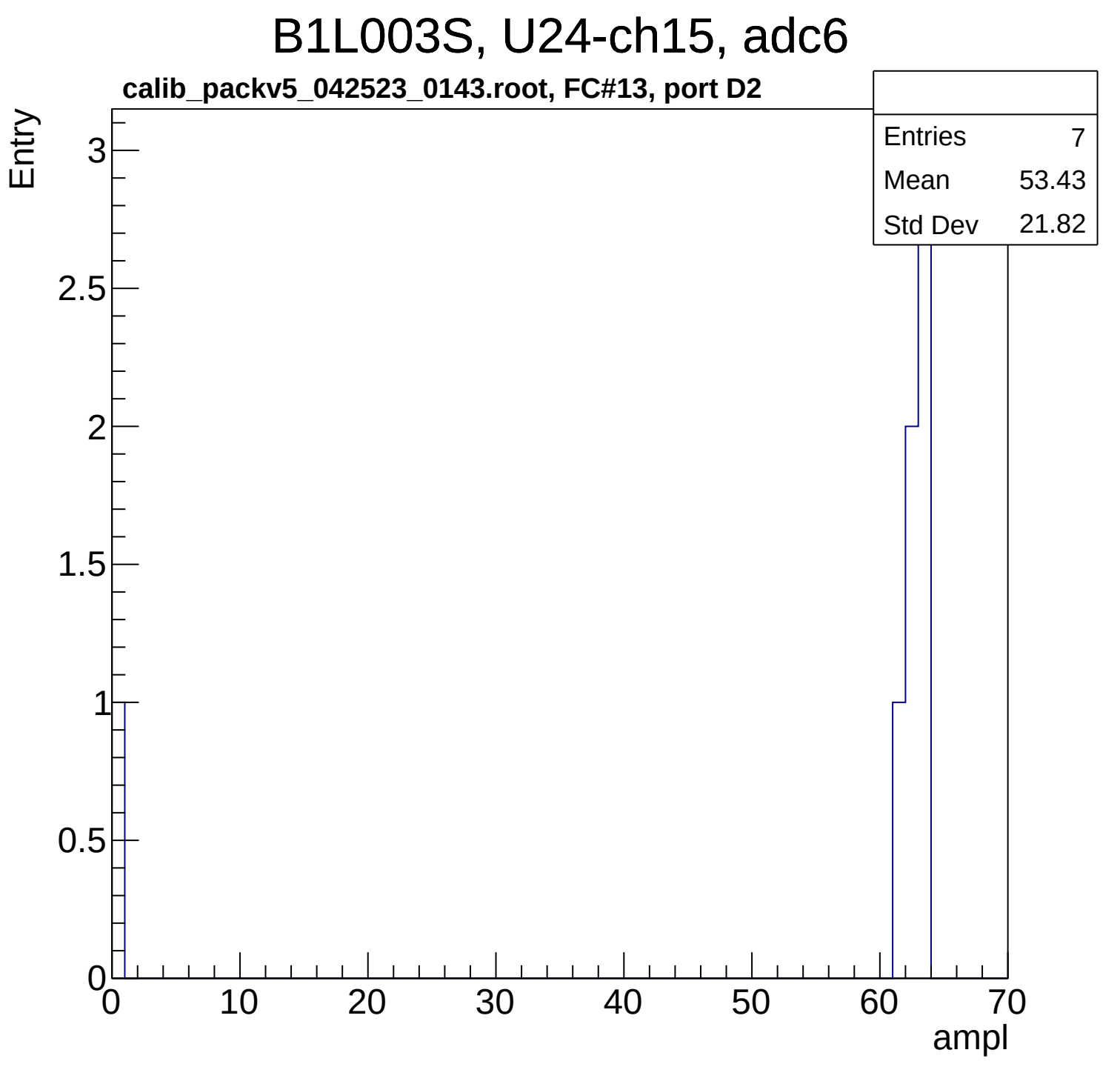
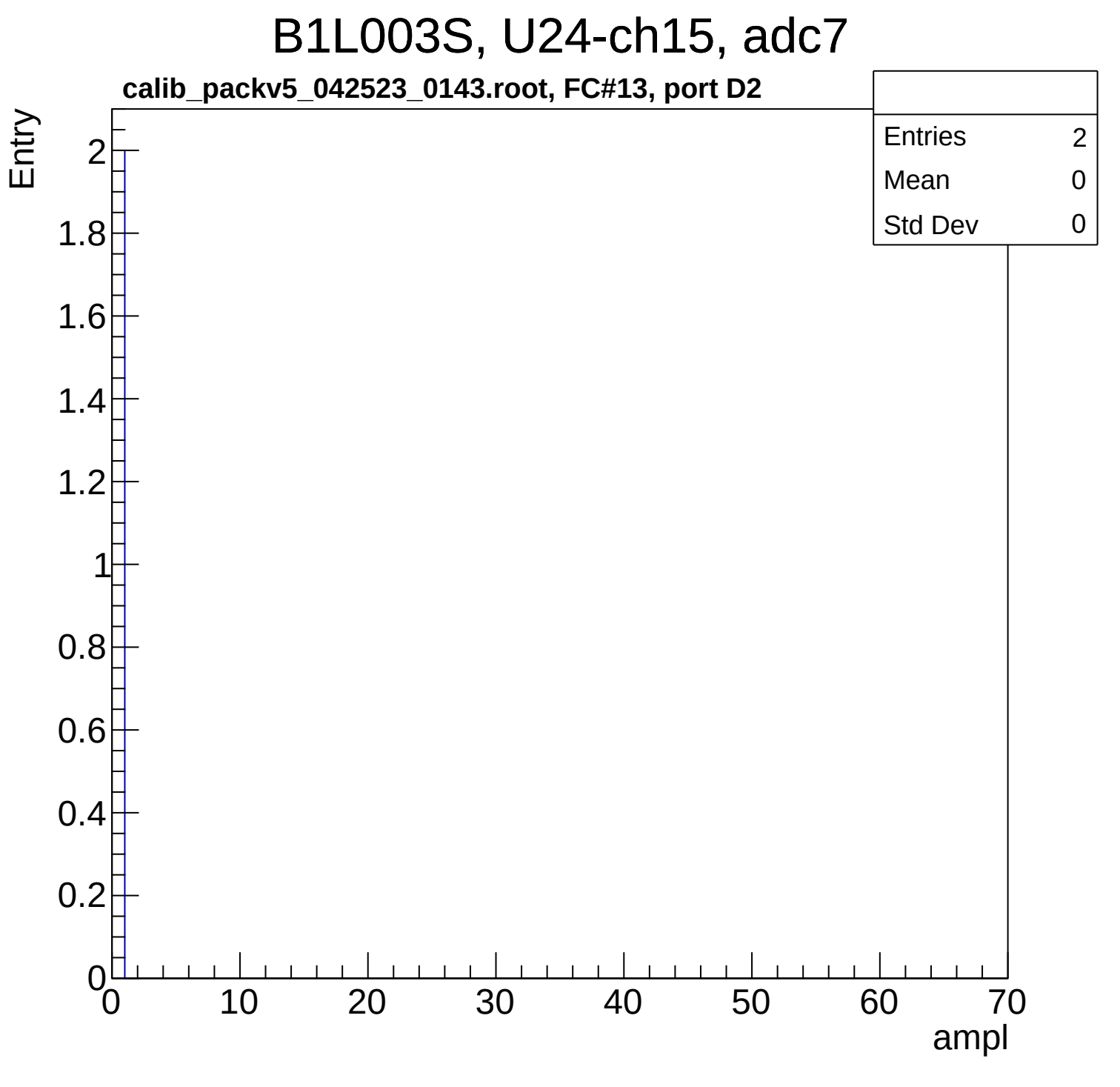
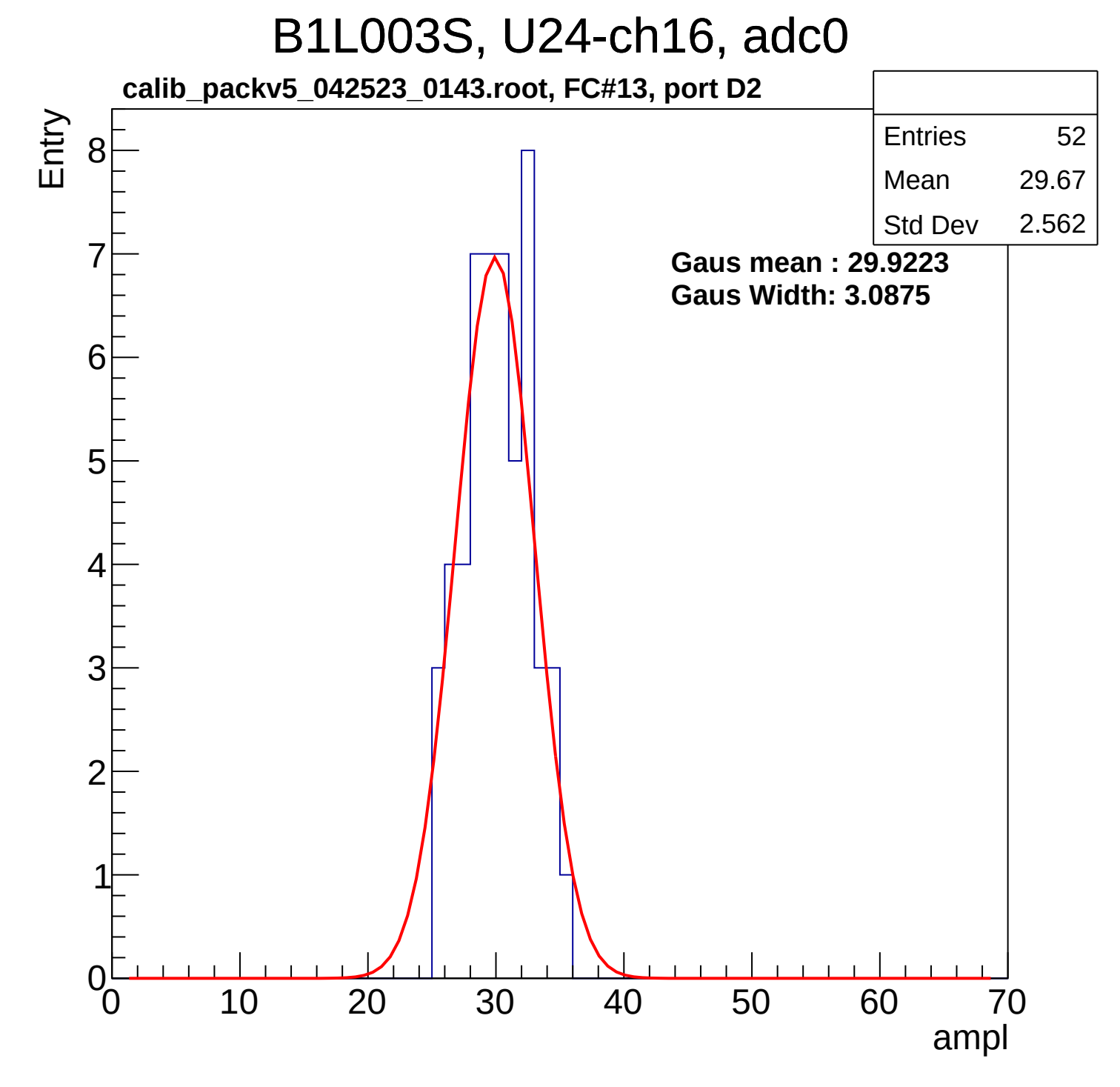
50
60
70
ampl

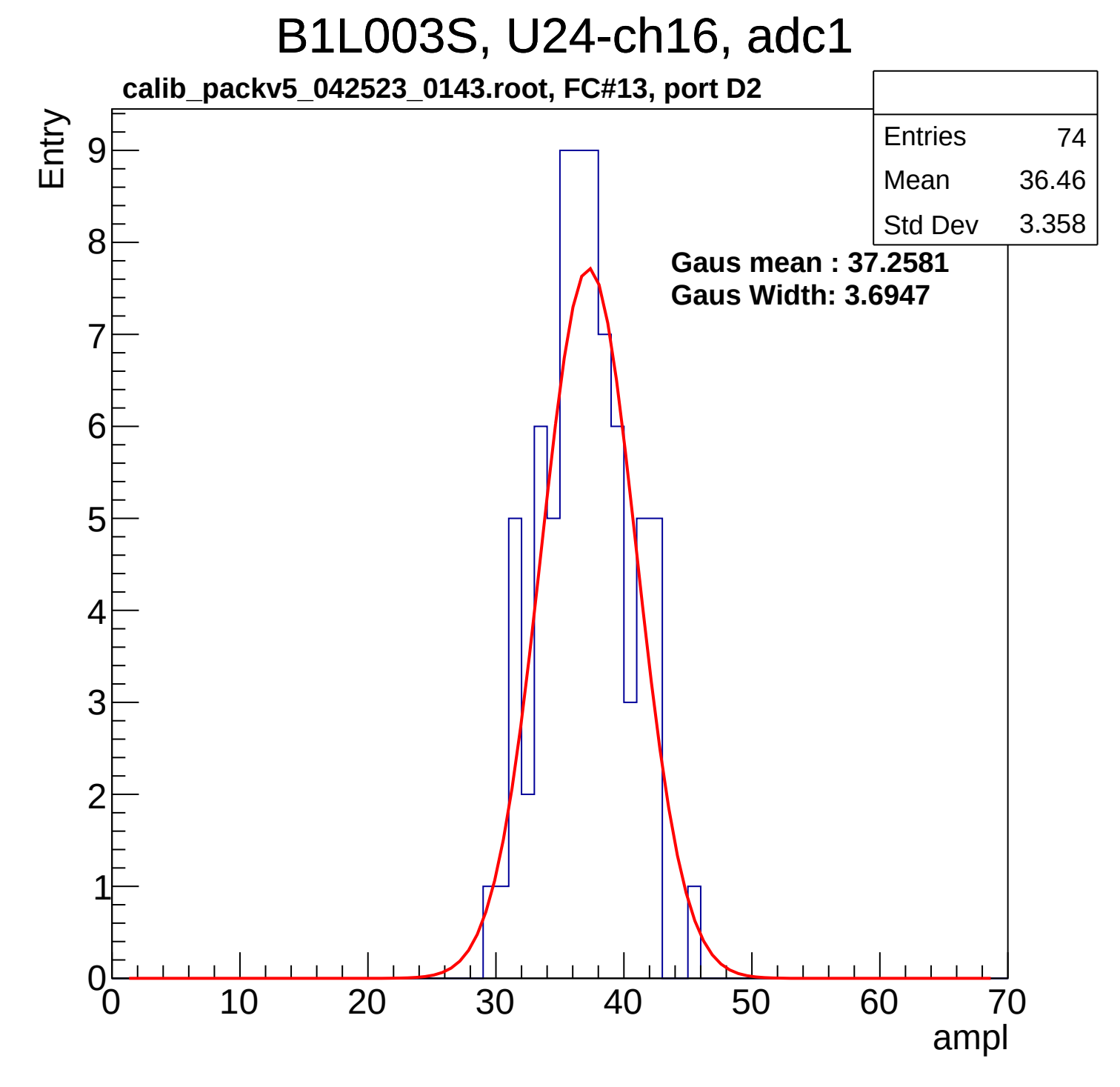

B1L003S, U24-ch15, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 56.98
Std Dev 2.919
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch15, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 60.03
Std Dev 2.378
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch15, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.43
Std Dev 21.82
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch15, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch16, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 29.67
Std Dev 2.562
Gaus mean : 29.9223
Gaus Width: 3.0875
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch16, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.46
Std Dev 3.358
Gaus mean : 37.2581
Gaus Width: 3.6947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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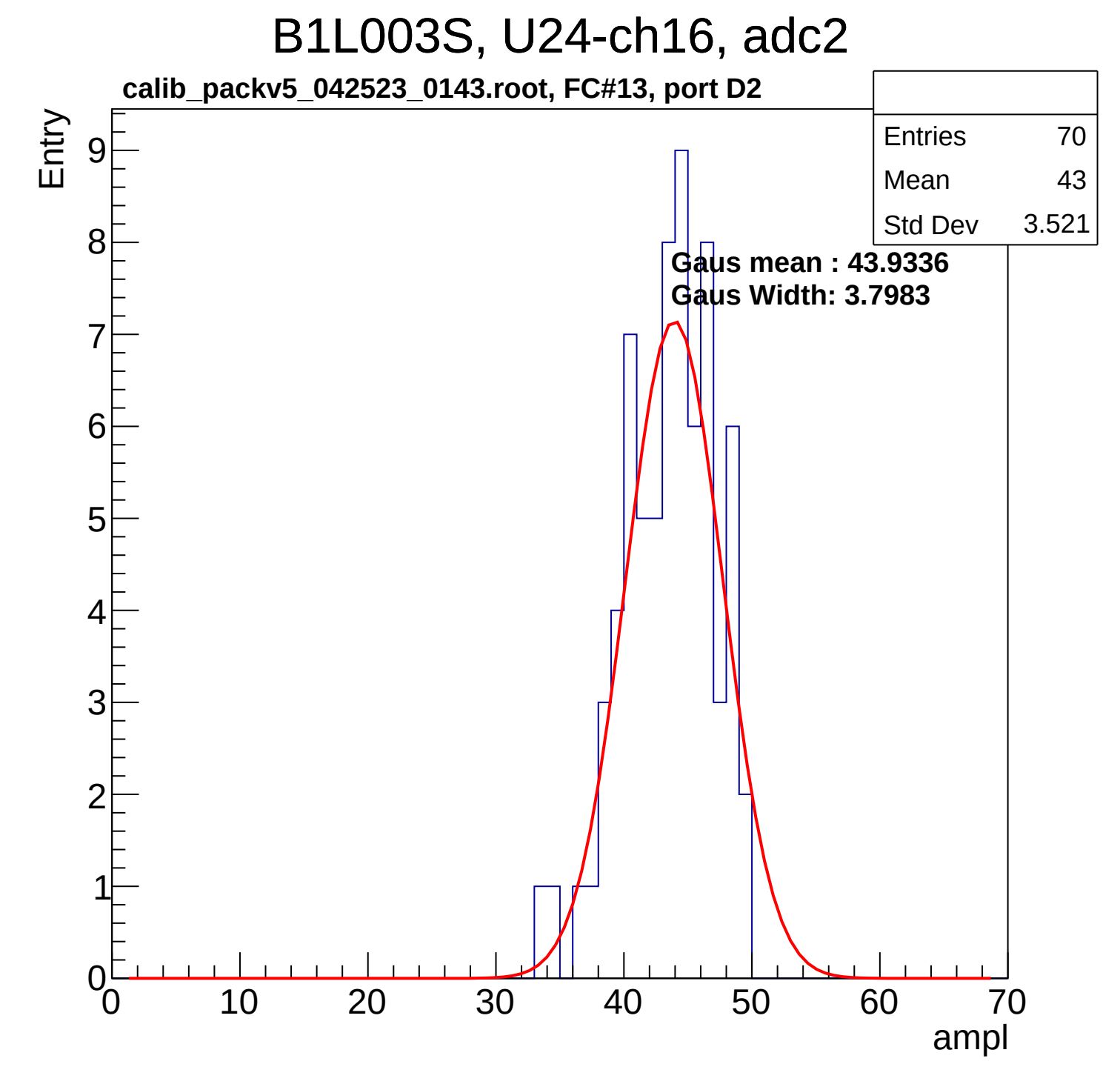
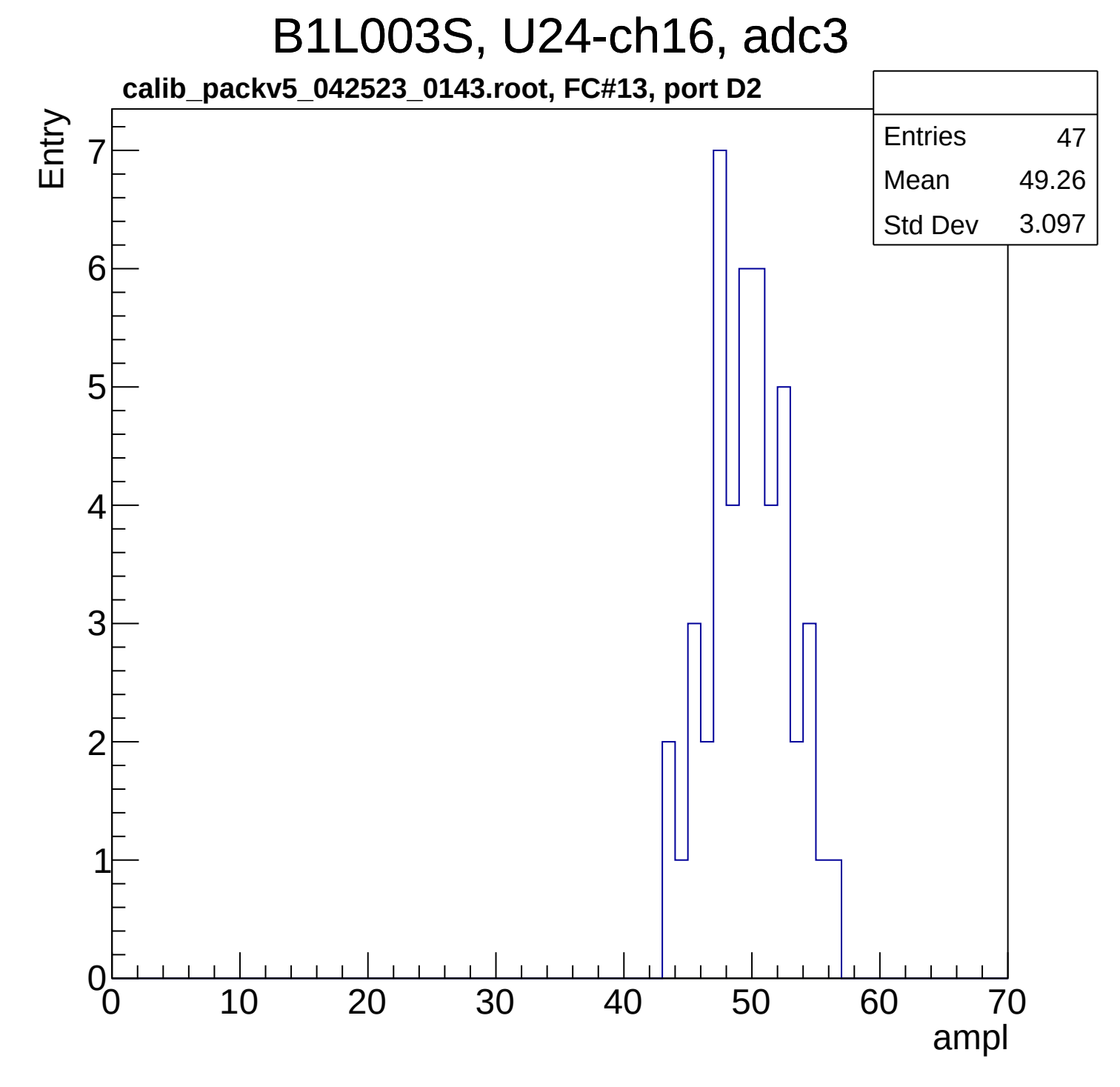
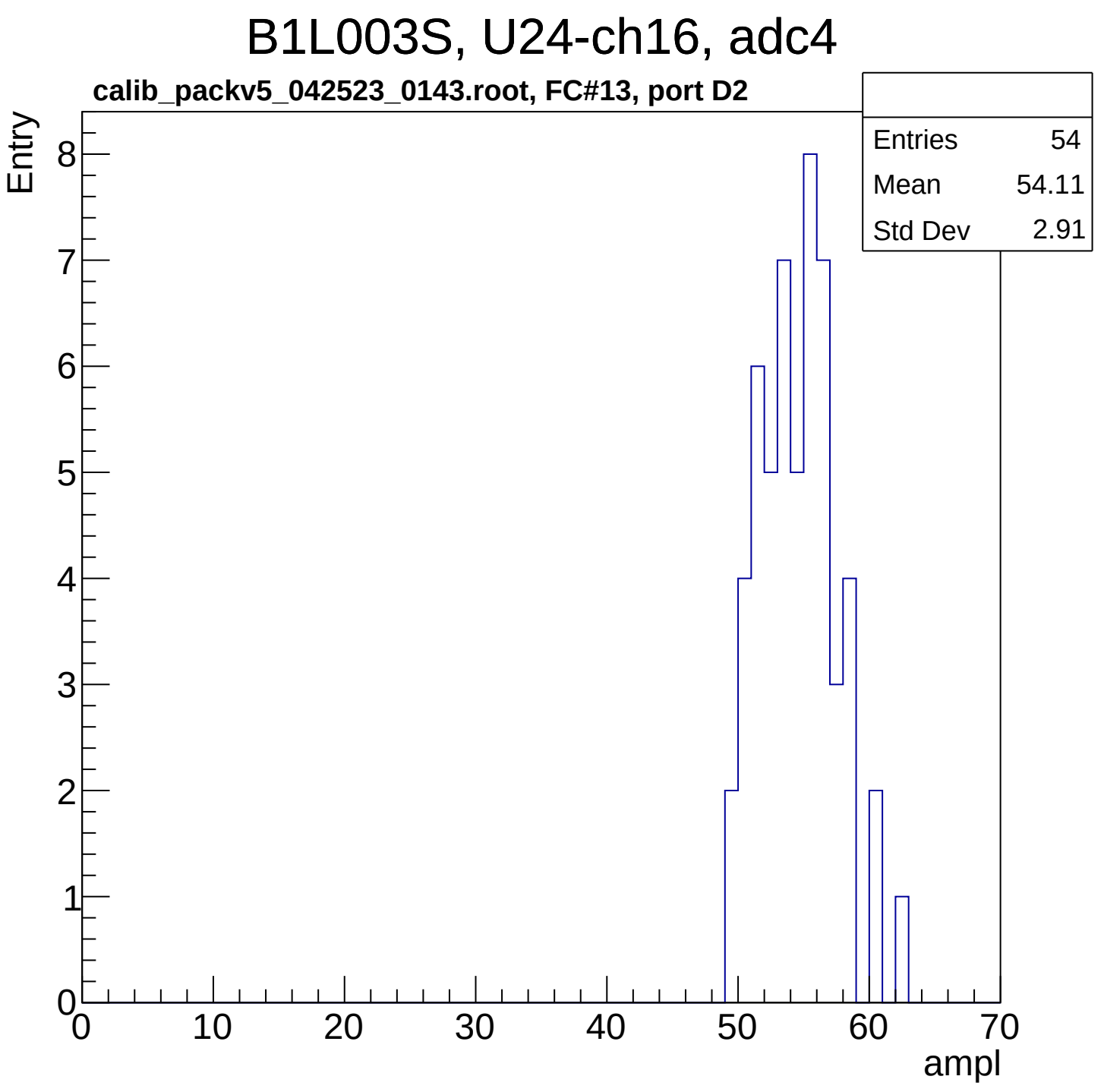
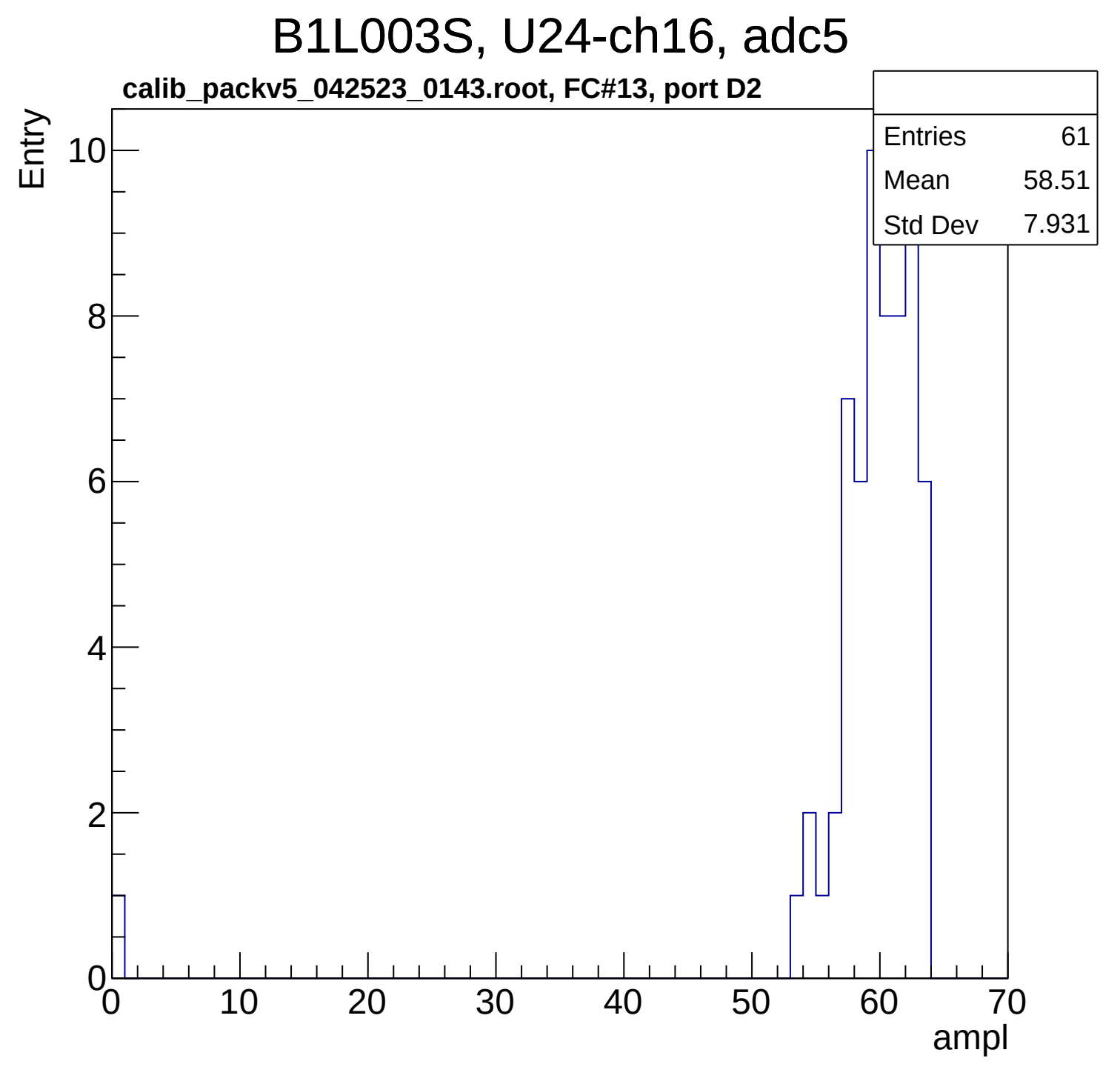
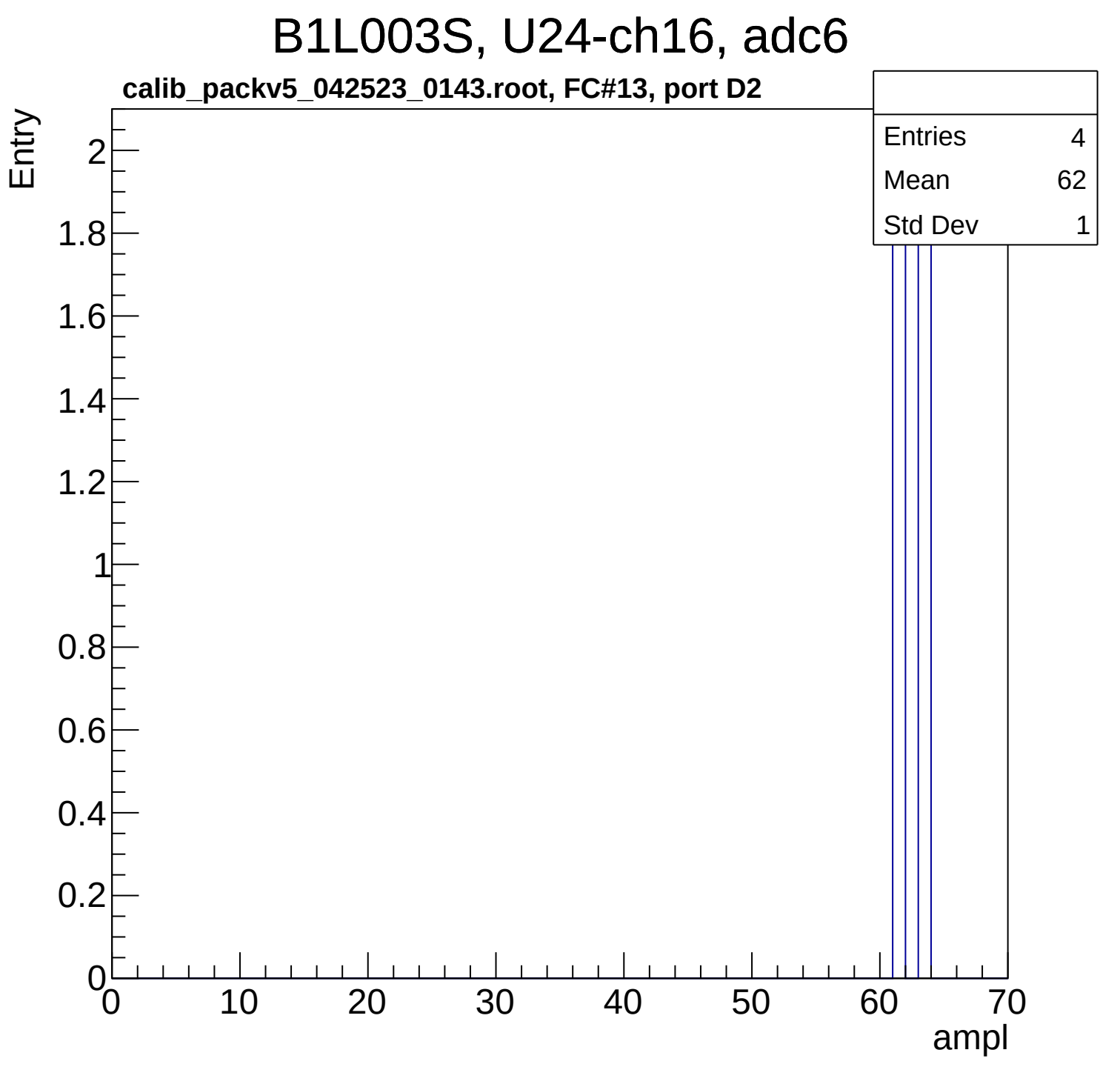
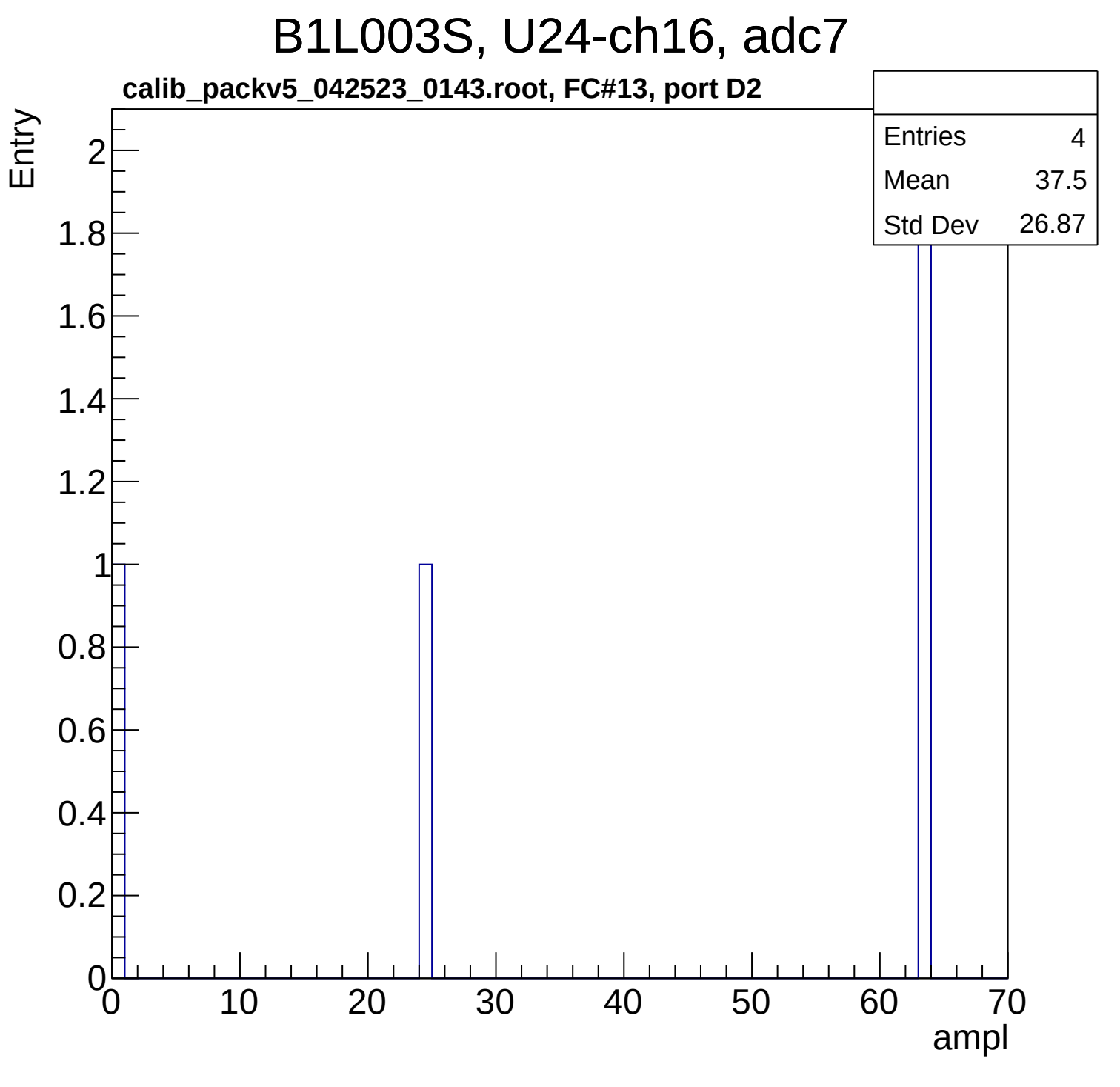

B1L003S, U24-ch16, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 43
Std Dev 3.521
Gaus mean : 43.9336
Gaus Width: 3.7983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

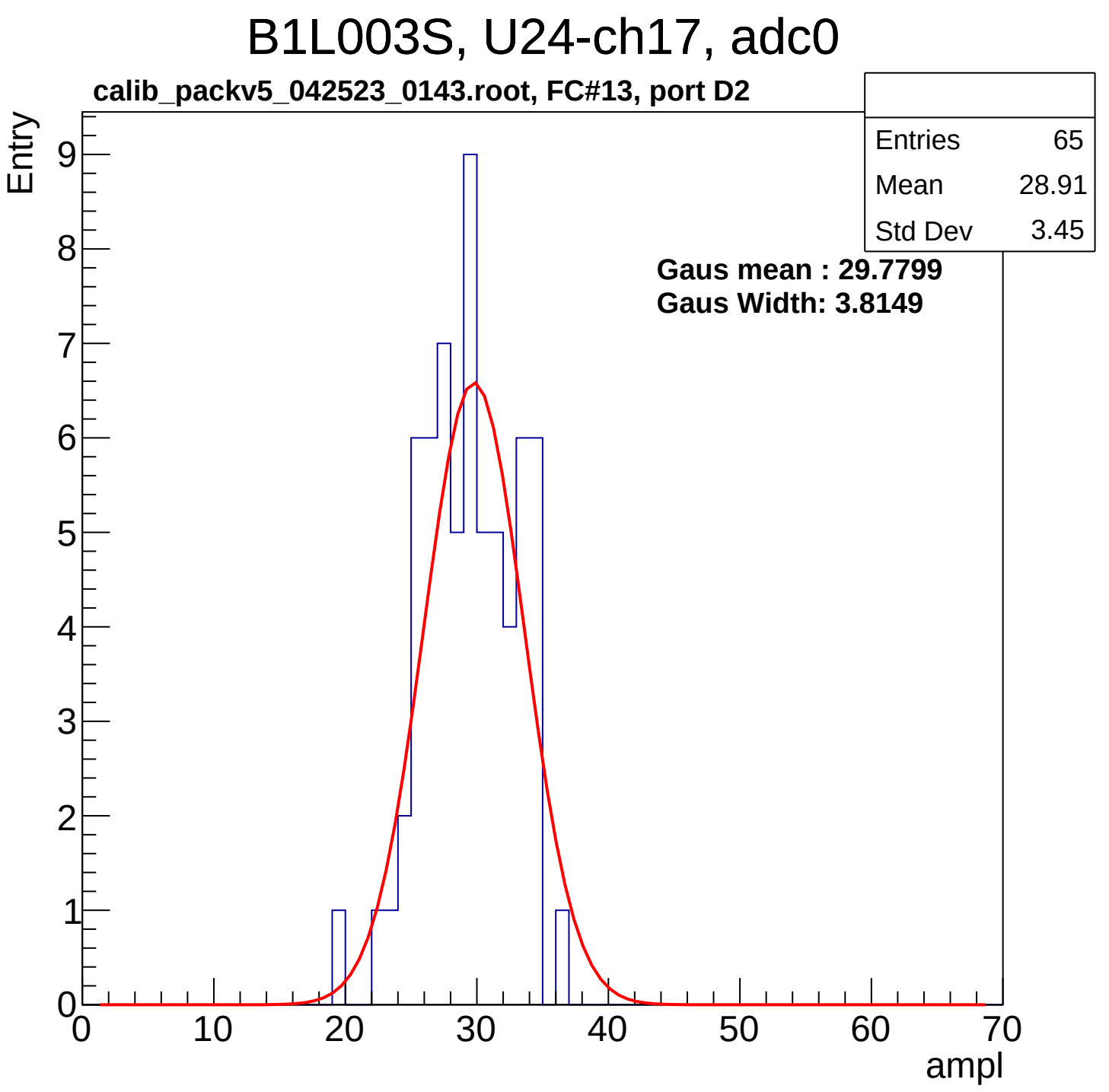

B1L003S, U24-ch16, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 49.26
Std Dev 3.097
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

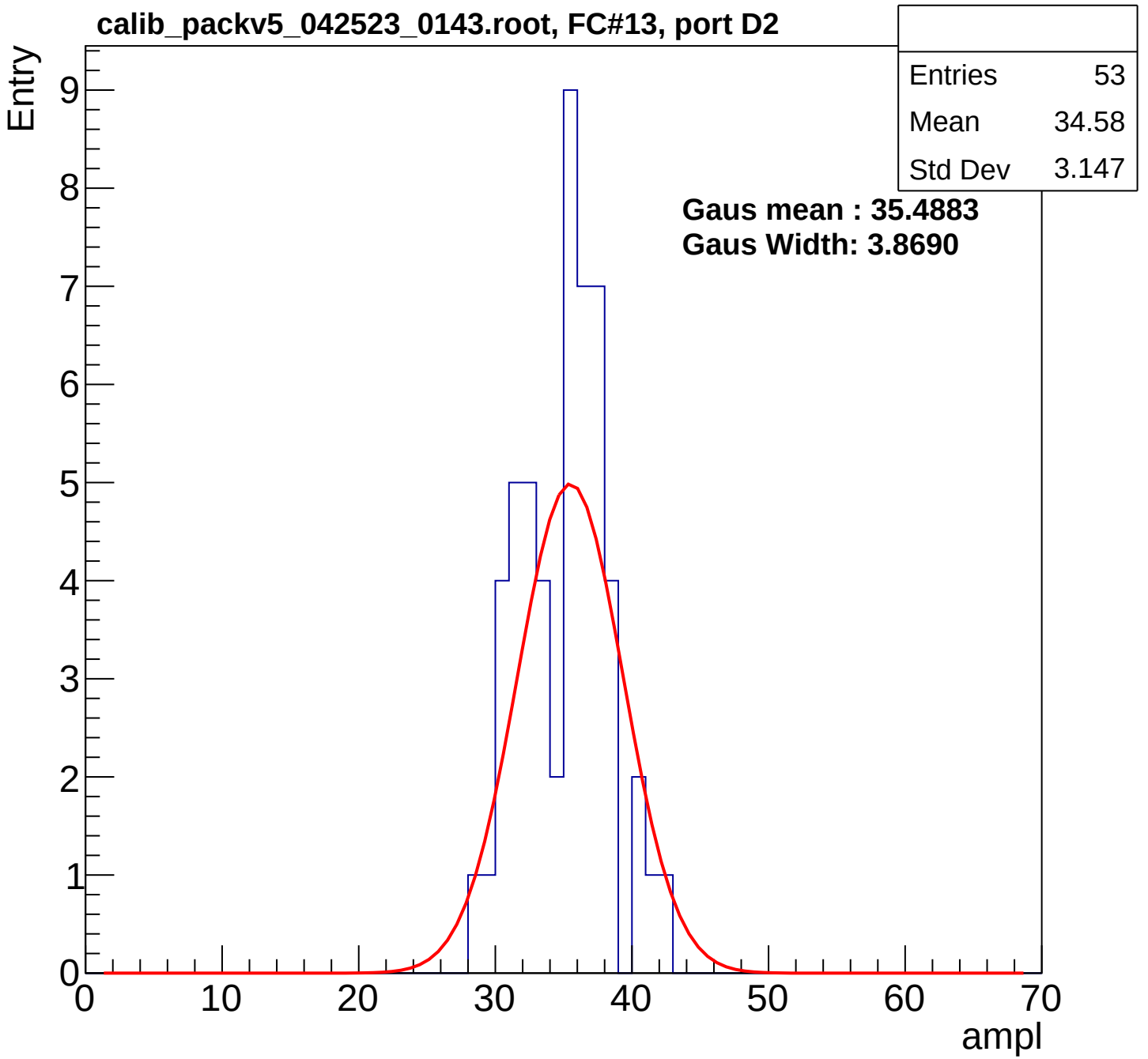

B1L003S, U24-ch16, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.11
Std Dev 2.91
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

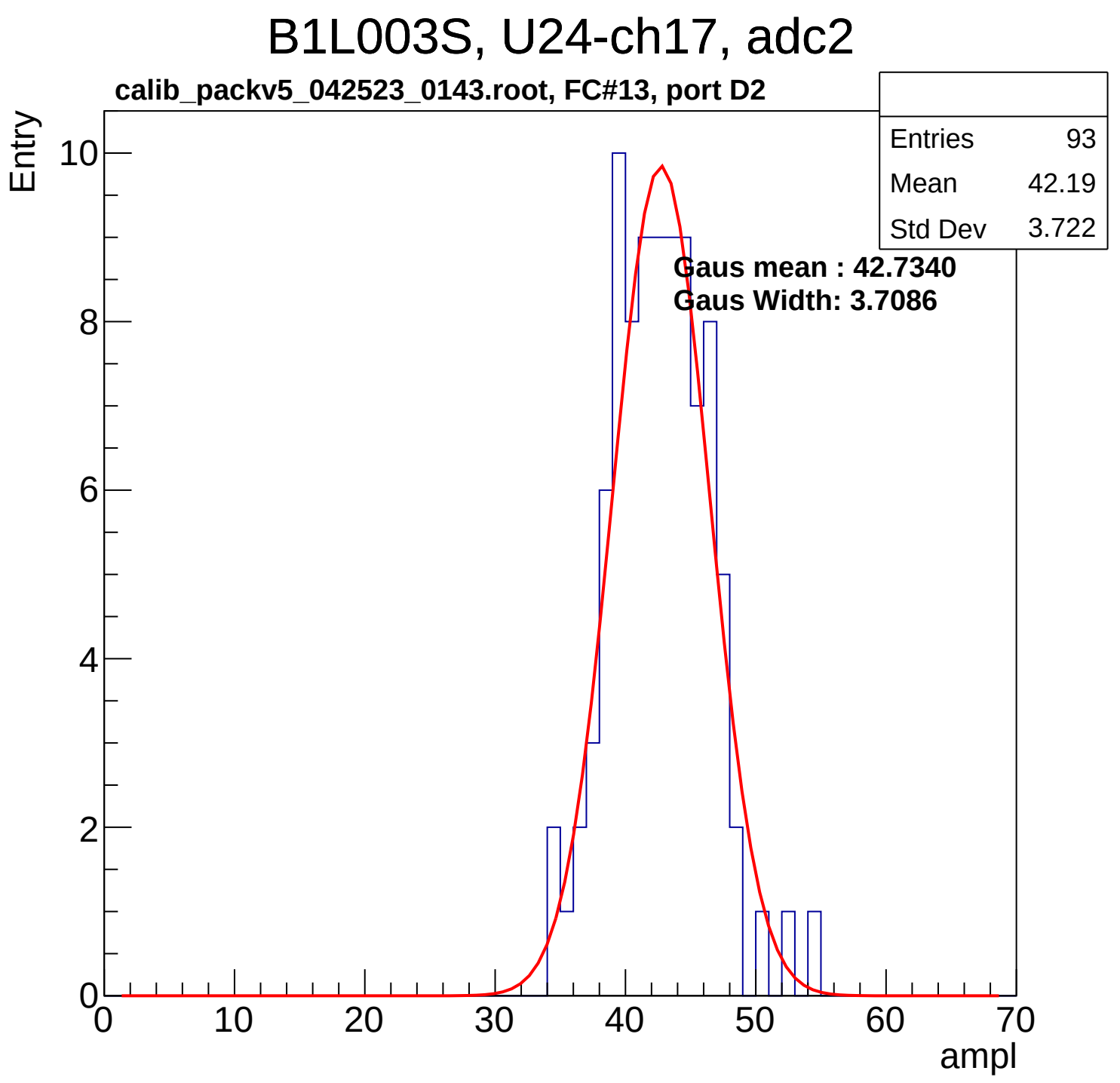

B1L003S, U24-ch16, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58.51
Std Dev 7.931
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

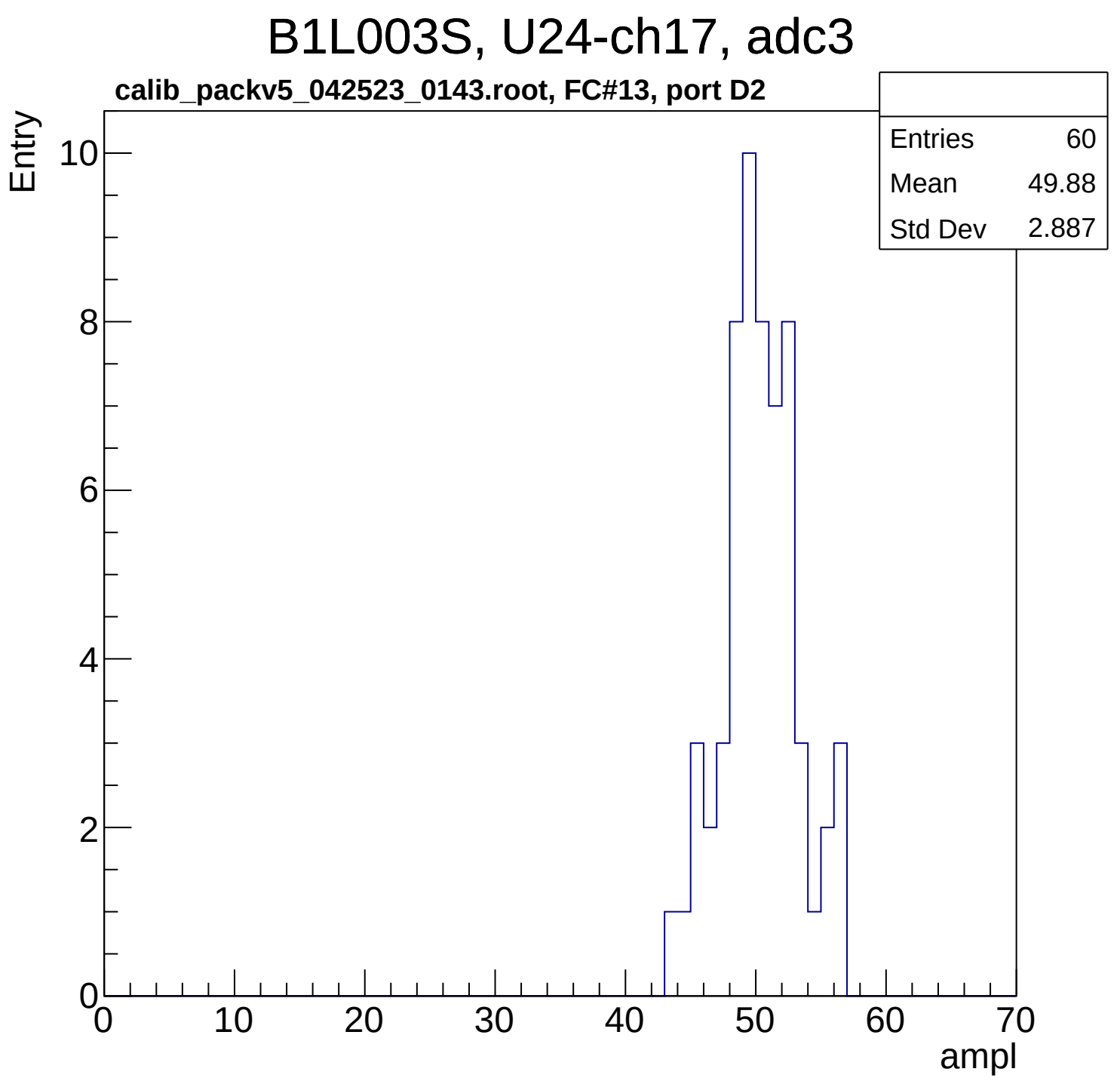

B1L003S, U24-ch16, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

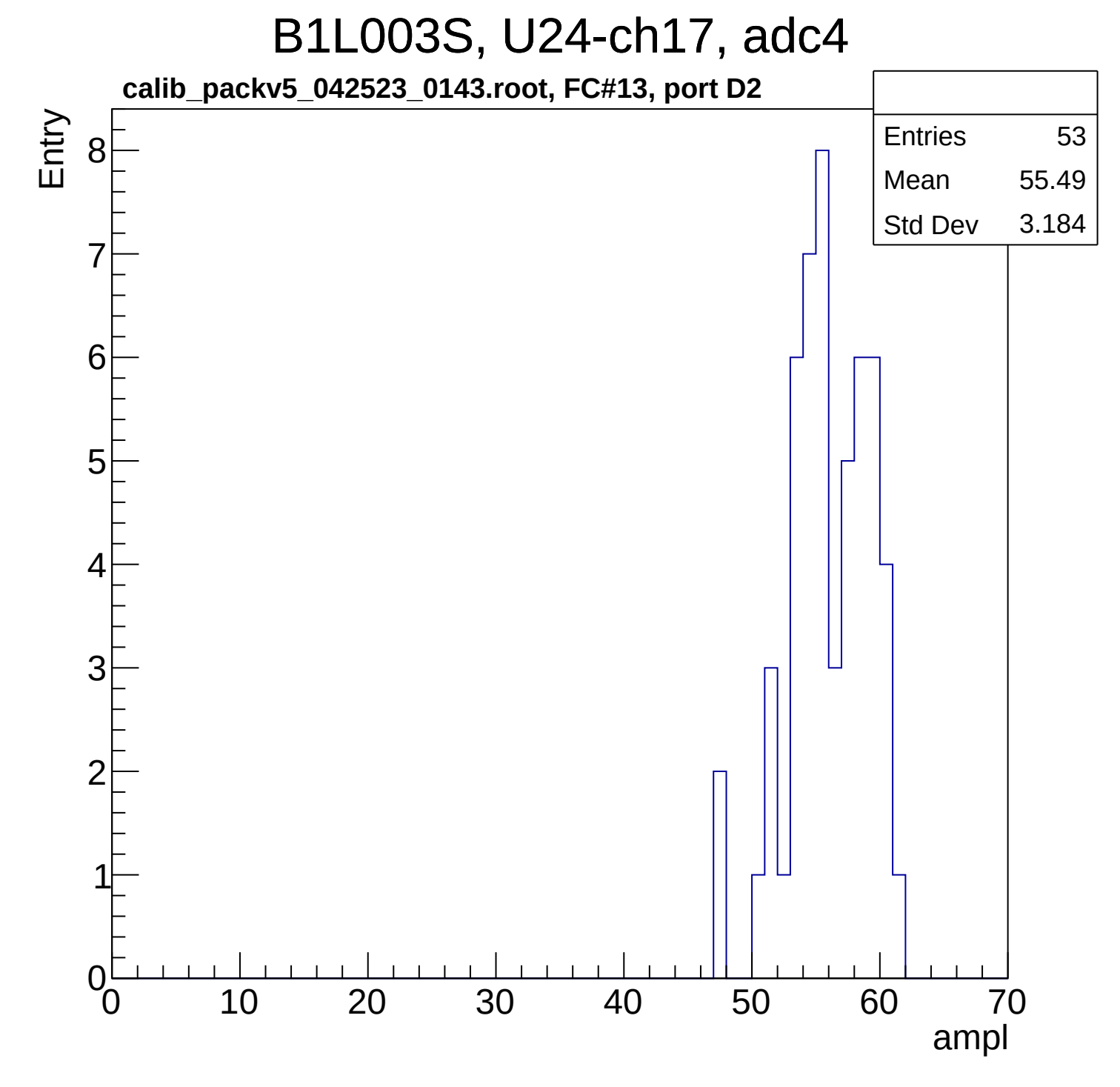

B1L003S, U24-ch16, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 37.5
Std Dev 26.87
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

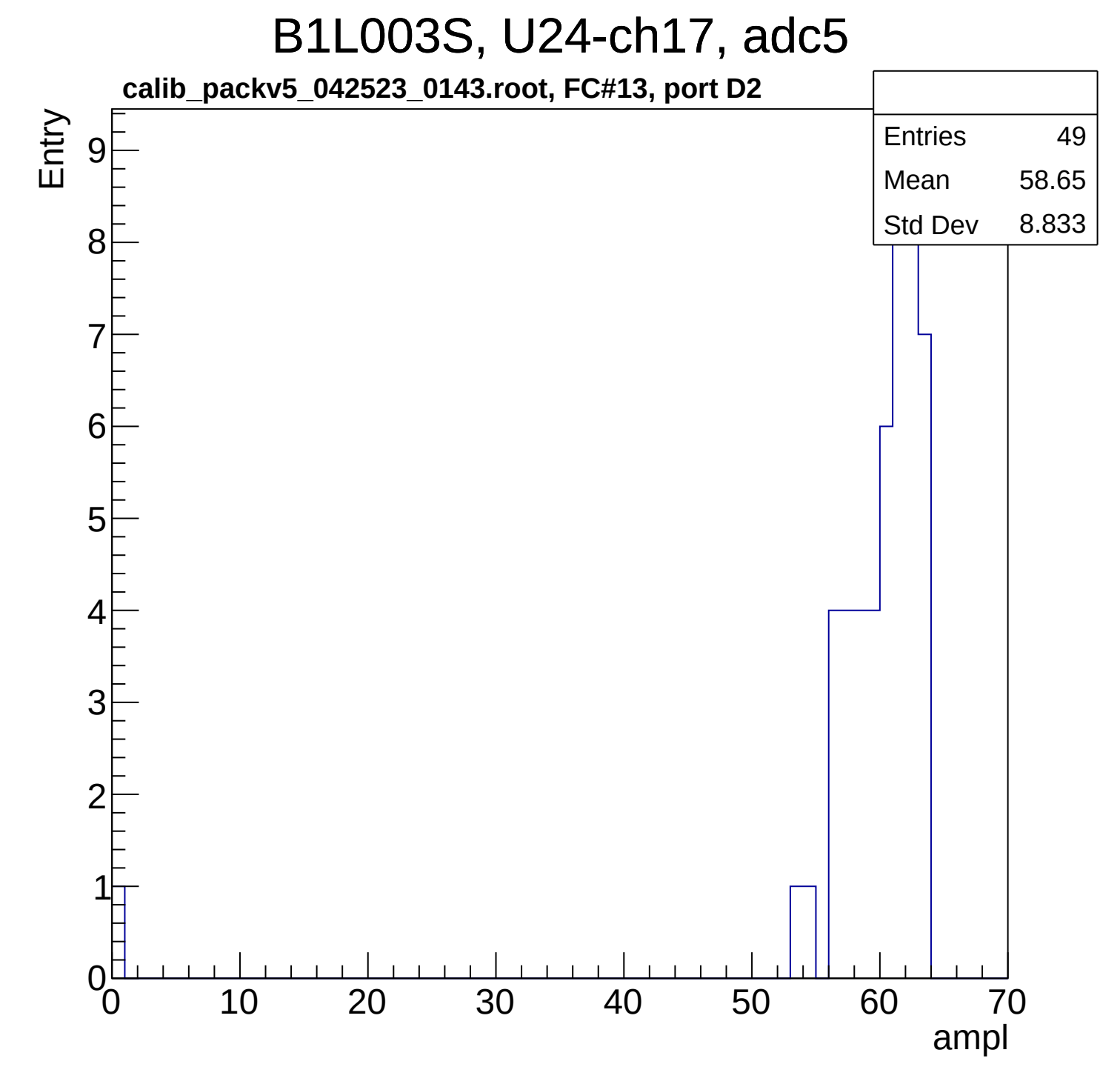

B1L003S, U24-ch17, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.91
Std Dev 3.45
Gaus mean : 29.7799
Gaus Width: 3.8149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch17, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 34.58
Std Dev 3.147
Gaus mean : 35.4883
Gaus Width: 3.8690
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch17, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 93
Mean 42.19
Std Dev 3.722
Gaus mean : 42.7340
Gaus Width: 3.7086
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch17, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.88
Std Dev 2.887
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch17, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.49
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch17, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.65
Std Dev 8.833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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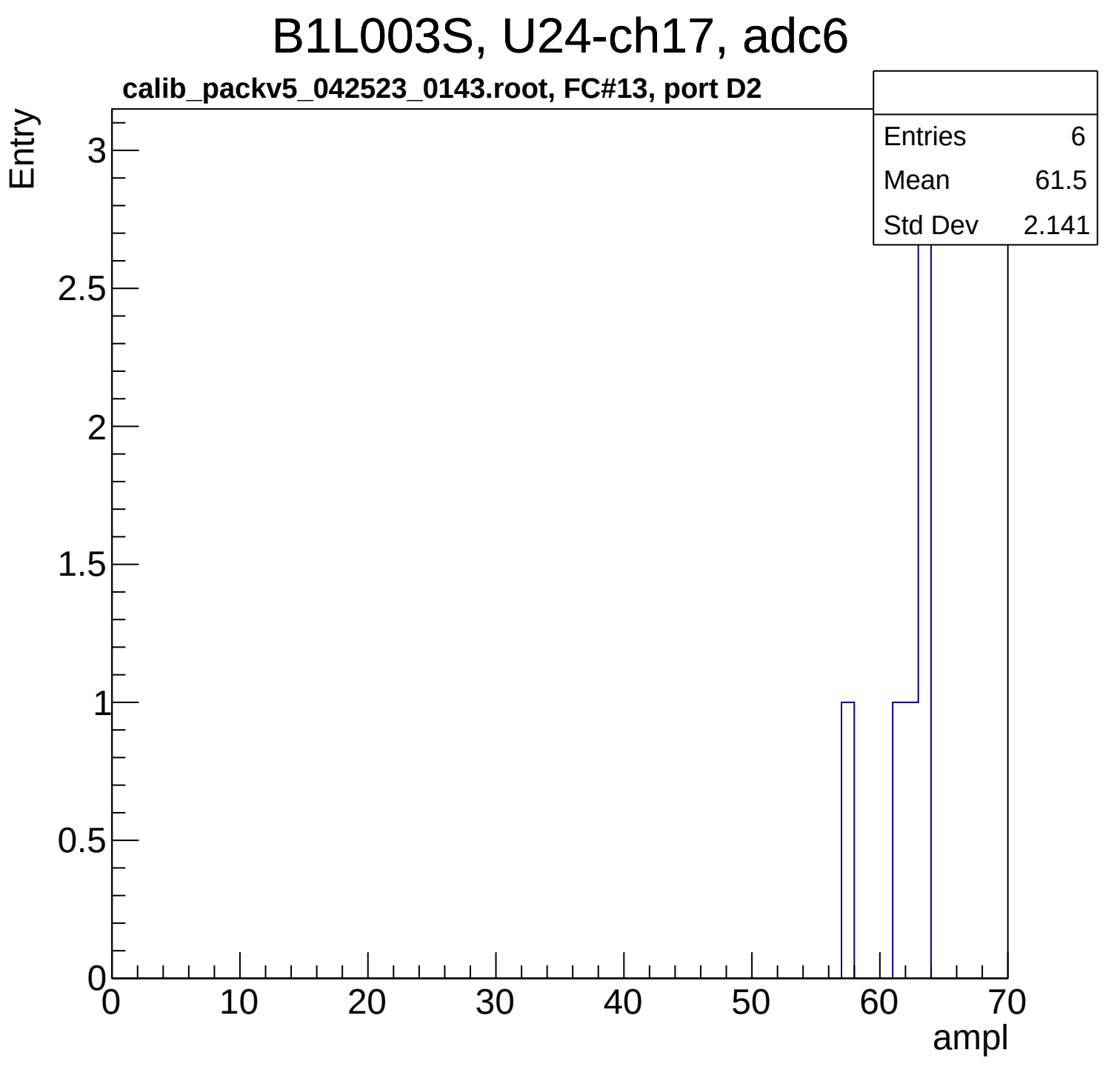
B1L003S, U24-ch17, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.5
Std Dev 2.141
Entry
ampl
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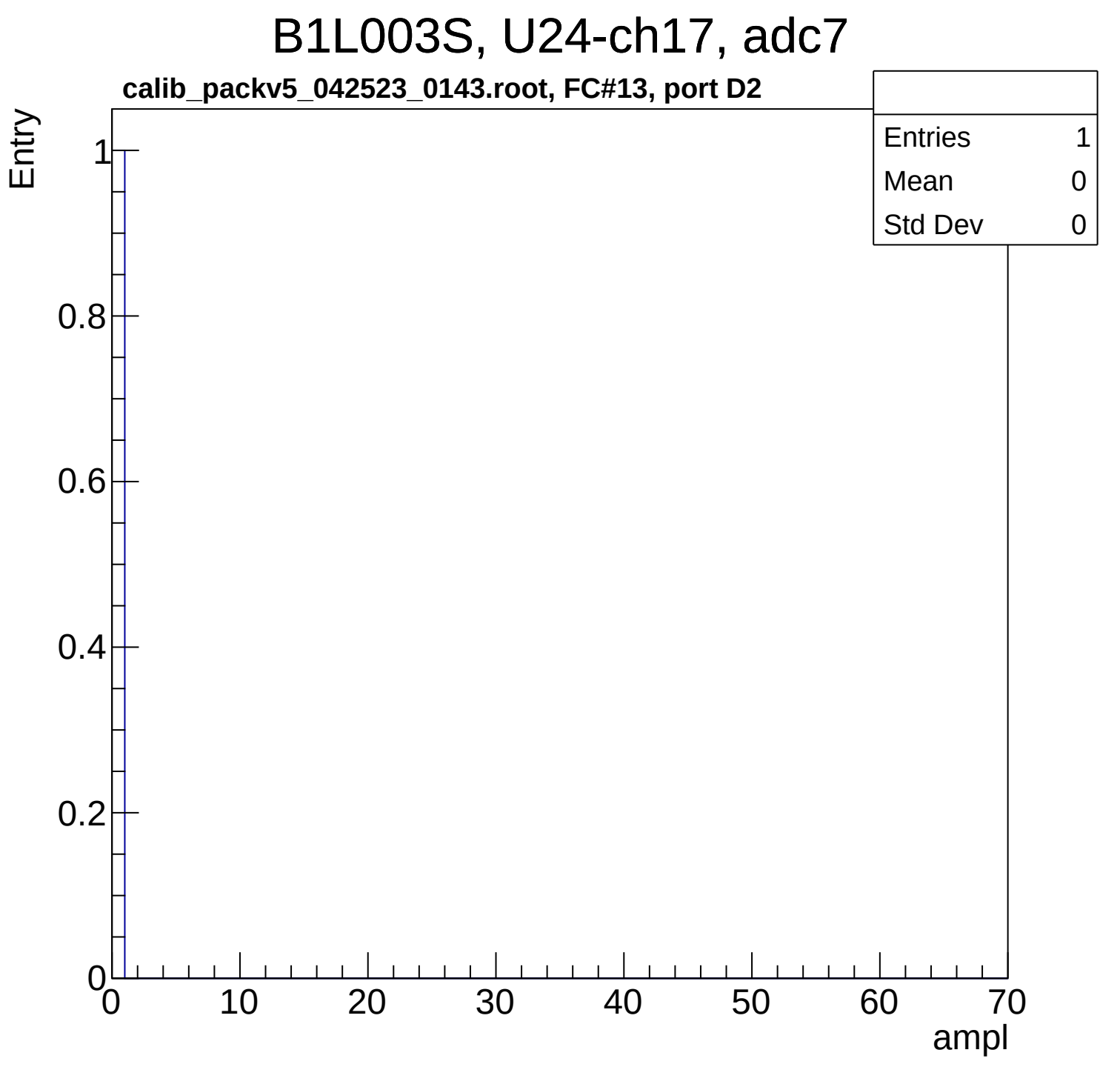
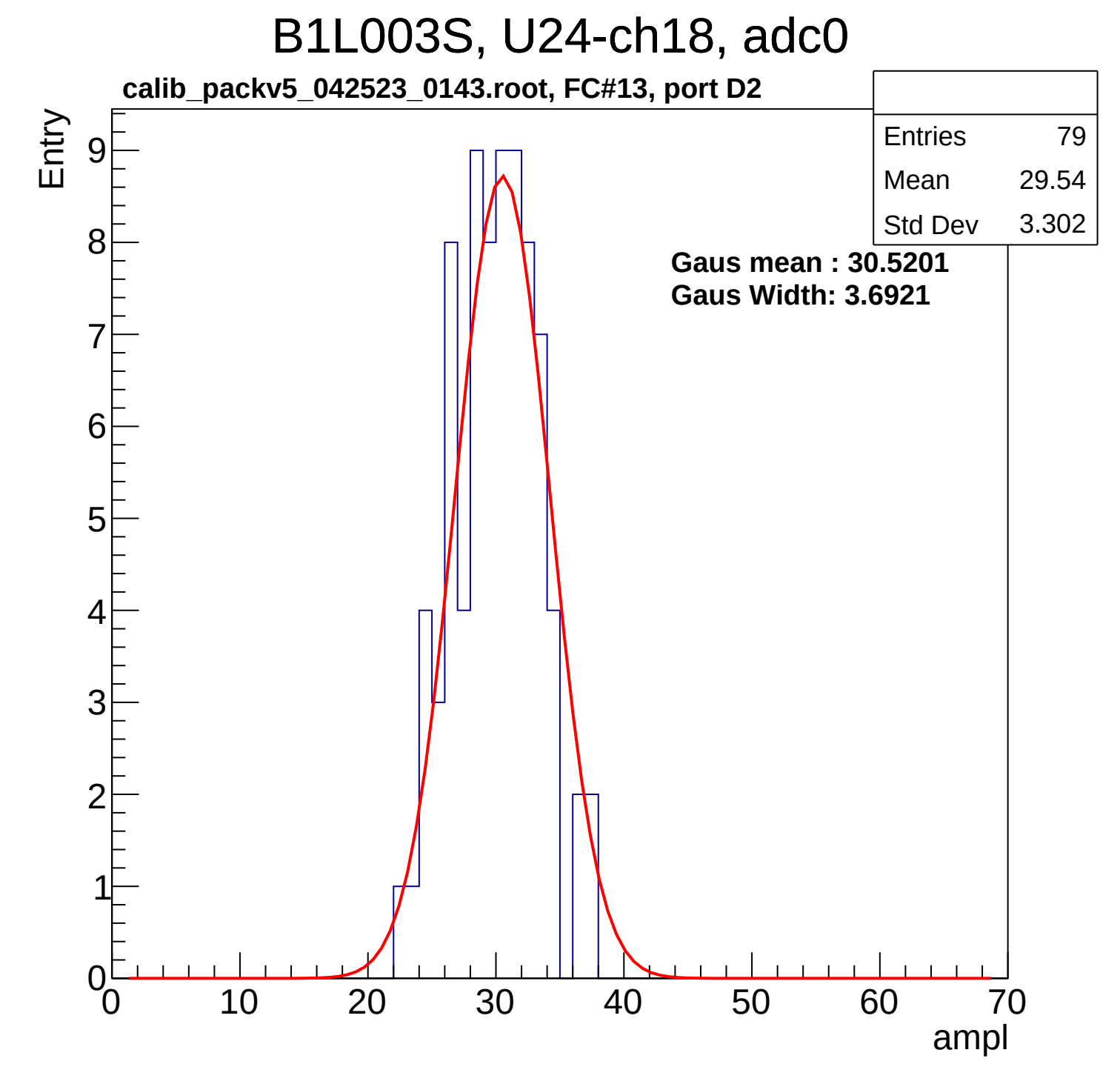
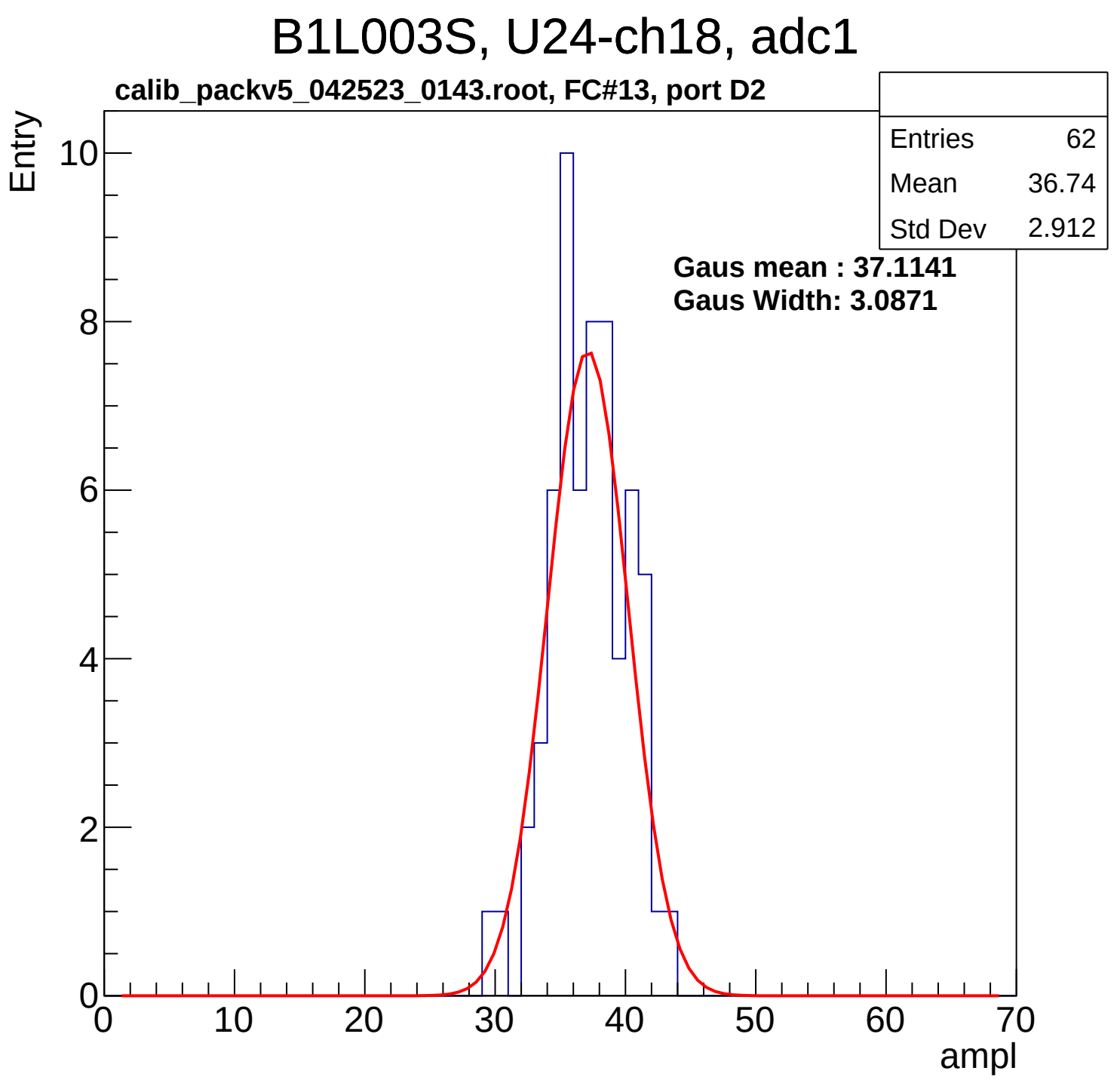
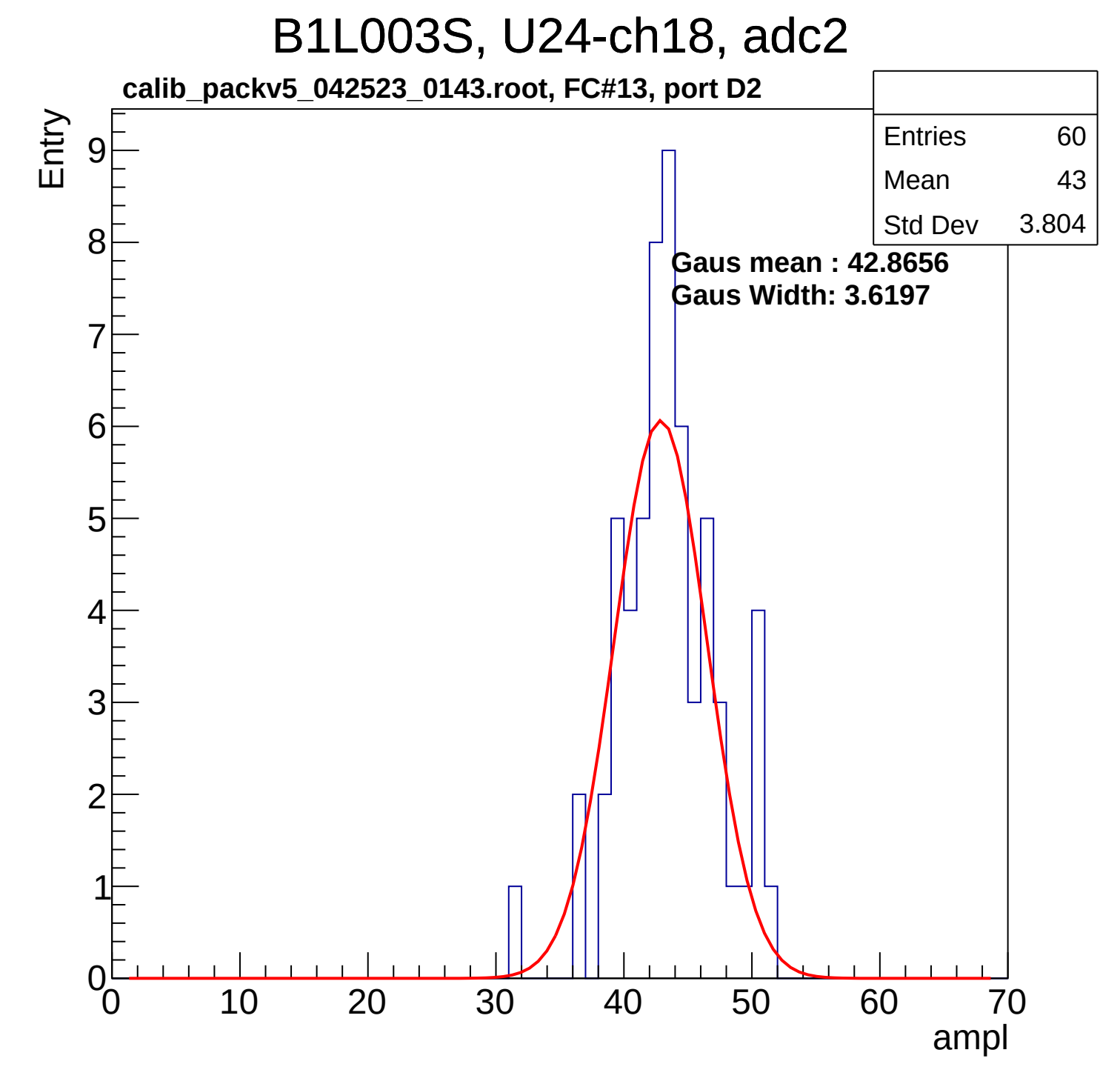
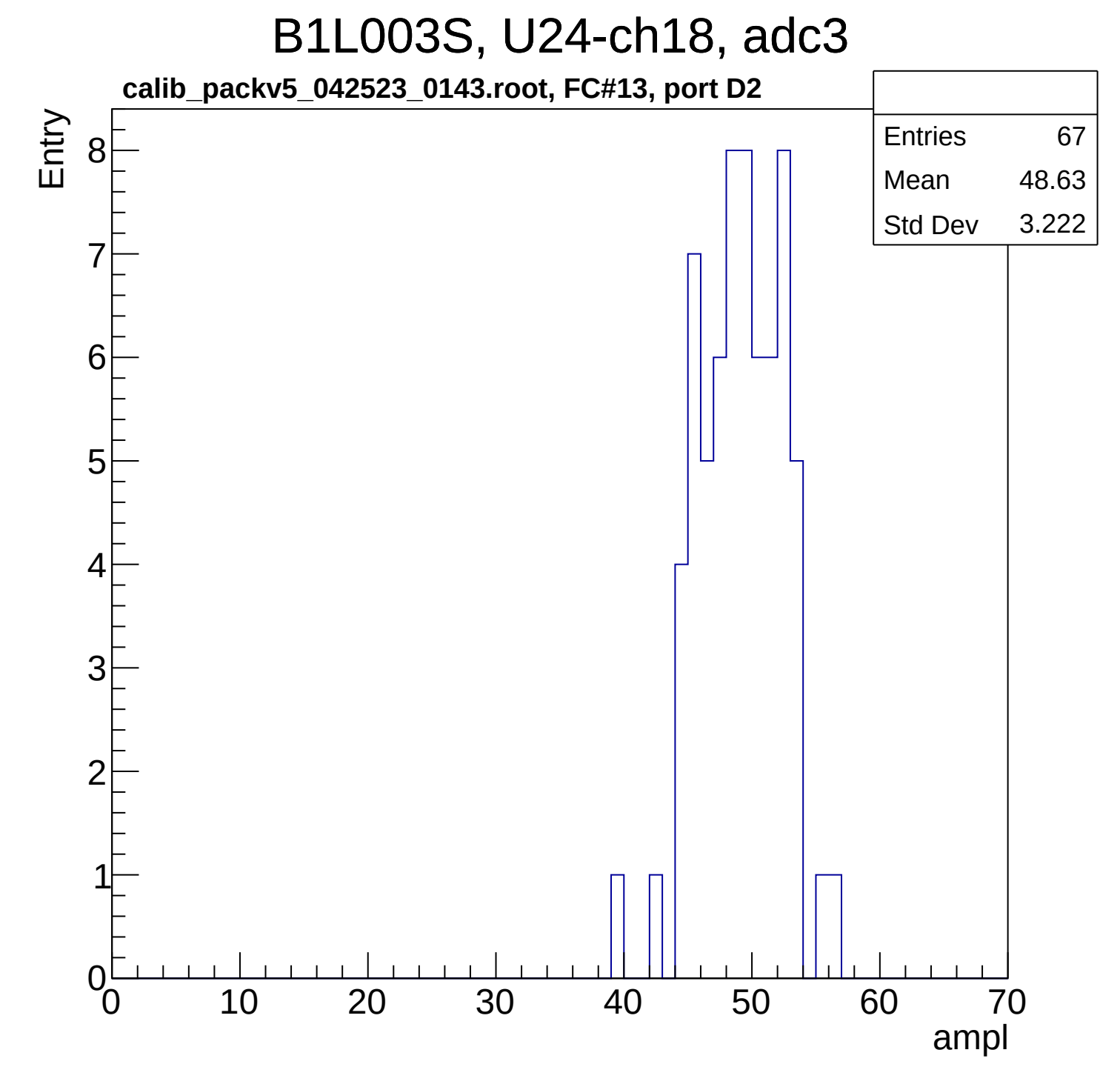
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch17, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch18, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 29.54
Std Dev 3.302
Gaus mean : 30.5201
Gaus Width: 3.6921
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch18, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.74
Std Dev 2.912
Gaus mean : 37.1141
Gaus Width: 3.0871
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch18, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43
Std Dev 3.804
Gaus mean : 42.8656
Gaus Width: 3.6197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch18, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.63
Std Dev 3.222
Entry
ampl
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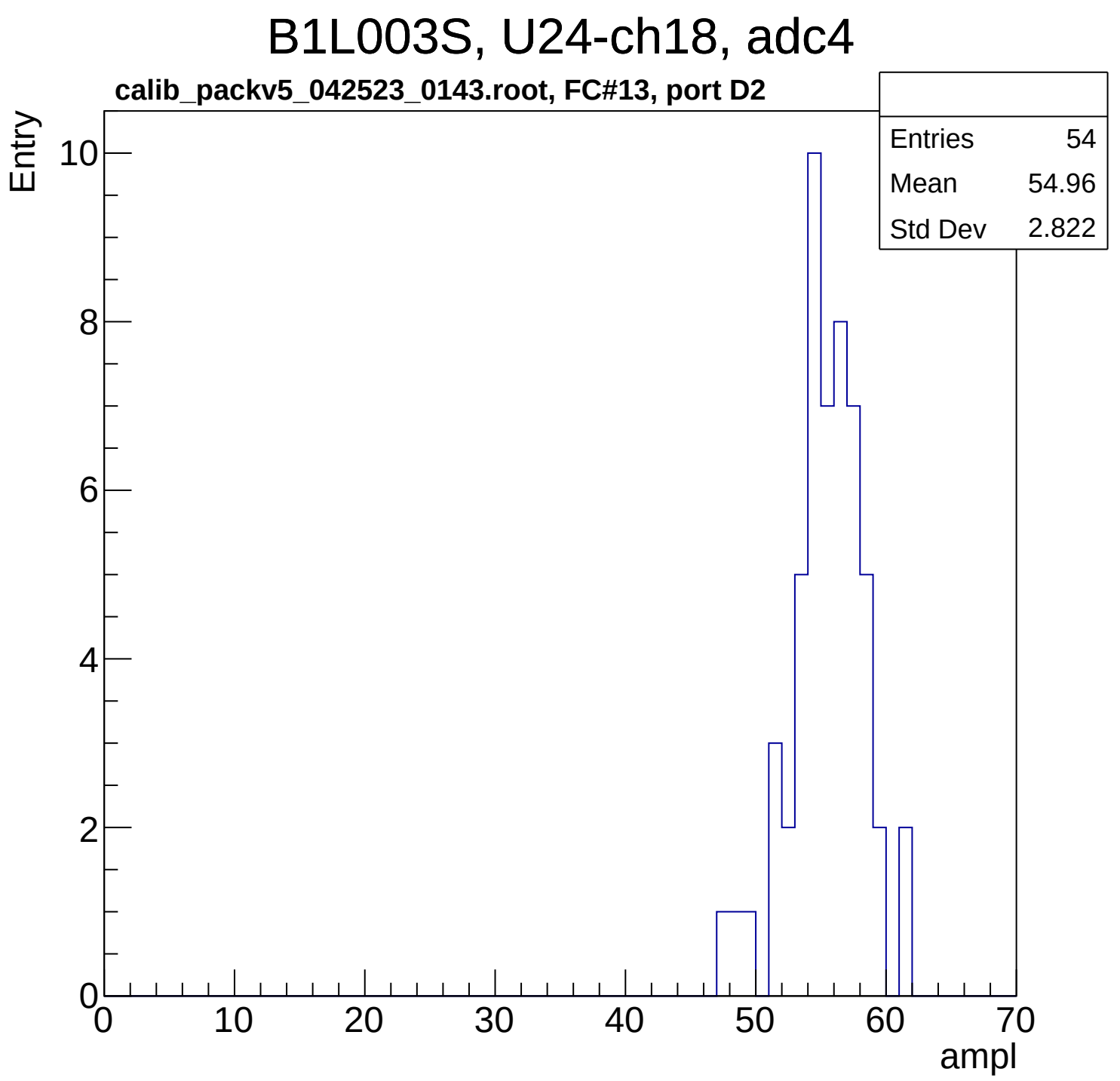
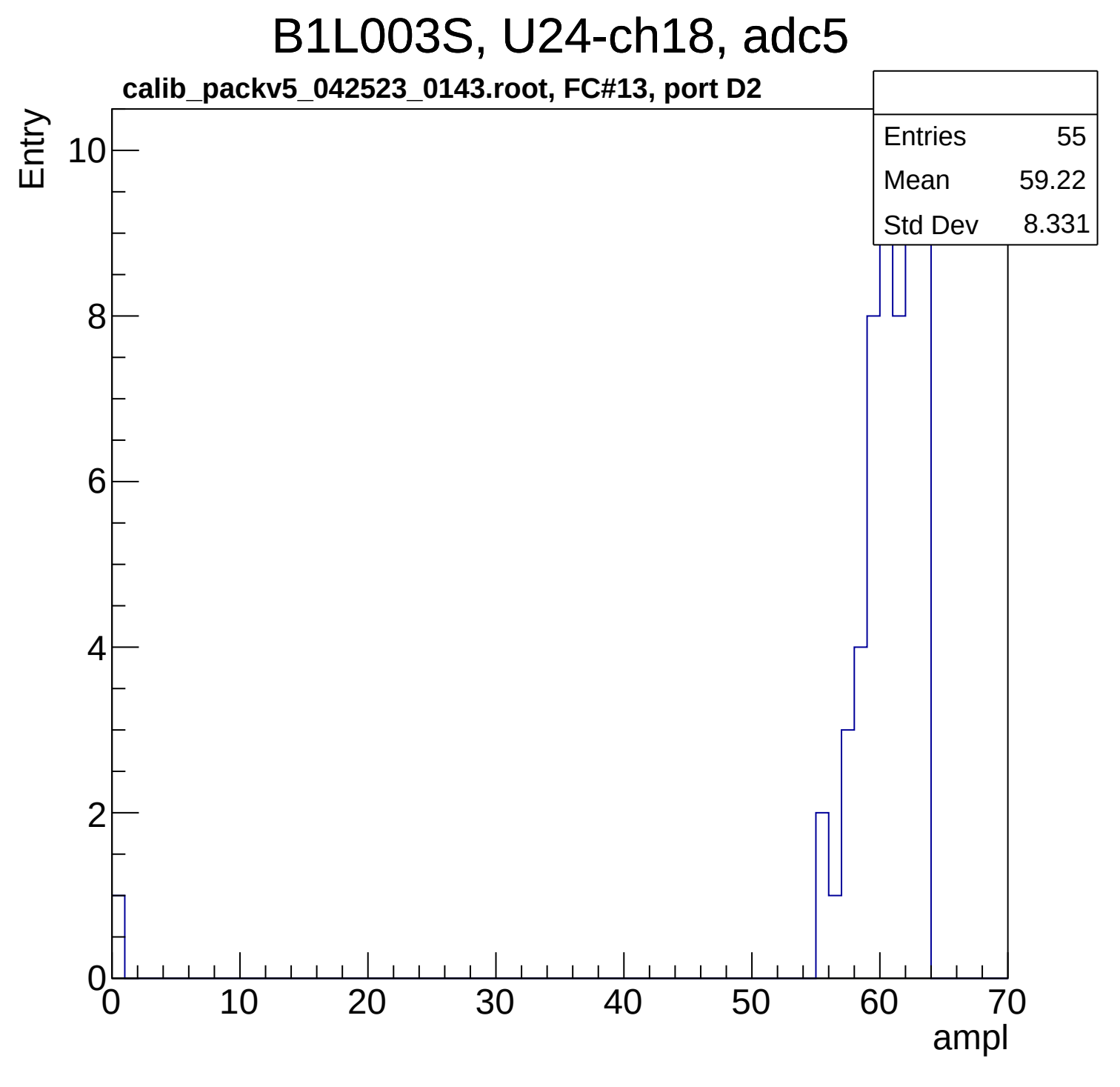
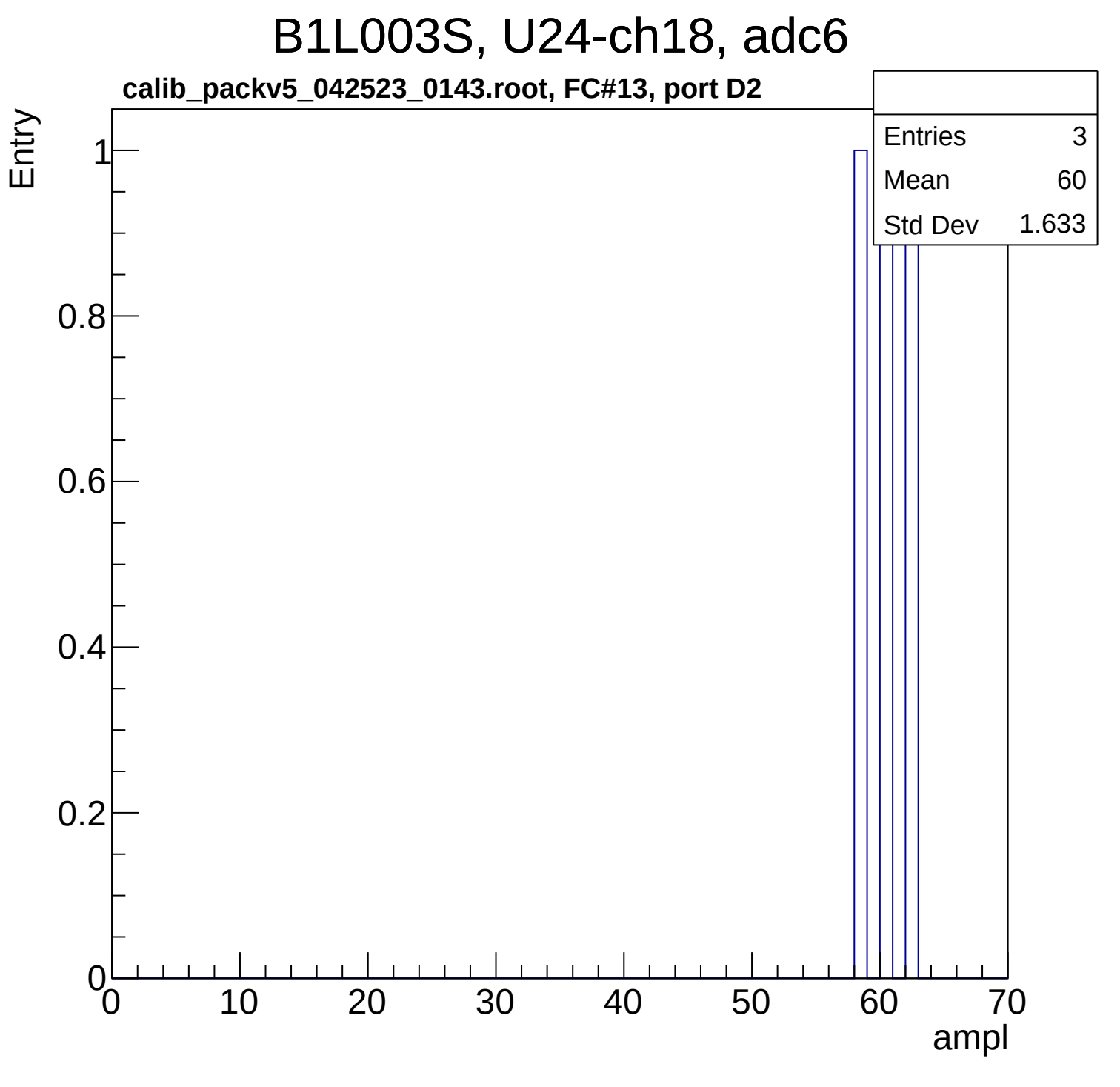
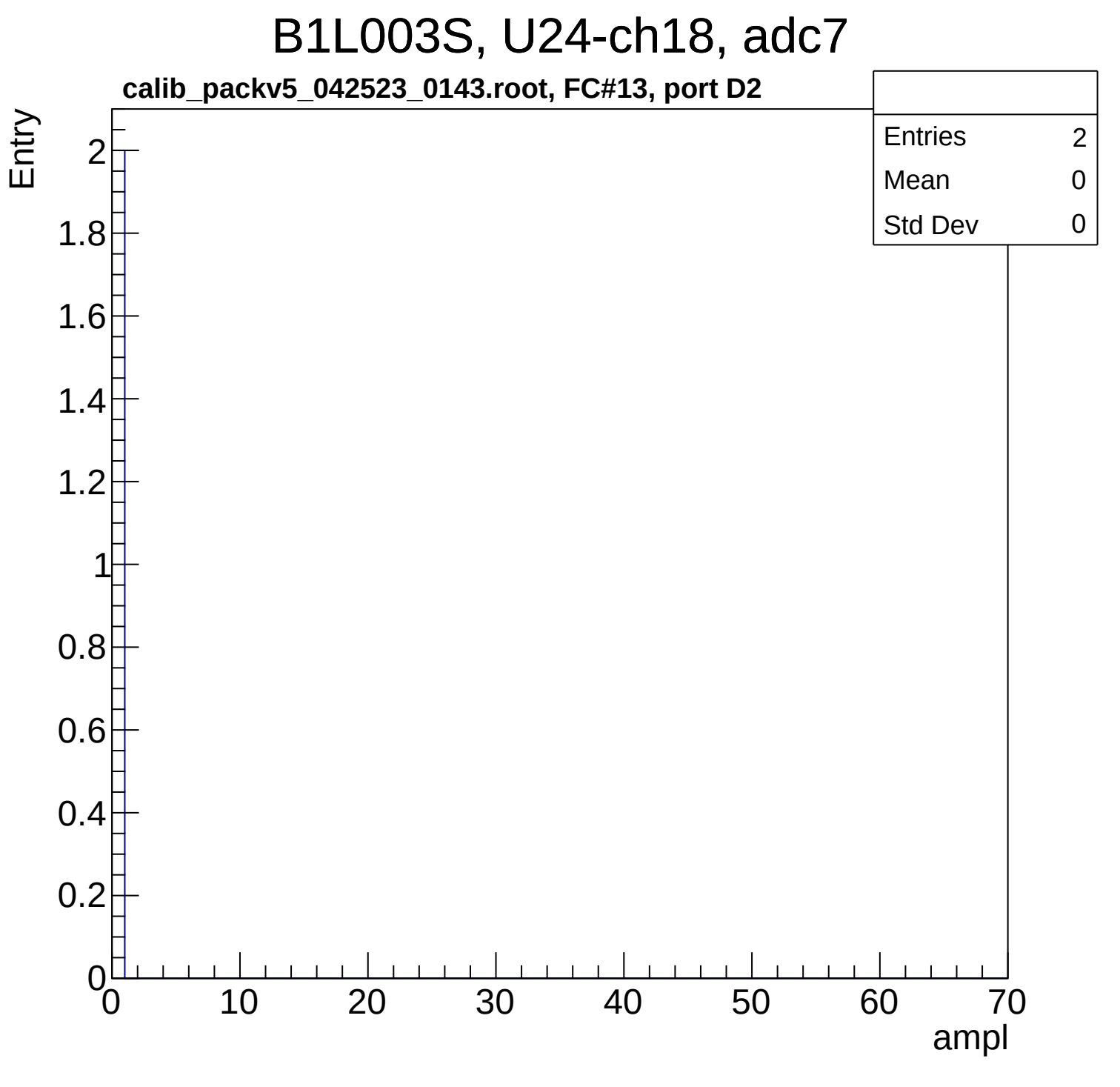
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch18, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.96
Std Dev 2.822
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch18, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.22
Std Dev 8.331
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch18, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 60
Std Dev 1.633
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L003S, U24-ch18, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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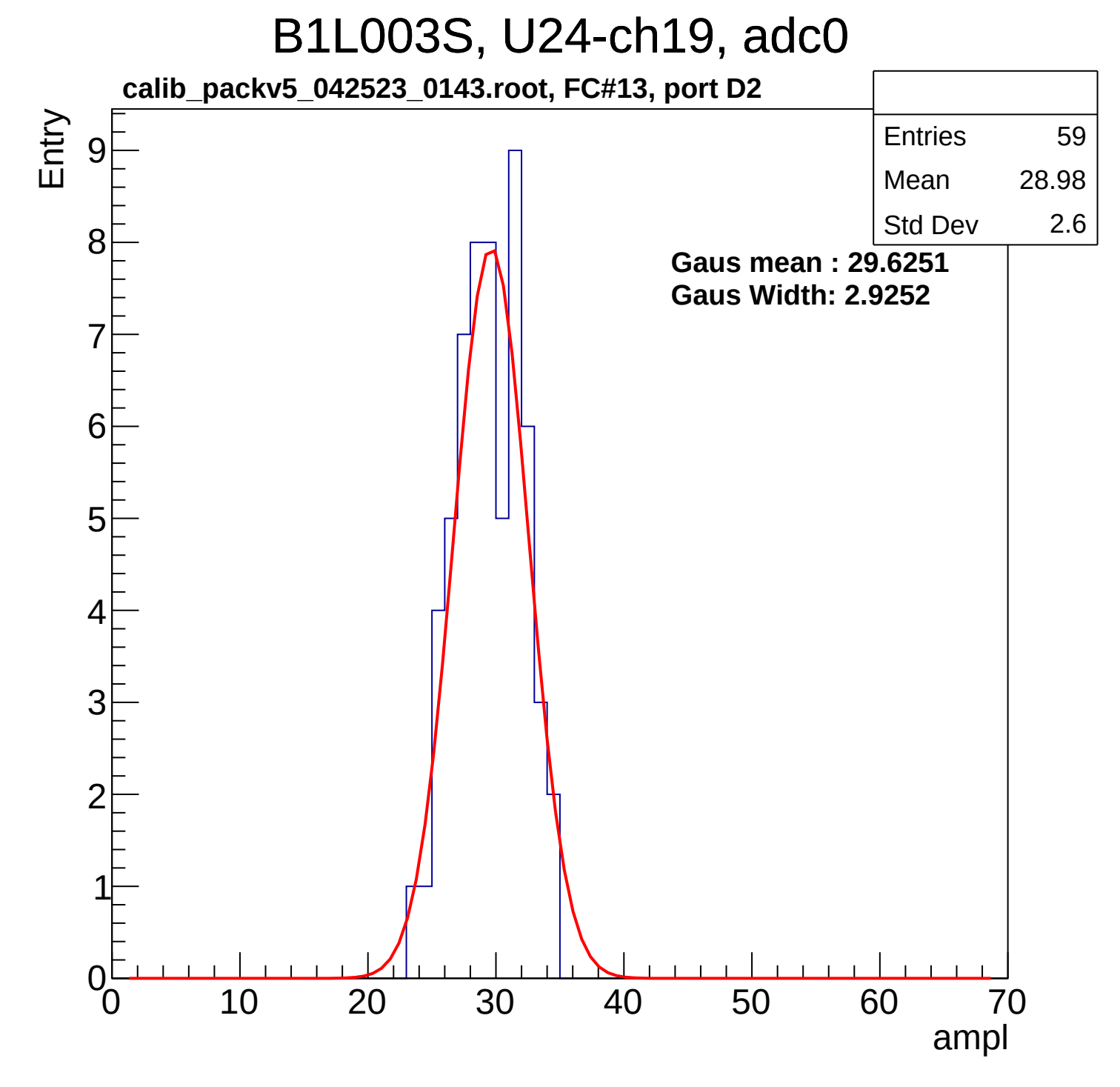
B1L003S, U24-ch19, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.98
Std Dev 2.6
Gaus mean : 29.6251
Gaus Width: 2.9252
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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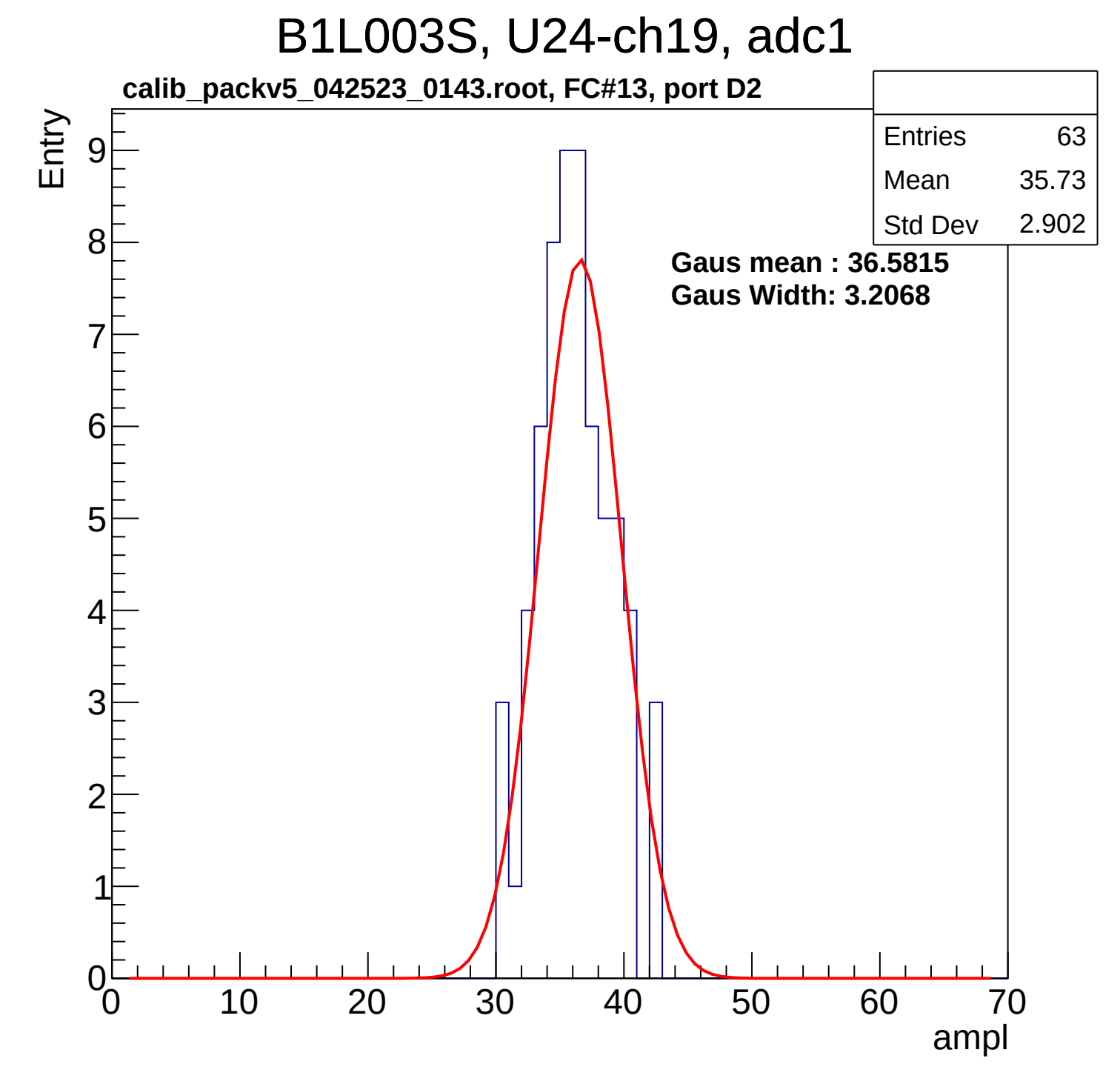
60
70


B1L003S, U24-ch19, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 35.73
Std Dev 2.902
Gaus mean : 36.5815
Gaus Width: 3.2068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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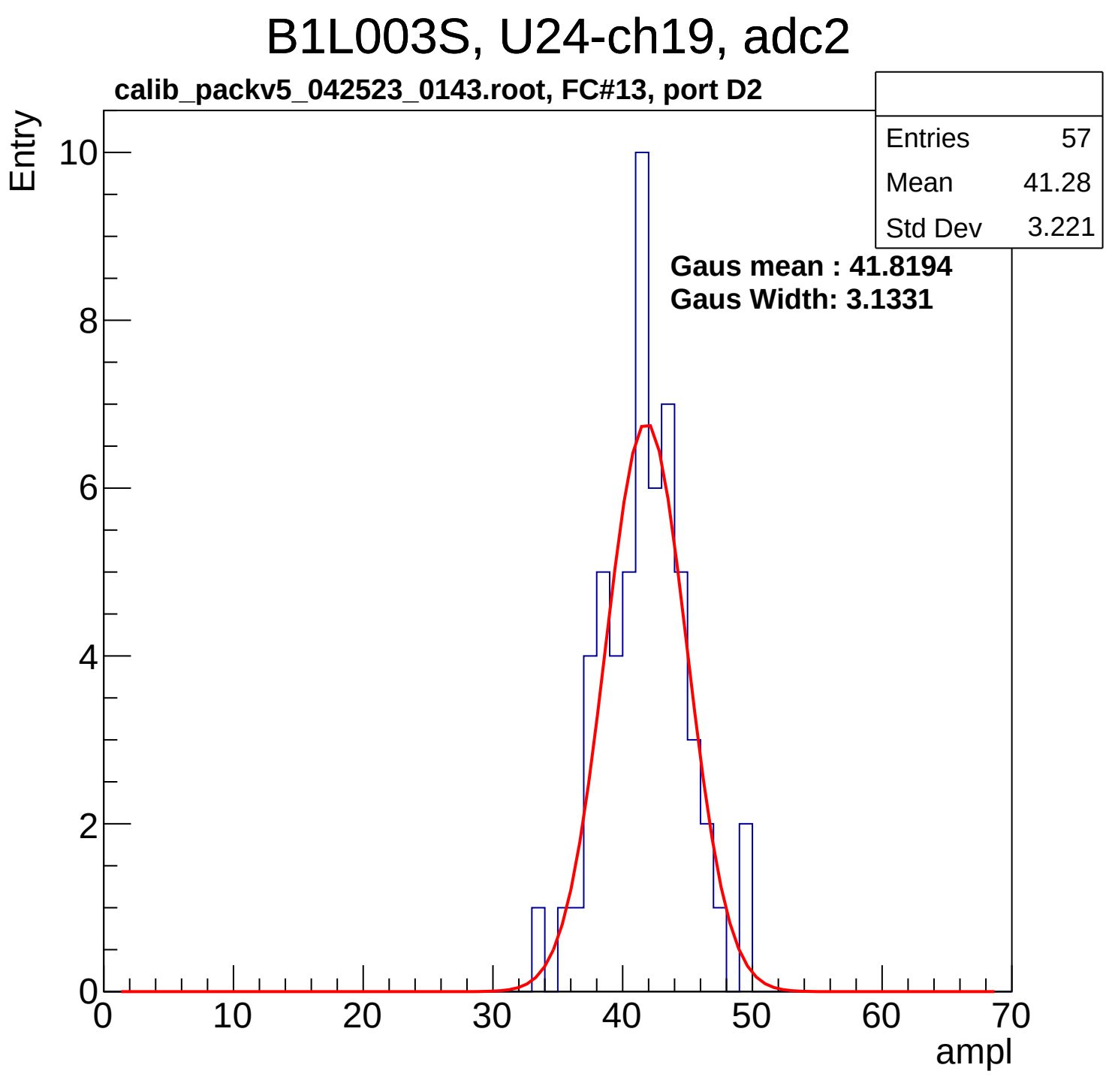
B1L003S, U24-ch19, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 41.28
Std Dev 3.221
Gaus mean : 41.8194
Gaus Width: 3.1331
Entry
ampl
0
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4
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0
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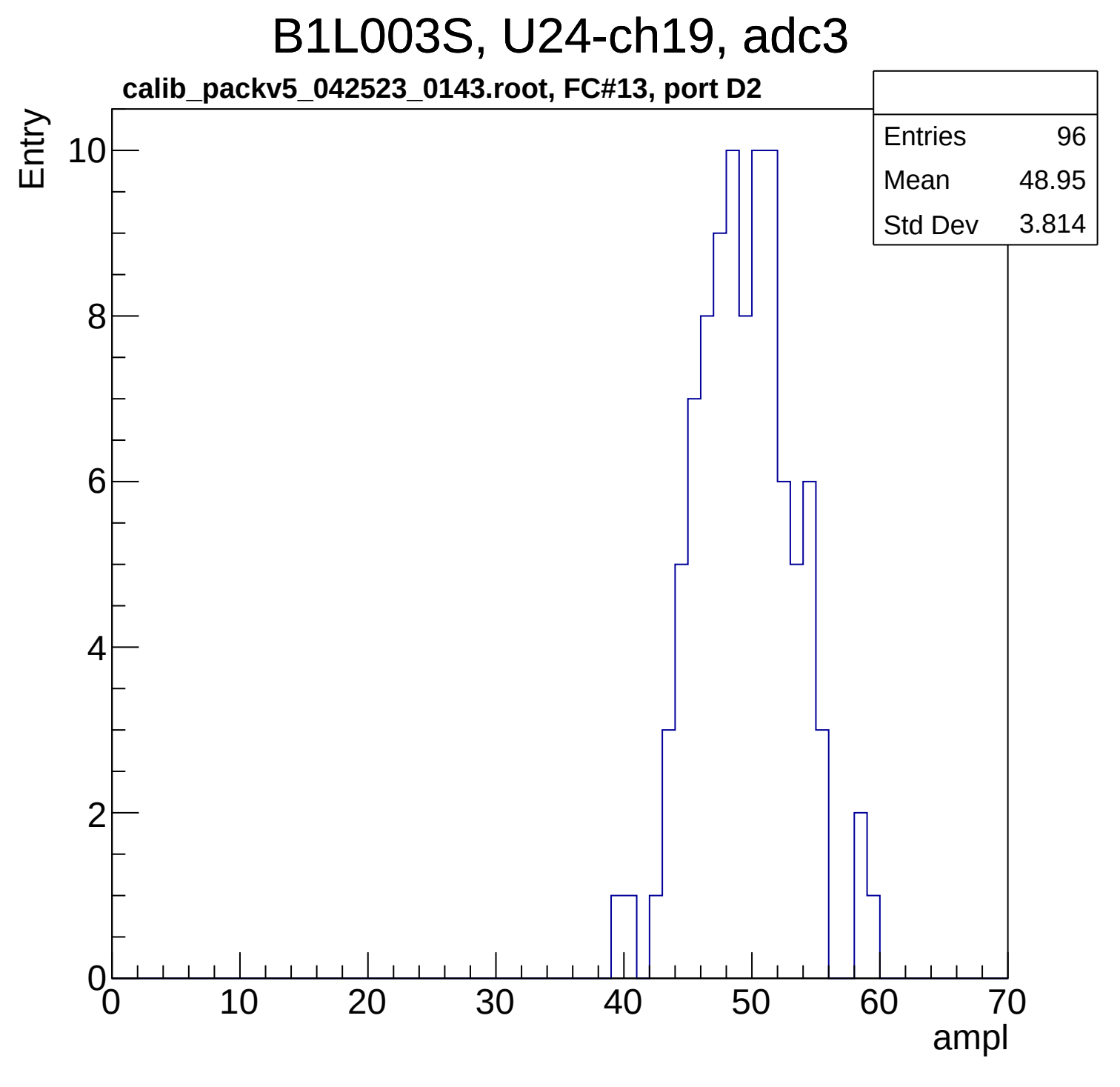
B1L003S, U24-ch19, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 96
Mean 48.95
Std Dev 3.814
Entry
0
2
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6
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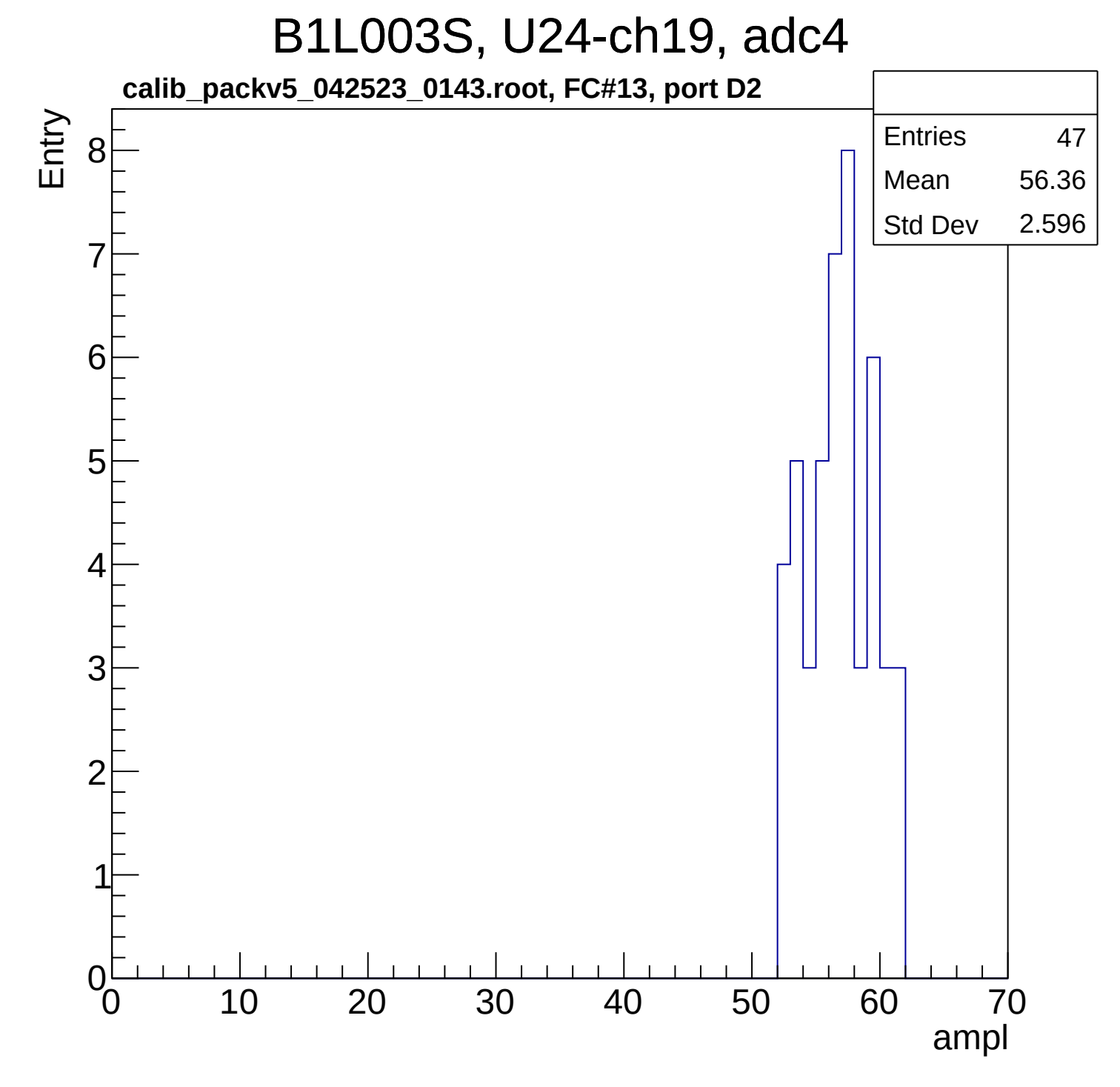
70
ampl


B1L003S, U24-ch19, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 56.36
Std Dev 2.596
Entry
ampl
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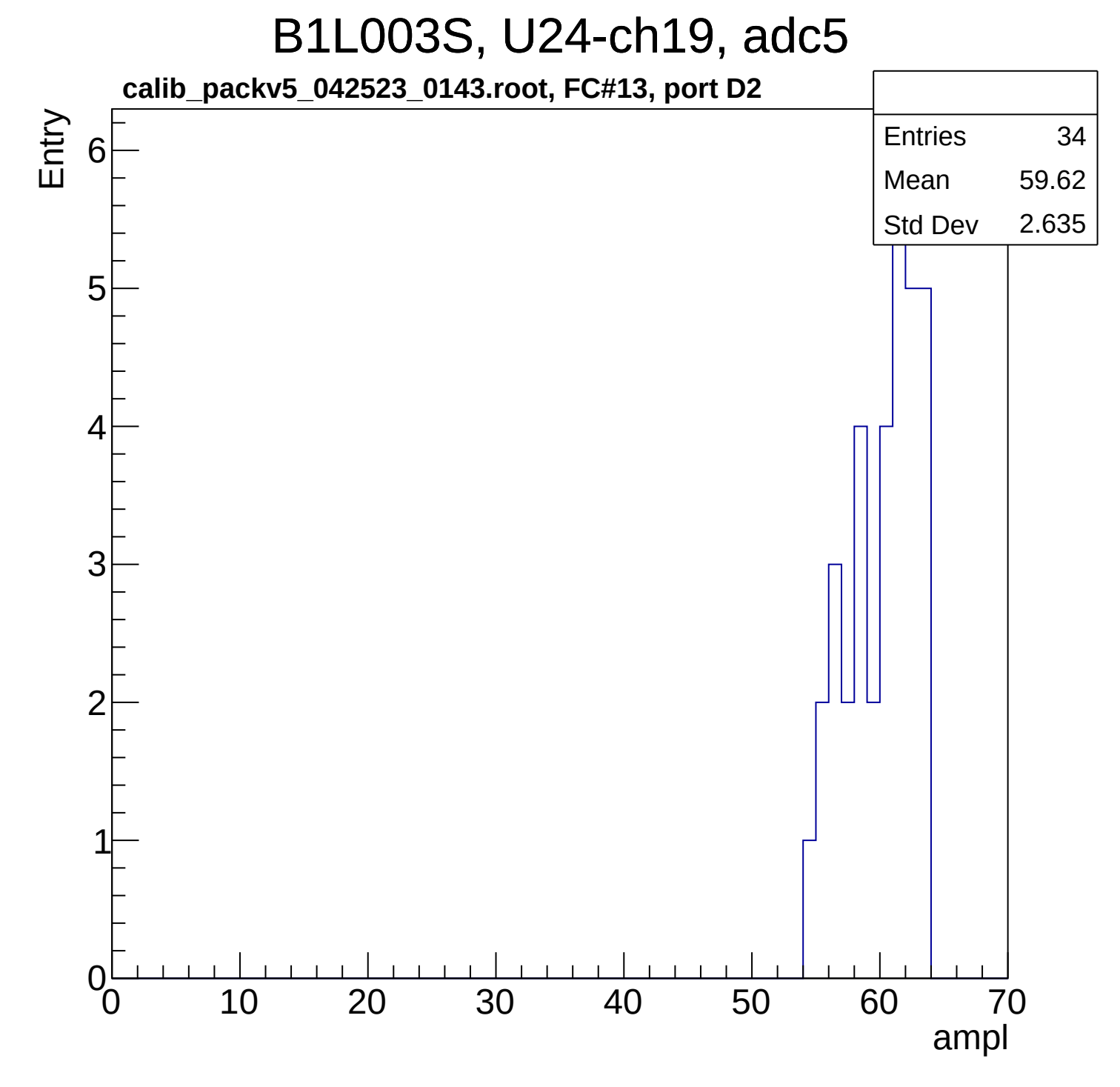
B1L003S, U24-ch19, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 59.62
Std Dev 2.635
Entry
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2
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6
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10
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50
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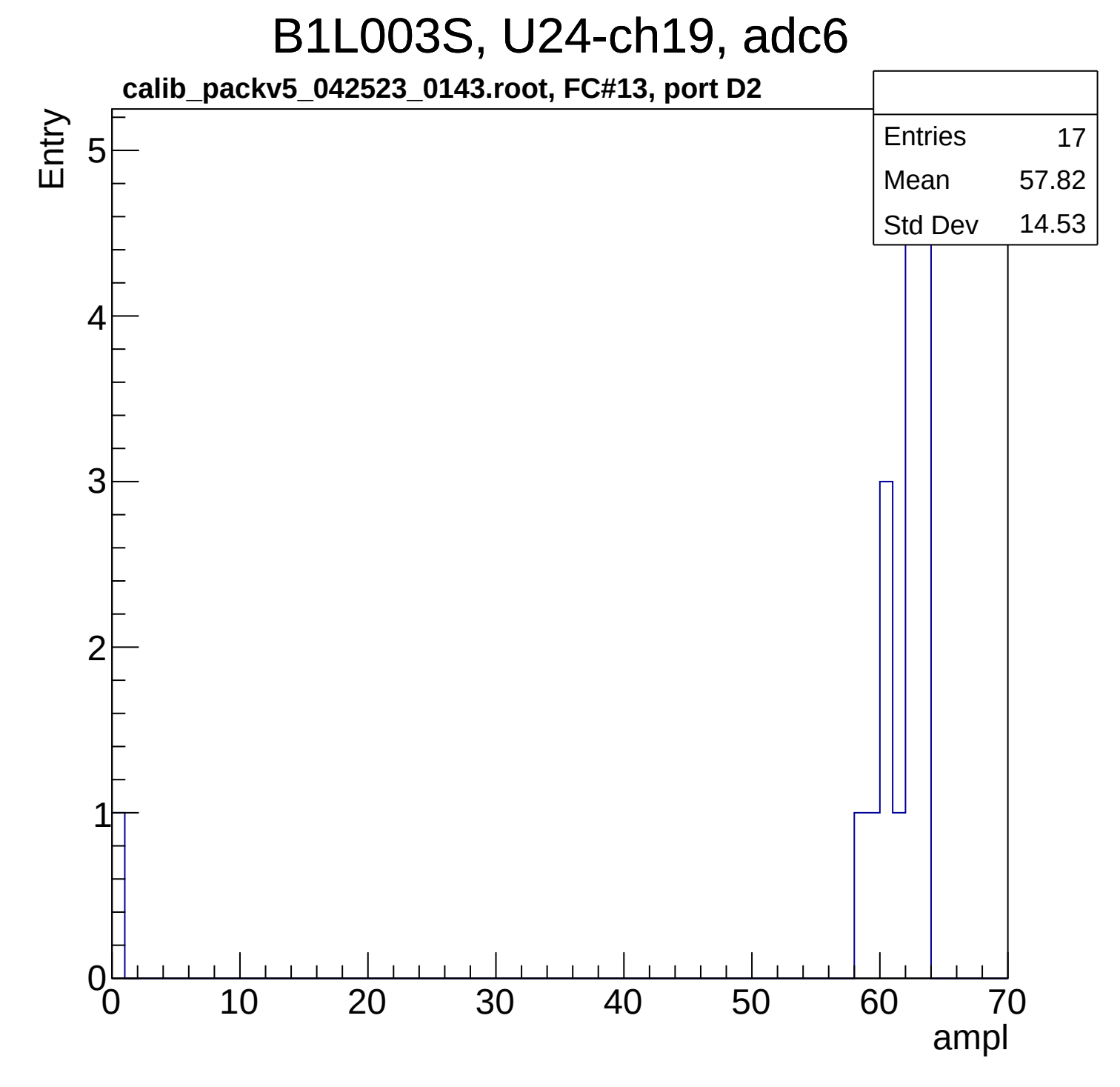
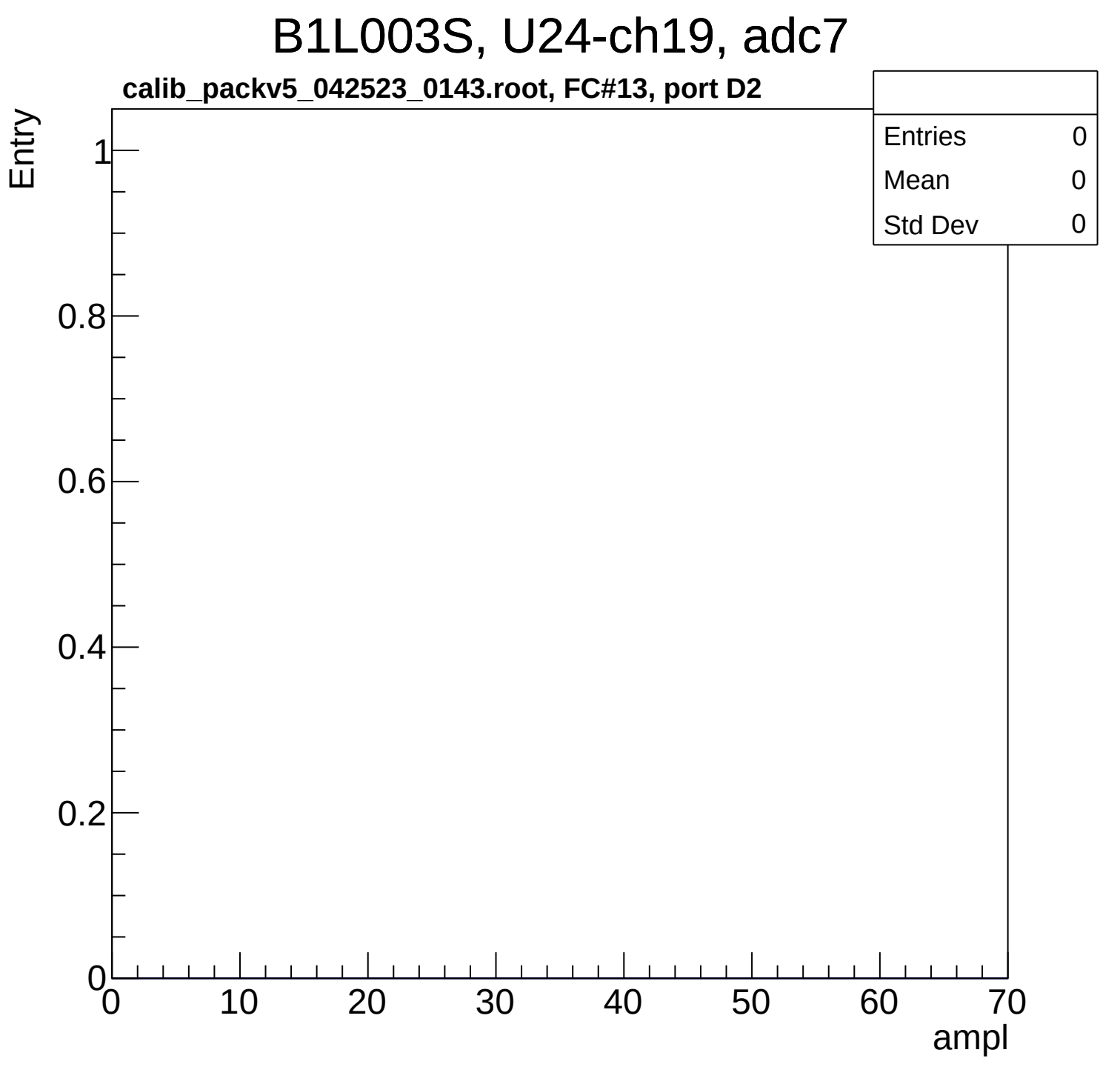
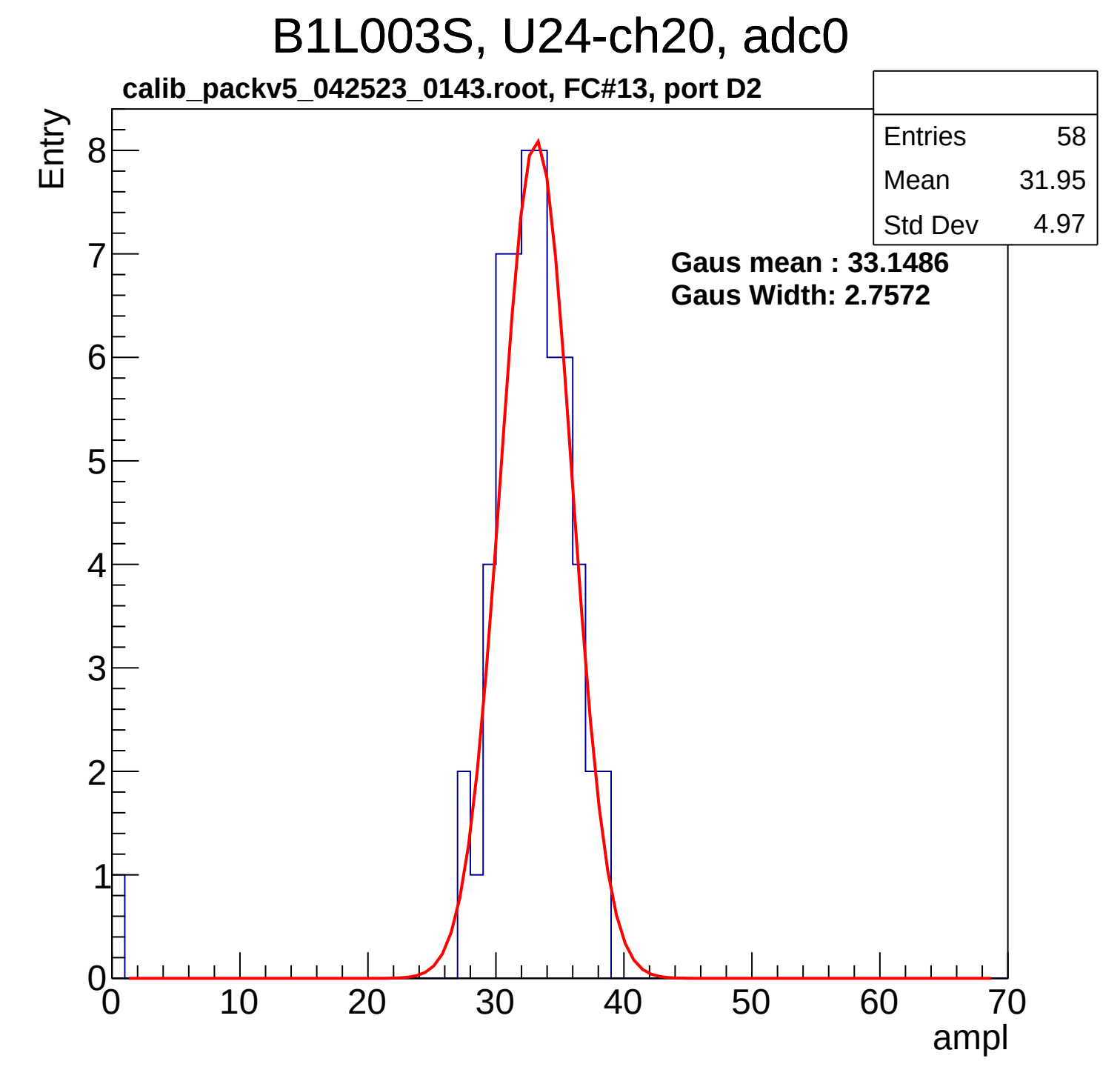
70
ampl


B1L003S, U24-ch19, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.82
Std Dev 14.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch19, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch20, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 31.95
Std Dev 4.97
Gaus mean : 33.1486
Gaus Width: 2.7572
Entry
ampl
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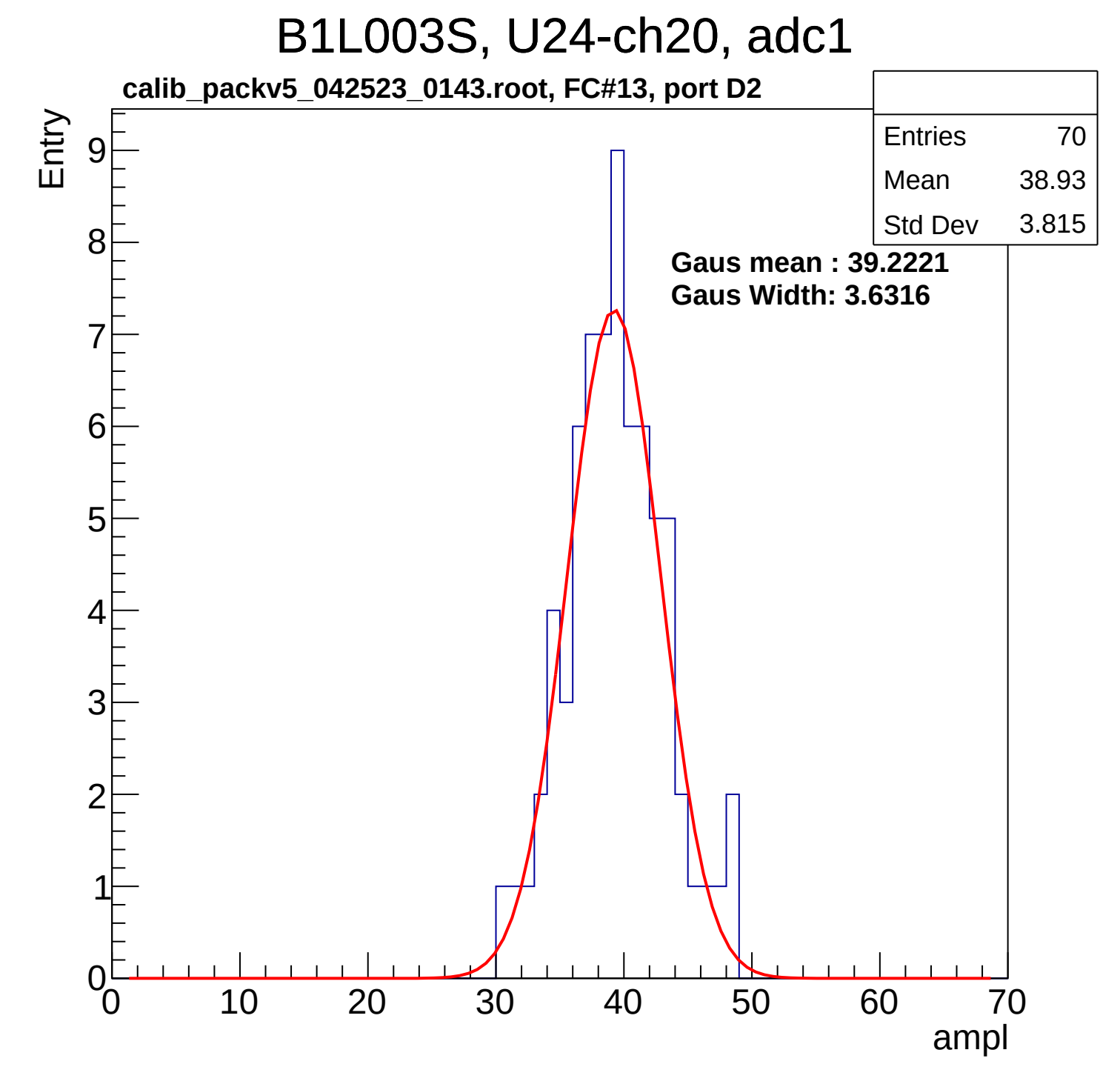
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch20, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 38.93
Std Dev 3.815
Gaus mean : 39.2221
Gaus Width: 3.6316
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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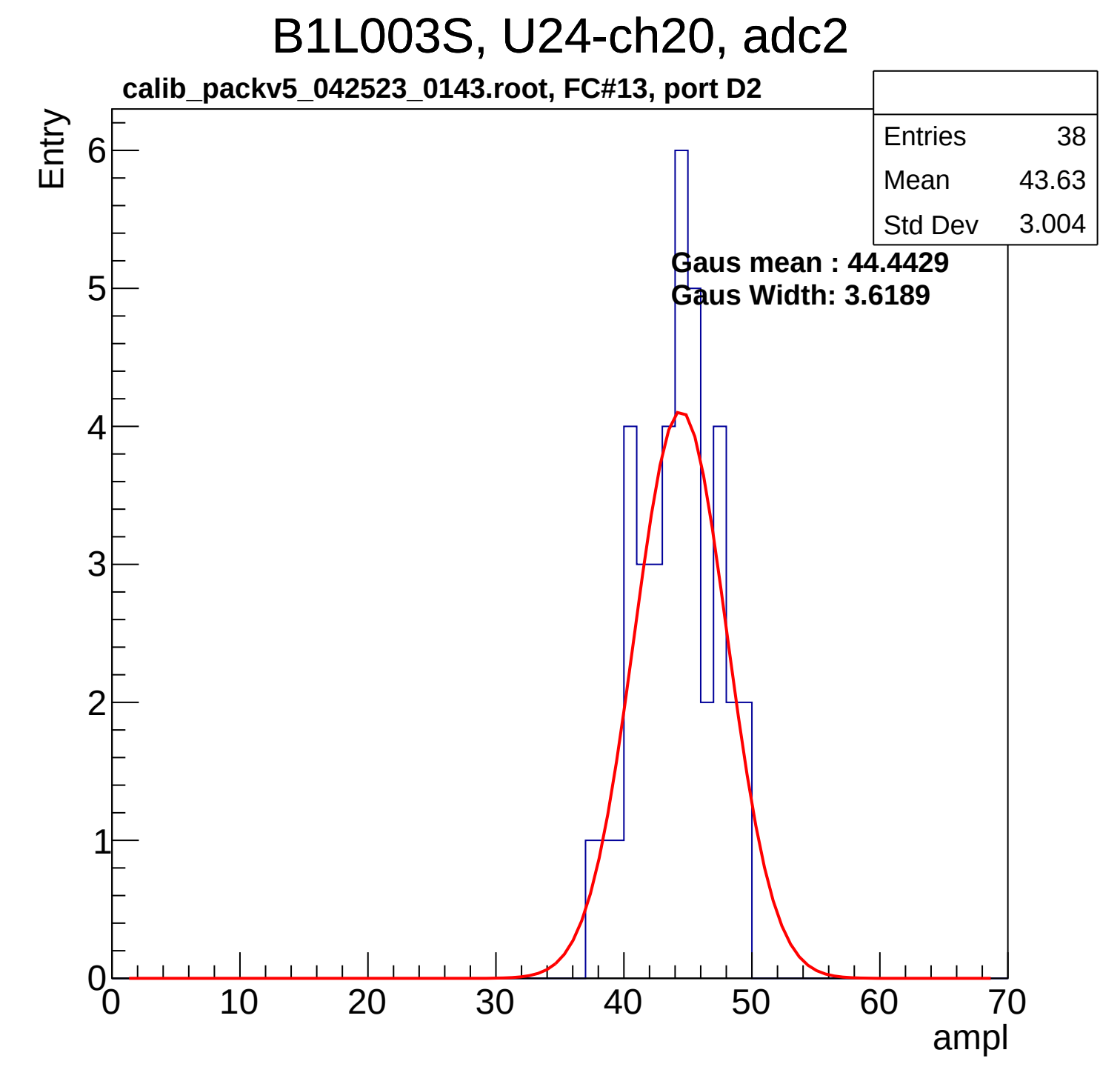
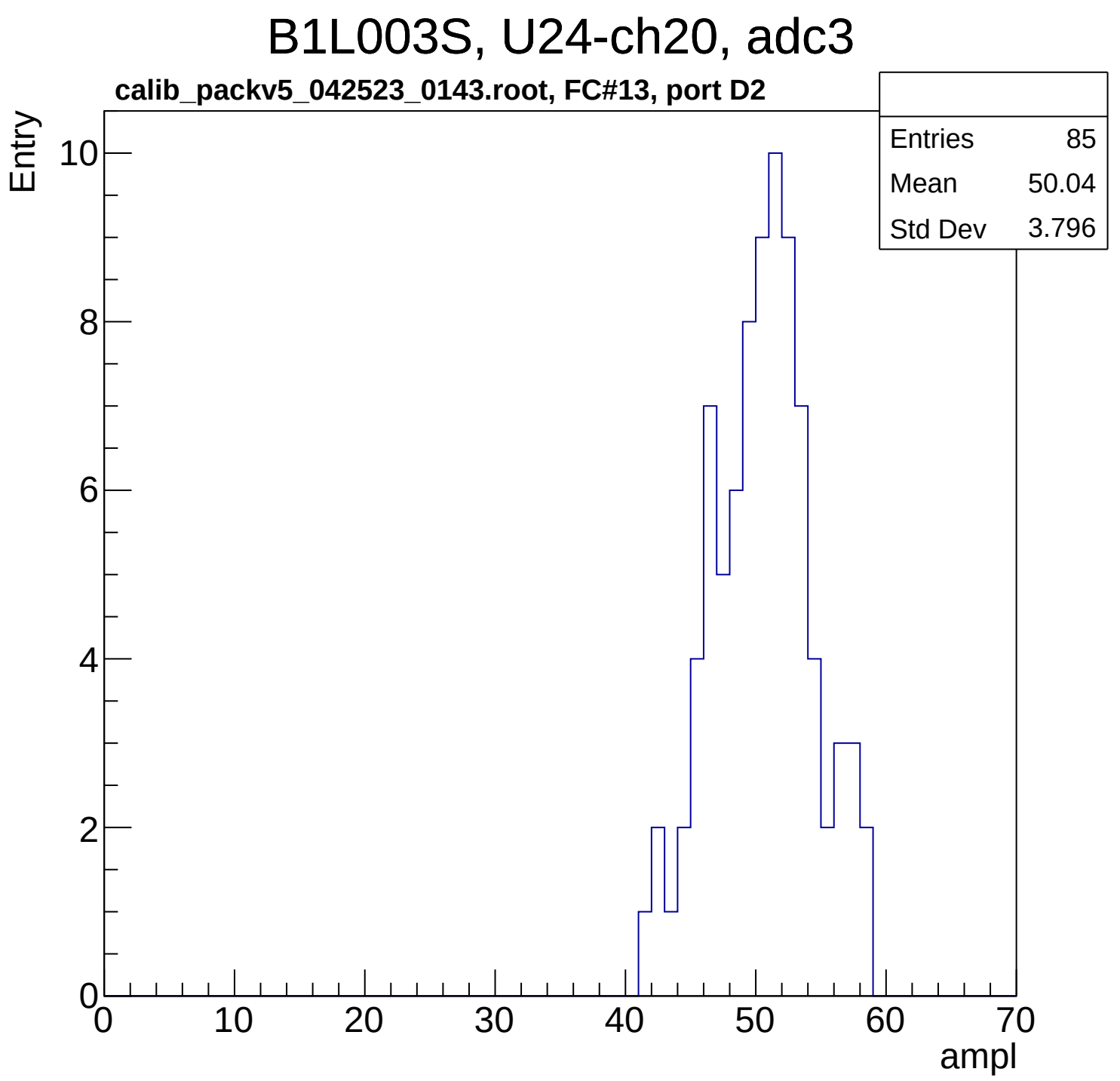
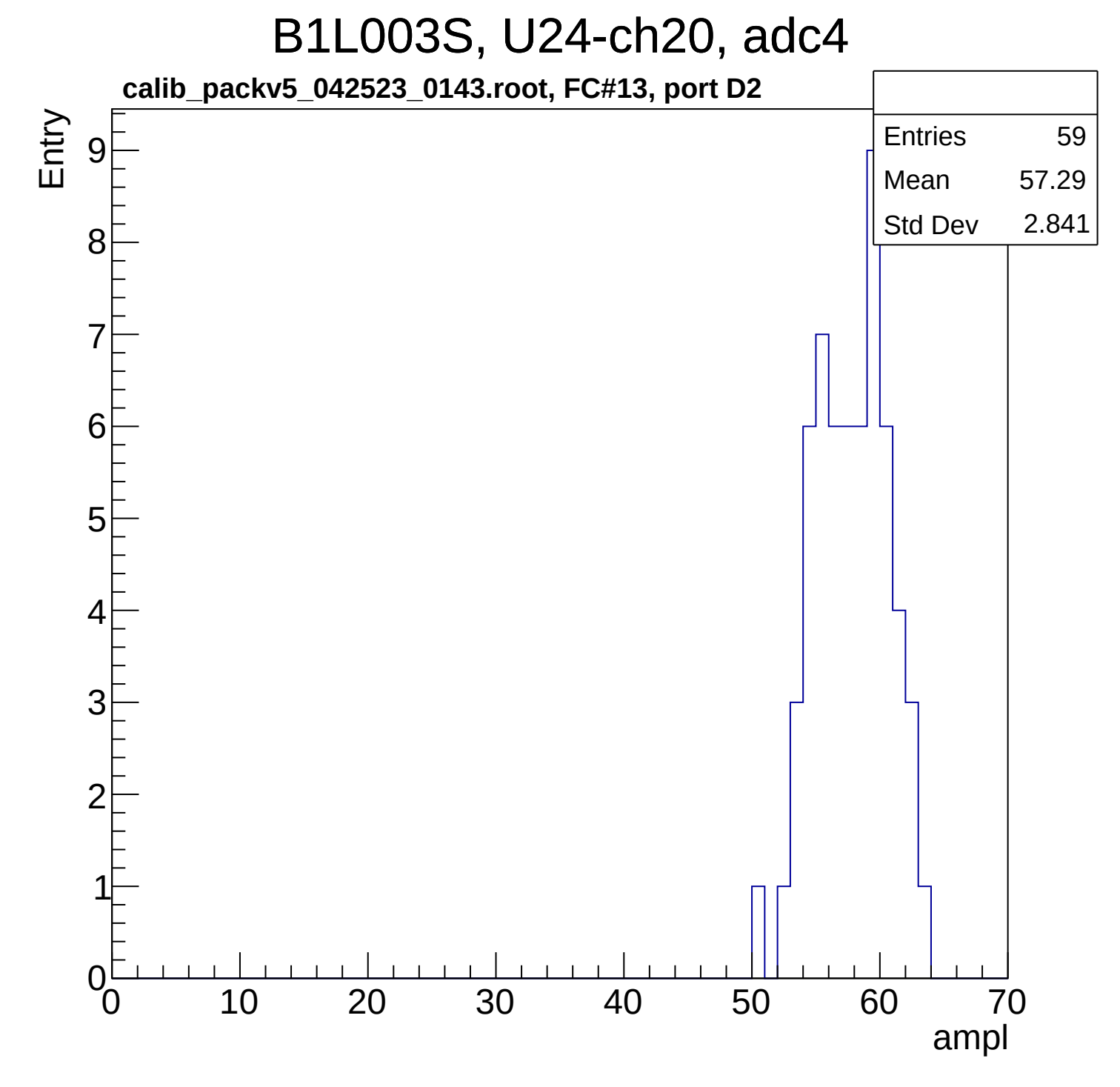

B1L003S, U24-ch20, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 43.63
Std Dev 3.004
Gaus mean : 44.4429
Gaus Width: 3.6189
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U24-ch20, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 50.04
Std Dev 3.796
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch20, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 57.29
Std Dev 2.841
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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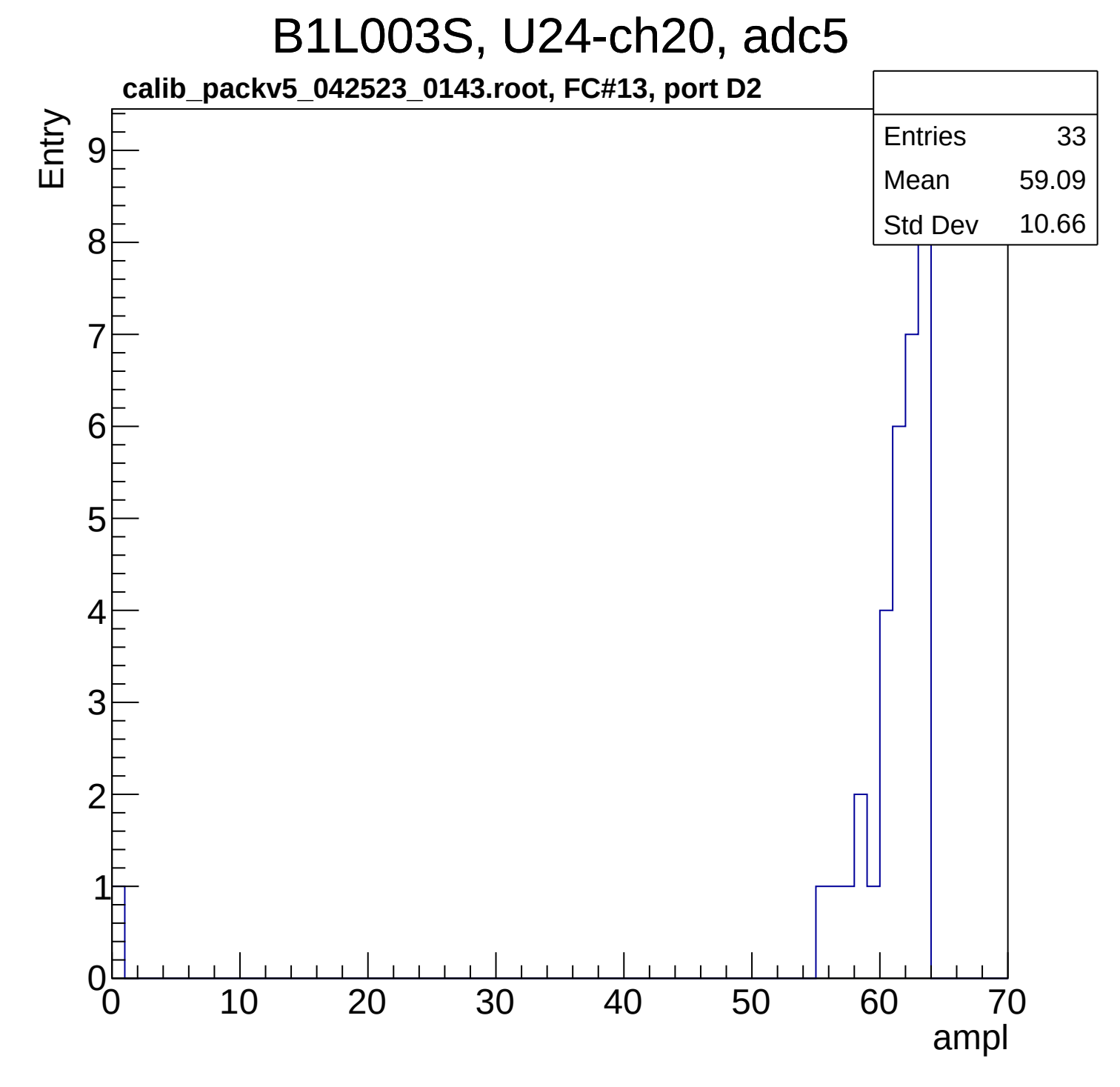
B1L003S, U24-ch20, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 59.09
Std Dev 10.66
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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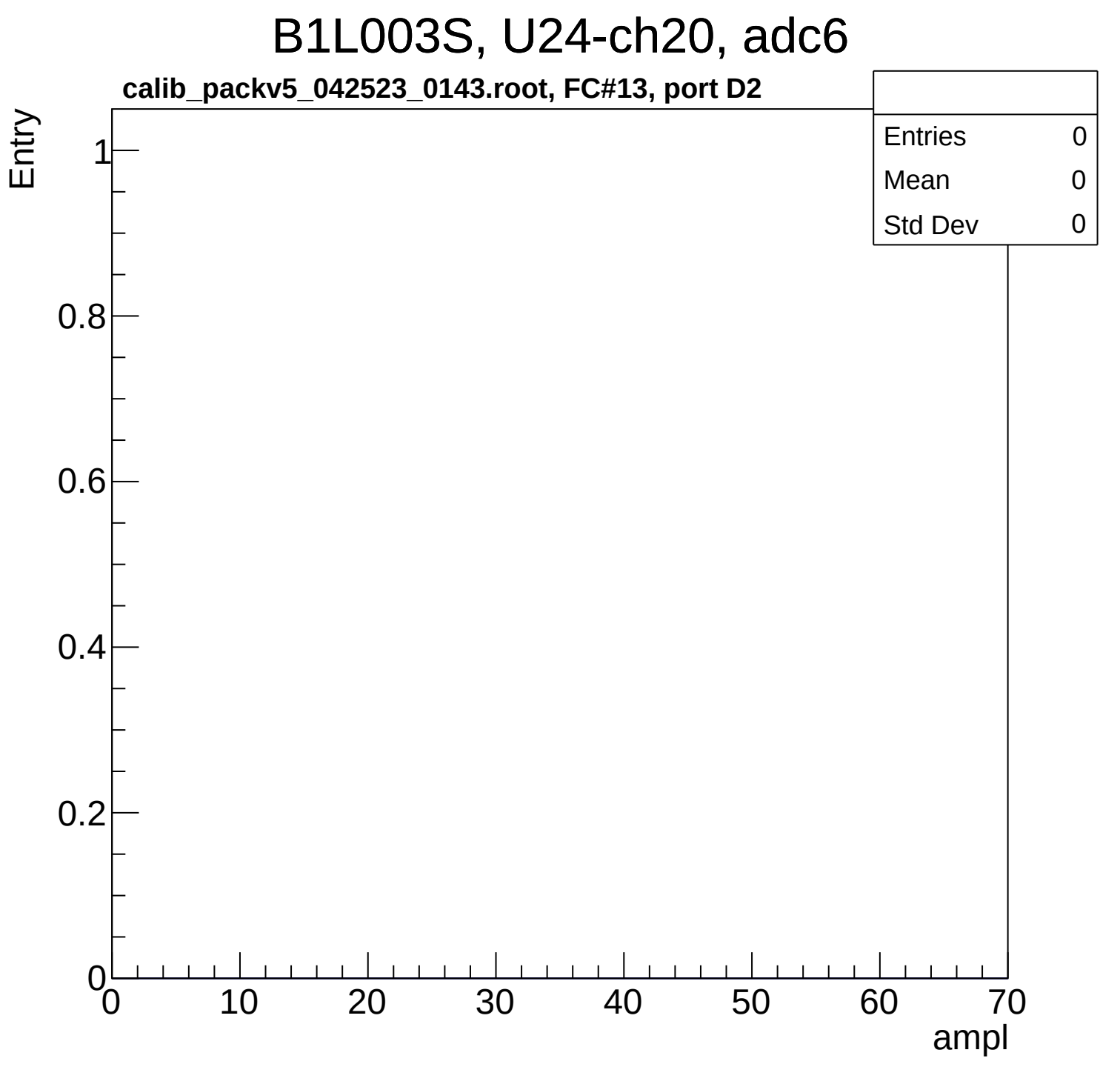
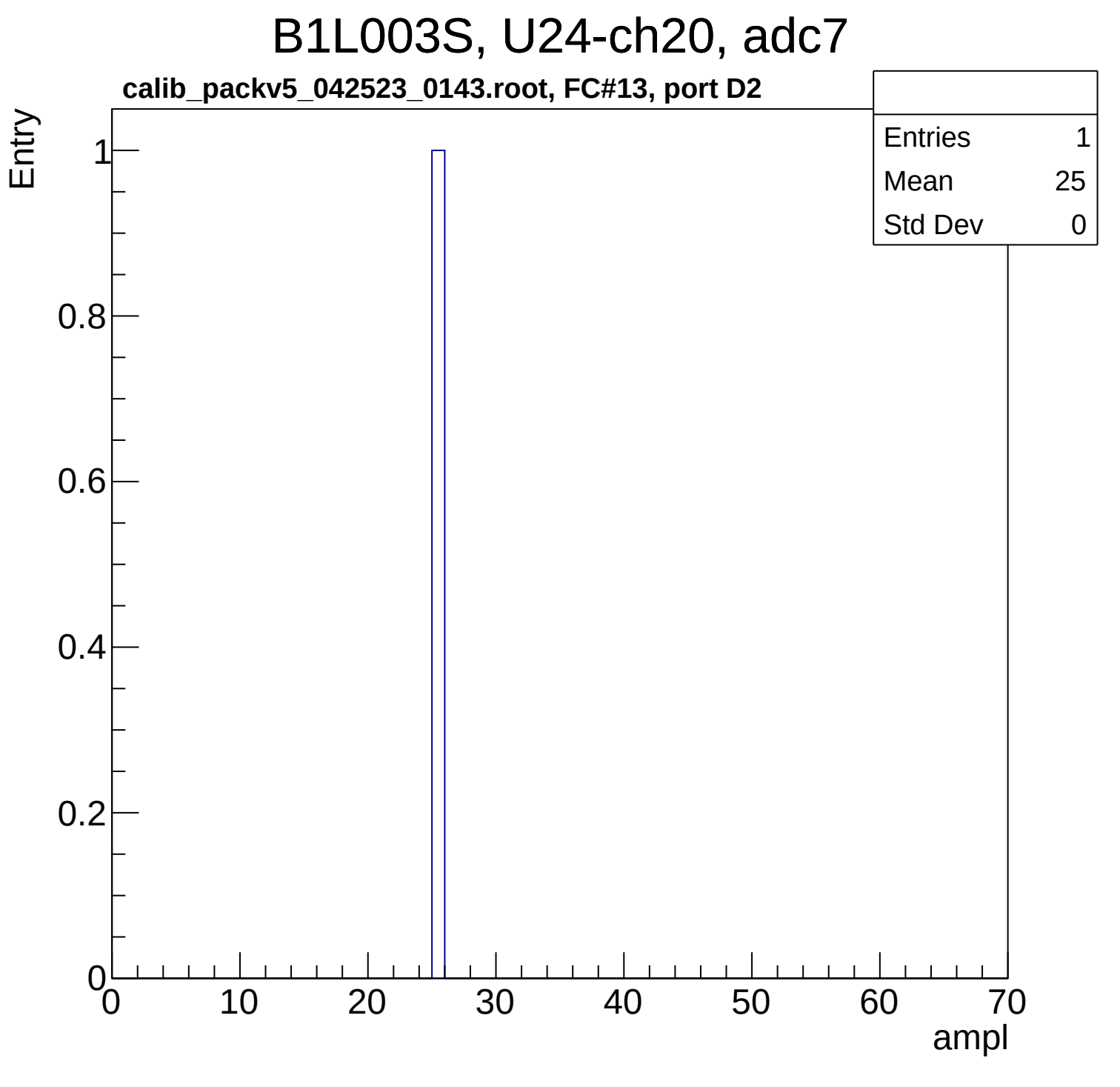

B1L003S, U24-ch20, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch20, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 25
Std Dev 0
Entry
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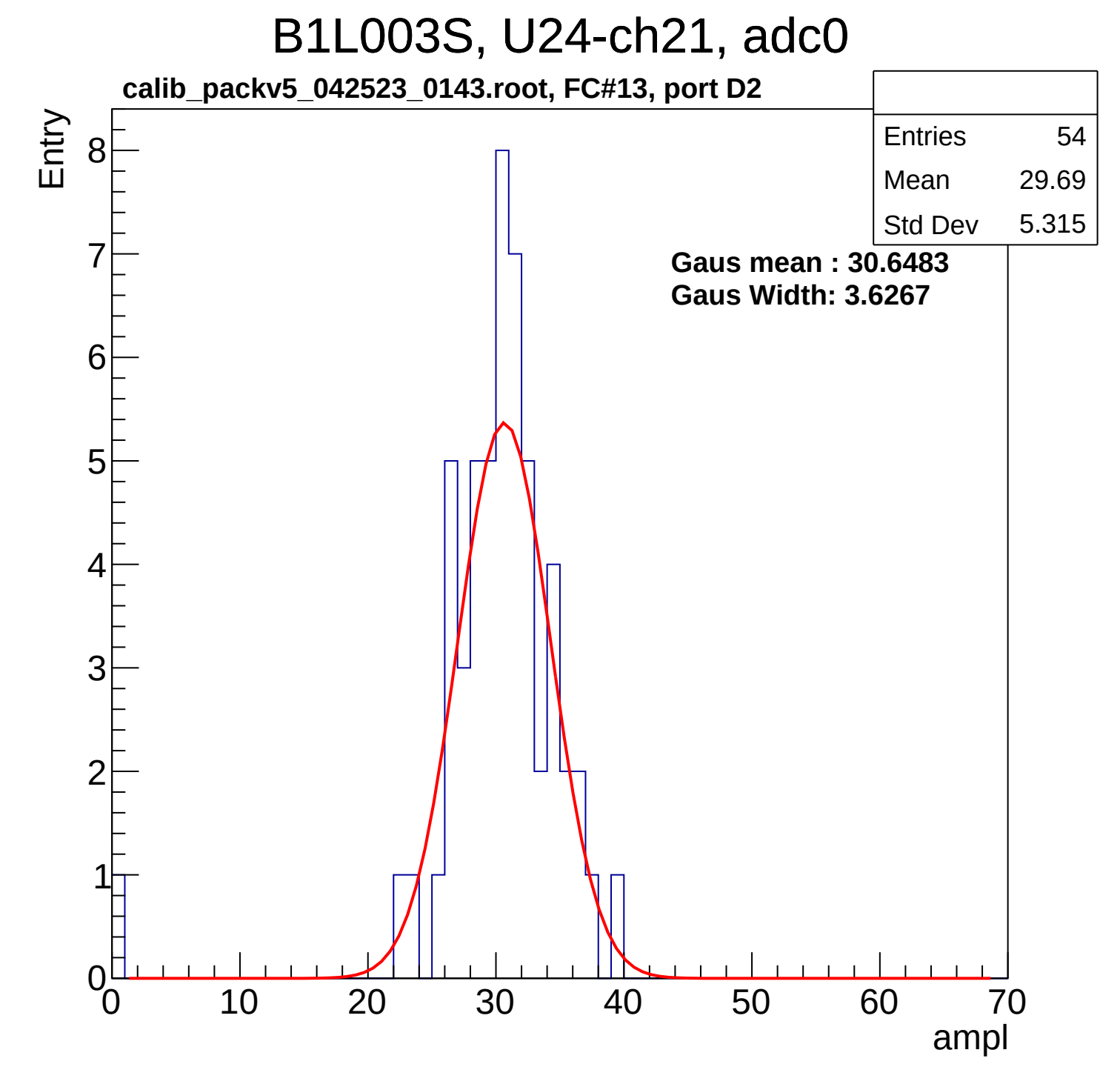
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch21, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.69
Std Dev 5.315
Gaus mean : 30.6483
Gaus Width: 3.6267
Entry
ampl
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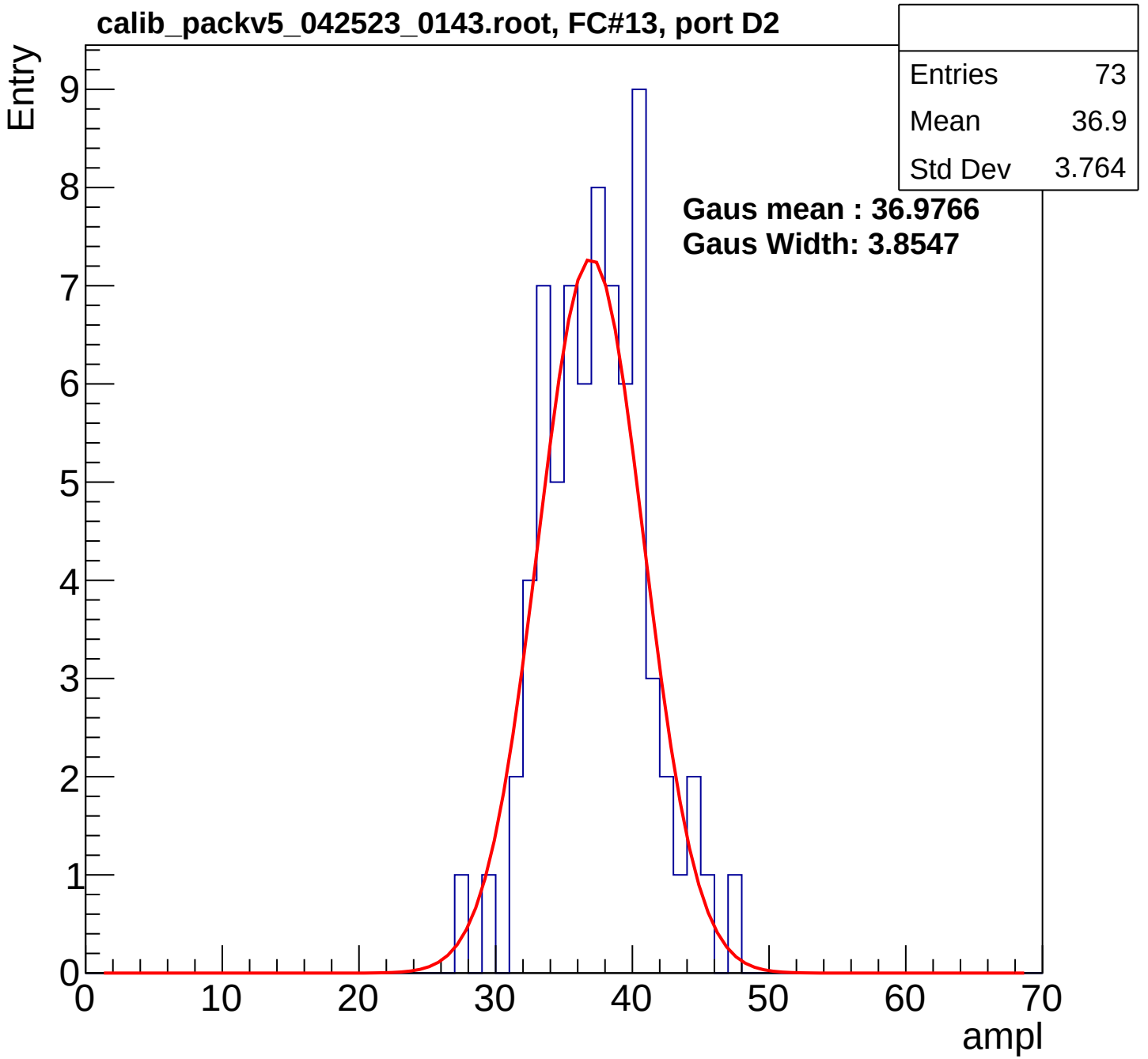
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch21, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.9
Std Dev 3.764
Gaus mean : 36.9766
Gaus Width: 3.8547
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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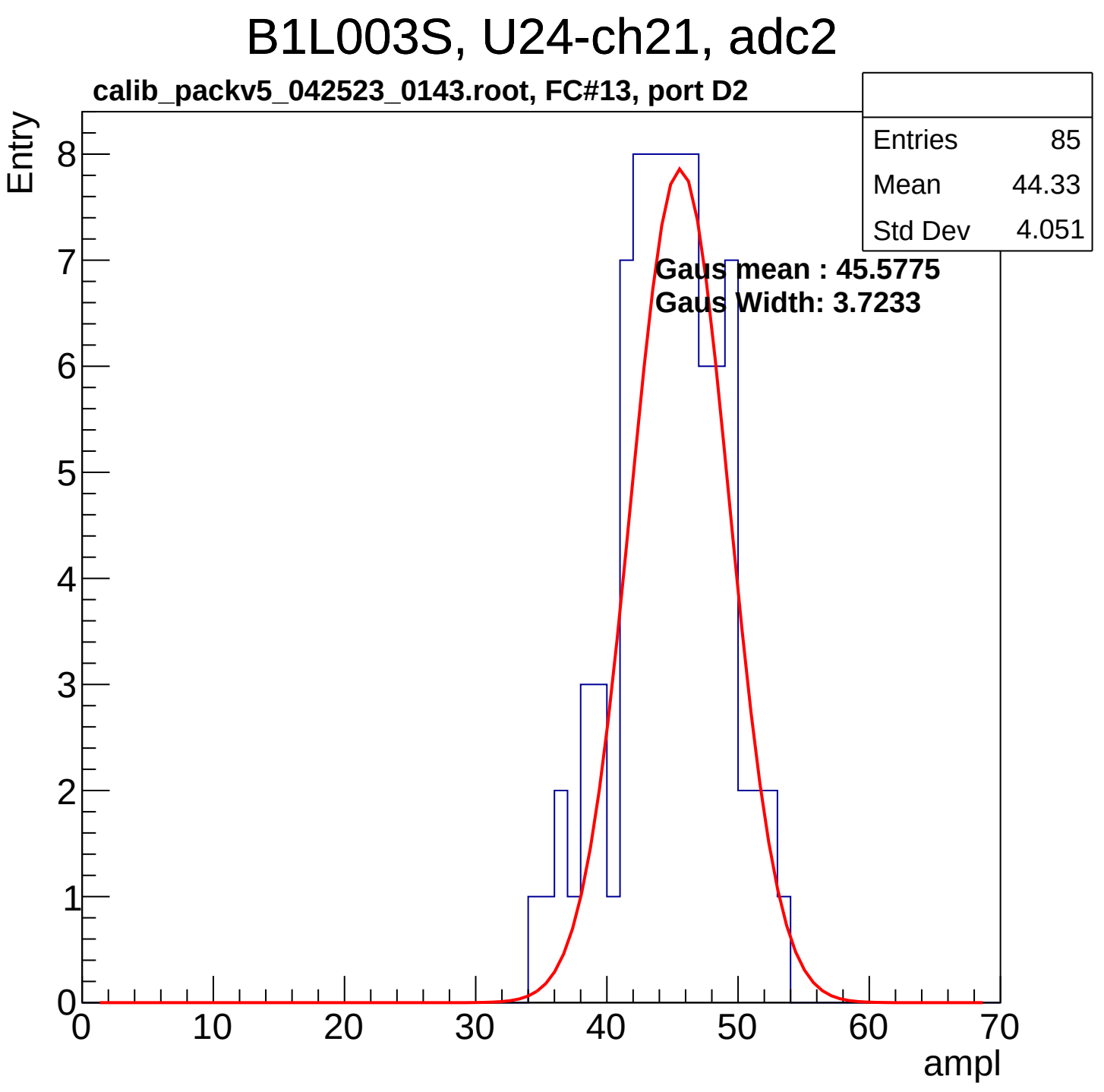
B1L003S, U24-ch21, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 44.33
Std Dev 4.051
Gaus mean : 45.5775
Gaus Width: 3.7233
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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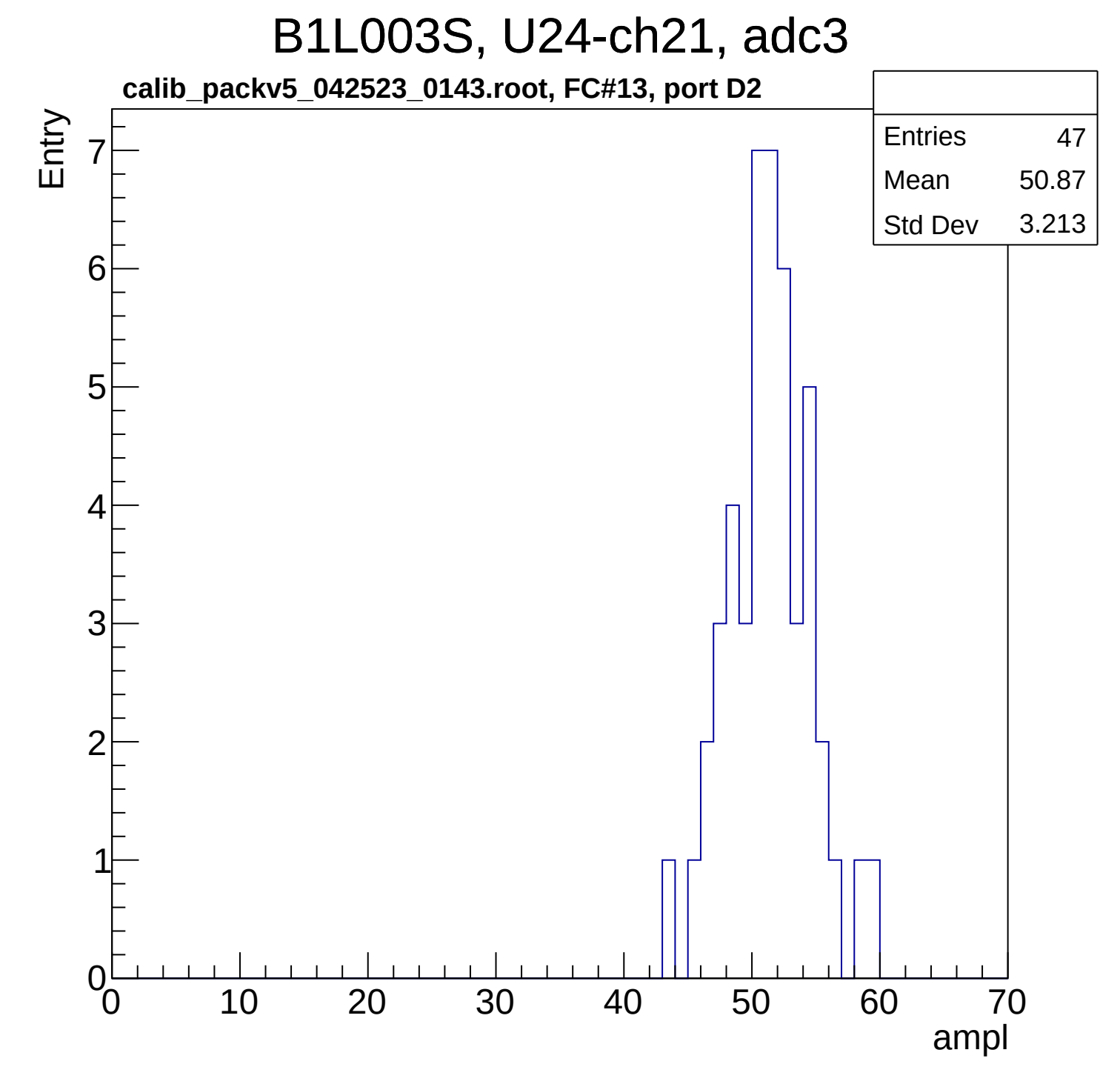
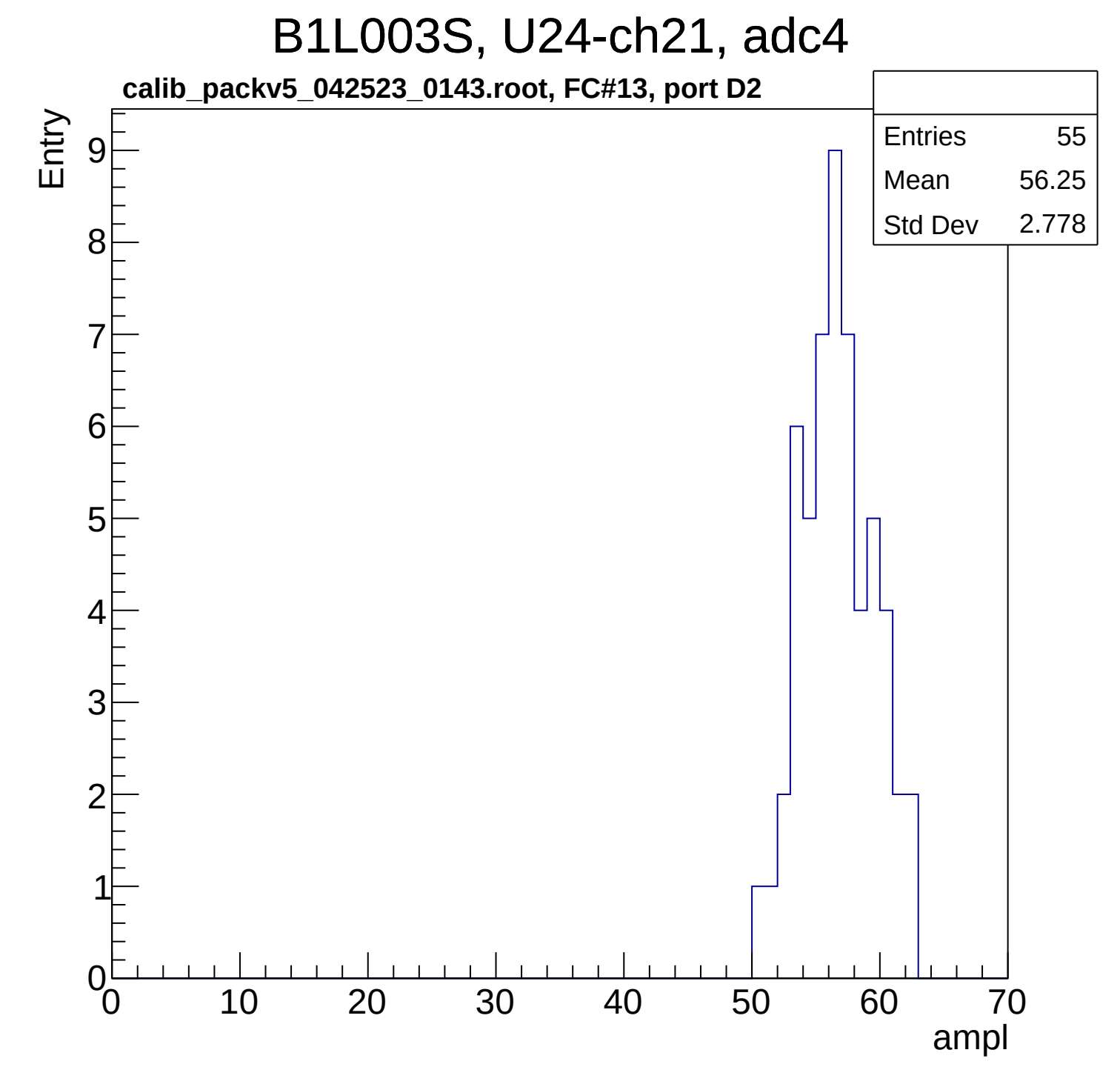

B1L003S, U24-ch21, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 50.87
Std Dev 3.213
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch21, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 56.25
Std Dev 2.778
Entry
ampl
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3
4
5
6
7
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9
0
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20
30
40
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60
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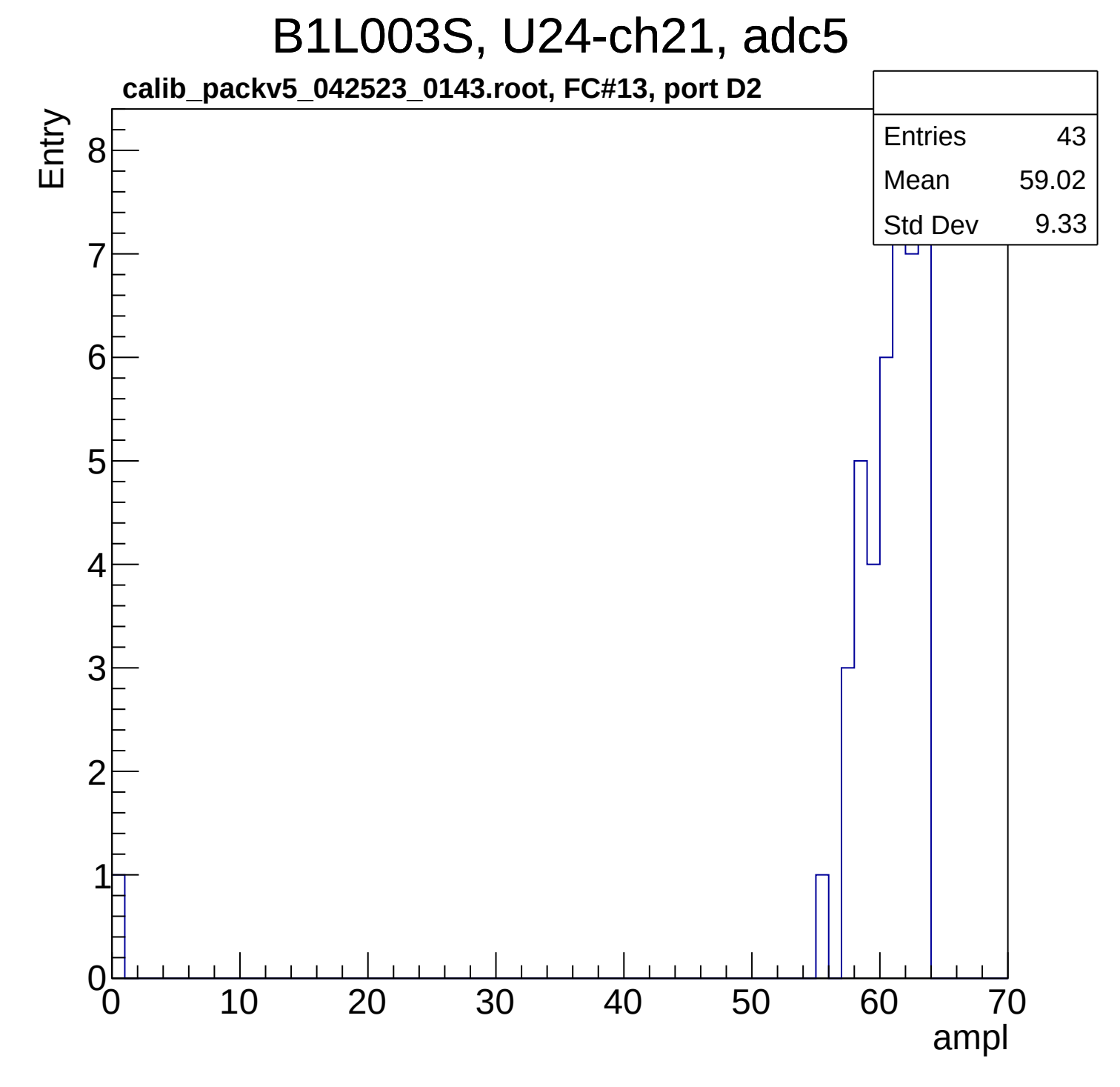
B1L003S, U24-ch21, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.02
Std Dev 9.33
Entry
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5
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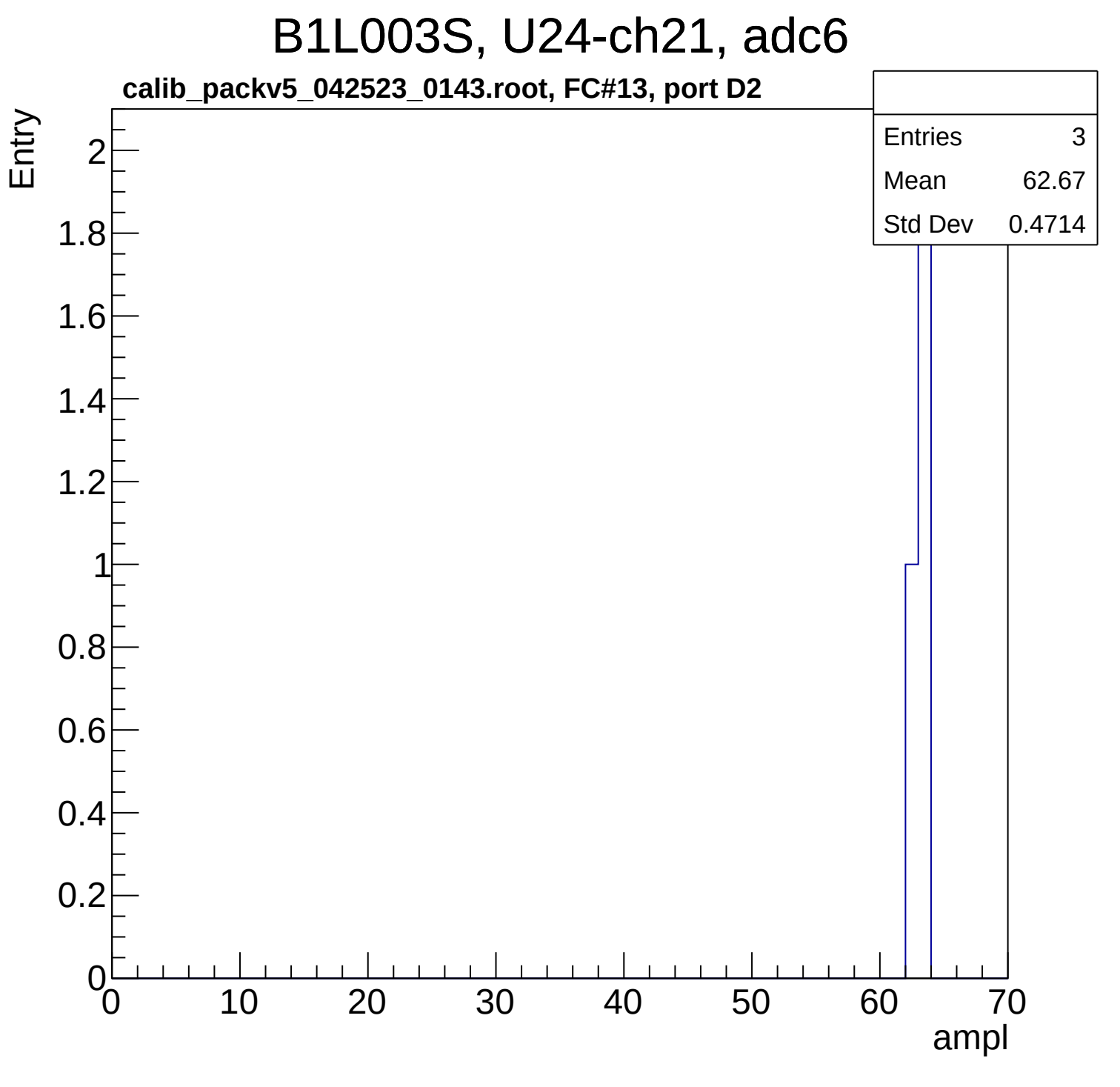
70
ampl


B1L003S, U24-ch21, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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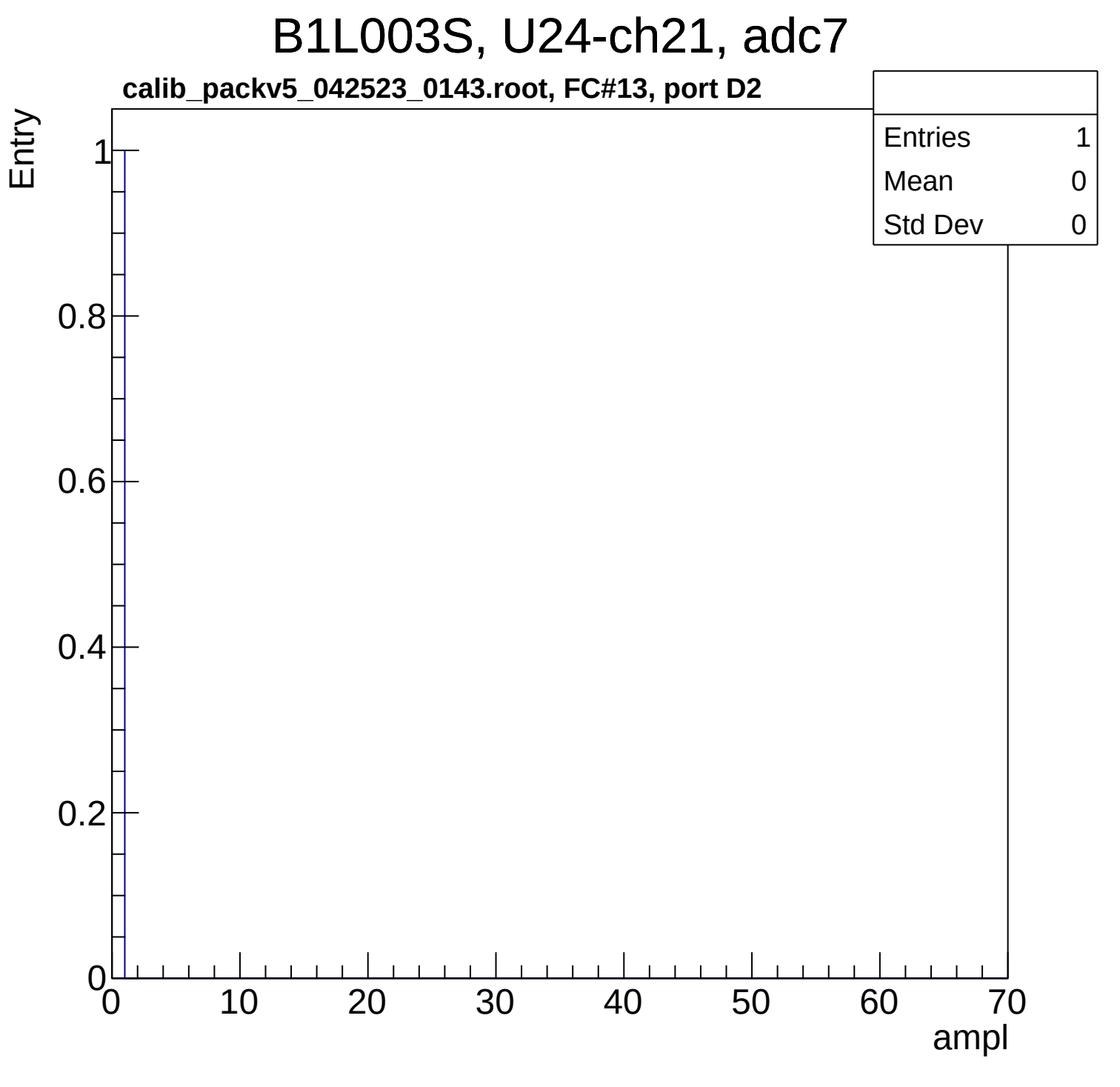
B1L003S, U24-ch21, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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30
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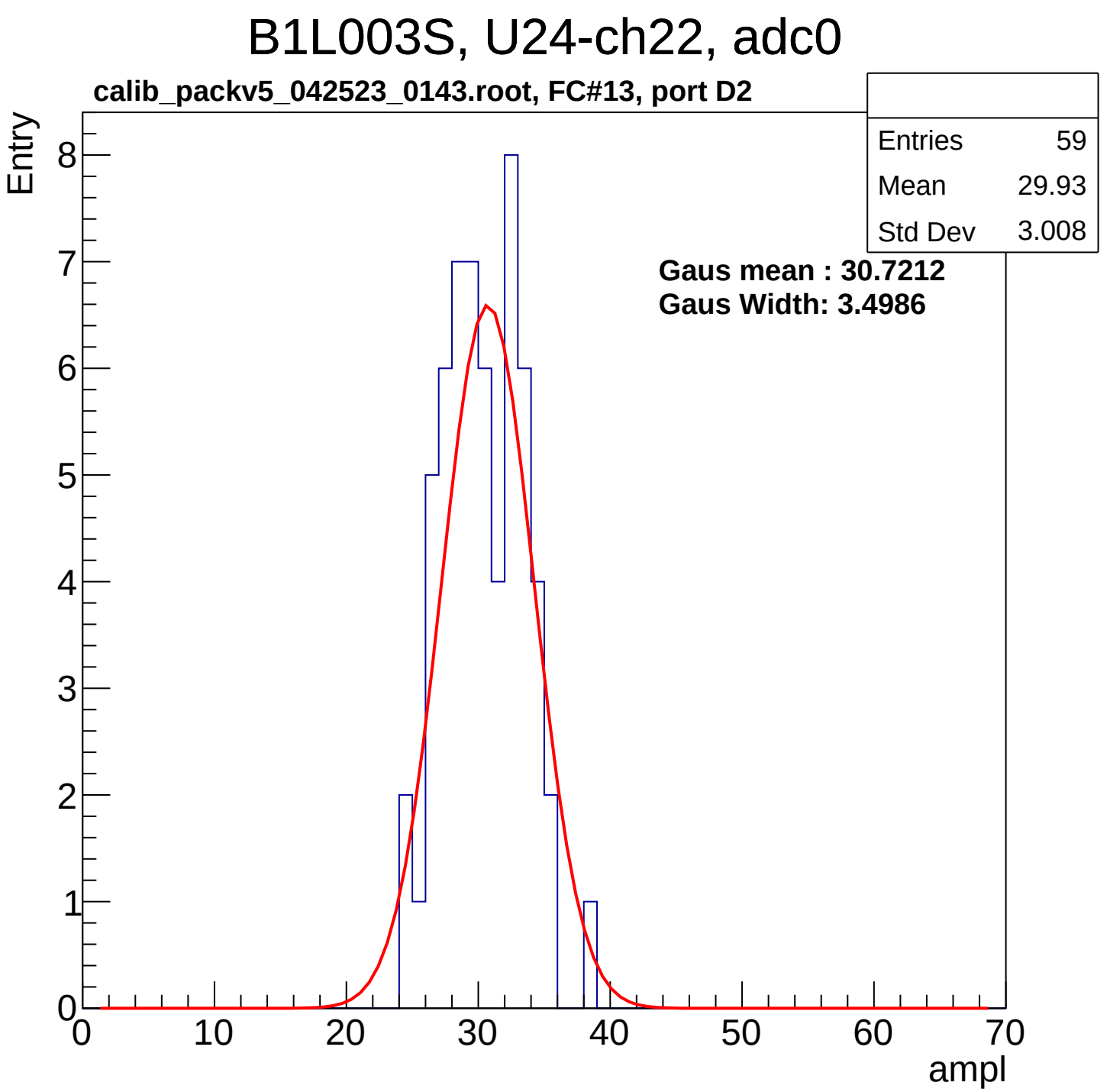
50
60
70
ampl


B1L003S, U24-ch22, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 29.93
Std Dev 3.008
Gaus mean : 30.7212
Gaus Width: 3.4986
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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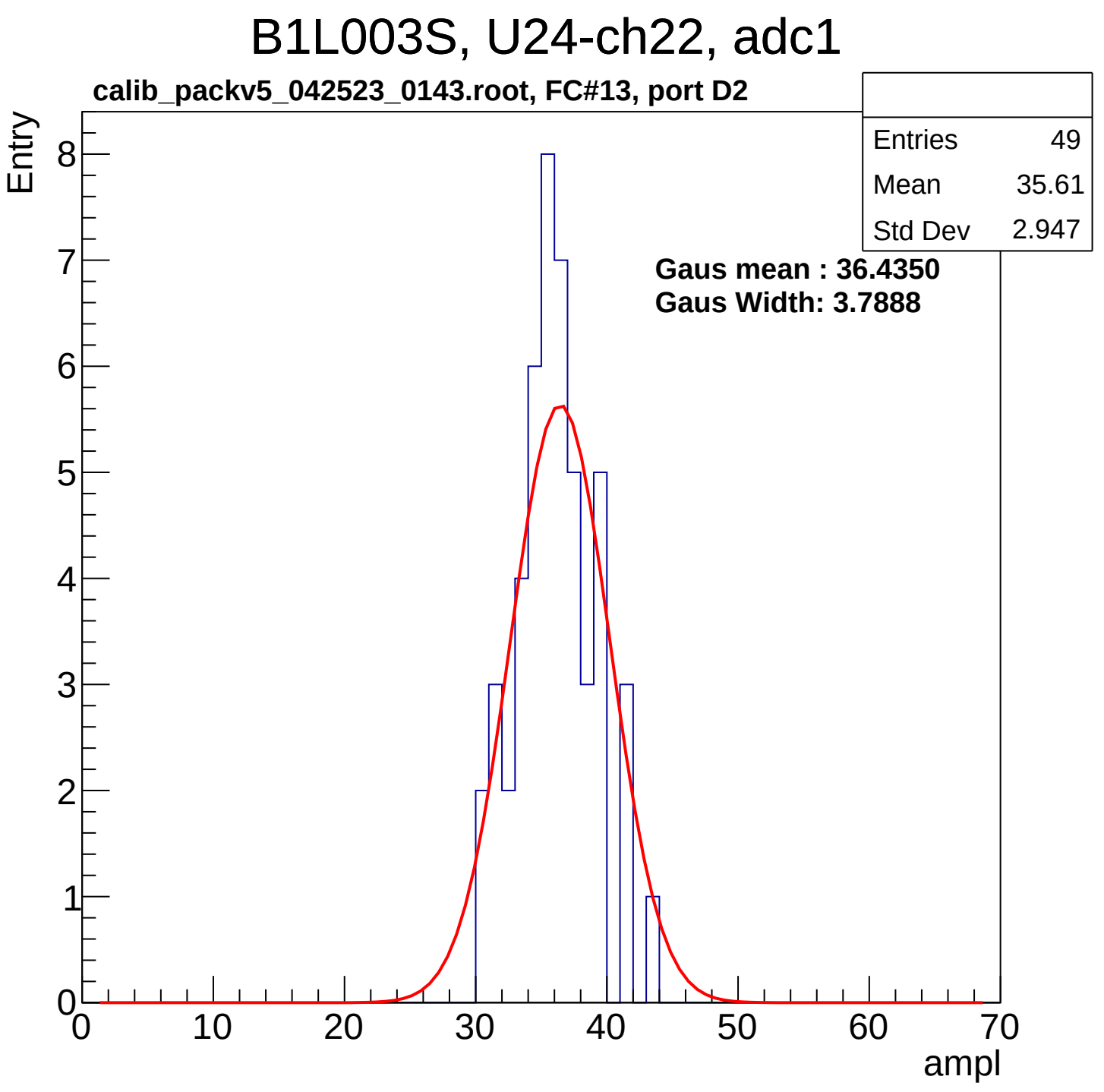
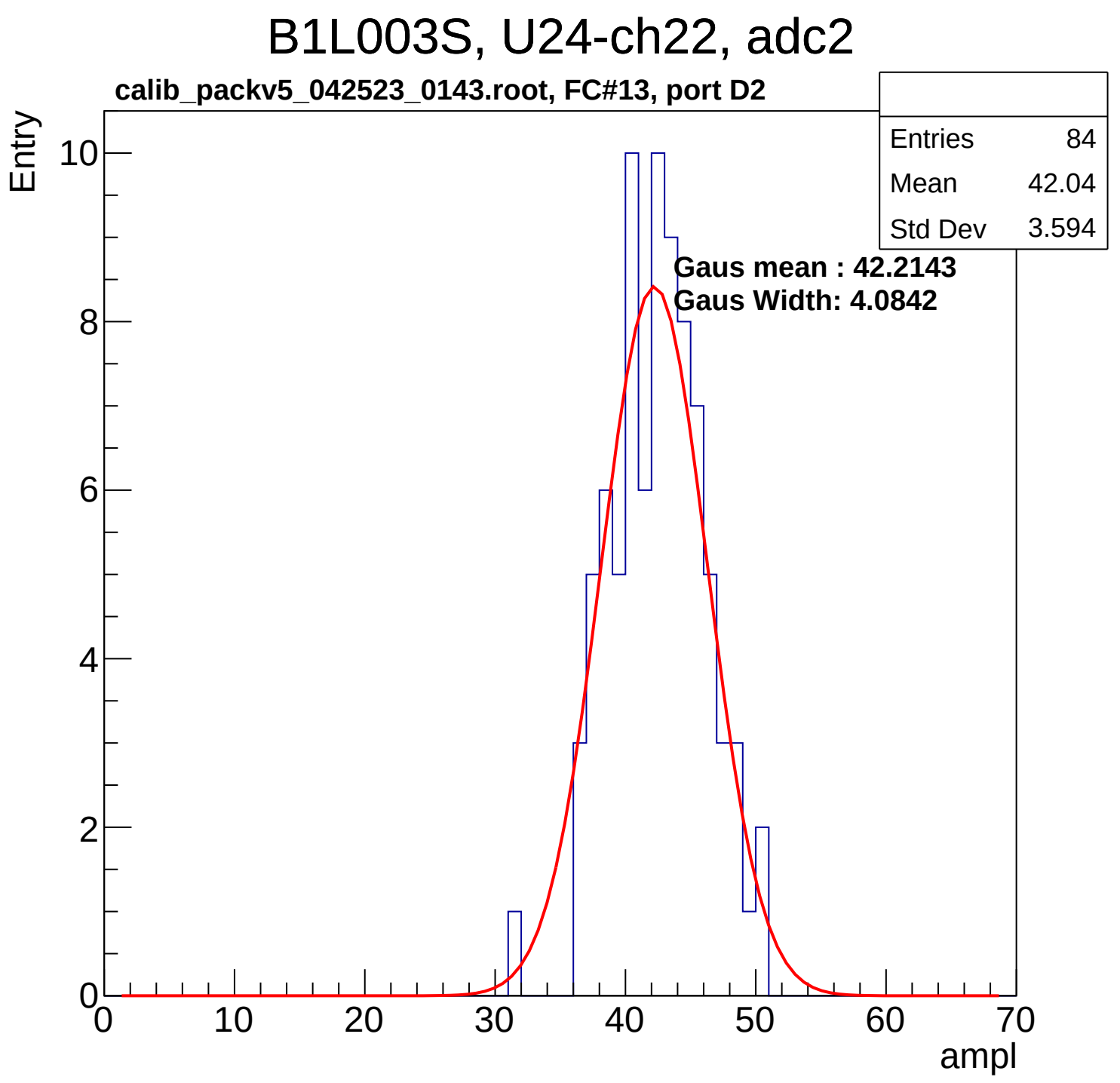
40
50
60
70


B1L003S, U24-ch22, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 35.61
Std Dev 2.947
Gaus mean : 36.4350
Gaus Width: 3.7888
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch22, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 42.04
Std Dev 3.594
Gaus mean : 42.2143
Gaus Width: 4.0842
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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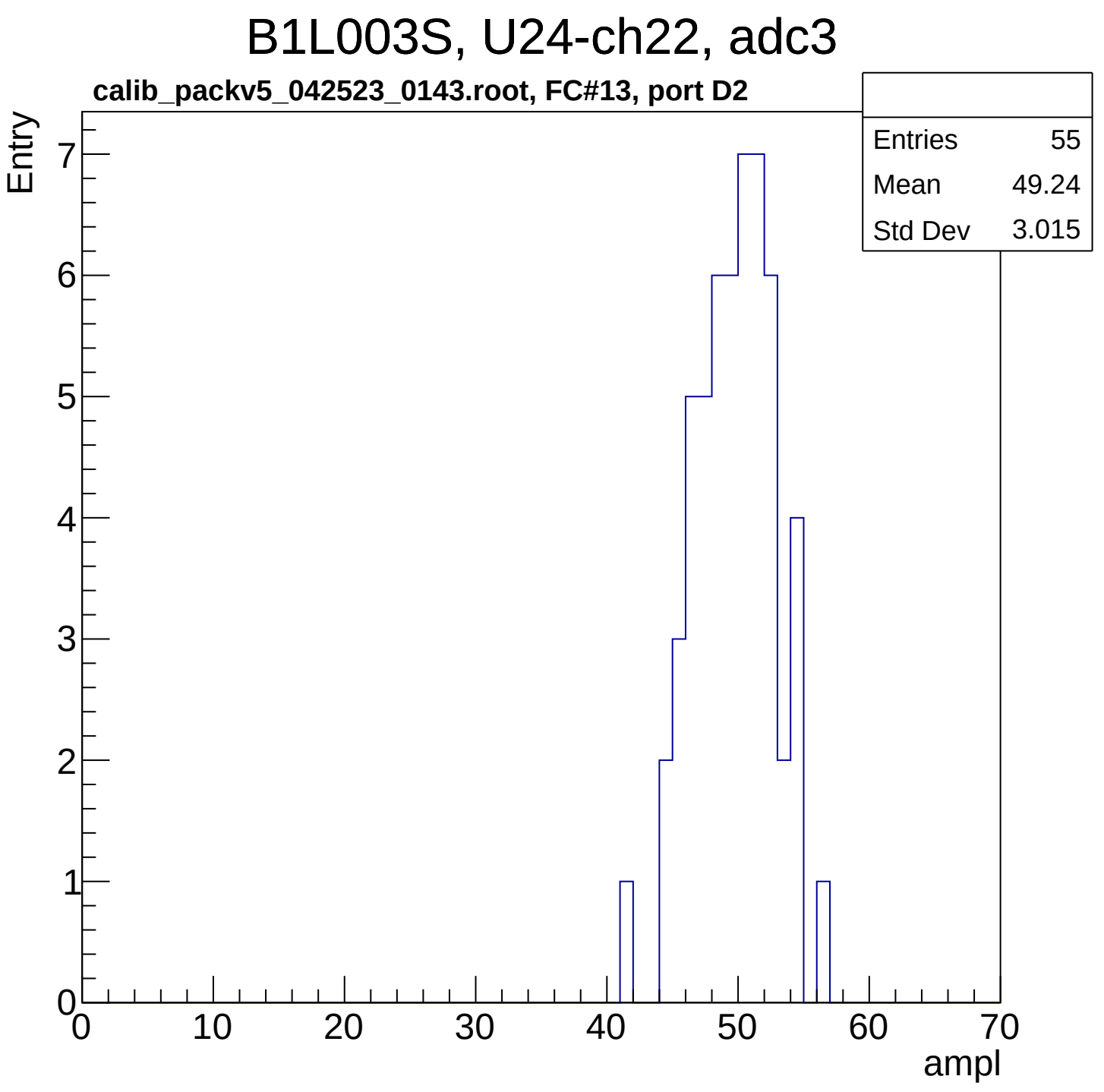
B1L003S, U24-ch22, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 49.24
Std Dev 3.015
Entry
ampl
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2
3
4
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0
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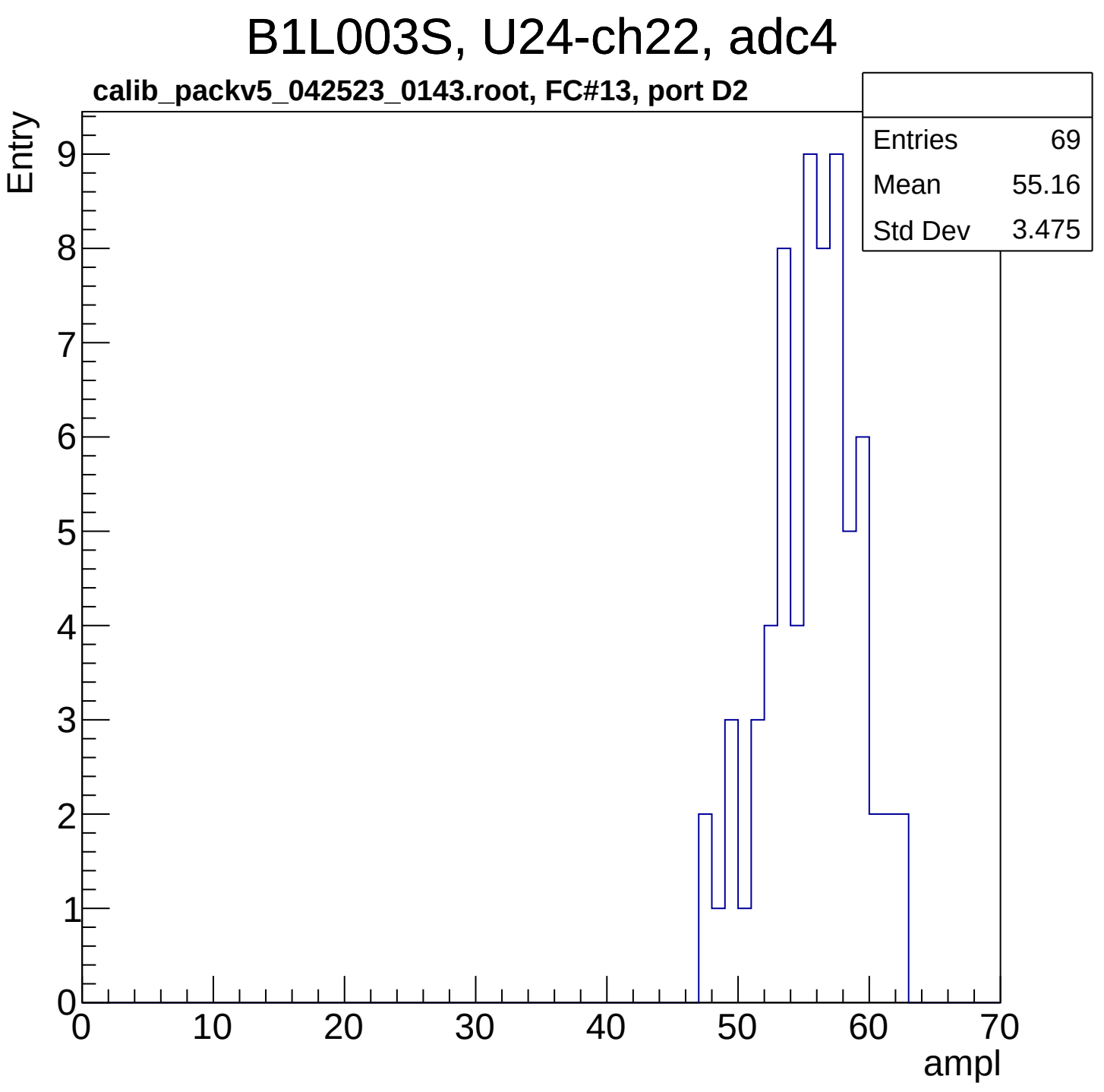
B1L003S, U24-ch22, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 55.16
Std Dev 3.475
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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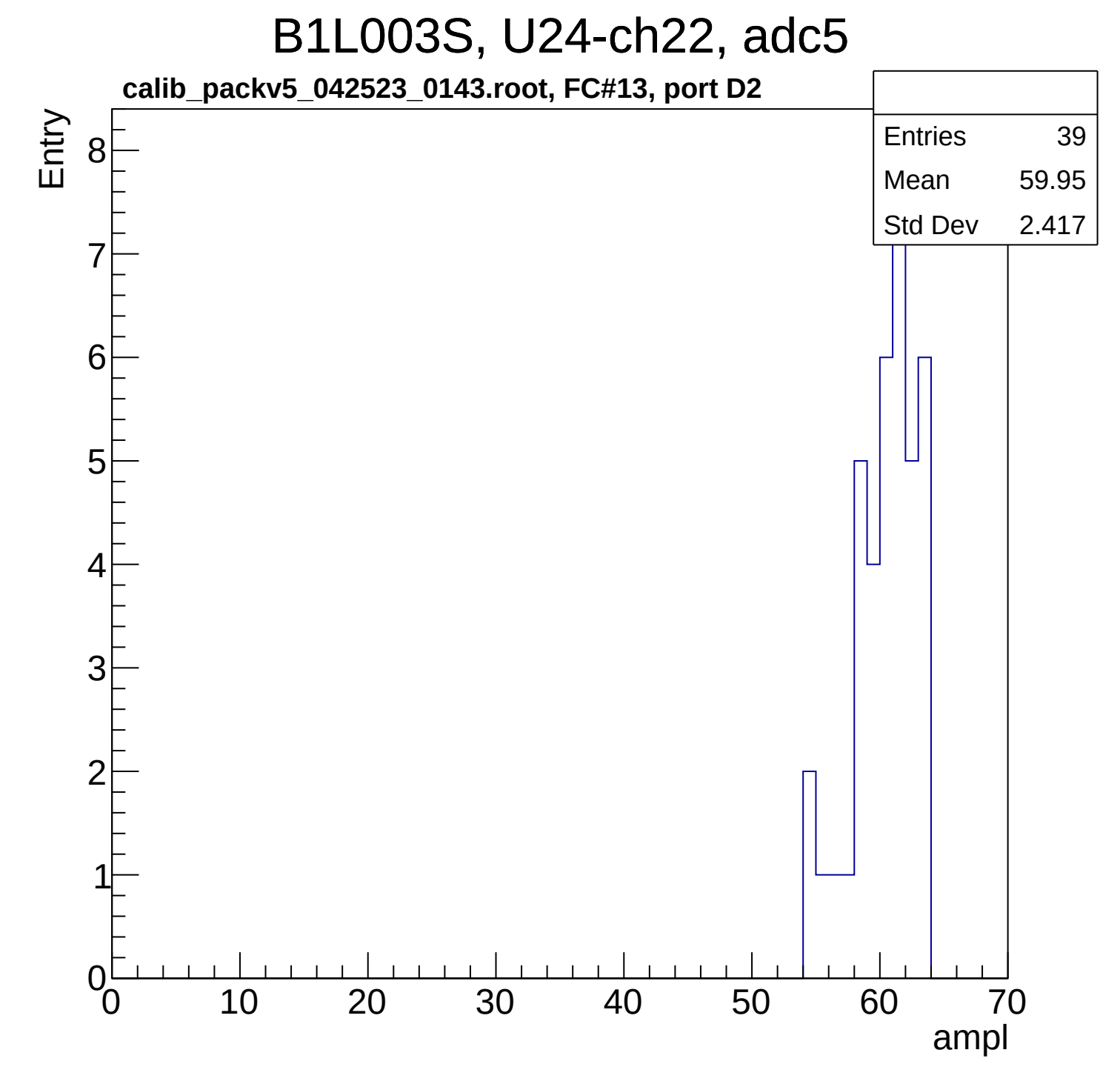
B1L003S, U24-ch22, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 59.95
Std Dev 2.417
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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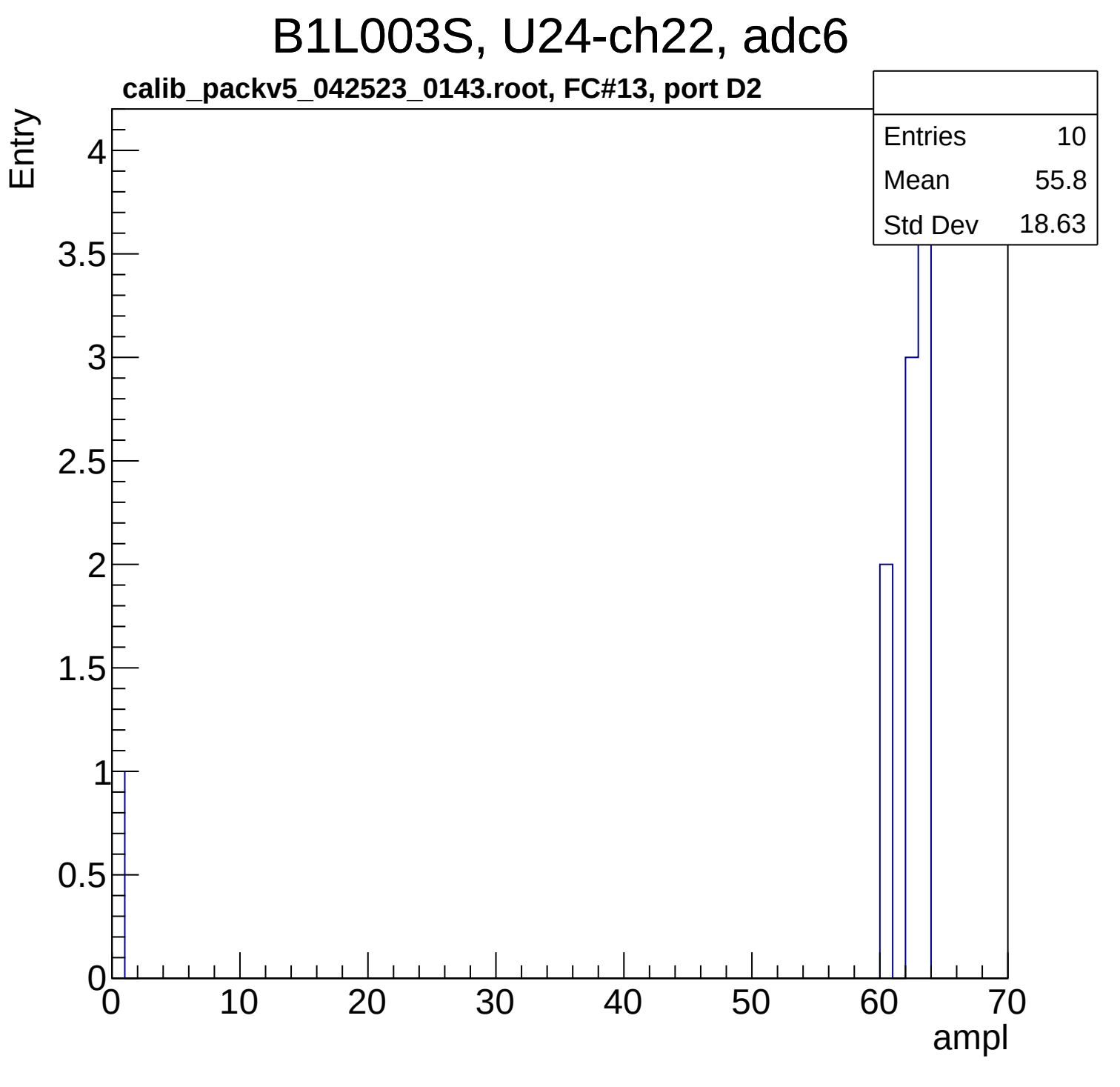
B1L003S, U24-ch22, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.8
Std Dev 18.63
Entry
ampl
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1.5
2
2.5
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3.5
4
0
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20
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60
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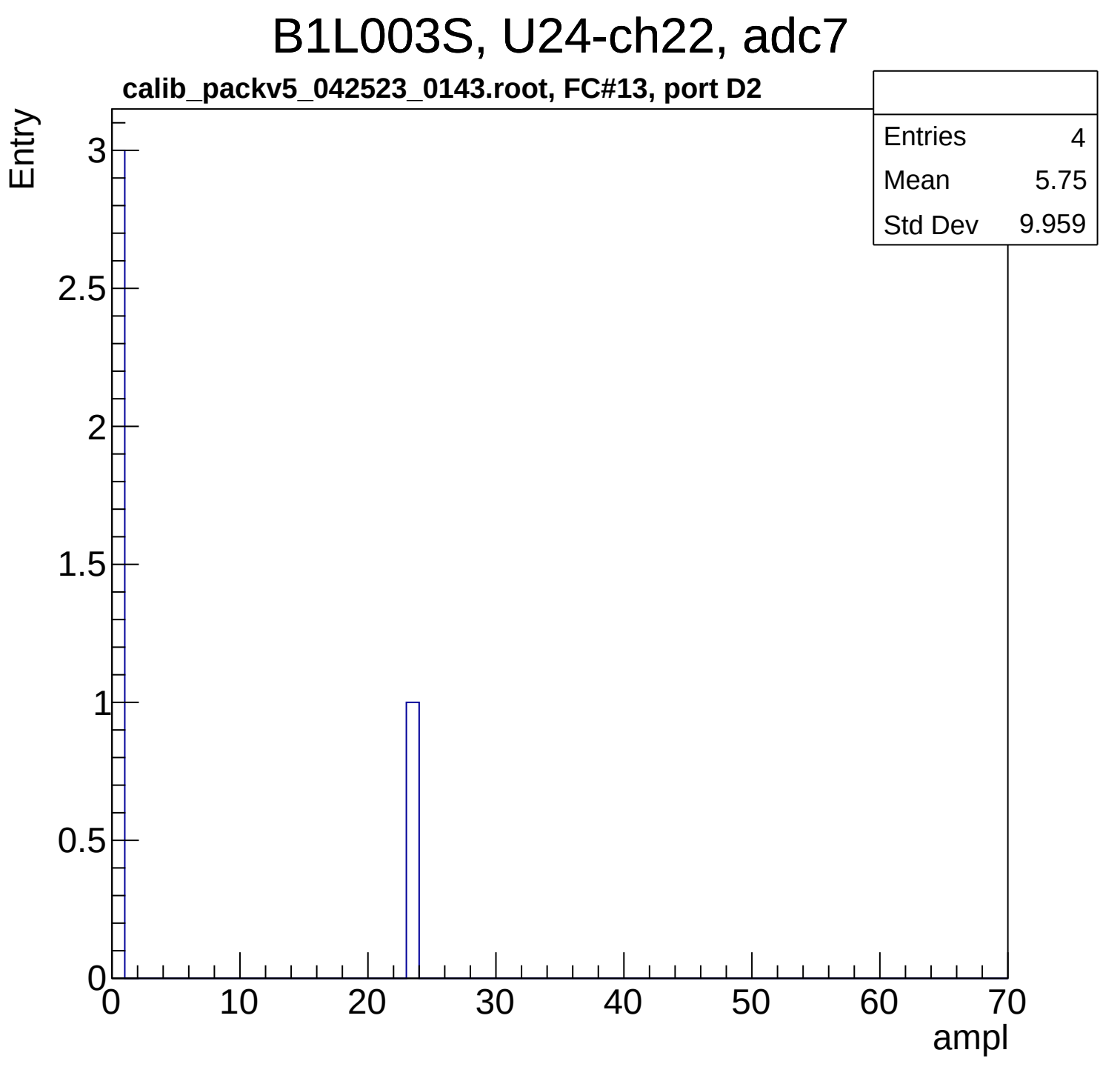
B1L003S, U24-ch22, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 5.75
Std Dev 9.959
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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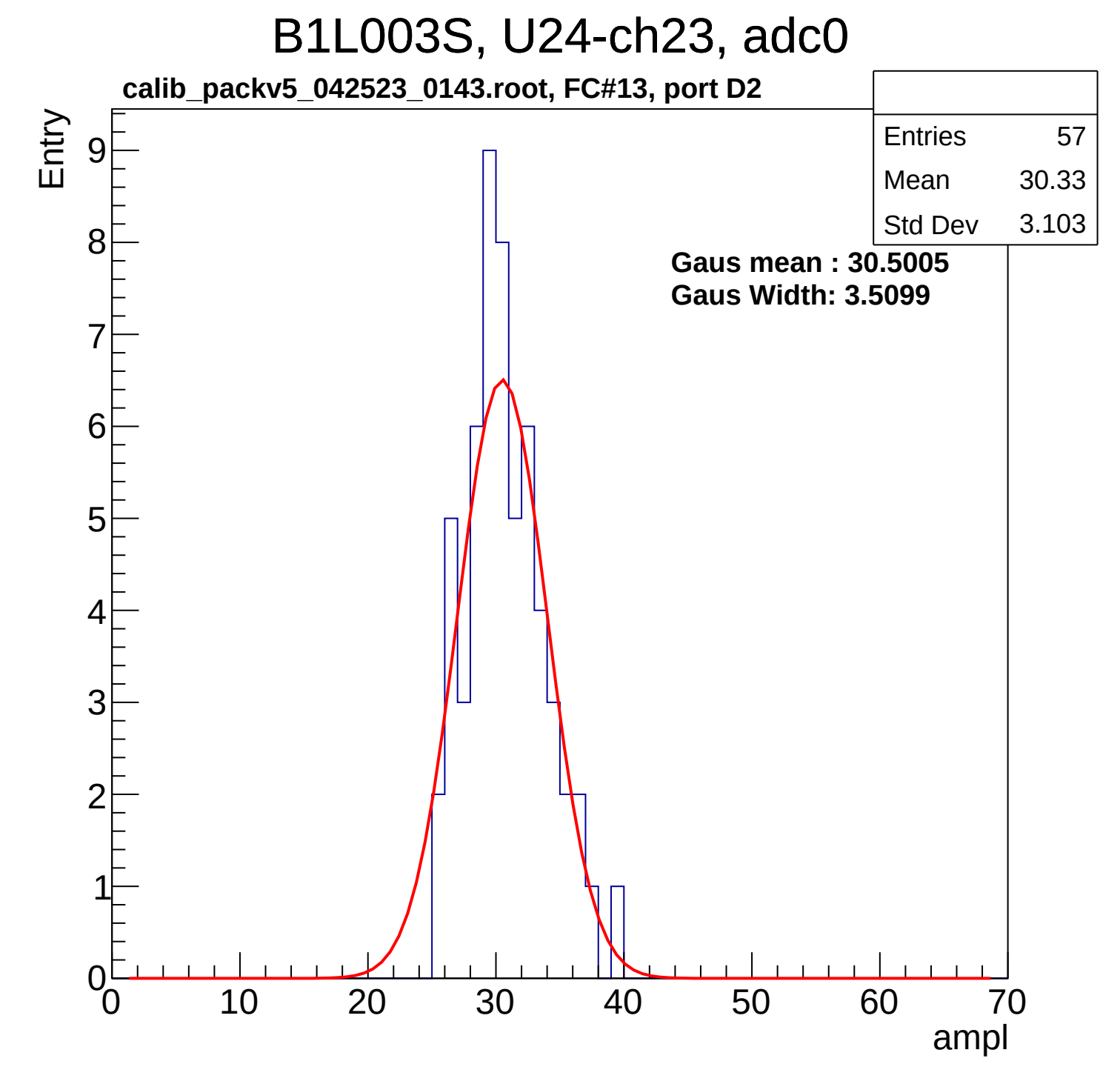
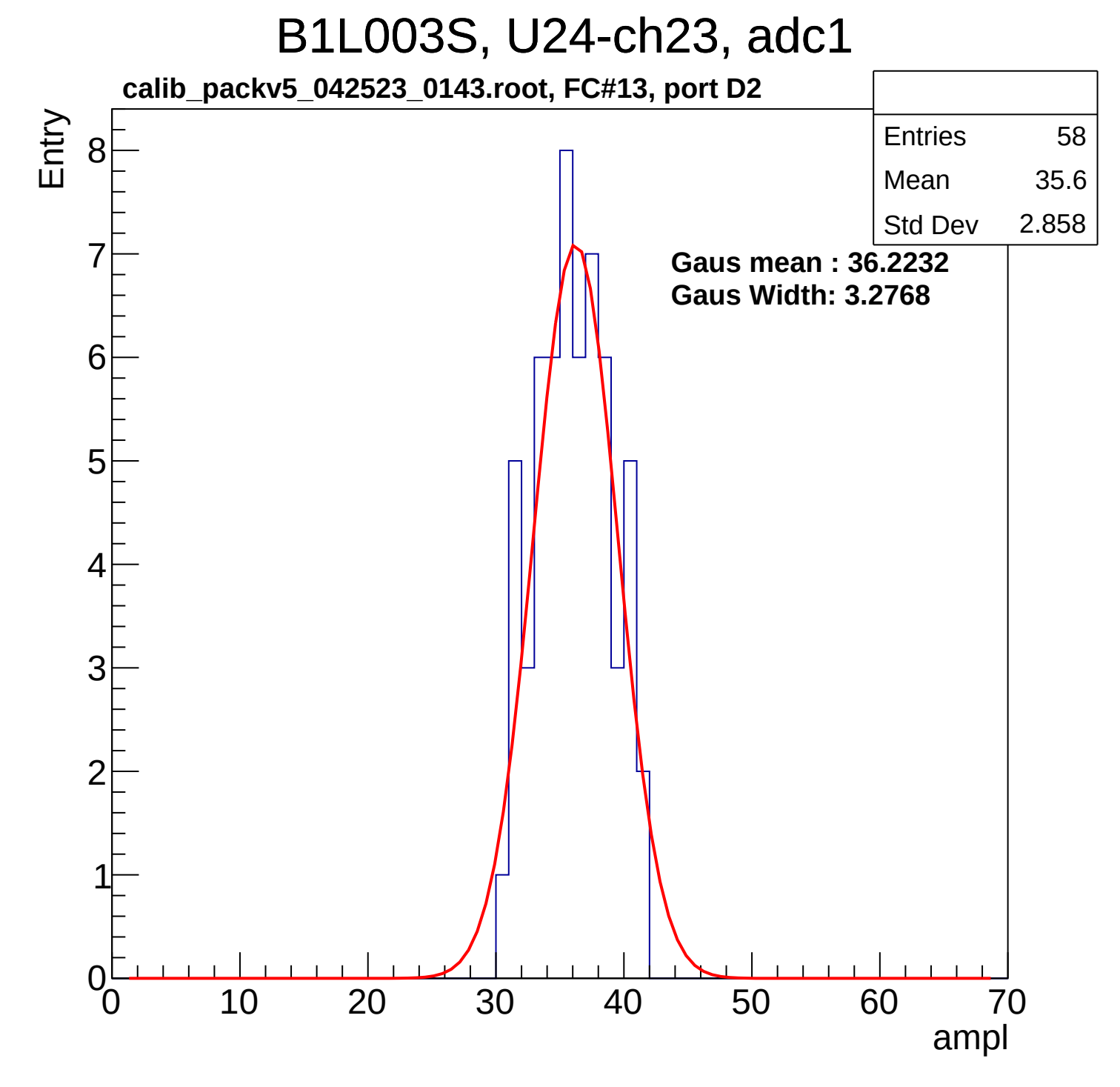

B1L003S, U24-ch23, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 30.33
Std Dev 3.103
Gaus mean : 30.5005
Gaus Width: 3.5099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch23, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 35.6
Std Dev 2.858
Gaus mean : 36.2232
Gaus Width: 3.2768
Entry
ampl
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1
2
3
4
5
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7
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0
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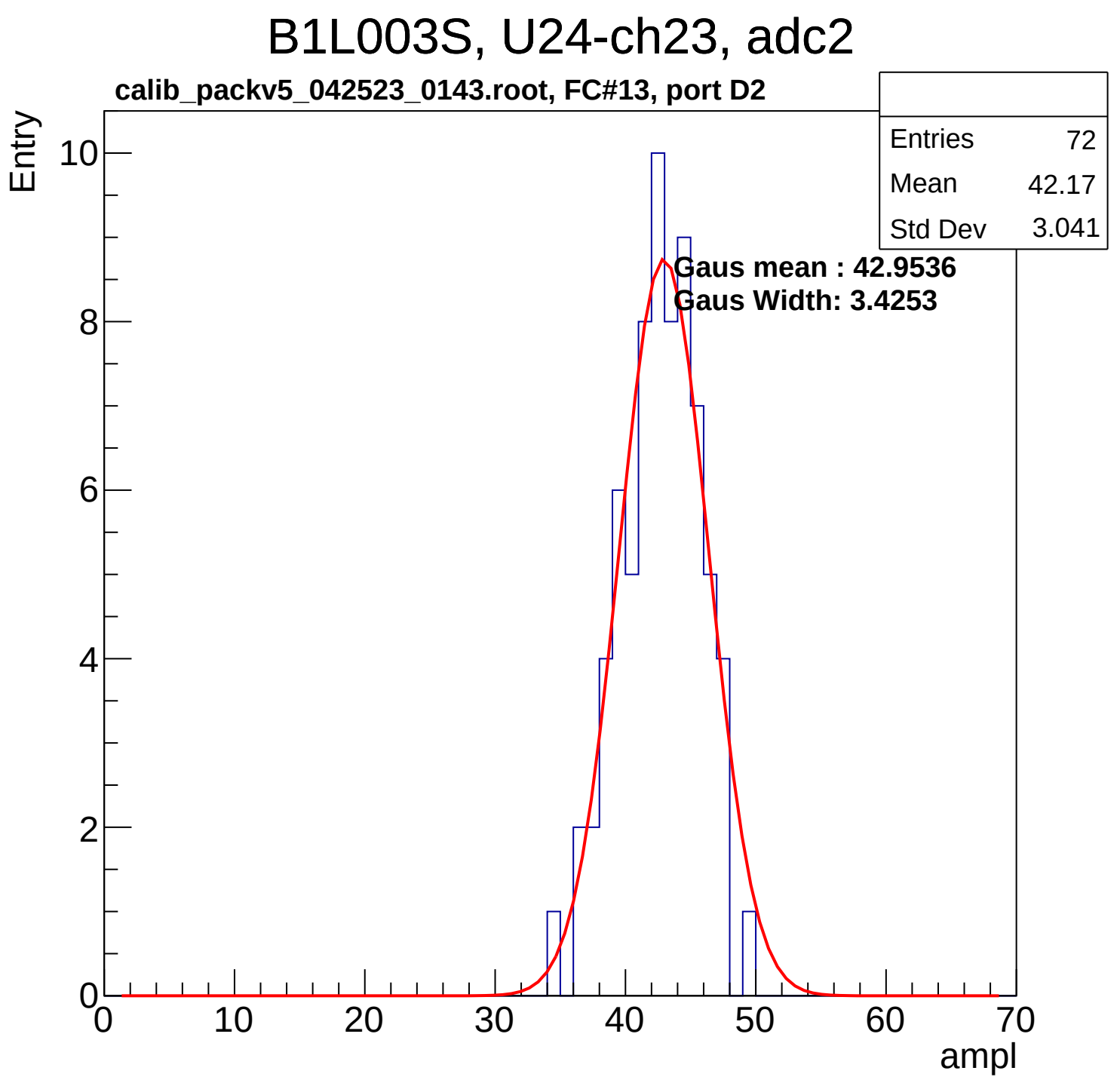
B1L003S, U24-ch23, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.17
Std Dev 3.041
Gaus mean : 42.9536
Gaus Width: 3.4253
Entry
ampl
0
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4
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8
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0
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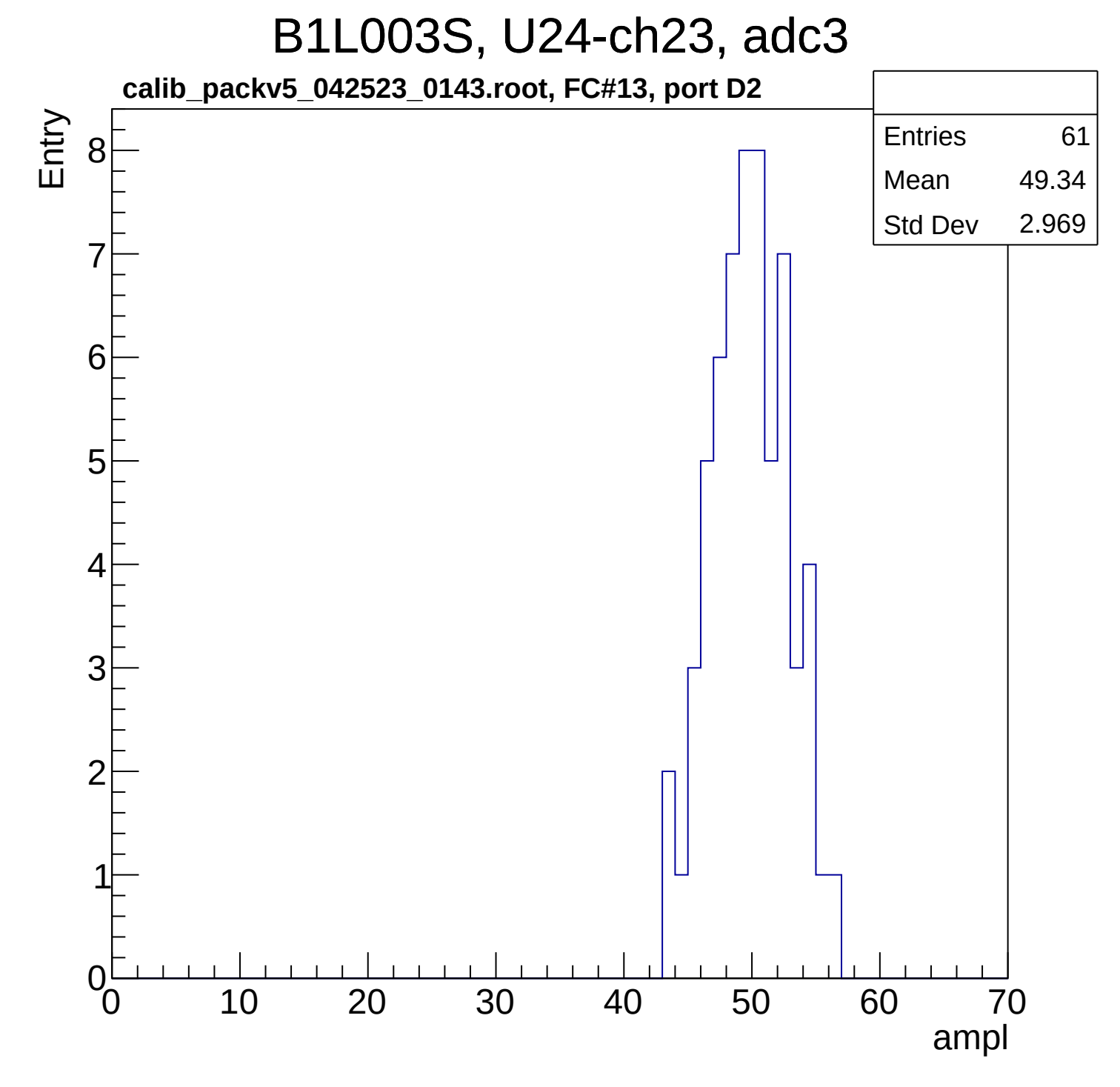
B1L003S, U24-ch23, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.34
Std Dev 2.969
Entry
ampl
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2
3
4
5
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0
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60
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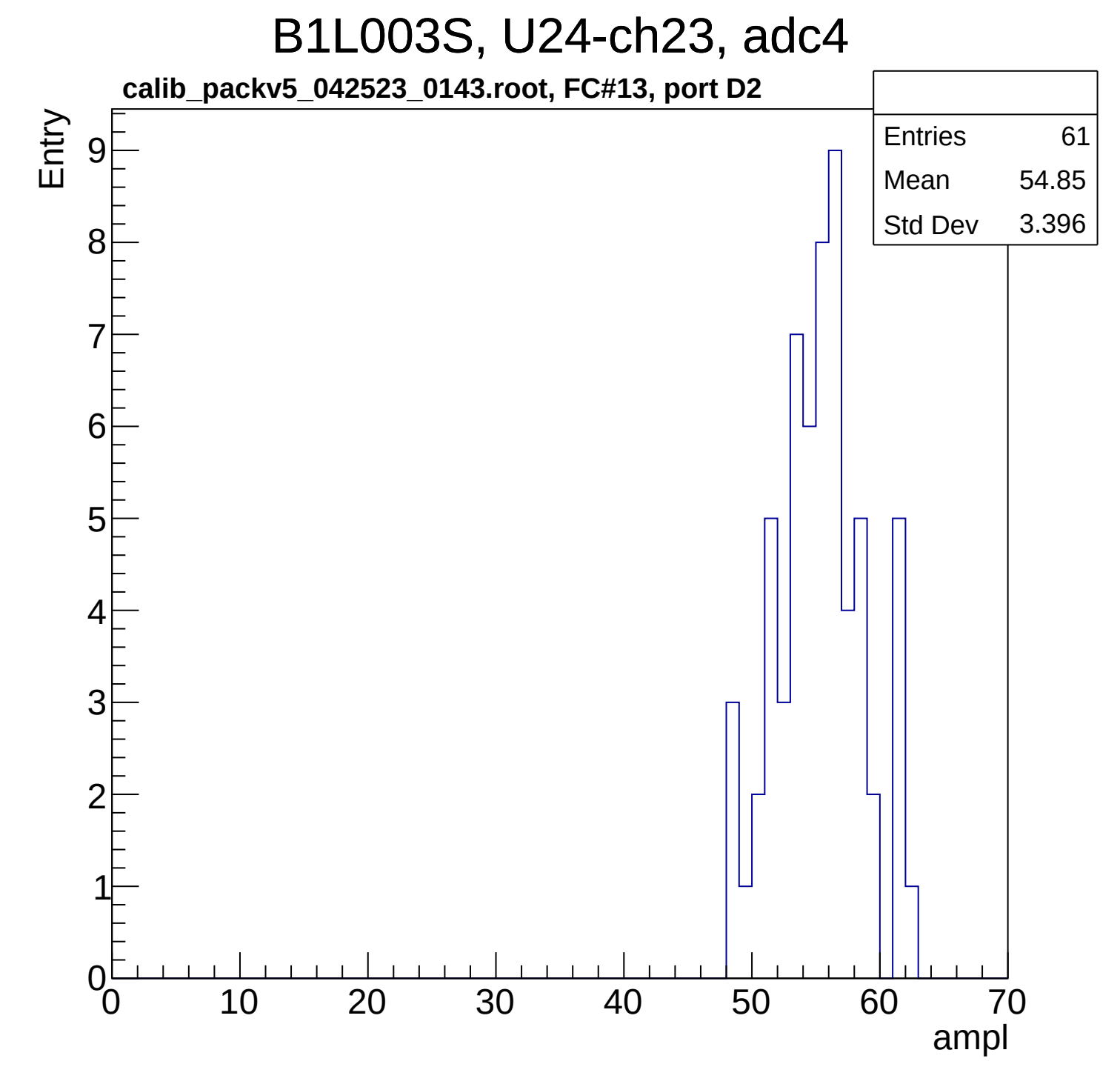
B1L003S, U24-ch23, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.85
Std Dev 3.396
Entry
ampl
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2
3
4
5
6
7
8
9
0
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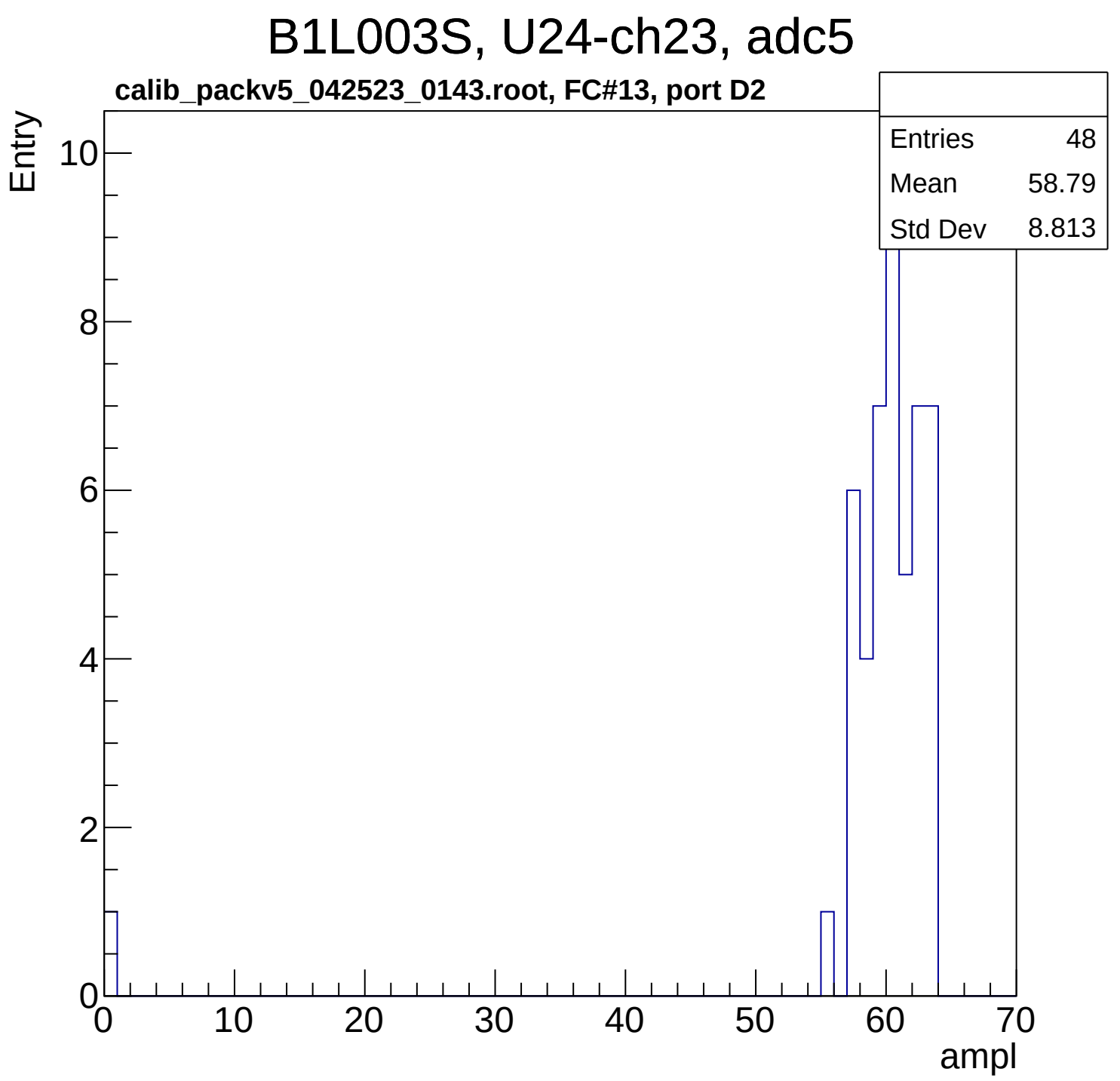
B1L003S, U24-ch23, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.79
Std Dev 8.813
Entry
ampl
0
2
4
6
8
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0
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20
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40
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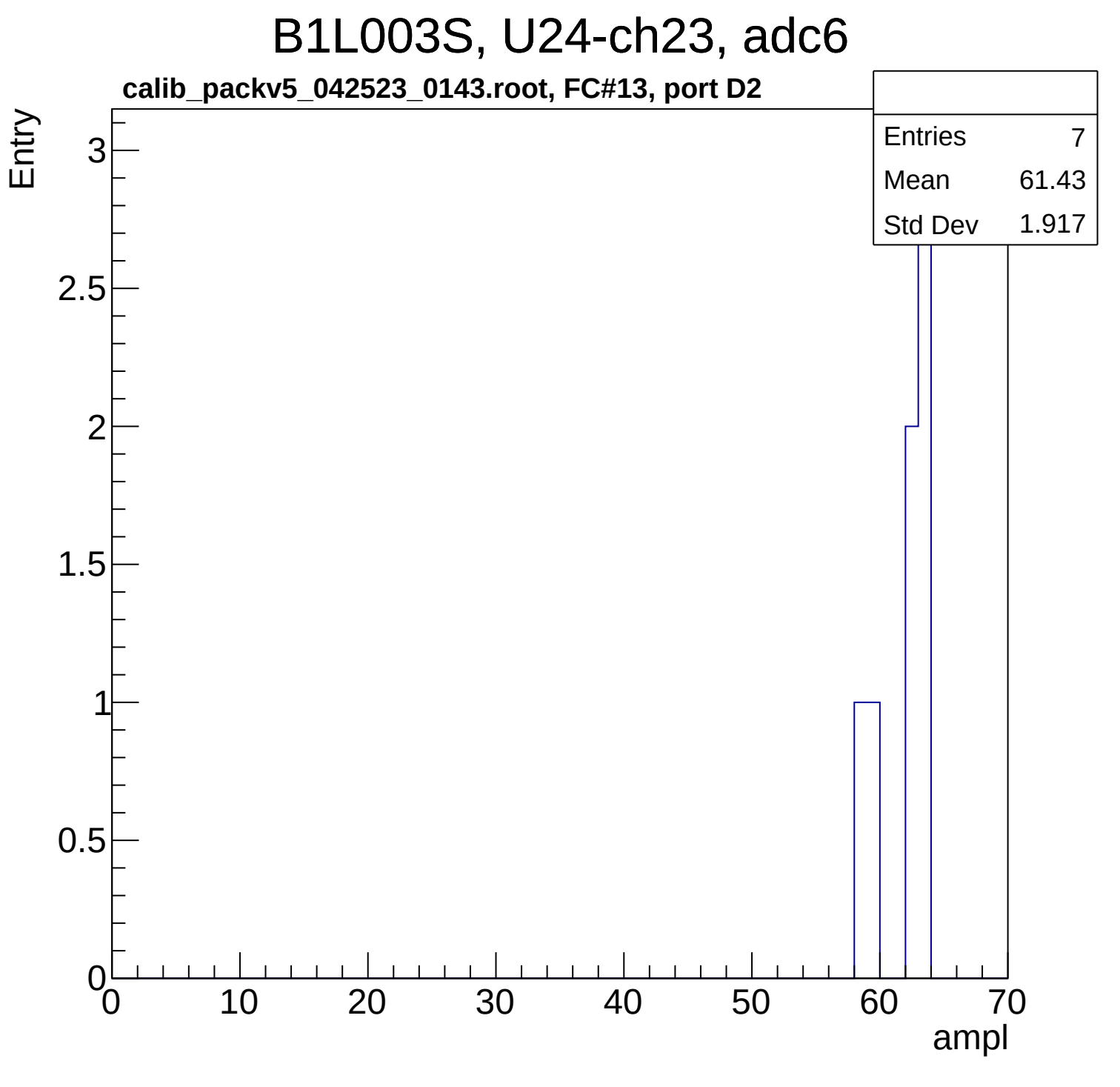
B1L003S, U24-ch23, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.43
Std Dev 1.917
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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60
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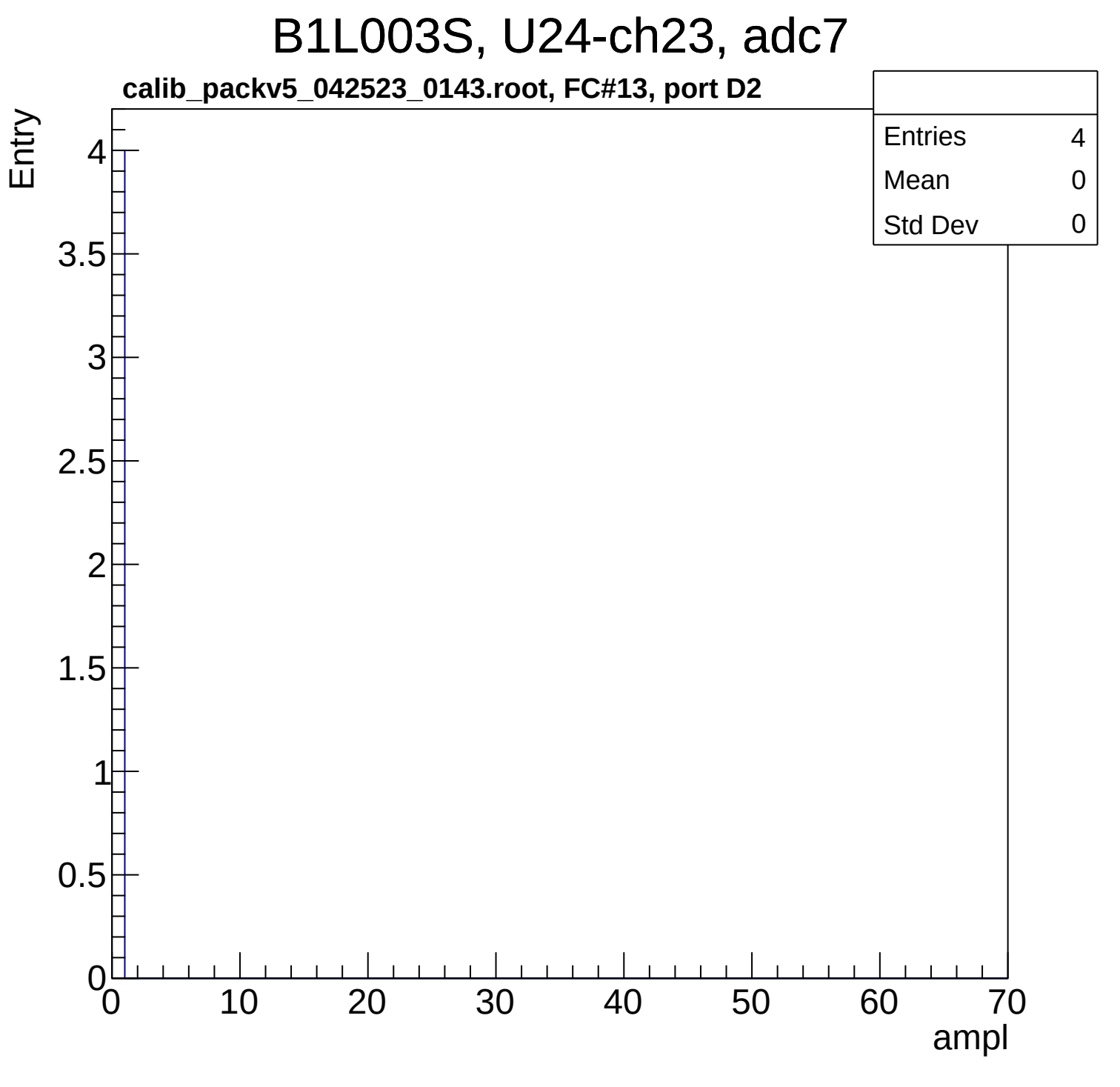
B1L003S, U24-ch23, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 0
Std Dev 0
Entry
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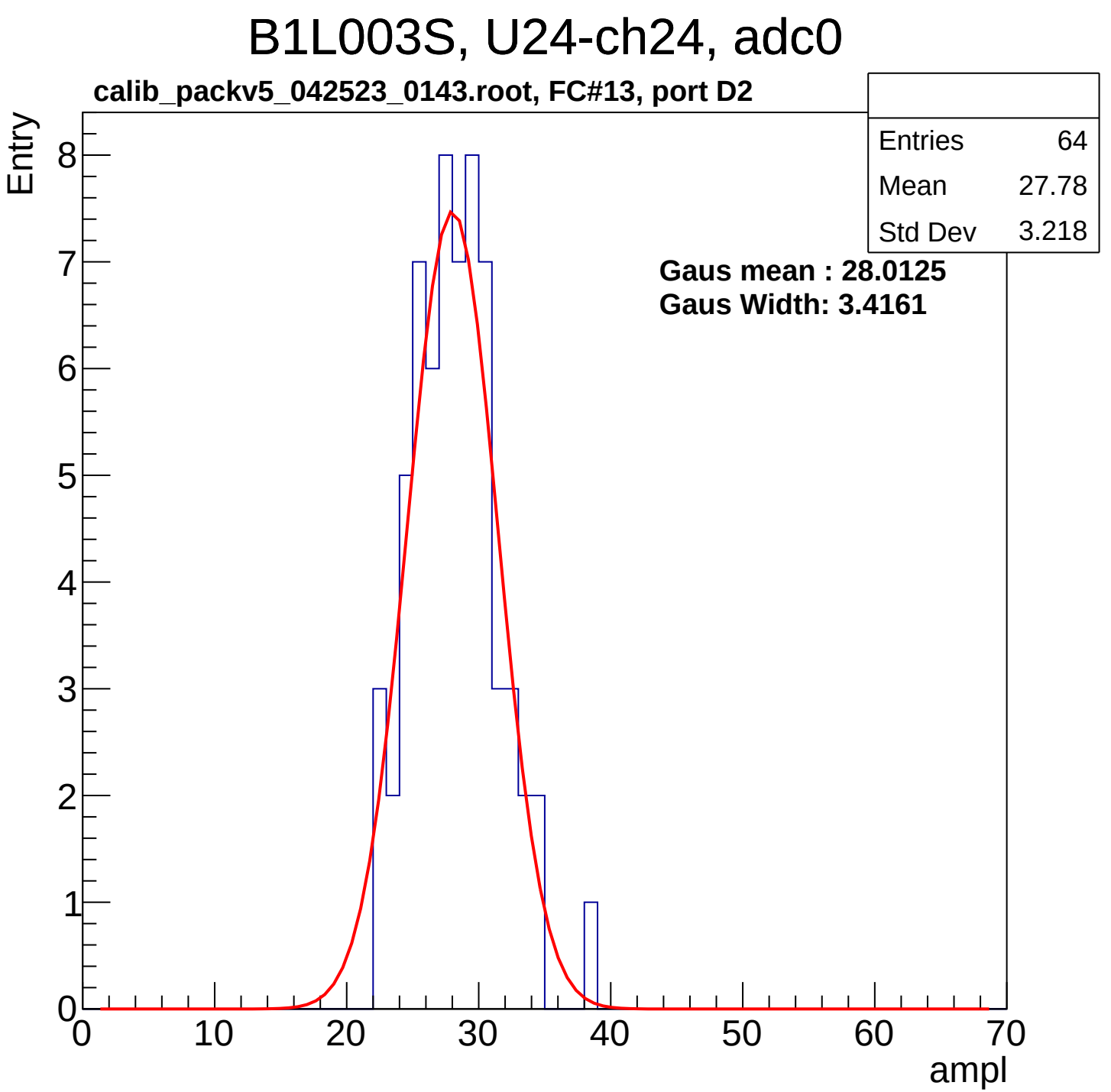
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch24, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 27.78
Std Dev 3.218
Gaus mean : 28.0125
Gaus Width: 3.4161
Entry
ampl
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1
2
3
4
5
6
7
8
0
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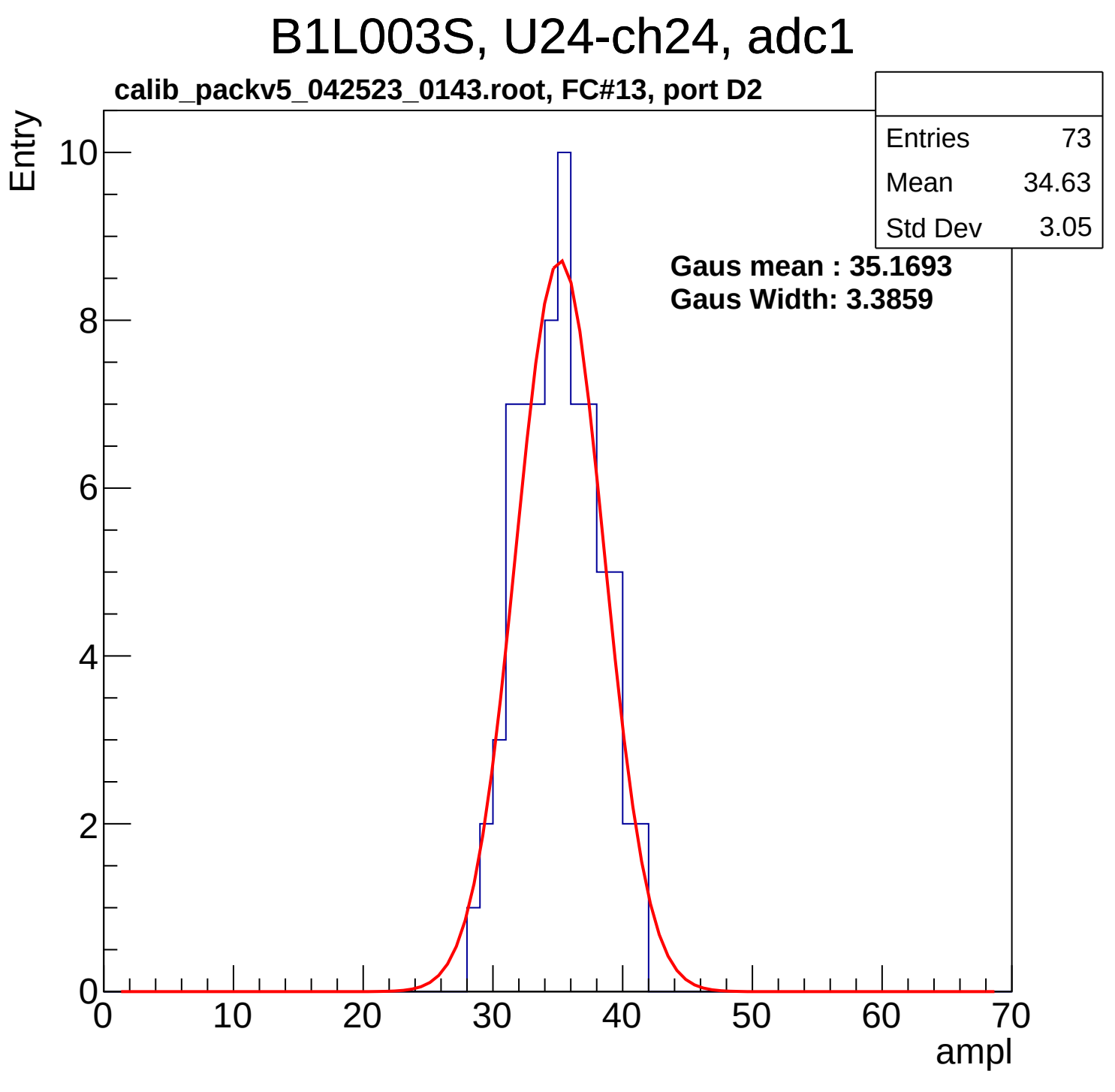
B1L003S, U24-ch24, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 34.63
Std Dev 3.05
Gaus mean : 35.1693
Gaus Width: 3.3859
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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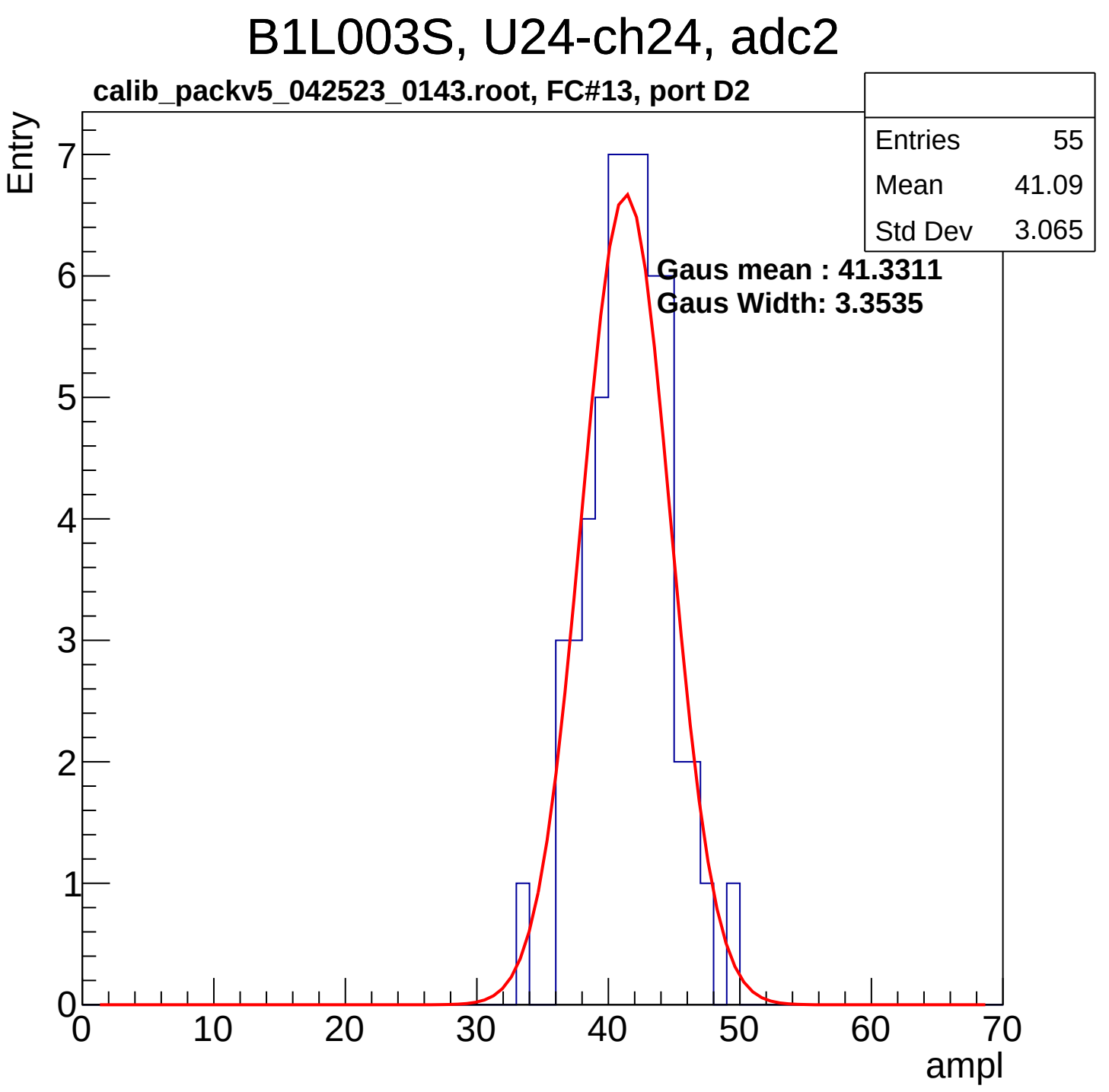
B1L003S, U24-ch24, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.09
Std Dev 3.065
Gaus mean : 41.3311
Gaus Width: 3.3535
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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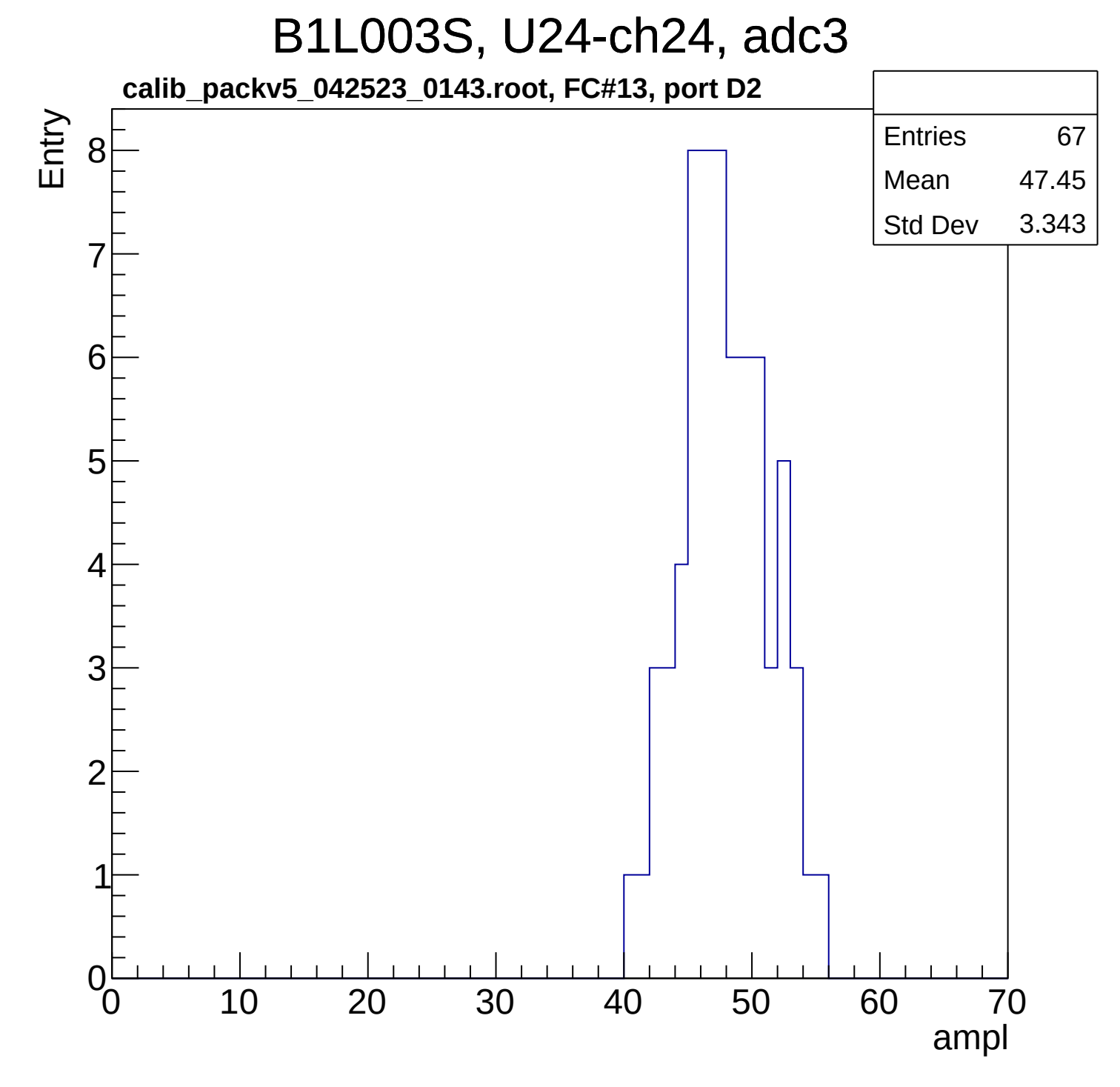
B1L003S, U24-ch24, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 47.45
Std Dev 3.343
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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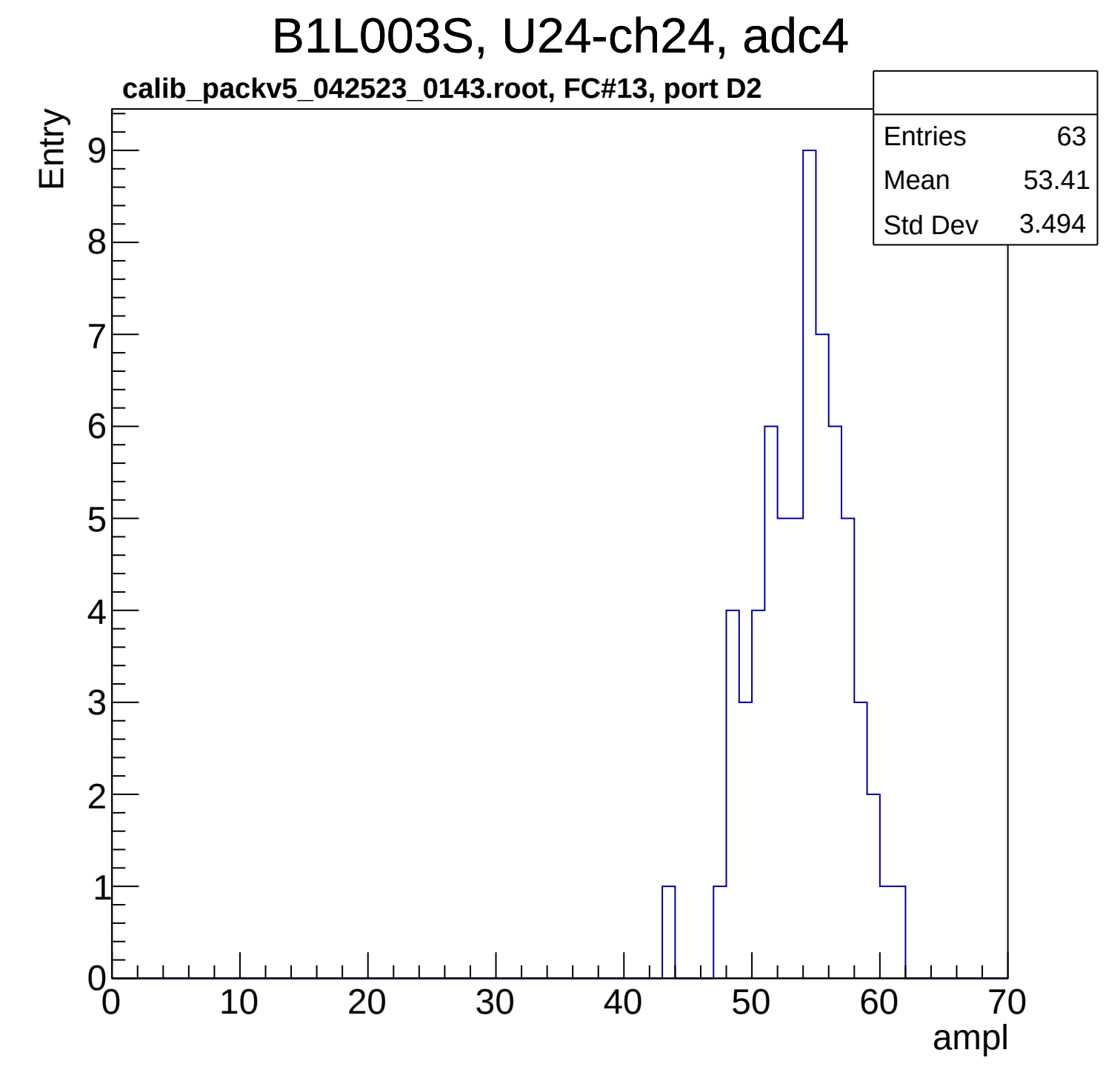
B1L003S, U24-ch24, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 53.41
Std Dev 3.494
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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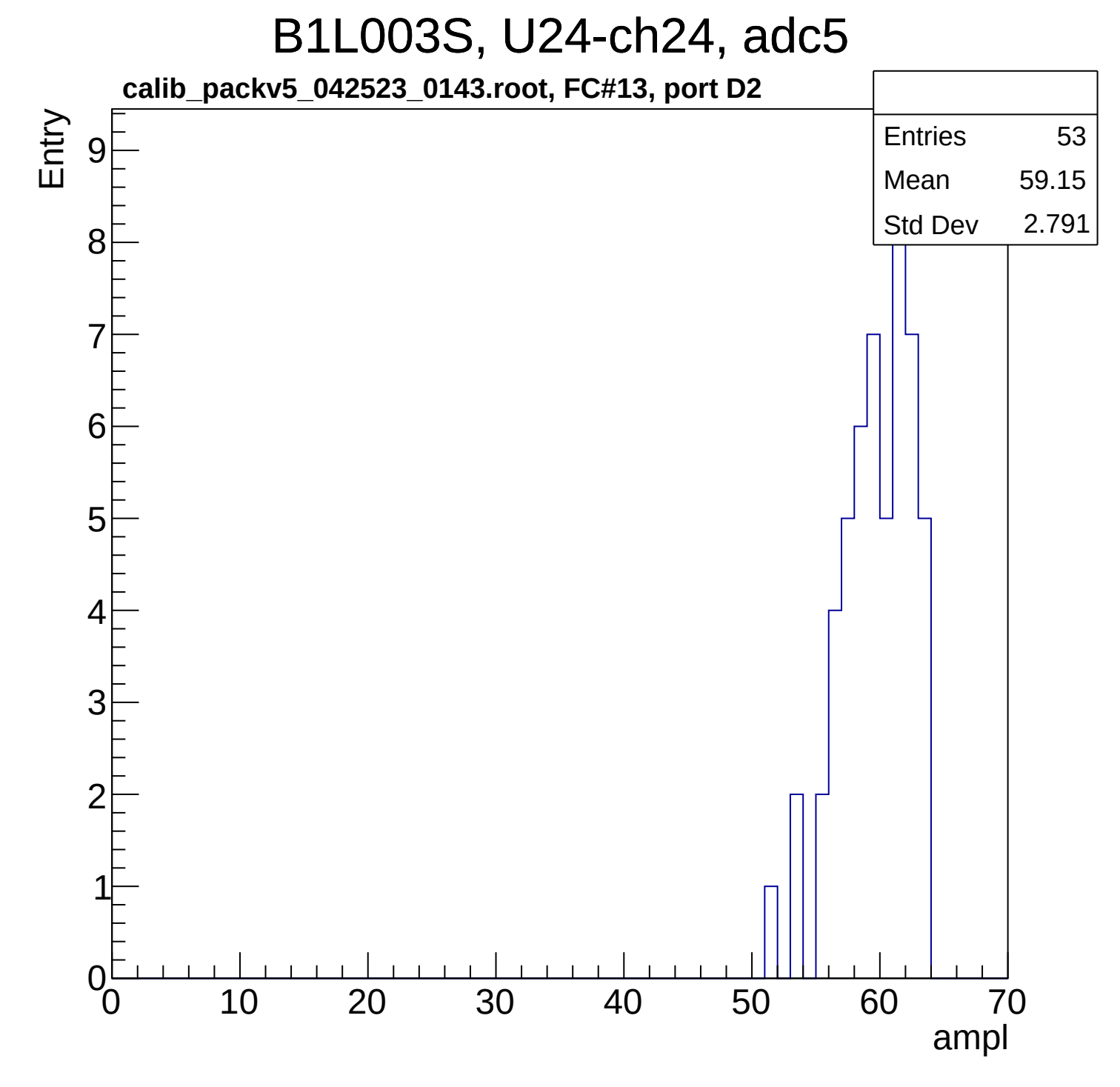
B1L003S, U24-ch24, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.15
Std Dev 2.791
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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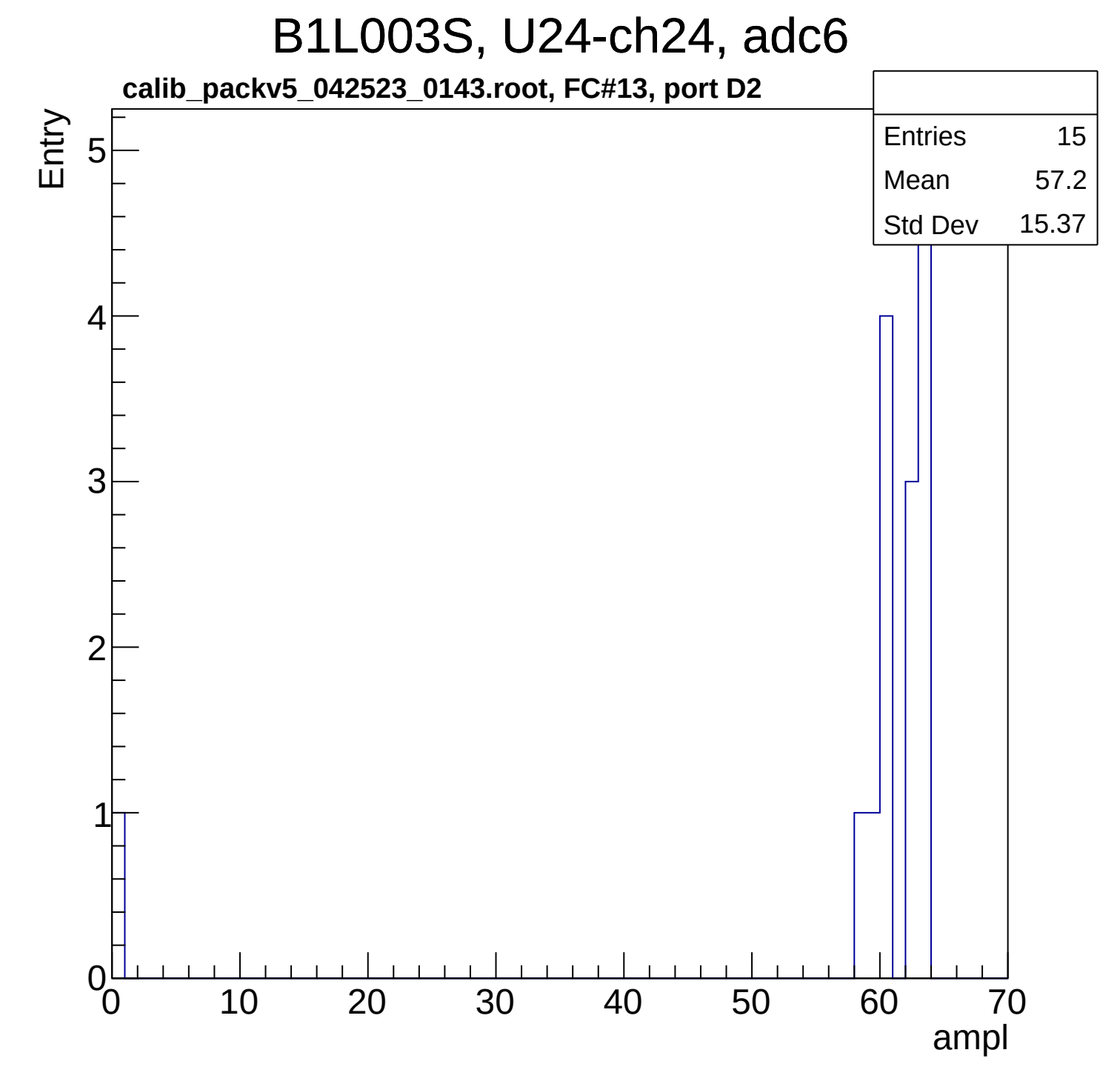
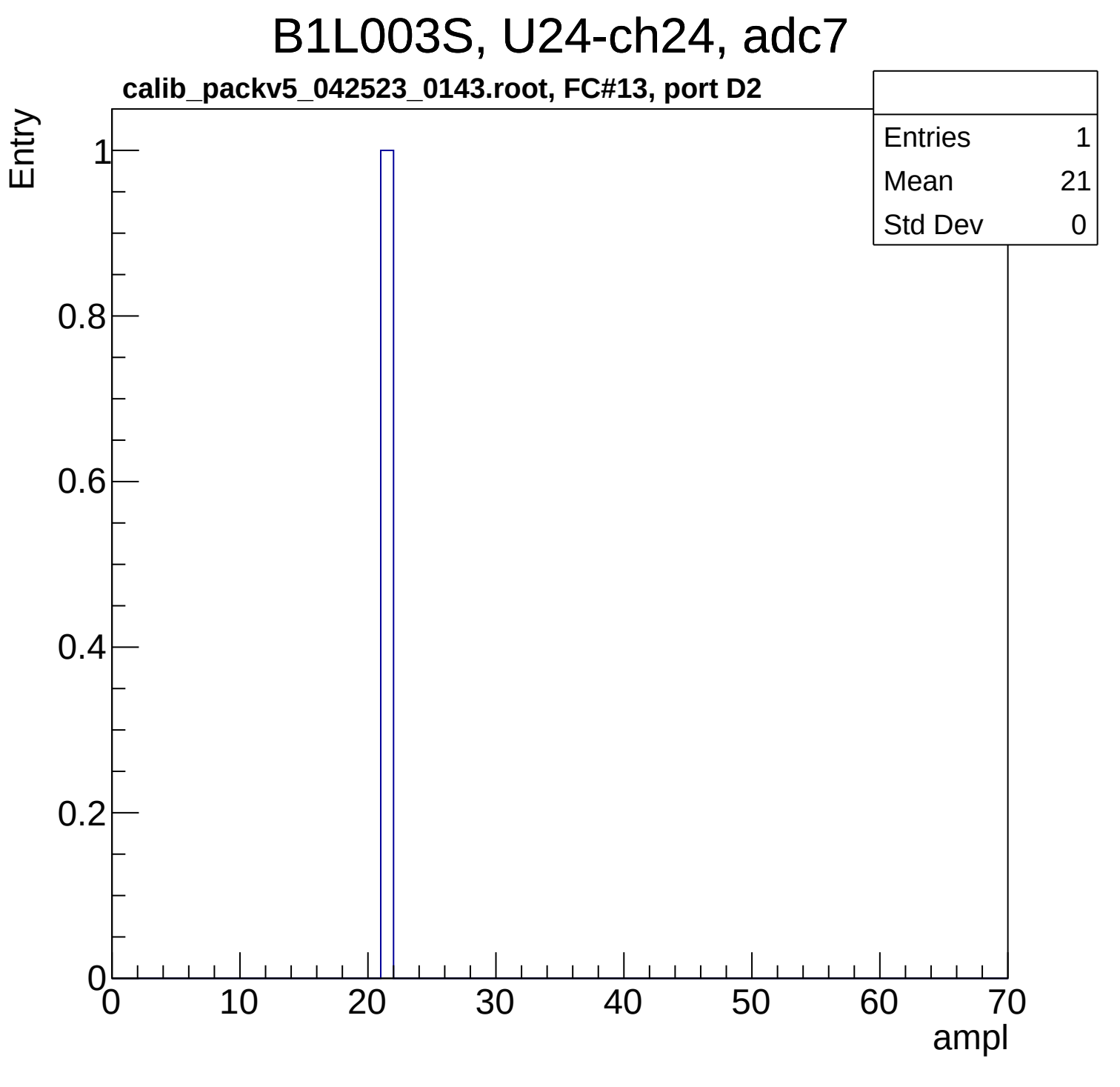

B1L003S, U24-ch24, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.2
Std Dev 15.37
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch24, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
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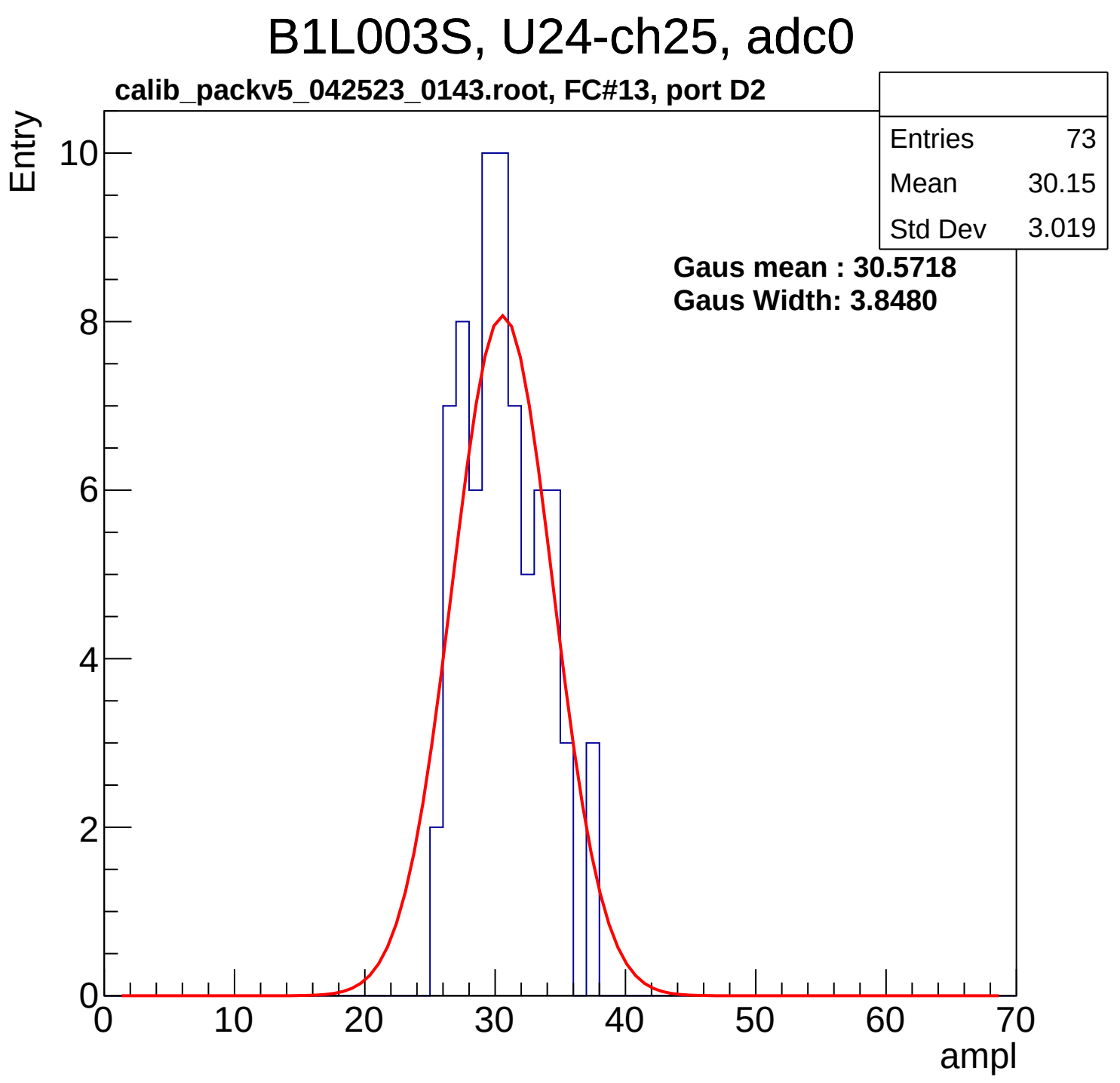
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch25, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 30.15
Std Dev 3.019
Gaus mean : 30.5718
Gaus Width: 3.8480
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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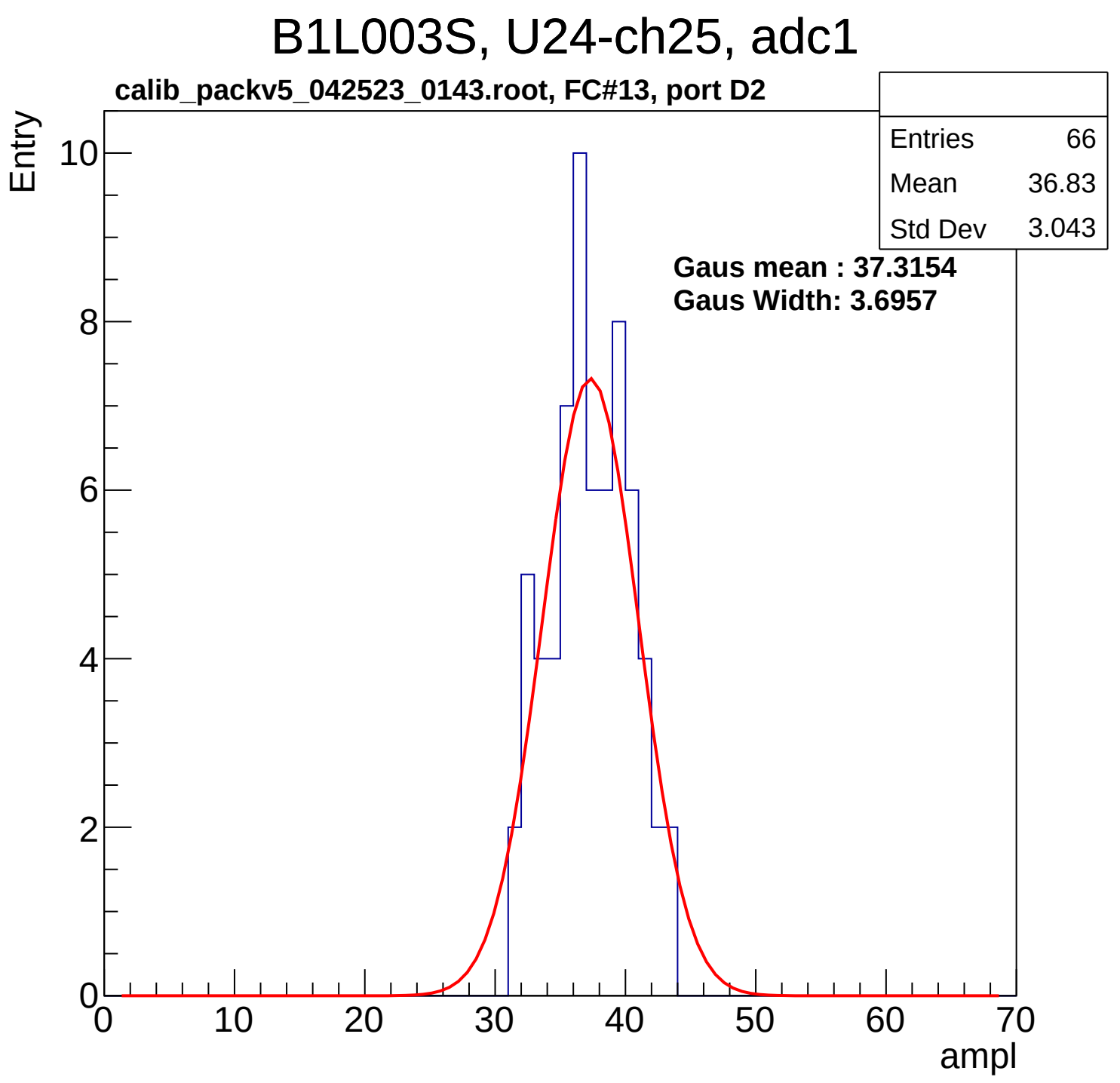
B1L003S, U24-ch25, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 36.83
Std Dev 3.043
Gaus mean : 37.3154
Gaus Width: 3.6957
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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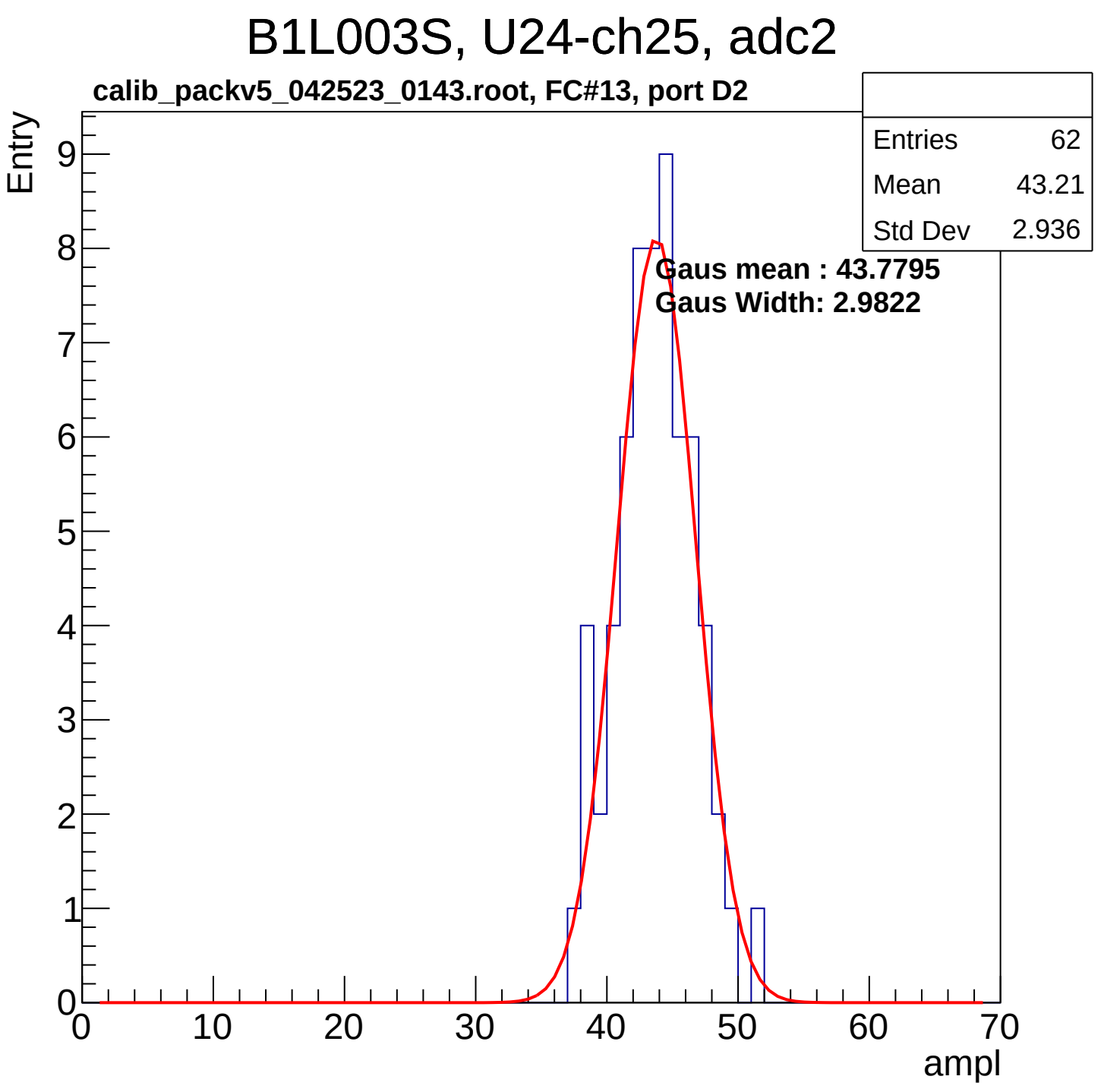
B1L003S, U24-ch25, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.21
Std Dev 2.936
Gaus mean : 43.7795
Gaus Width: 2.9822
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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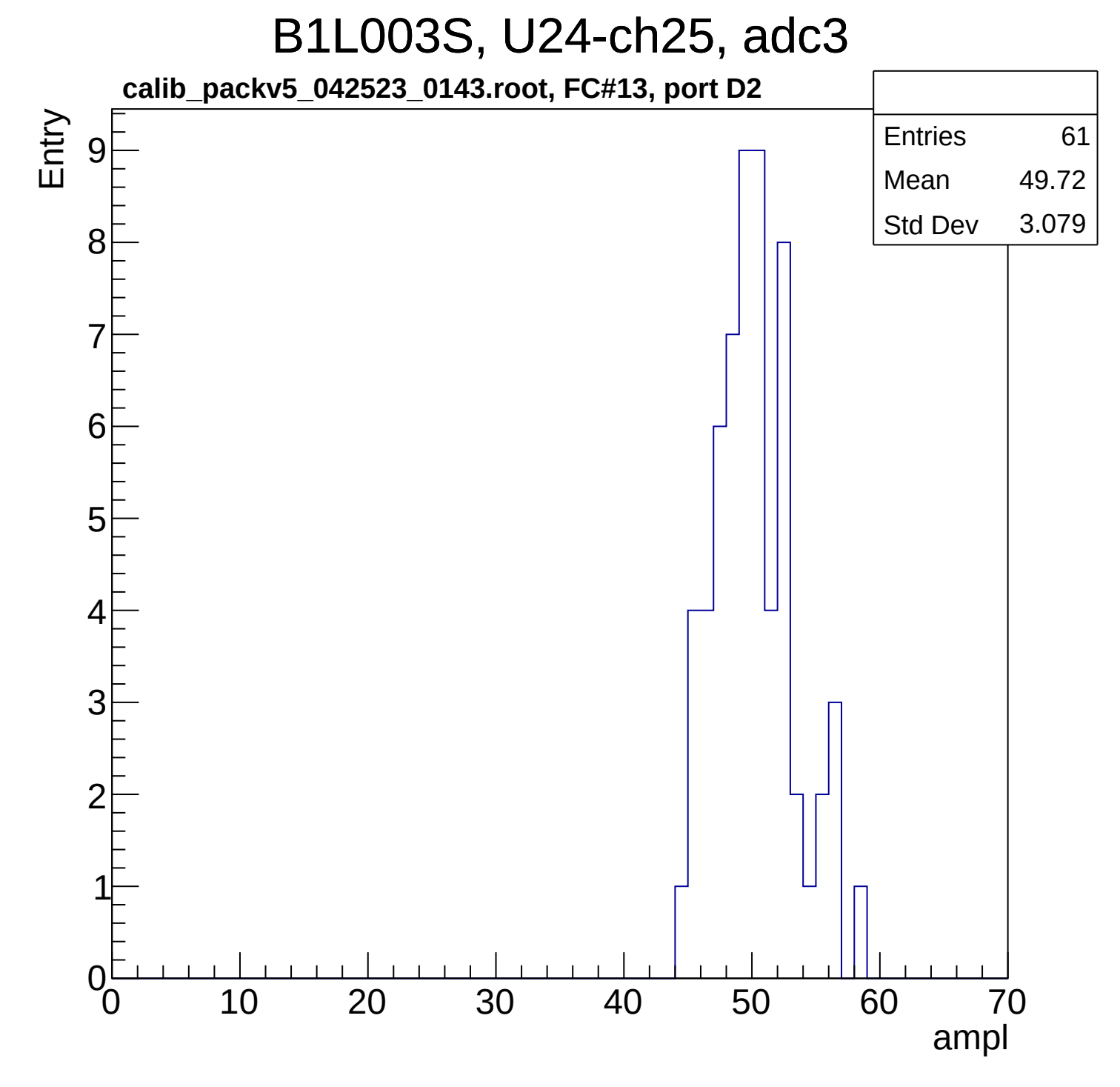
B1L003S, U24-ch25, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.72
Std Dev 3.079
Entry
ampl
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4
5
6
7
8
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0
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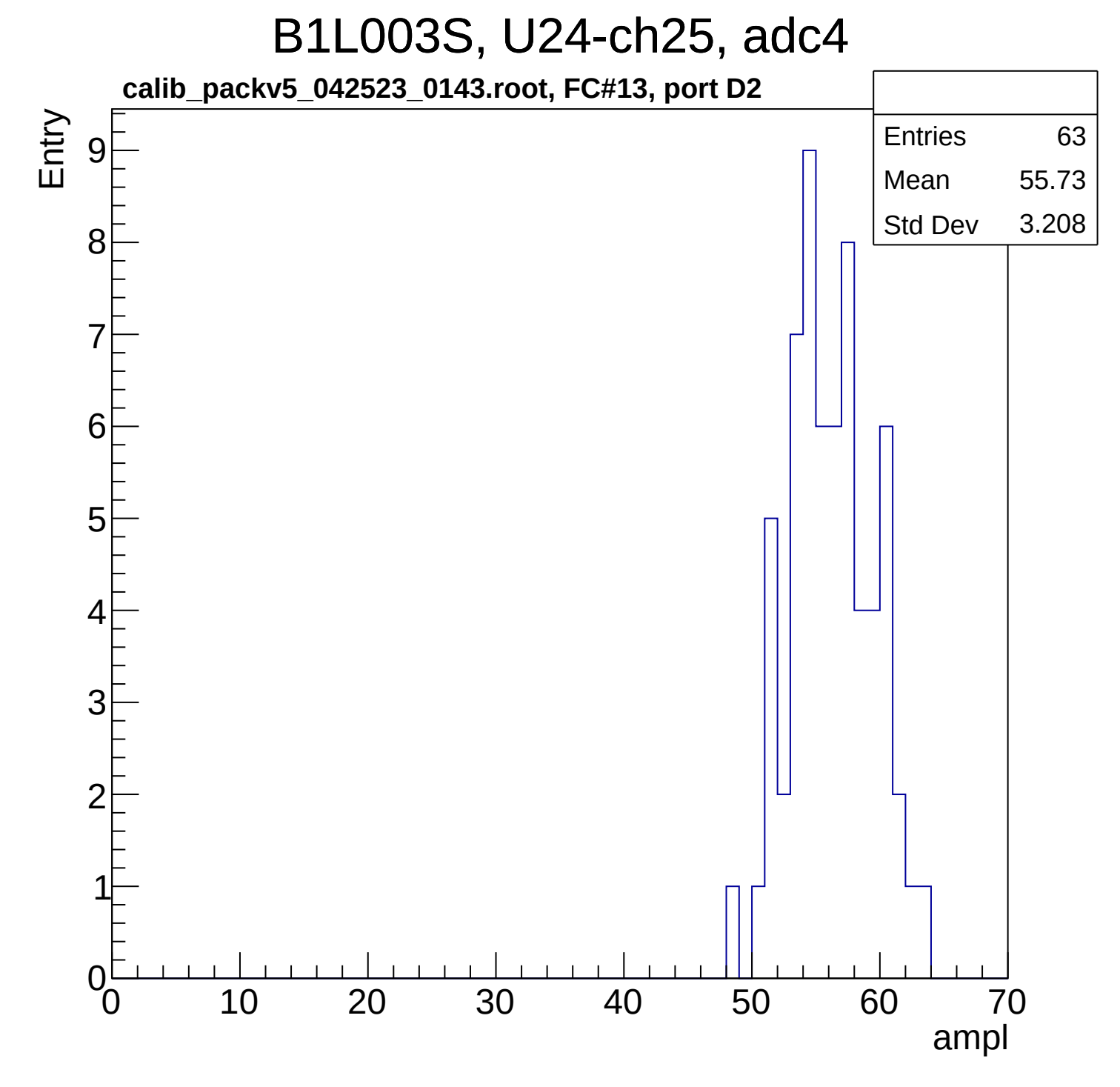
B1L003S, U24-ch25, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.73
Std Dev 3.208
Entry
ampl
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2
3
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5
6
7
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0
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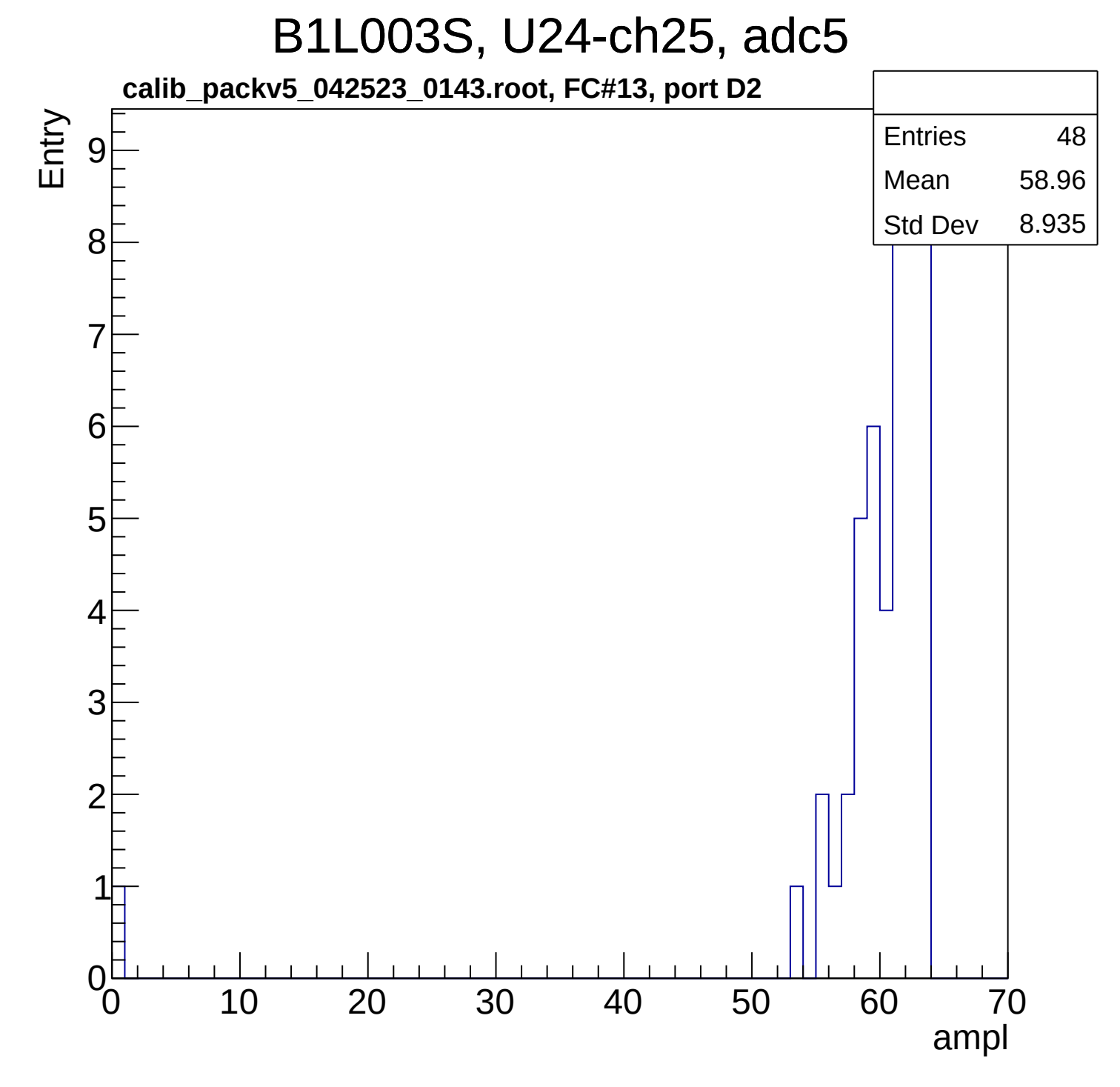
B1L003S, U24-ch25, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.96
Std Dev 8.935
Entry
ampl
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4
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6
7
8
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0
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60
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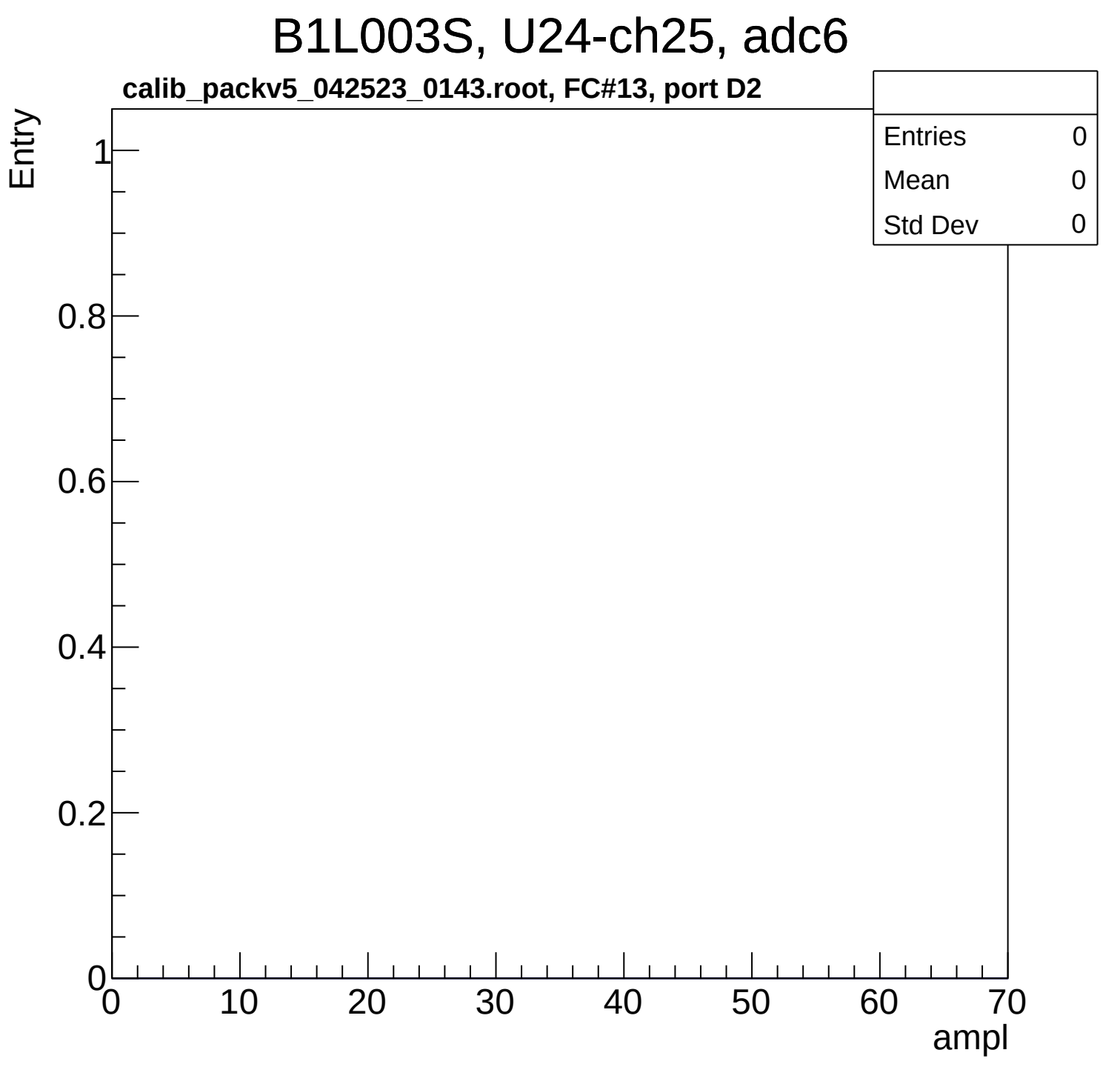
B1L003S, U24-ch25, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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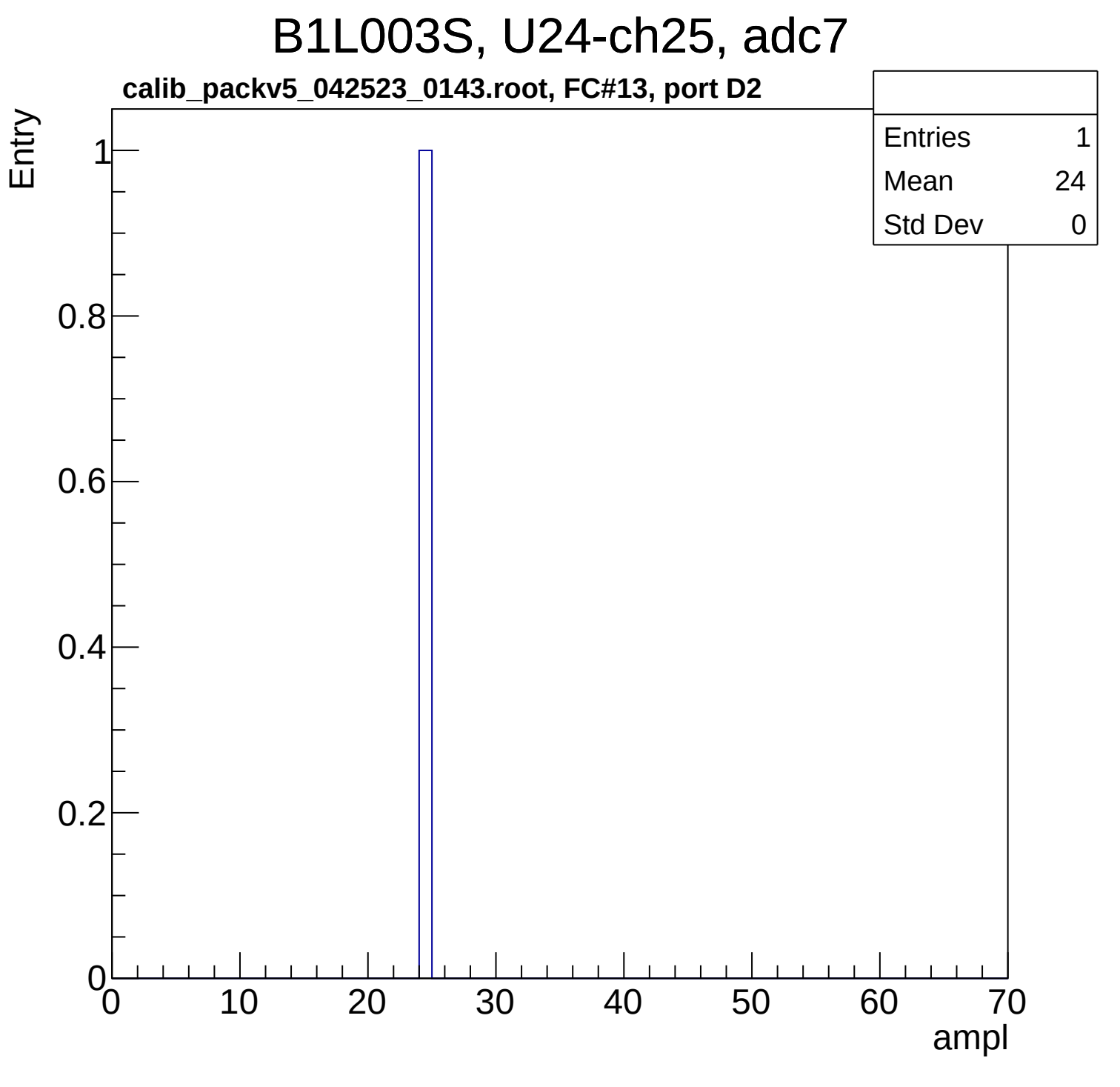
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch25, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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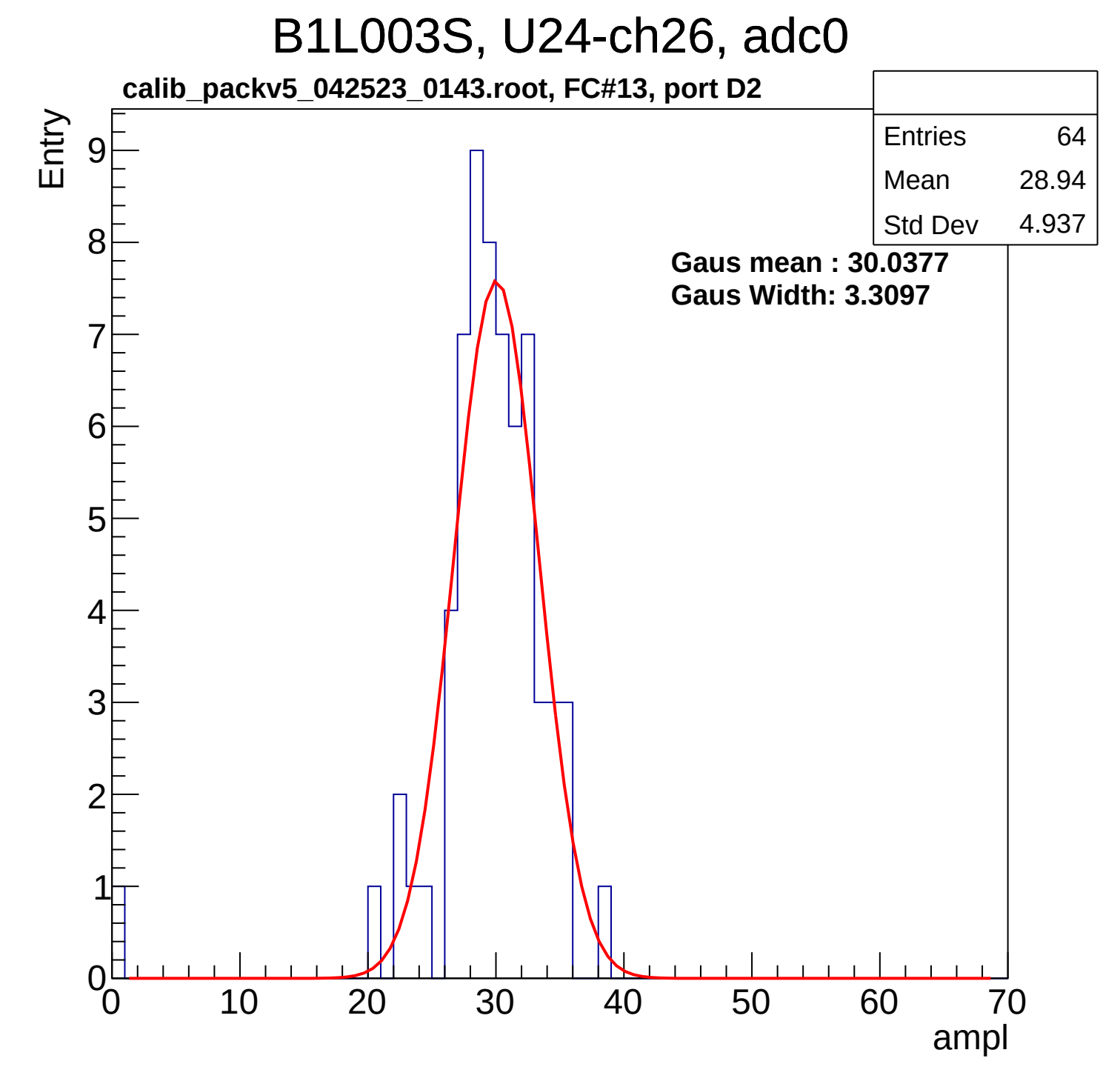
30
40
50
60
70
ampl


B1L003S, U24-ch26, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 28.94
Std Dev 4.937
Gaus mean : 30.0377
Gaus Width: 3.3097
Entry
ampl
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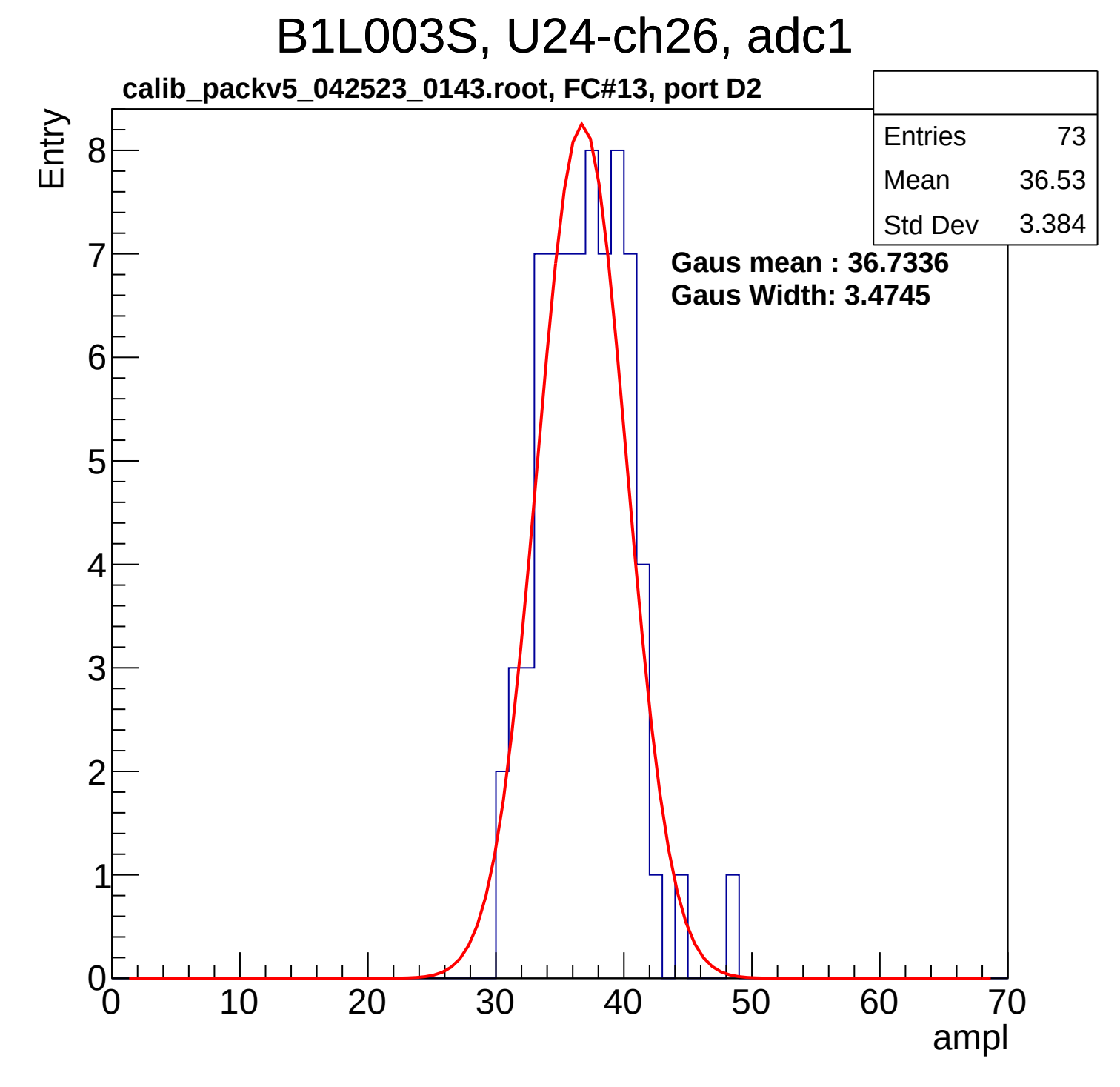
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch26, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.53
Std Dev 3.384
Gaus mean : 36.7336
Gaus Width: 3.4745
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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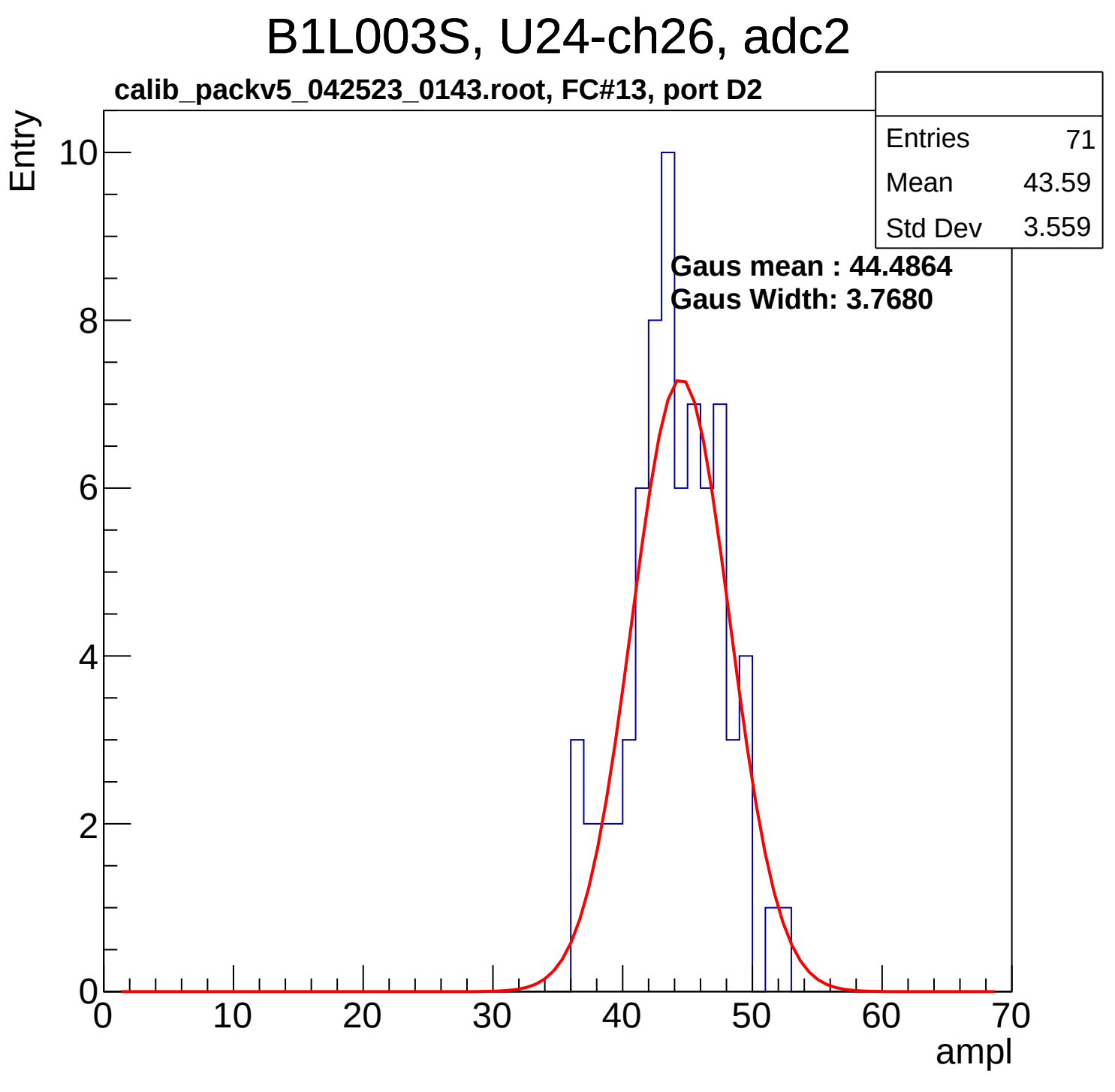
B1L003S, U24-ch26, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 43.59
Std Dev 3.559
Gaus mean : 44.4864
Gaus Width: 3.7680
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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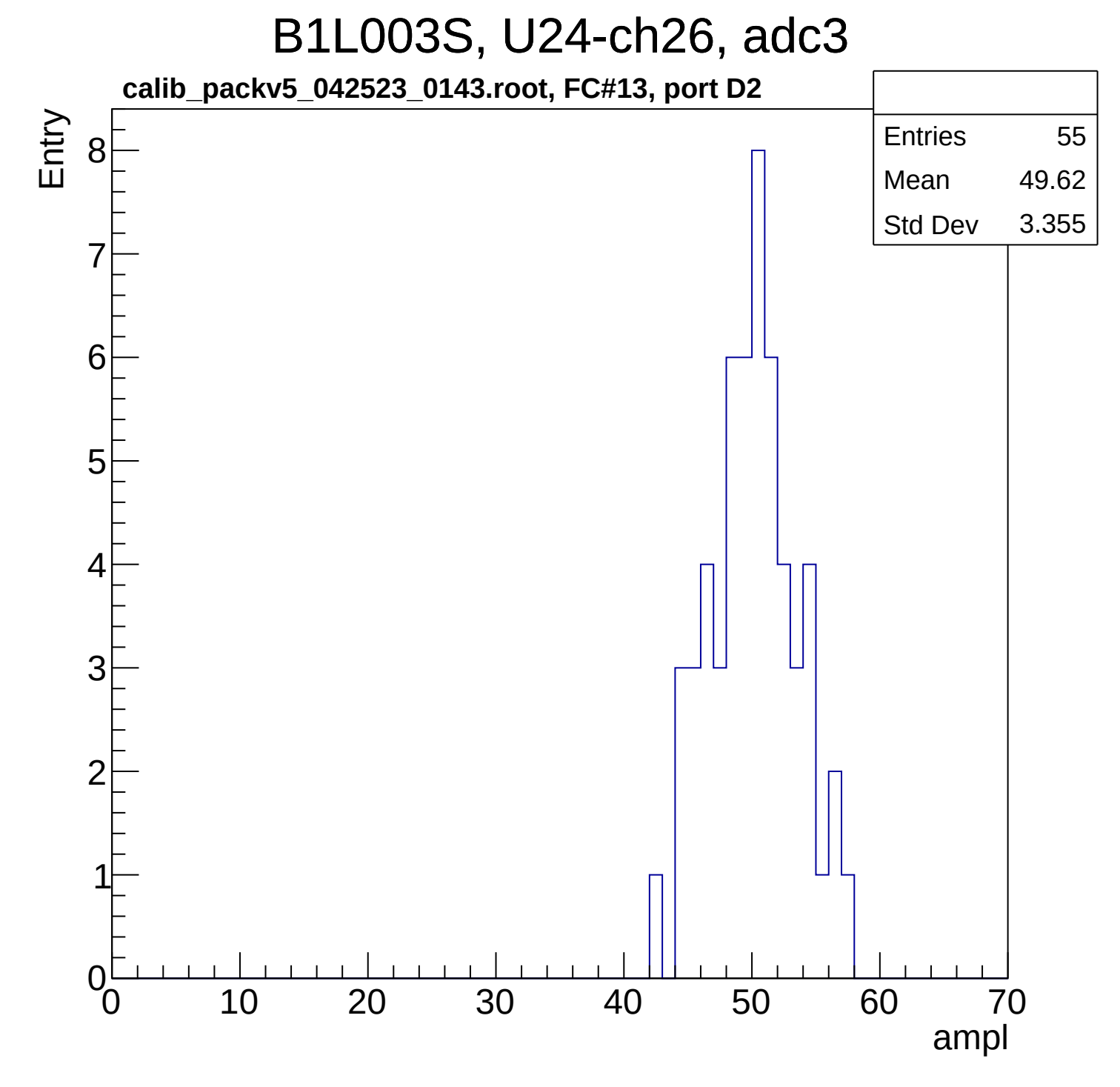
B1L003S, U24-ch26, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 49.62
Std Dev 3.355
Entry
ampl
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2
3
4
5
6
7
8
0
10
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60
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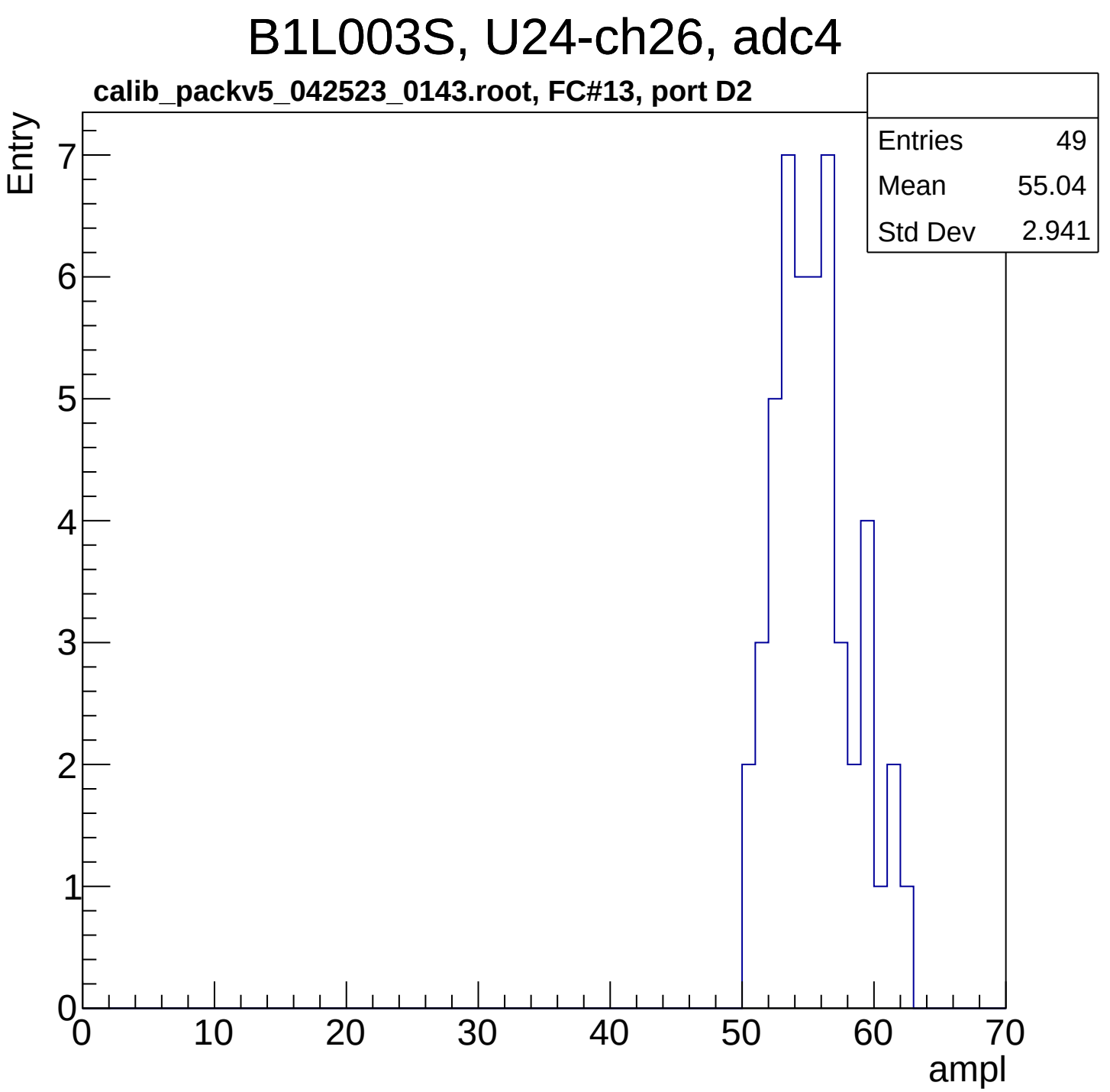
B1L003S, U24-ch26, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 55.04
Std Dev 2.941
Entry
ampl
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1
2
3
4
5
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7
0
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40
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60
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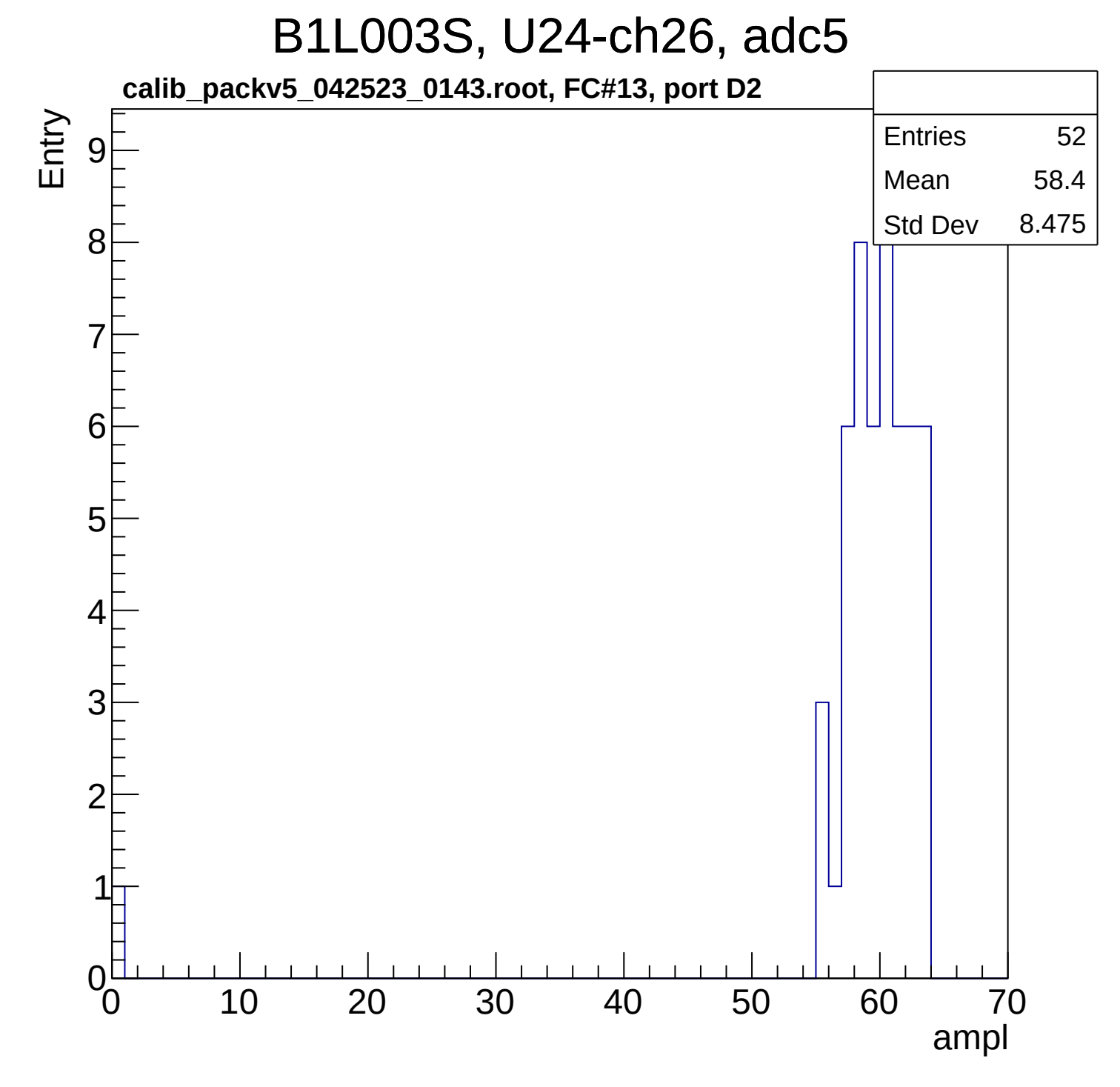
B1L003S, U24-ch26, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.4
Std Dev 8.475
Entry
ampl
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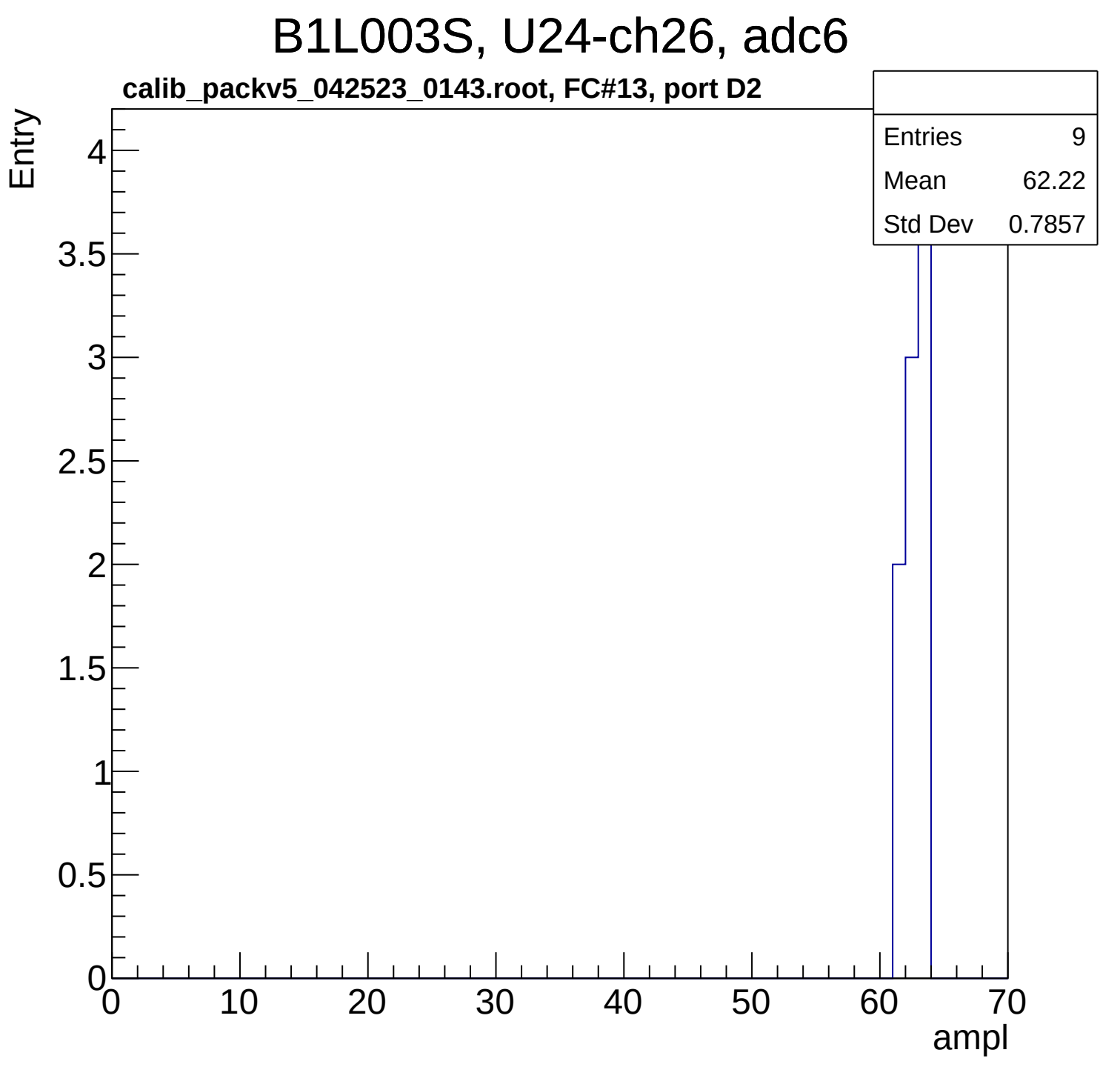
B1L003S, U24-ch26, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.22
Std Dev 0.7857
Entry
ampl
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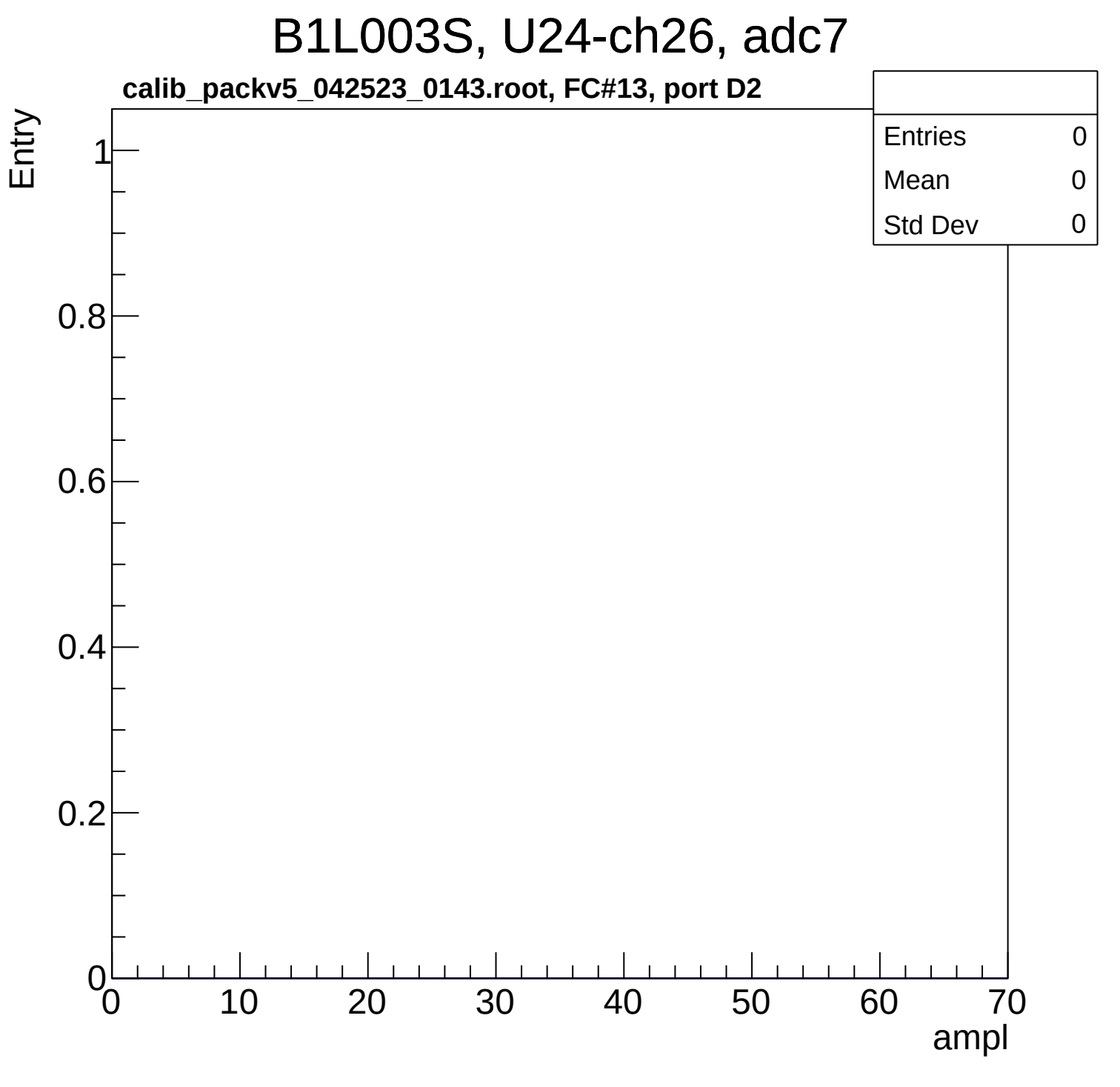
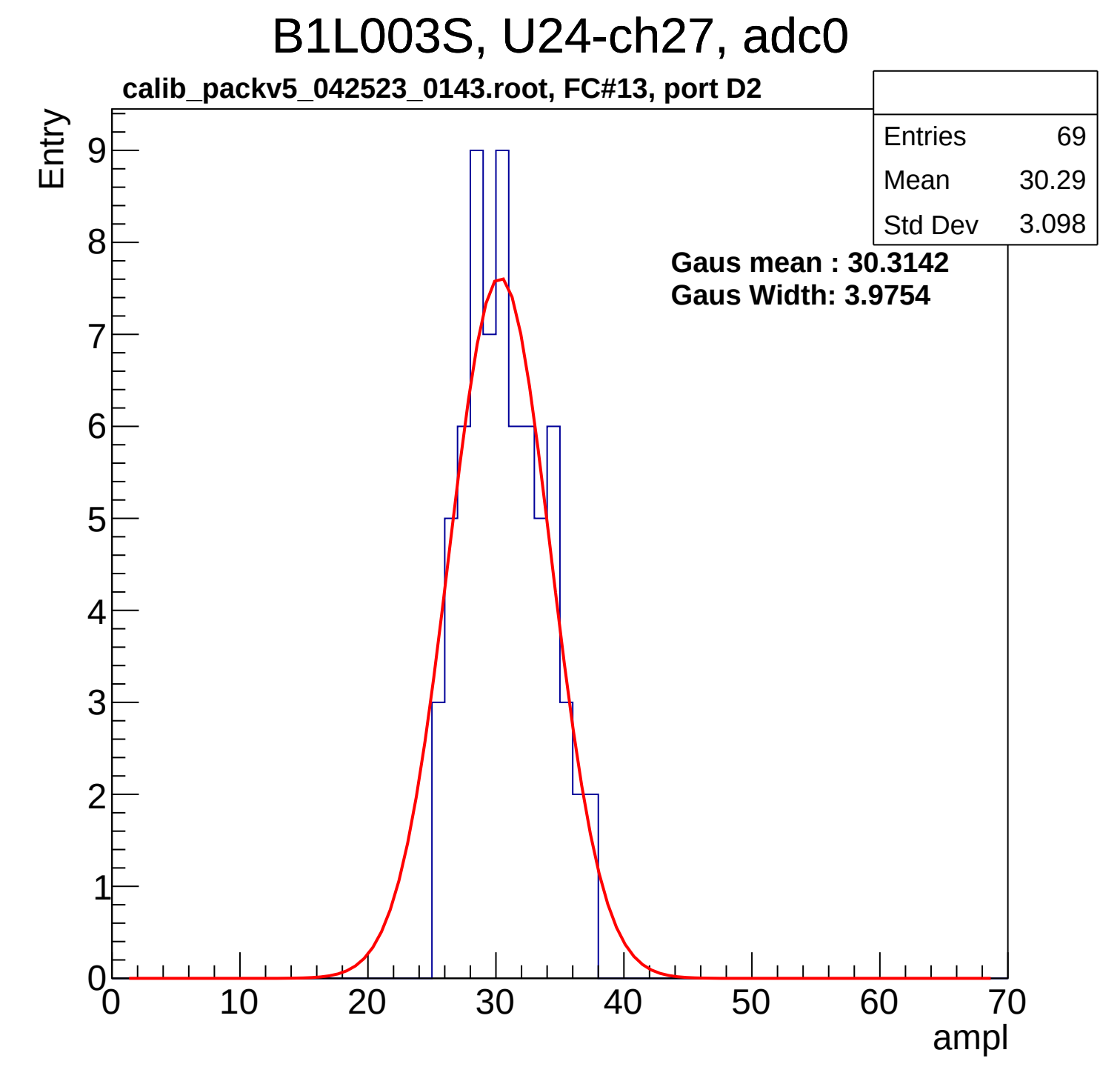
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U24-ch26, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch27, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 30.29
Std Dev 3.098
Gaus mean : 30.3142
Gaus Width: 3.9754
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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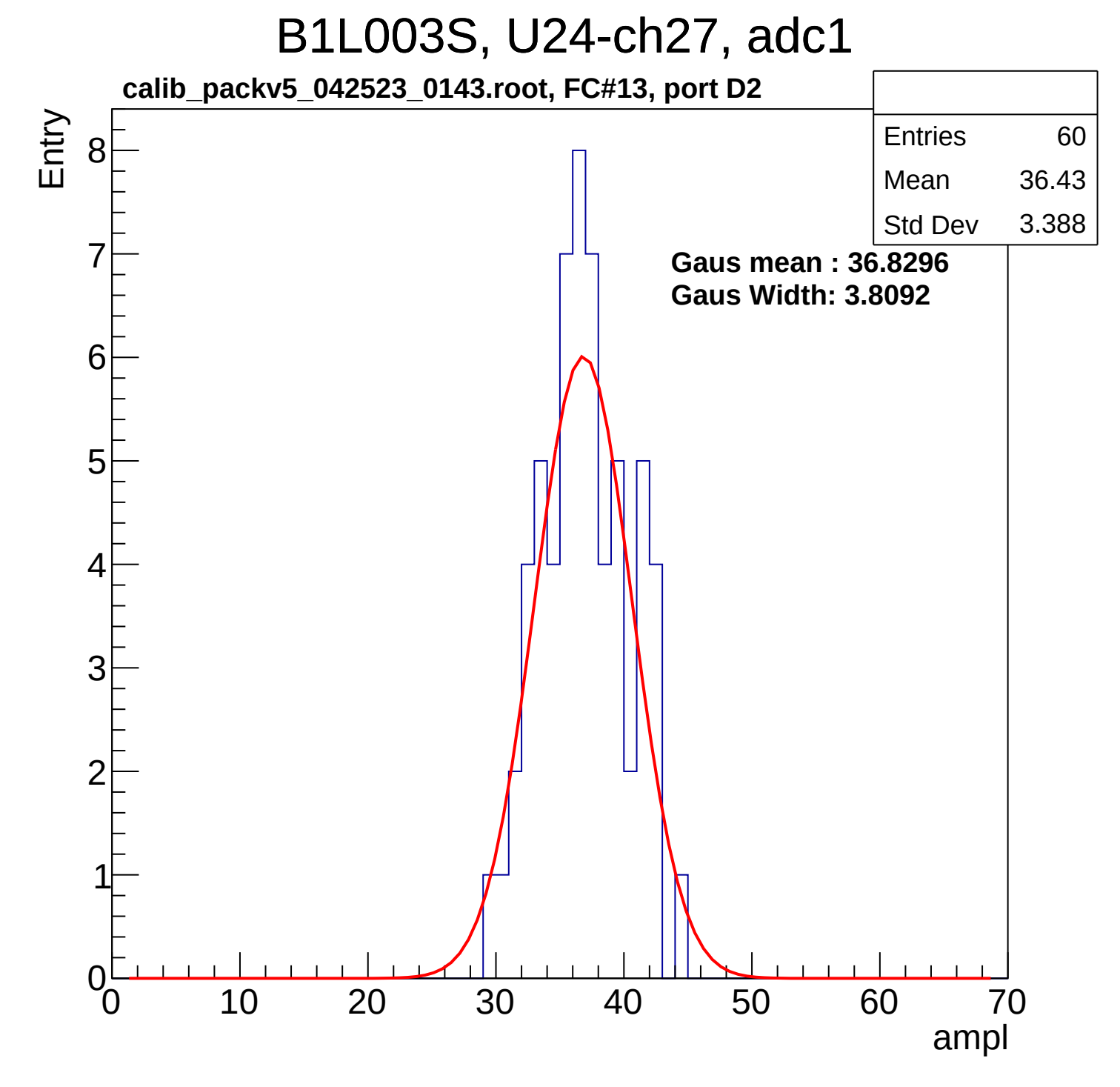
B1L003S, U24-ch27, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.43
Std Dev 3.388
Gaus mean : 36.8296
Gaus Width: 3.8092
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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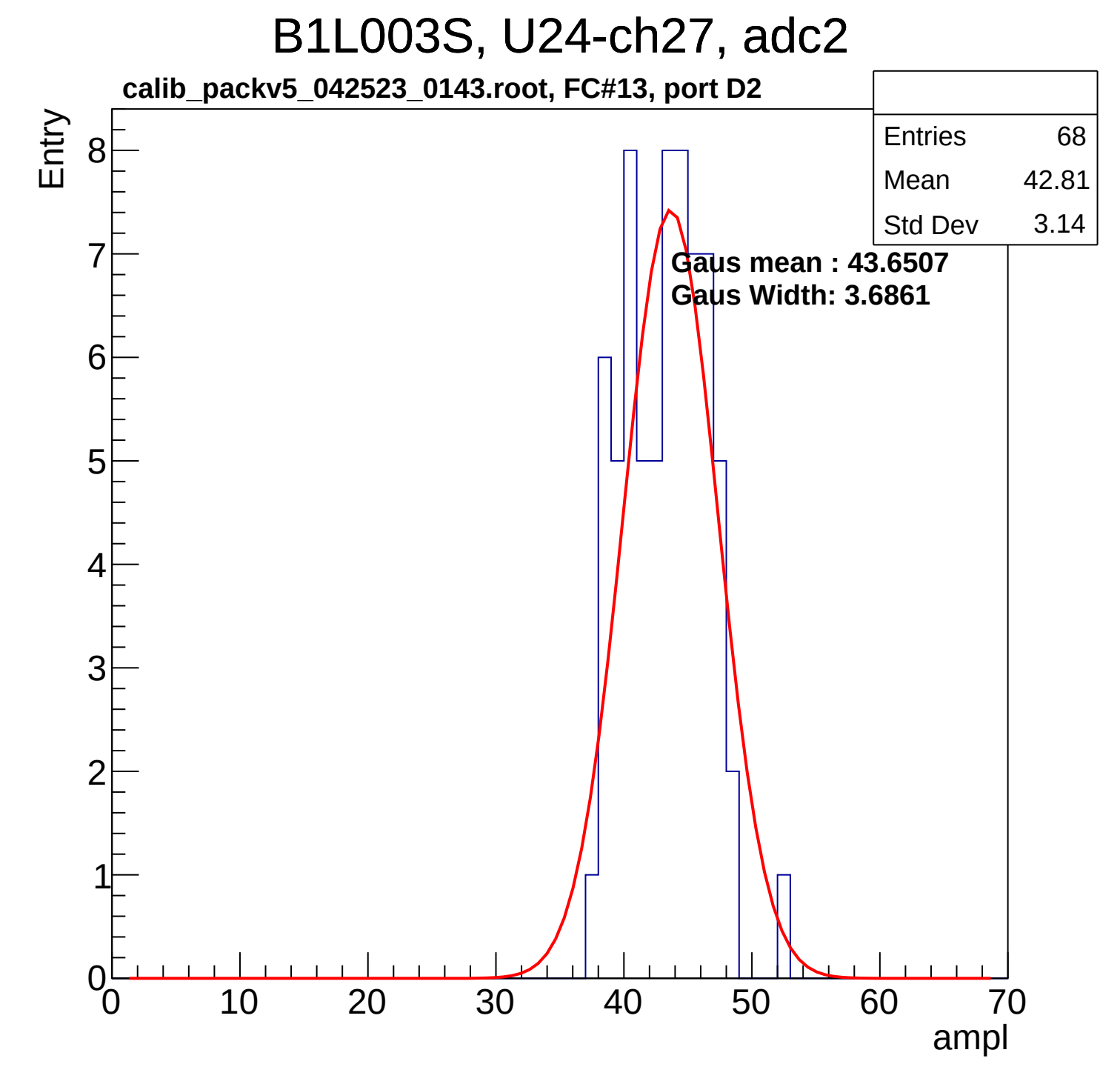
B1L003S, U24-ch27, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.81
Std Dev 3.14
Gaus mean : 43.6507
Gaus Width: 3.6861
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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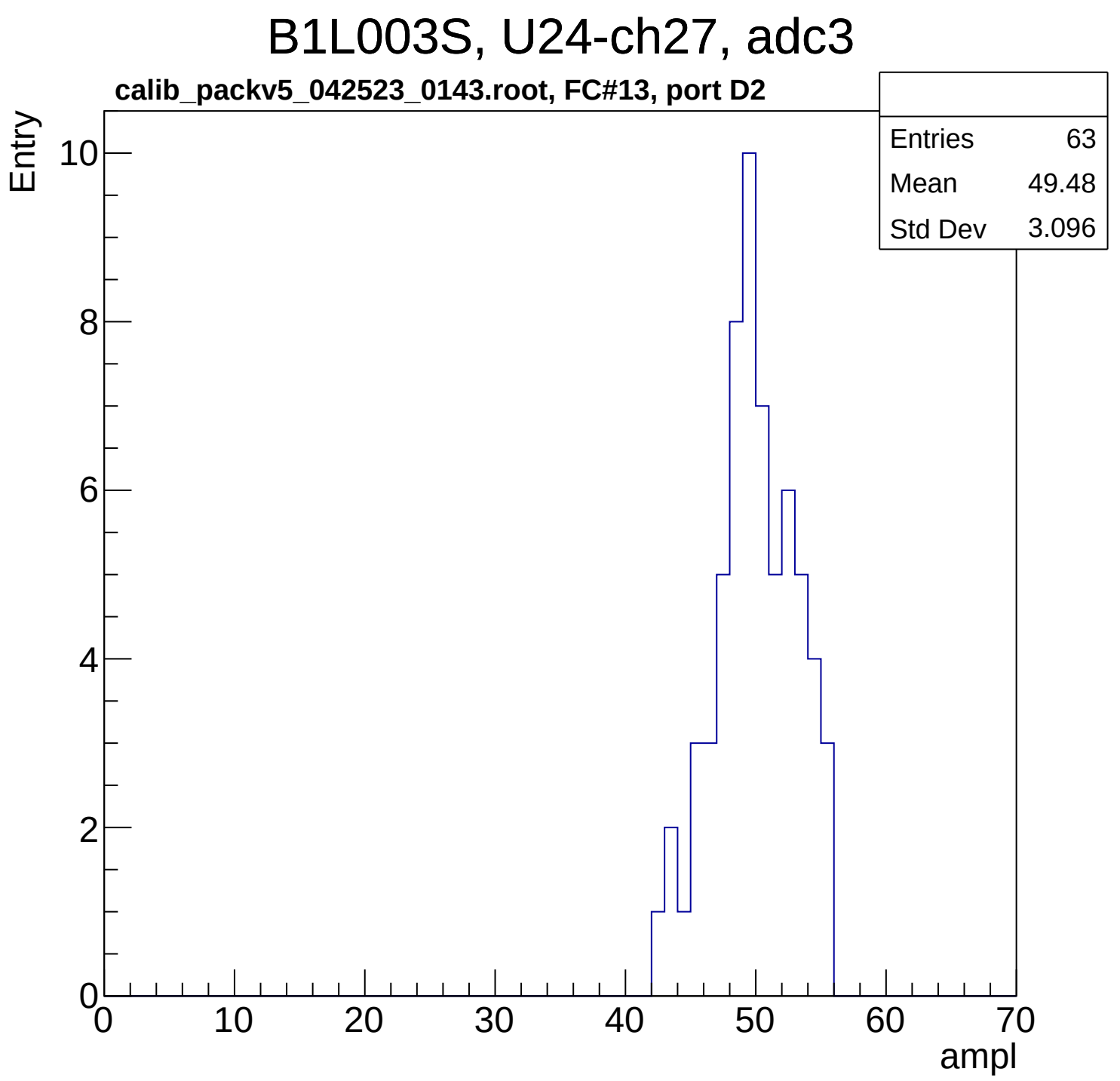
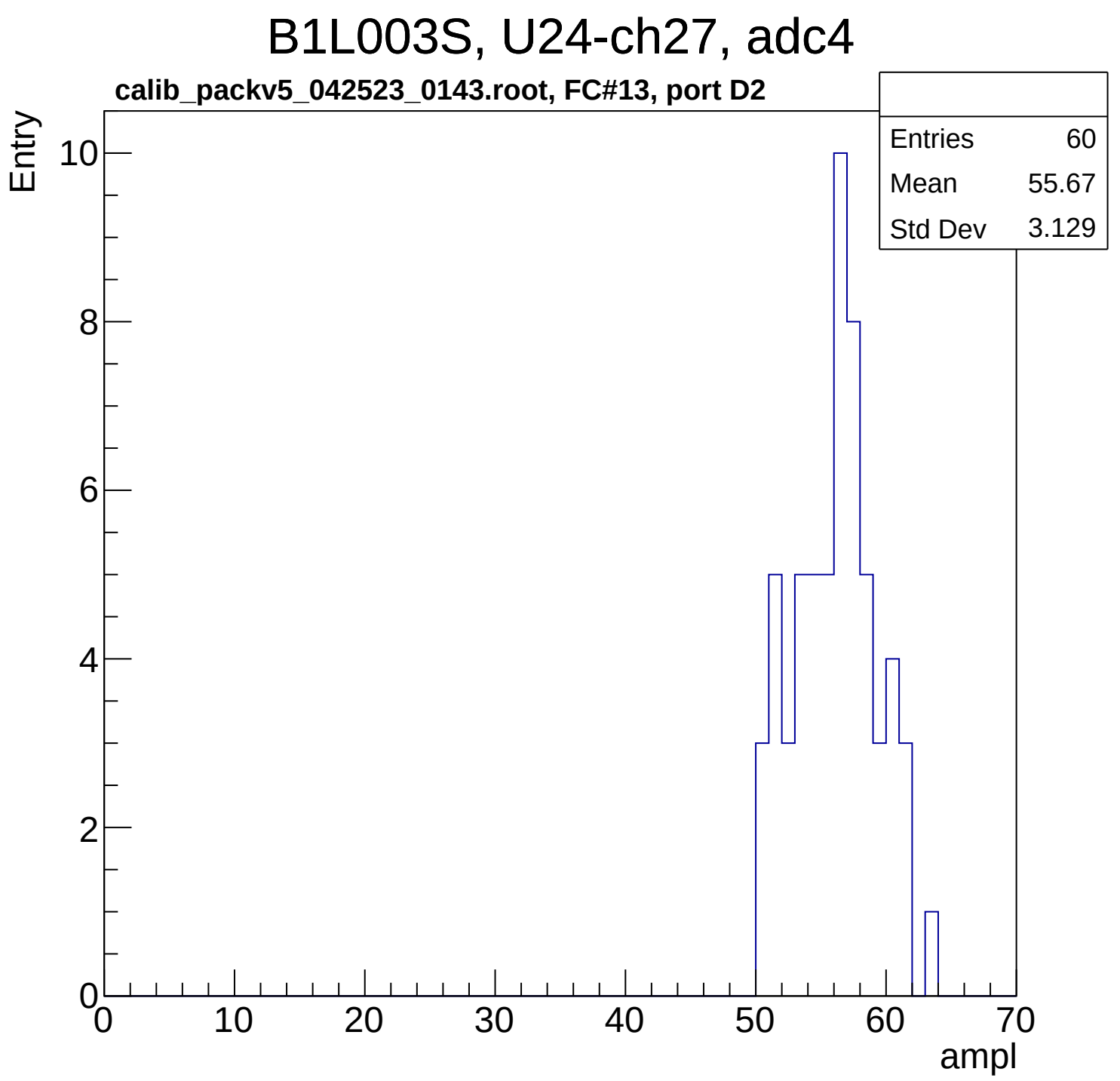
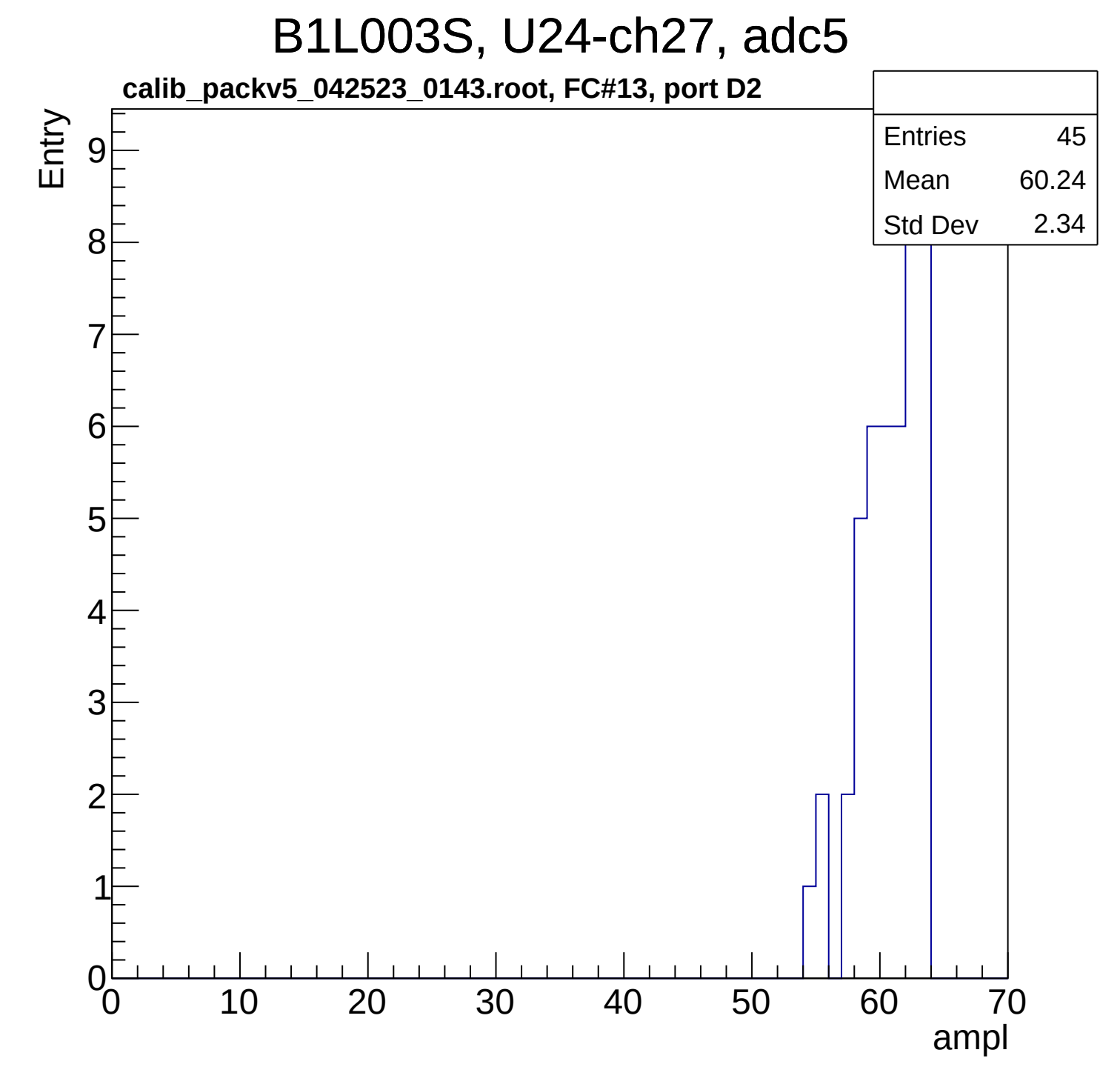

B1L003S, U24-ch27, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.48
Std Dev 3.096
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch27, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.67
Std Dev 3.129
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch27, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 60.24
Std Dev 2.34
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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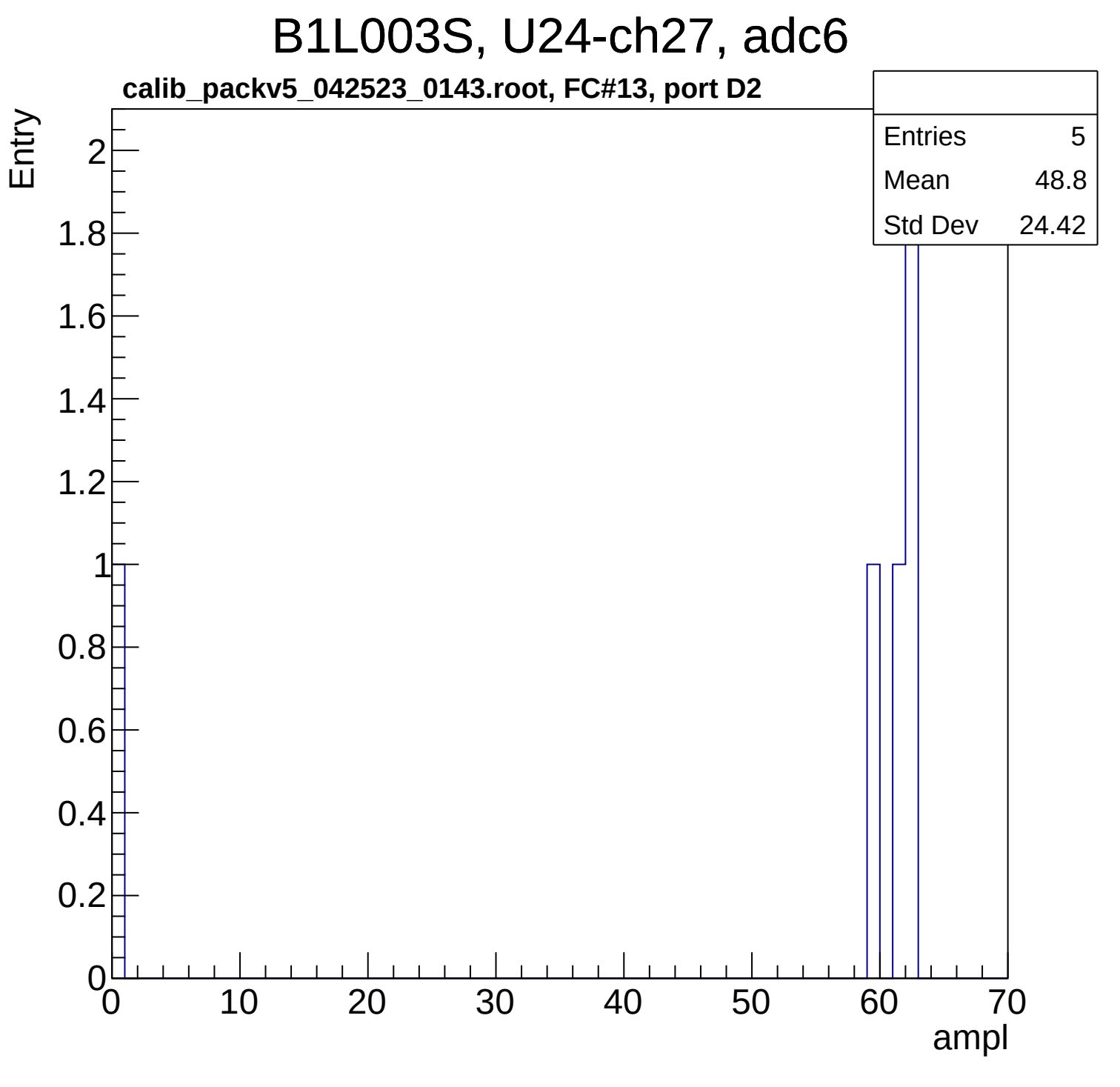
B1L003S, U24-ch27, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 48.8
Std Dev 24.42
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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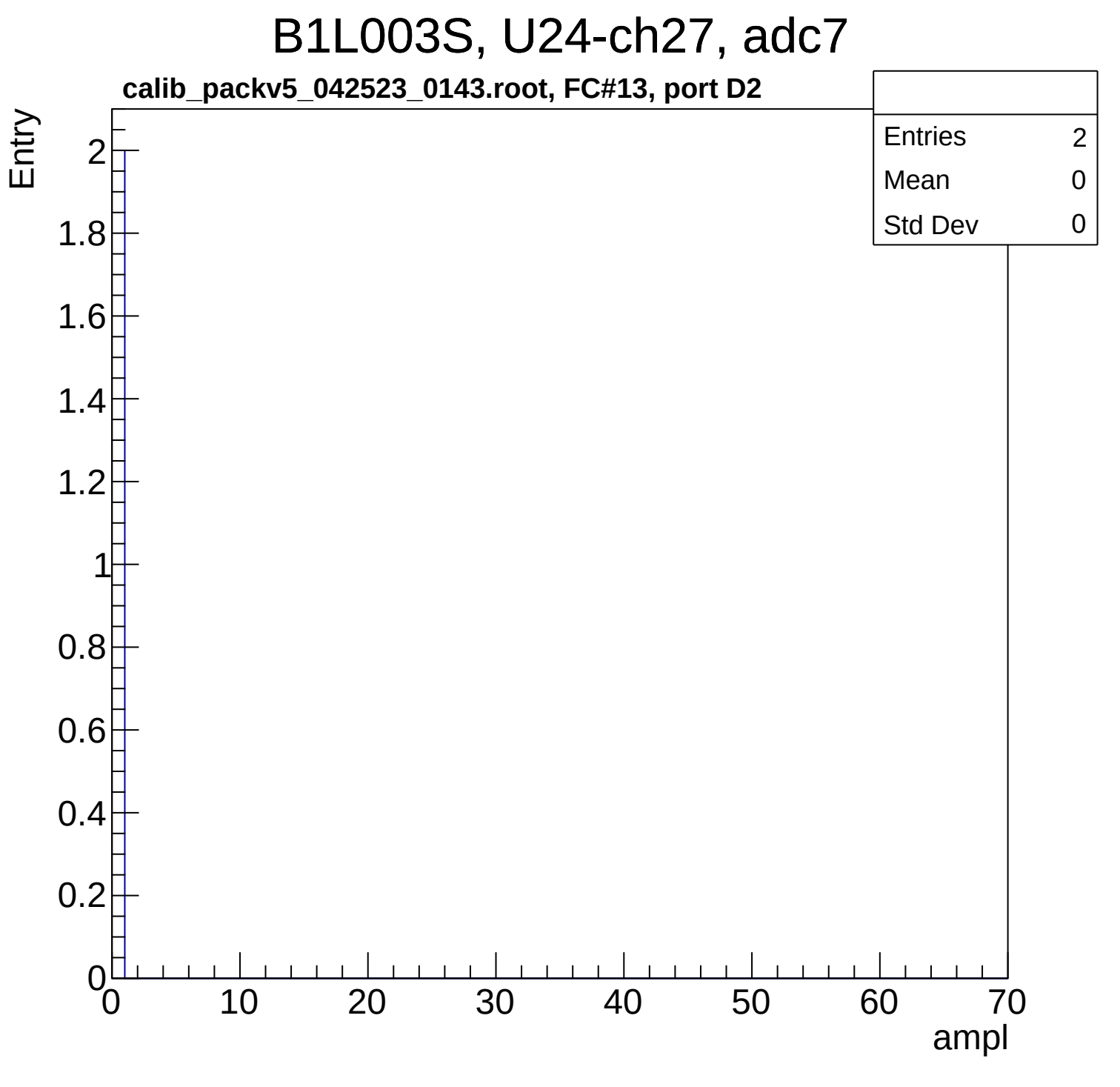
B1L003S, U24-ch27, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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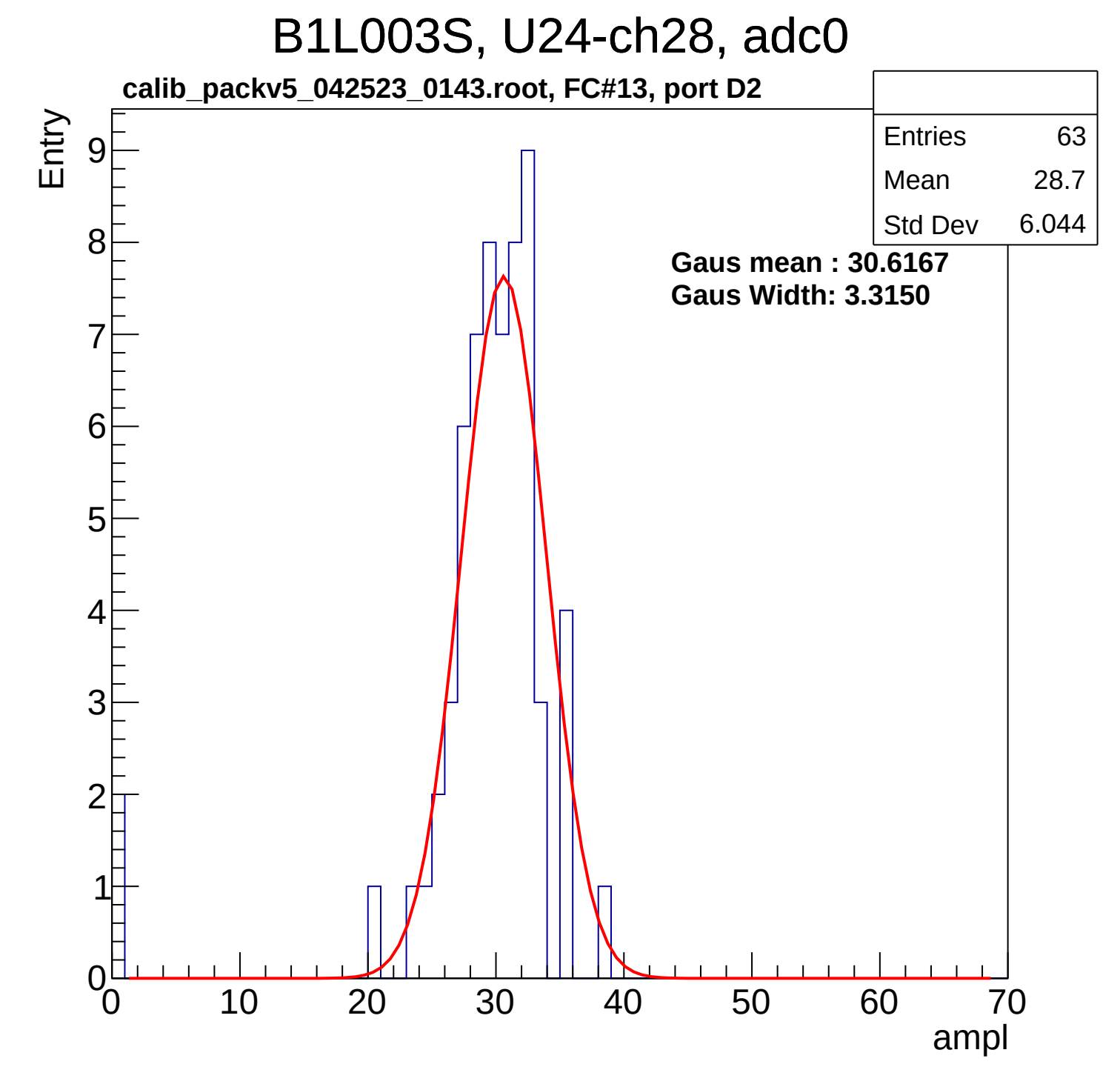
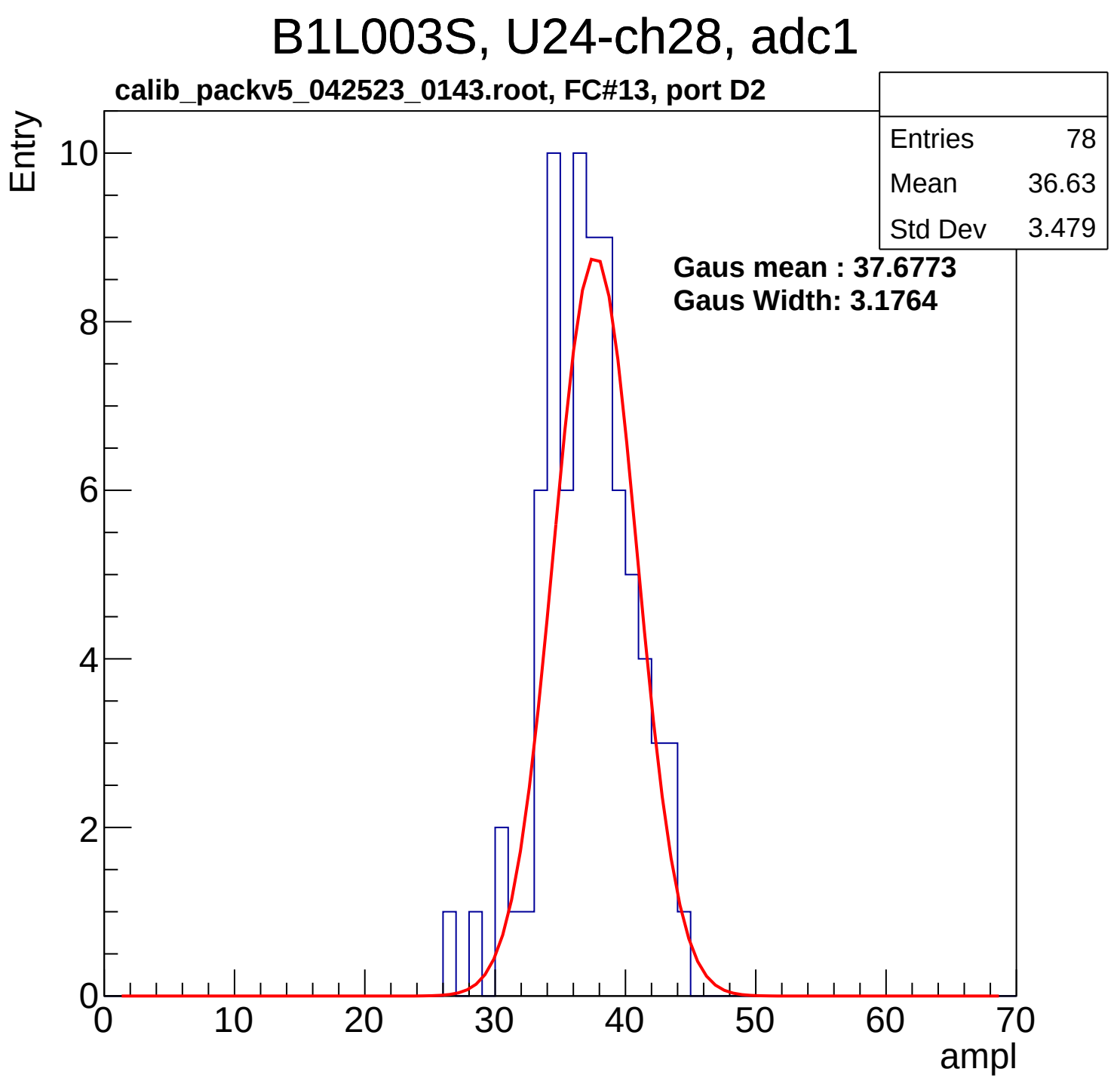
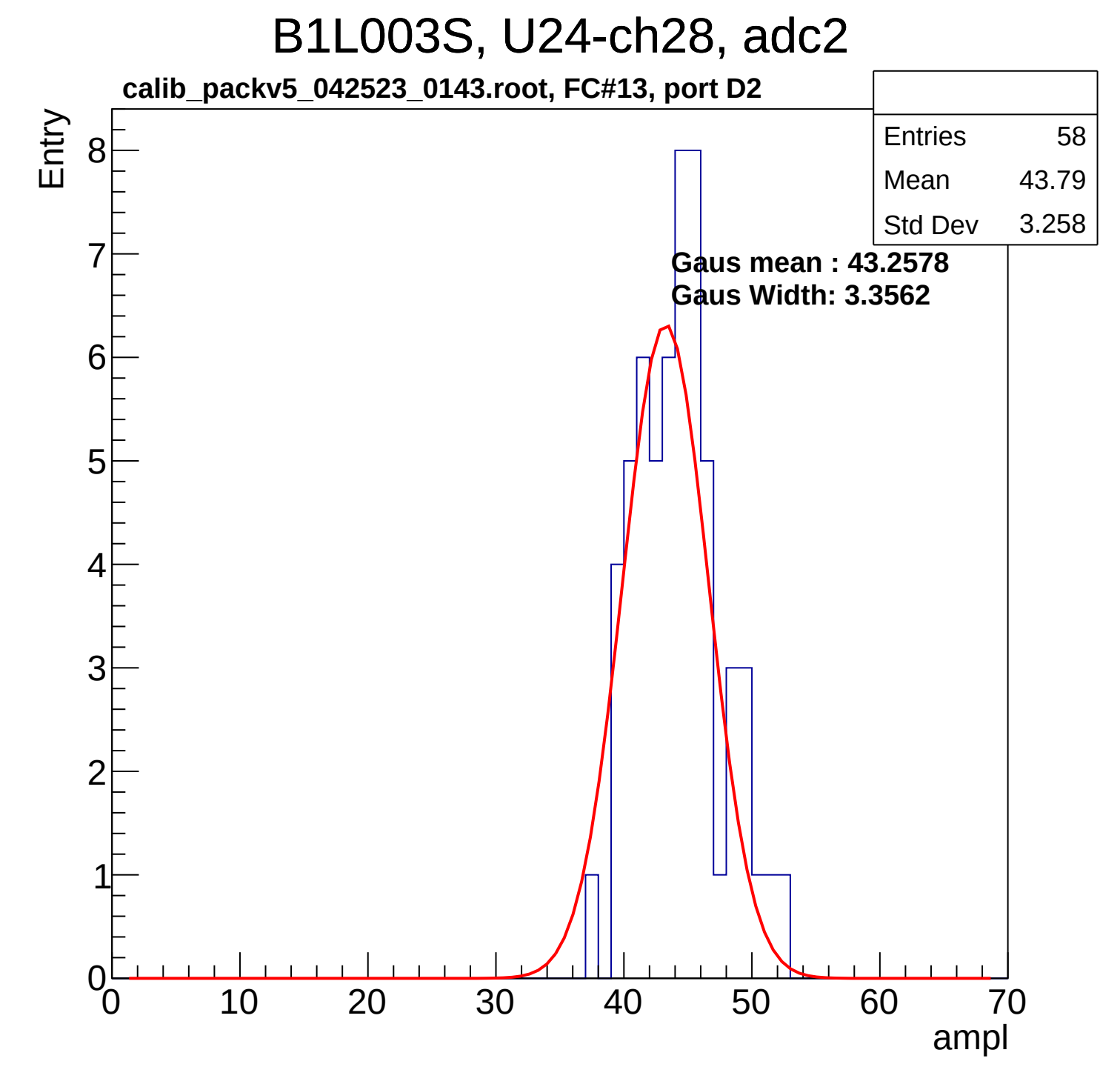

B1L003S, U24-ch28, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.7
Std Dev 6.044
Gaus mean : 30.6167
Gaus Width: 3.3150
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch28, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 36.63
Std Dev 3.479
Gaus mean : 37.6773
Gaus Width: 3.1764
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch28, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 43.79
Std Dev 3.258
Gaus mean : 43.2578
Gaus Width: 3.3562
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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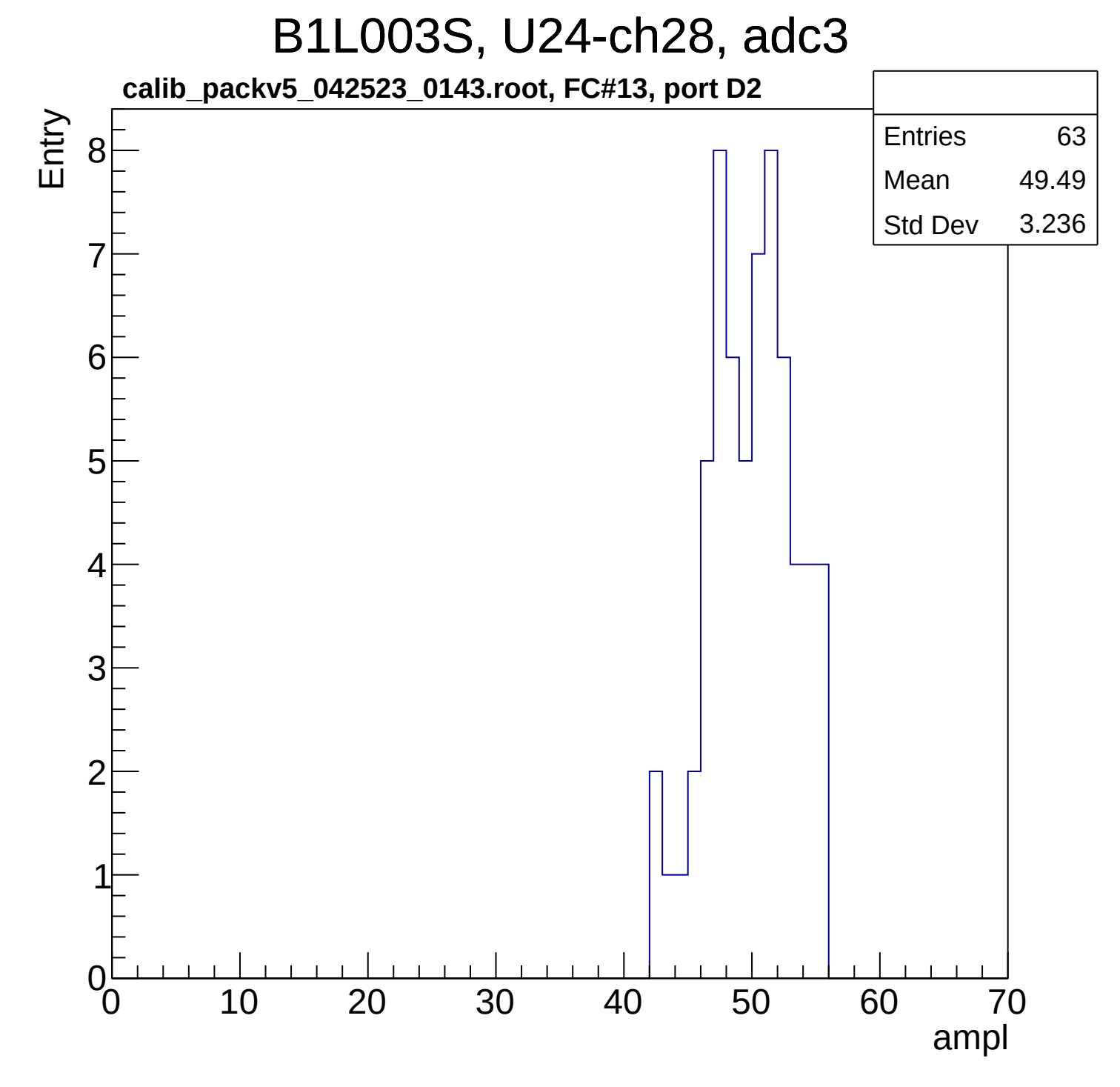
B1L003S, U24-ch28, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.49
Std Dev 3.236
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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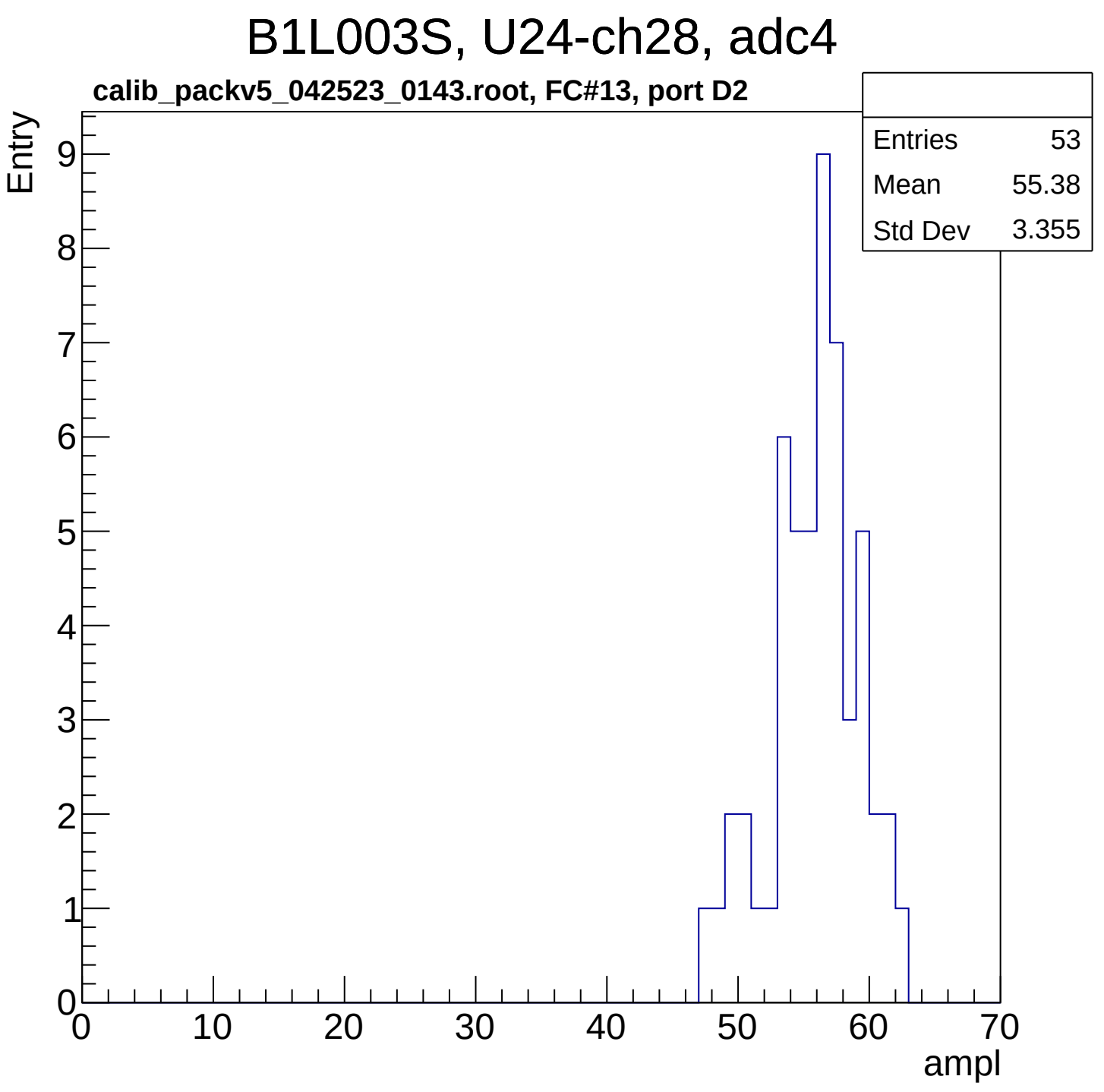
B1L003S, U24-ch28, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.38
Std Dev 3.355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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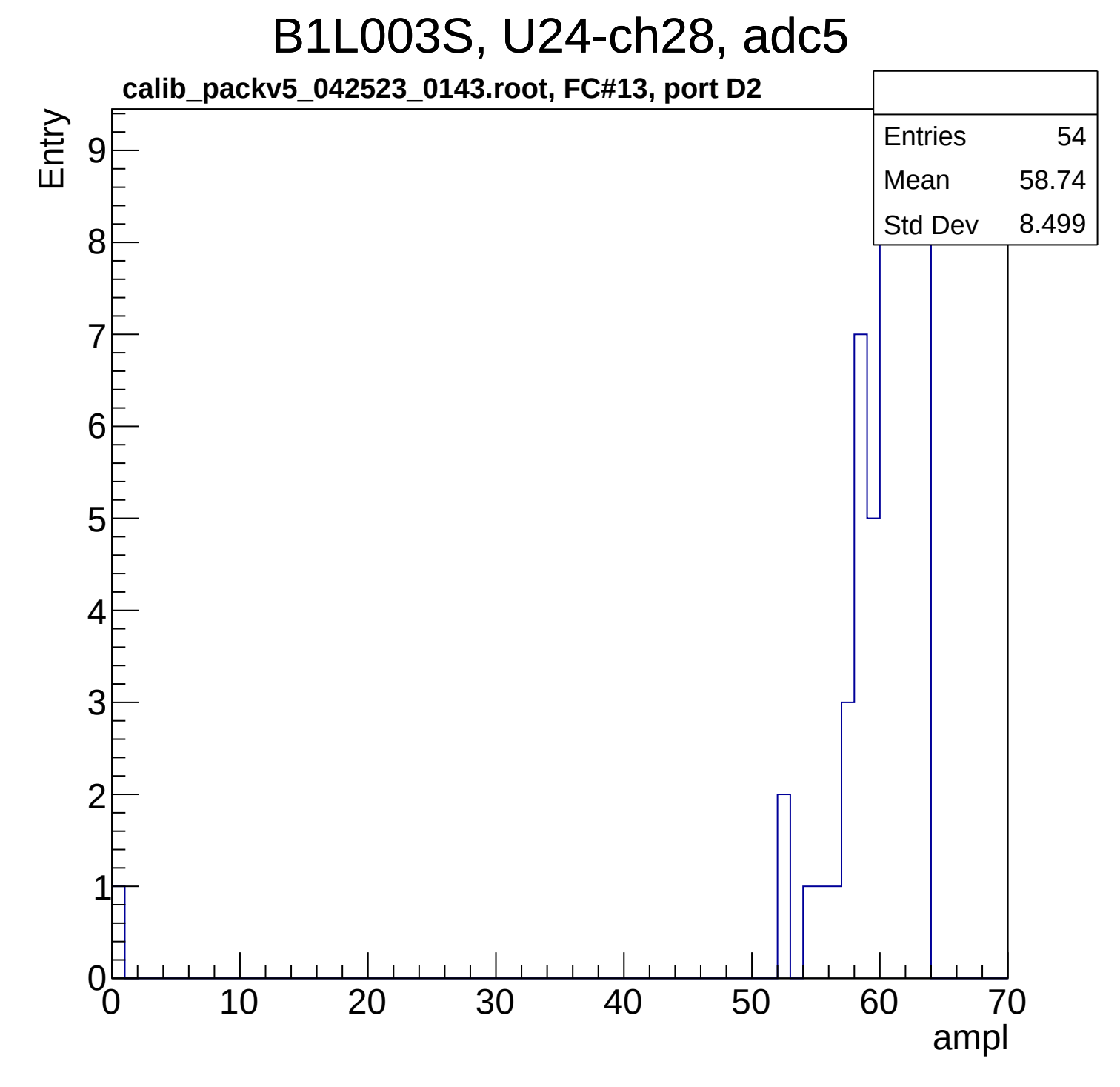
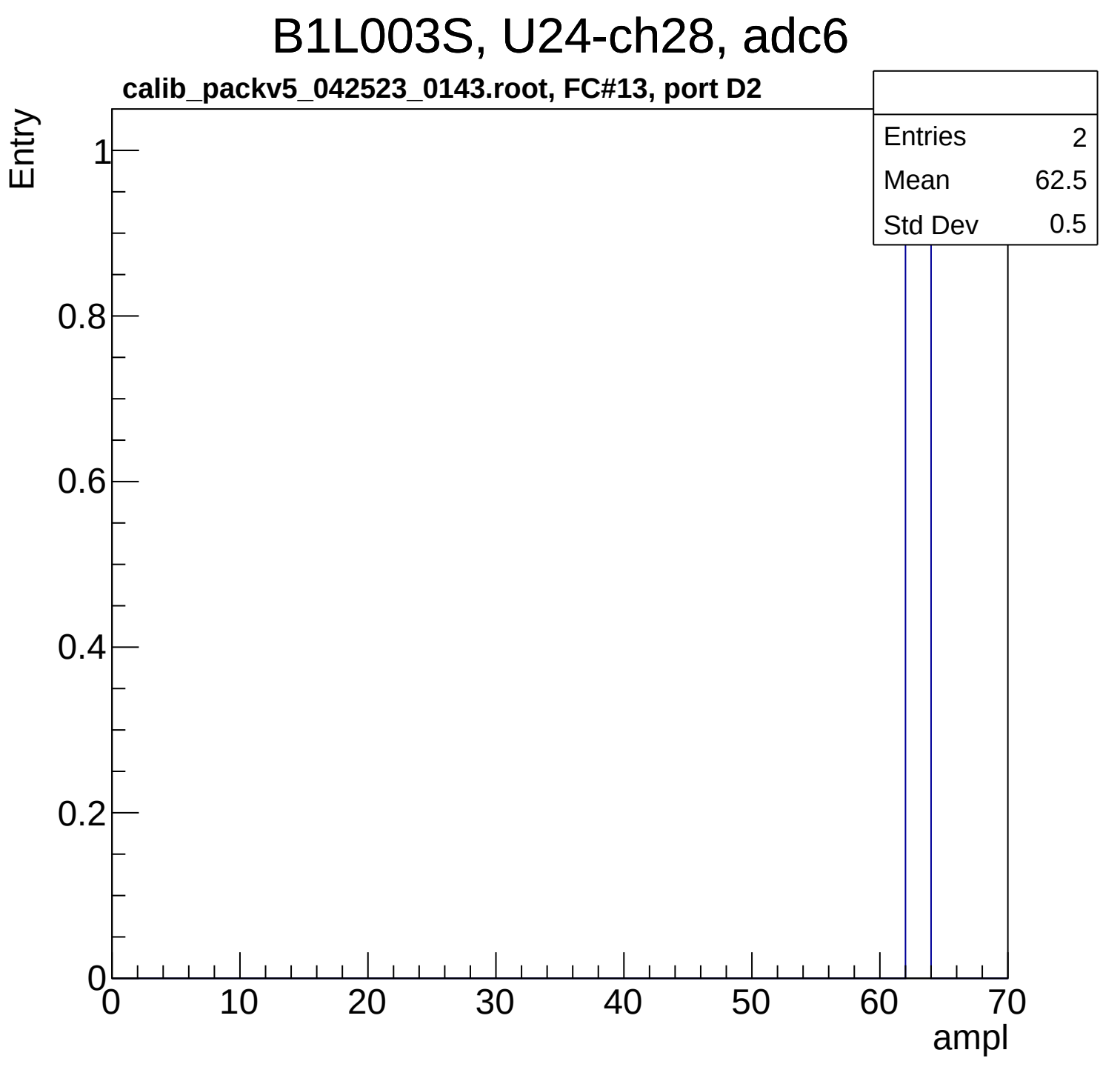
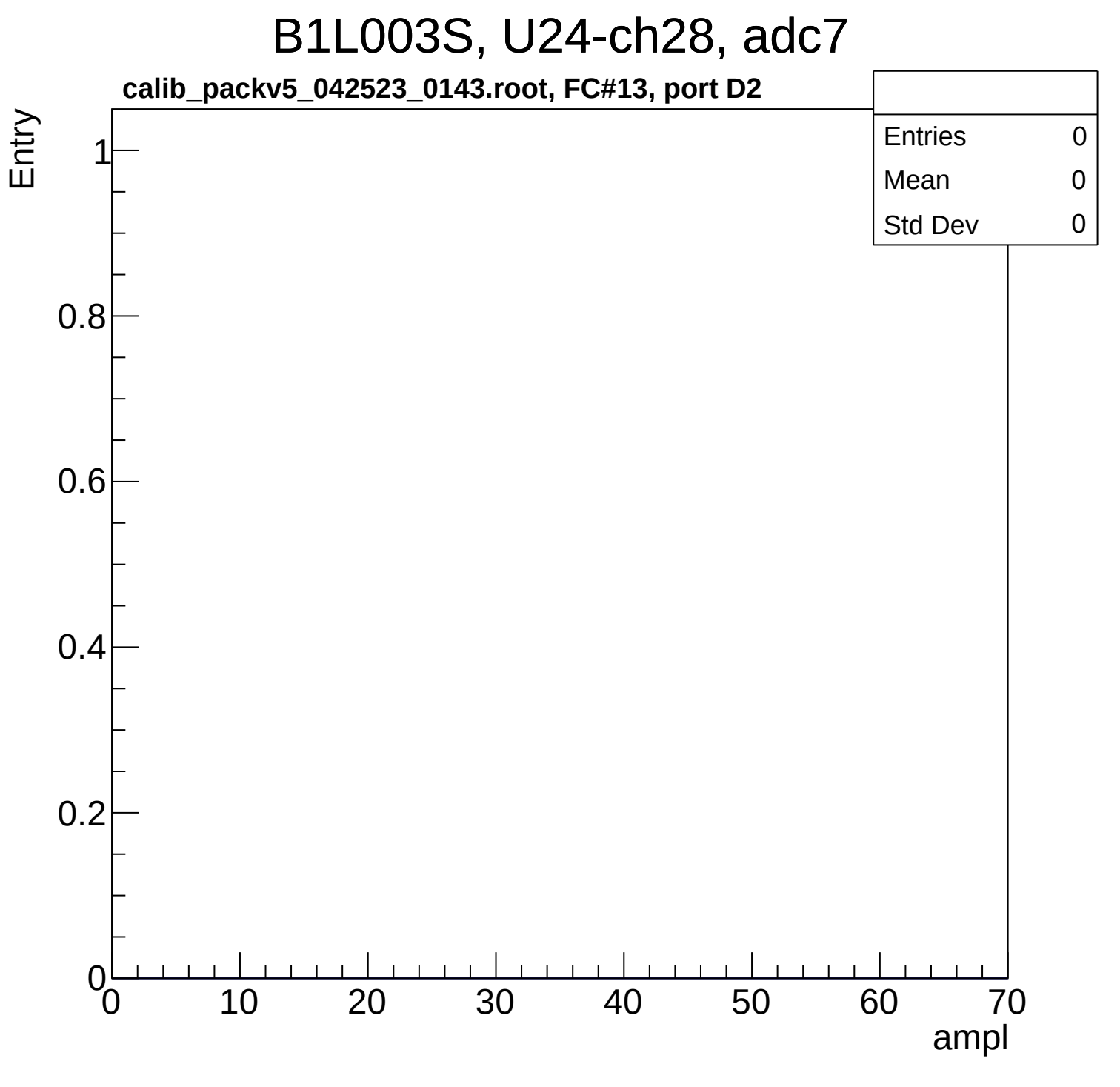
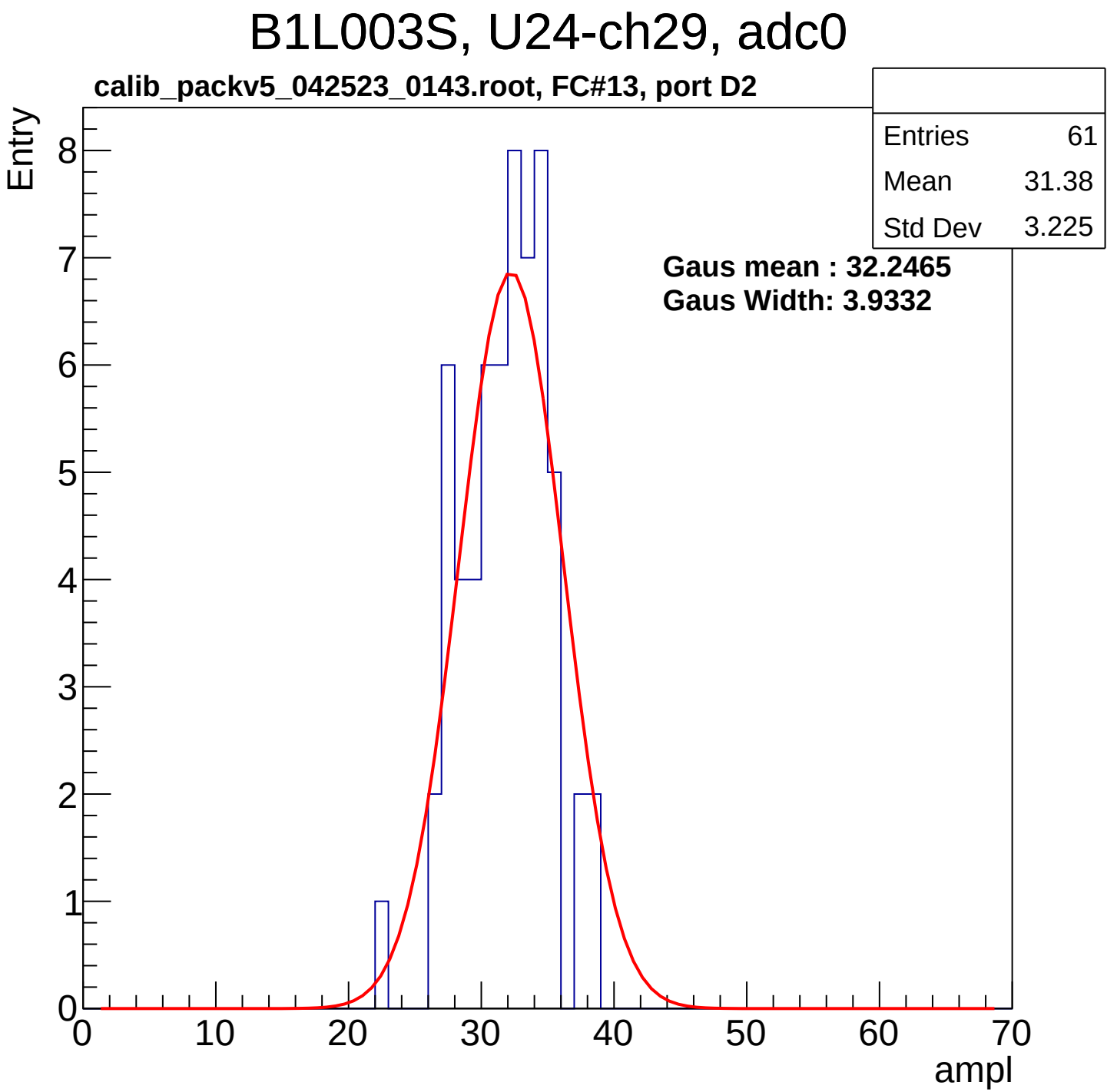
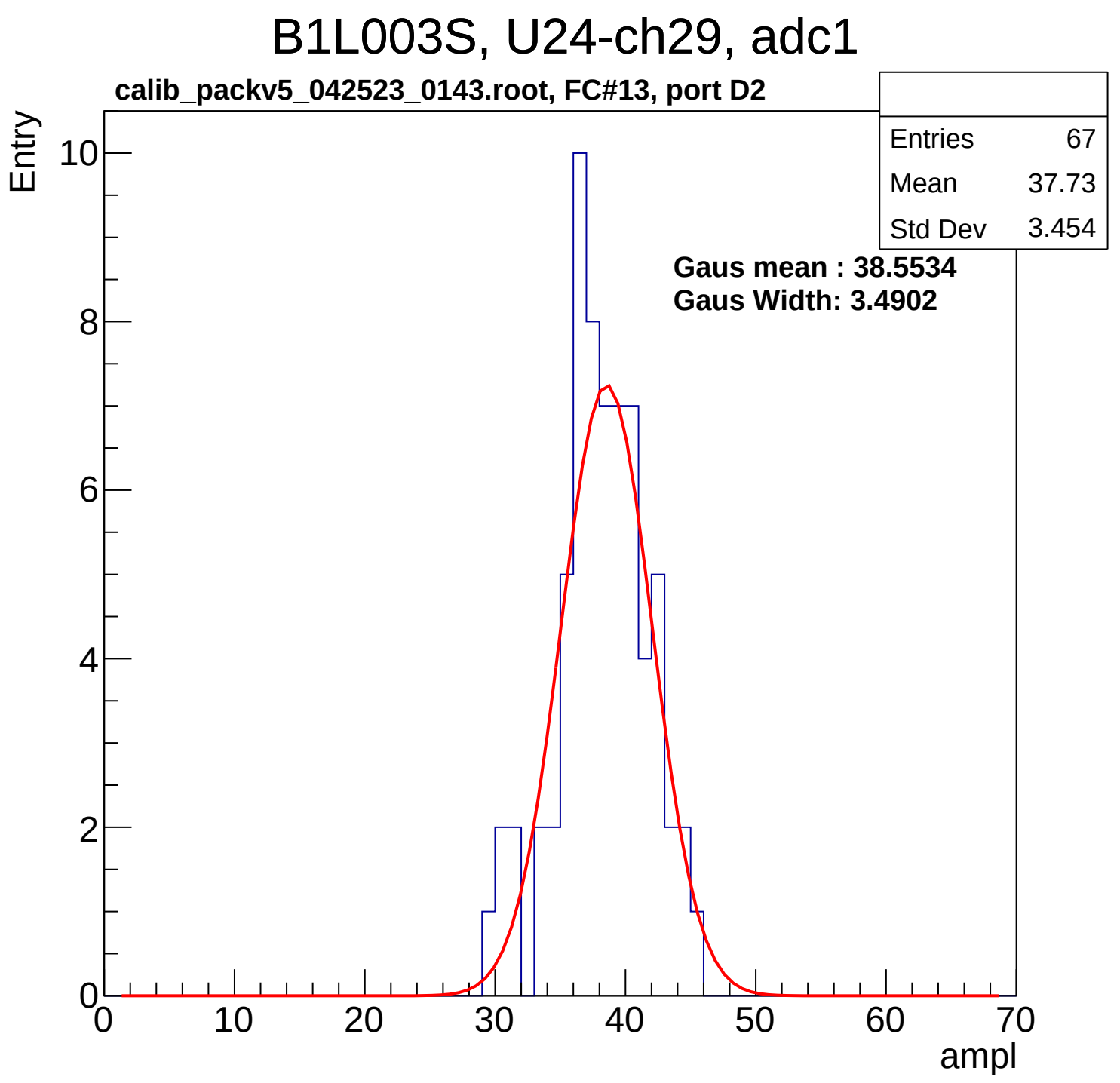
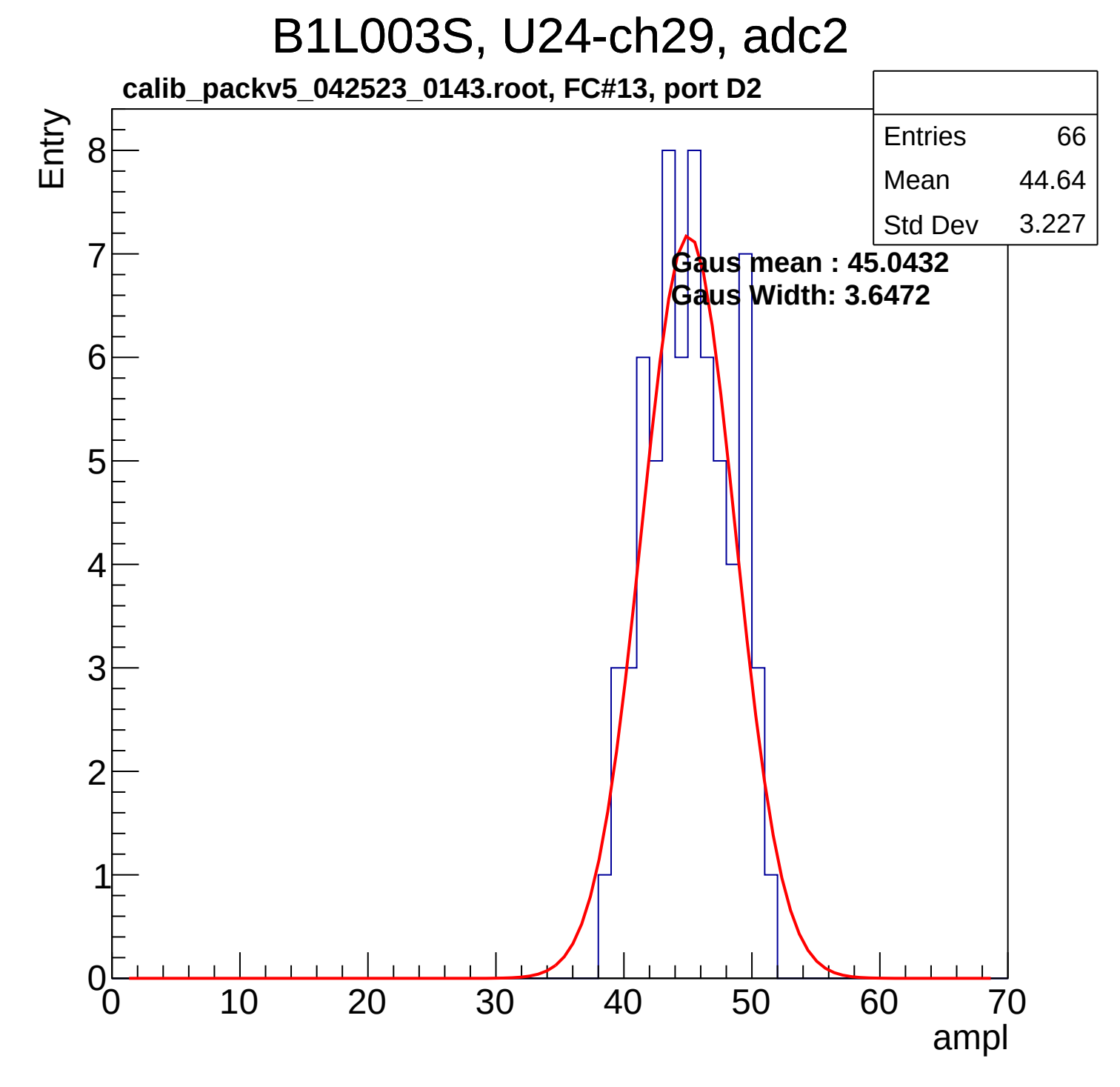

B1L003S, U24-ch28, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.74
Std Dev 8.499
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch28, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch28, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch29, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 31.38
Std Dev 3.225
Gaus mean : 32.2465
Gaus Width: 3.9332
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch29, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.73
Std Dev 3.454
Gaus mean : 38.5534
Gaus Width: 3.4902
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch29, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 44.64
Std Dev 3.227
Gaus mean : 45.0432
Gaus Width: 3.6472
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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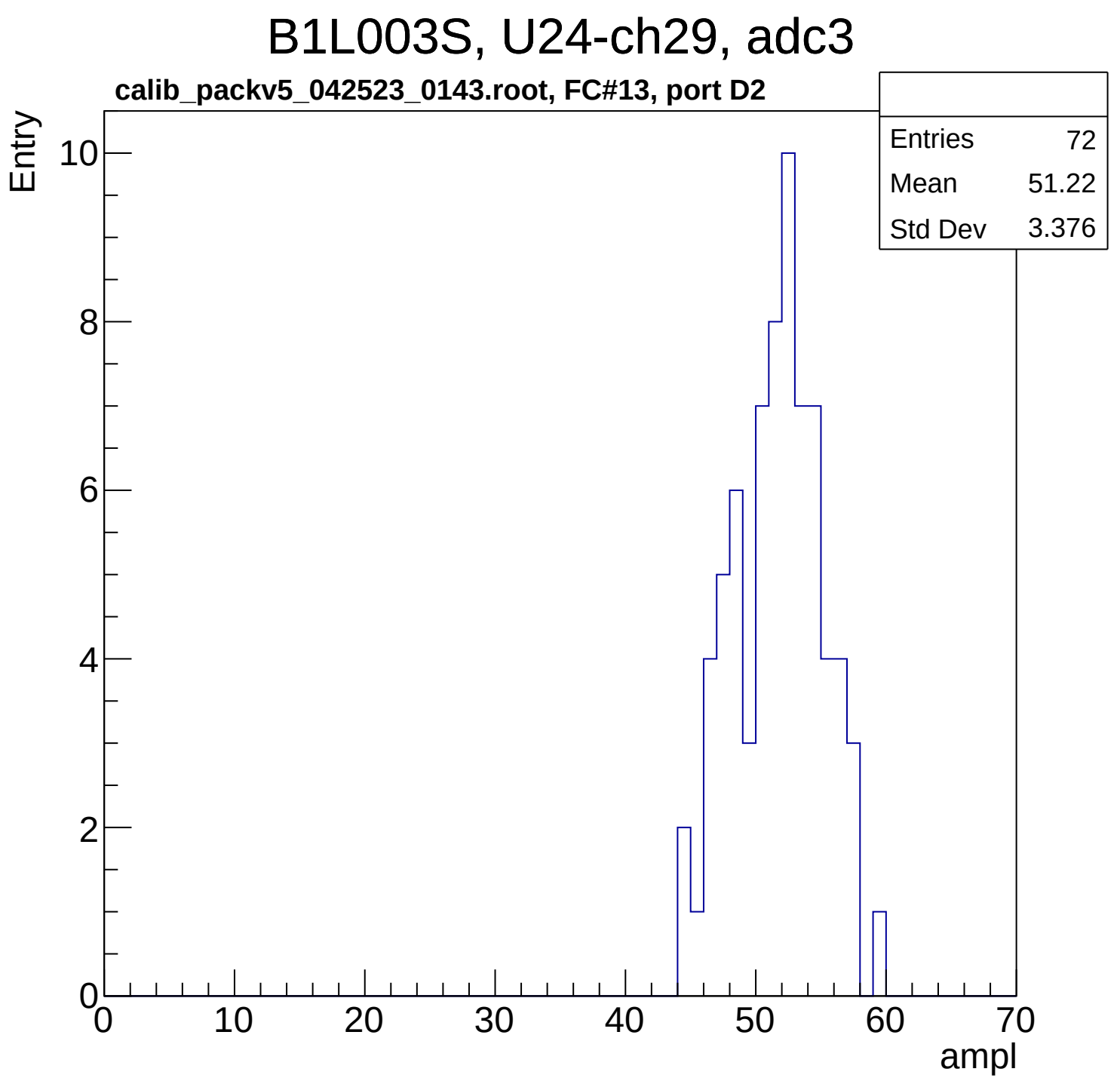
B1L003S, U24-ch29, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 51.22
Std Dev 3.376
Entry
0
2
4
6
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10
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10
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30
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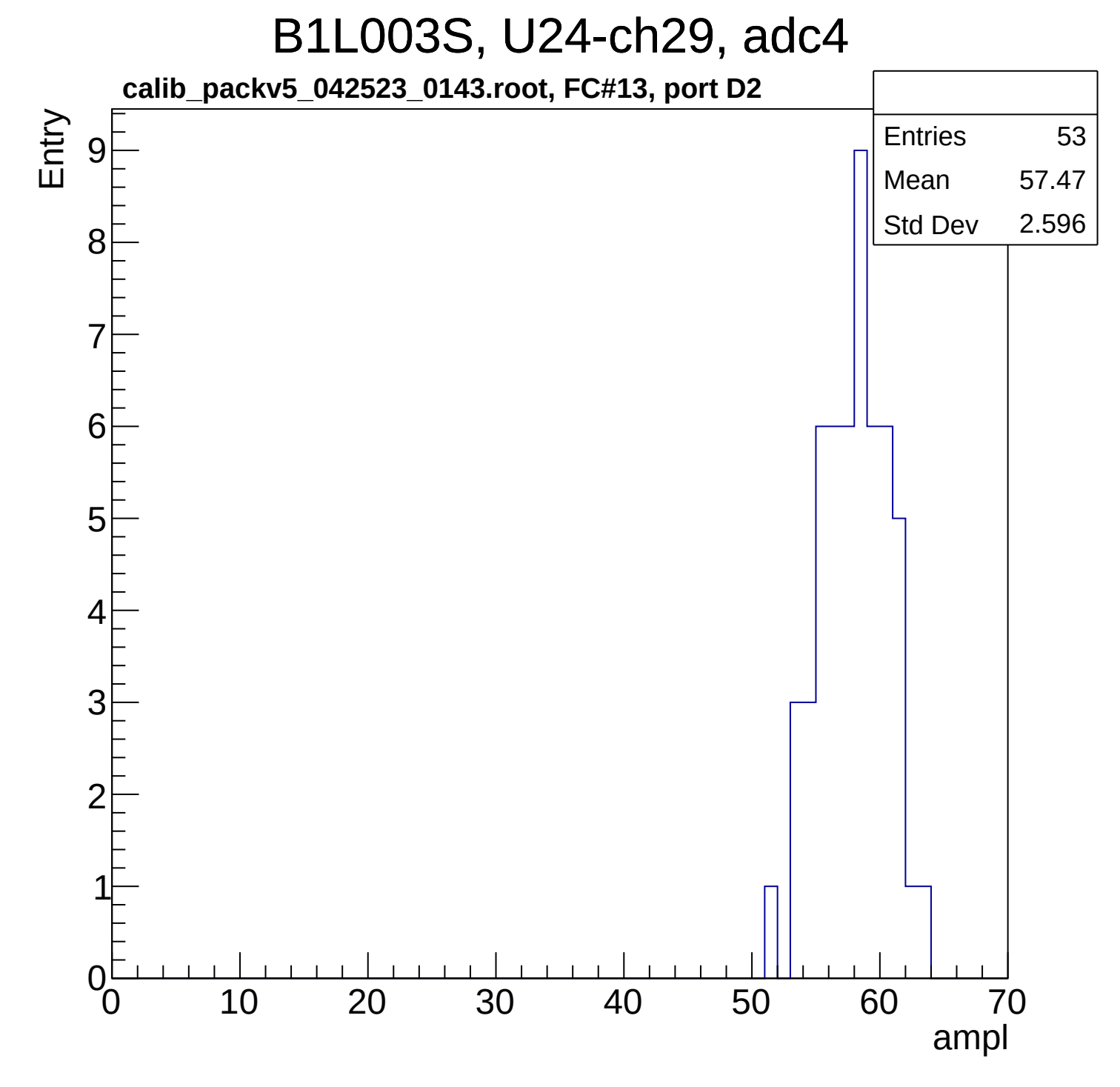
50
60
70
ampl


B1L003S, U24-ch29, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 57.47
Std Dev 2.596
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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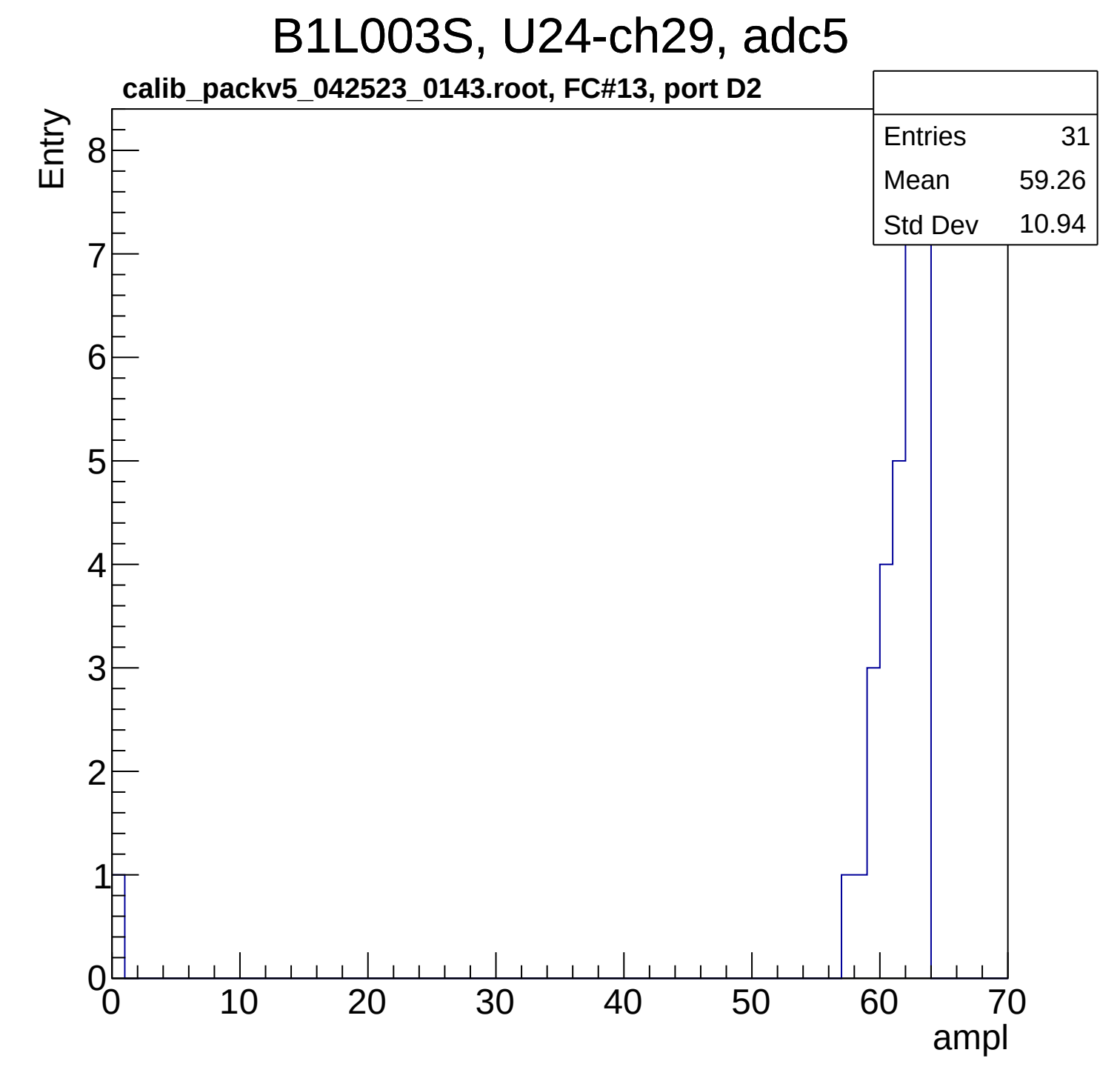
B1L003S, U24-ch29, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 31
Mean 59.26
Std Dev 10.94
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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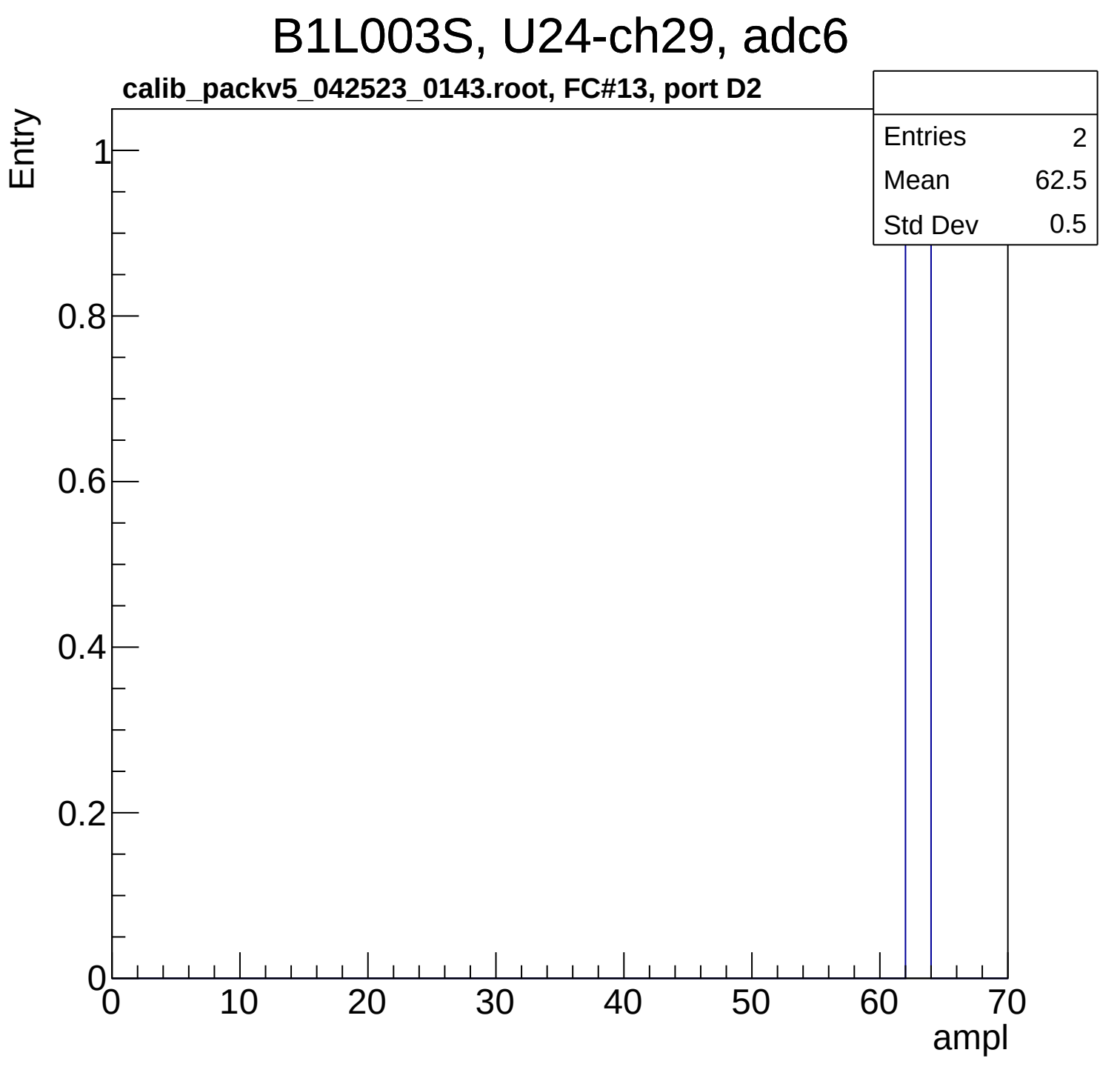
B1L003S, U24-ch29, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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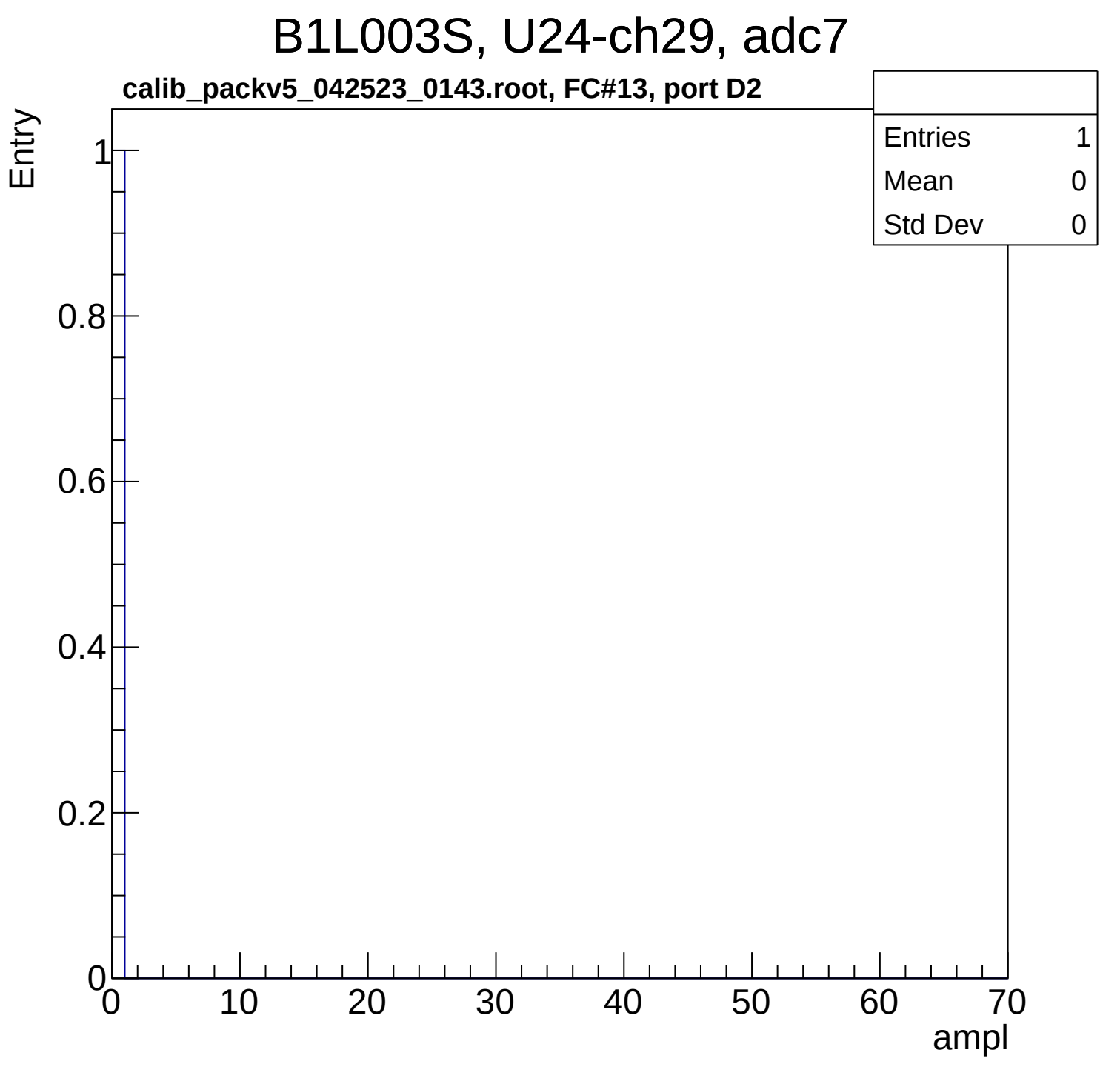
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch29, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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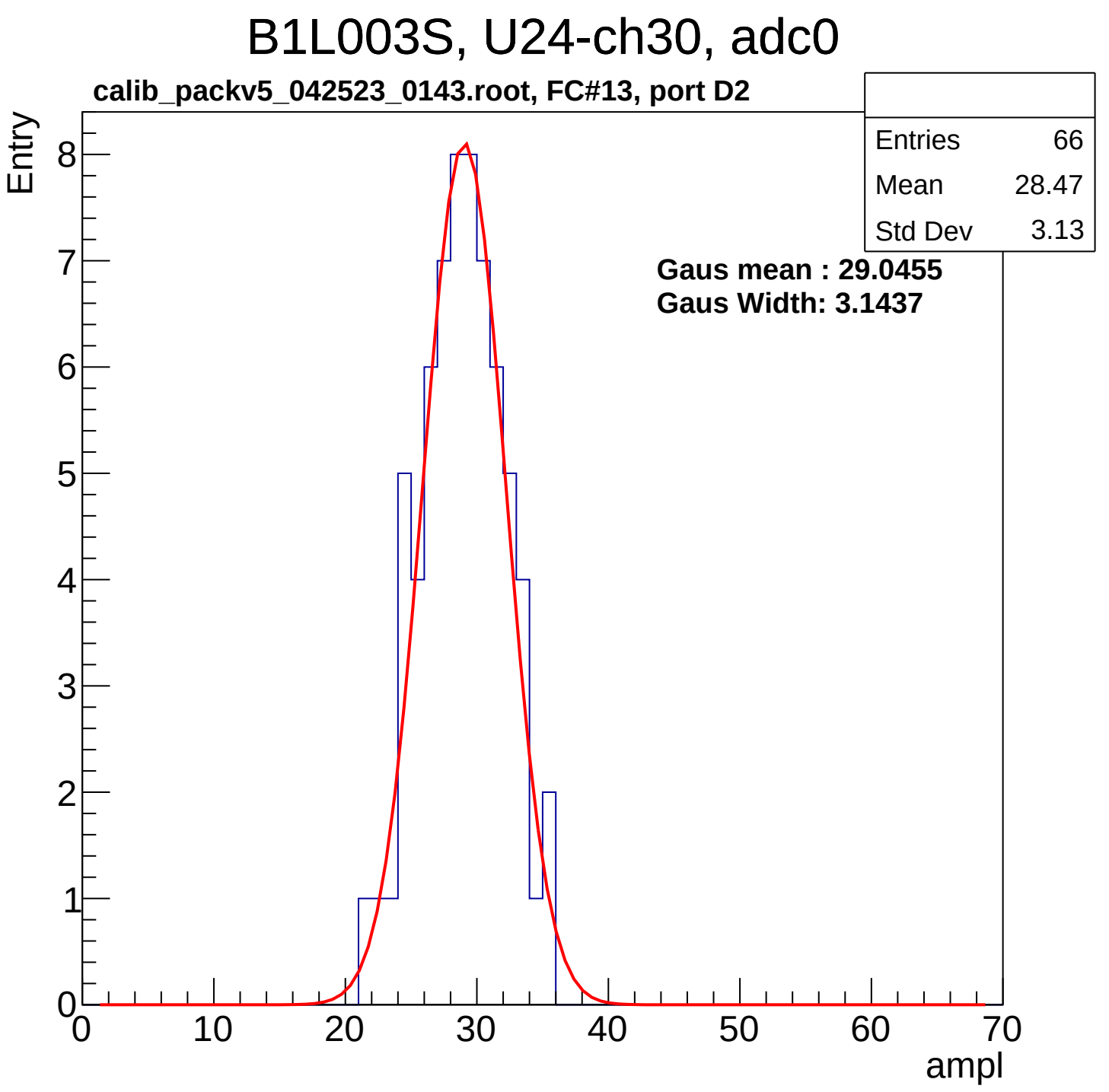
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch30, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.47
Std Dev 3.13
Gaus mean : 29.0455
Gaus Width: 3.1437
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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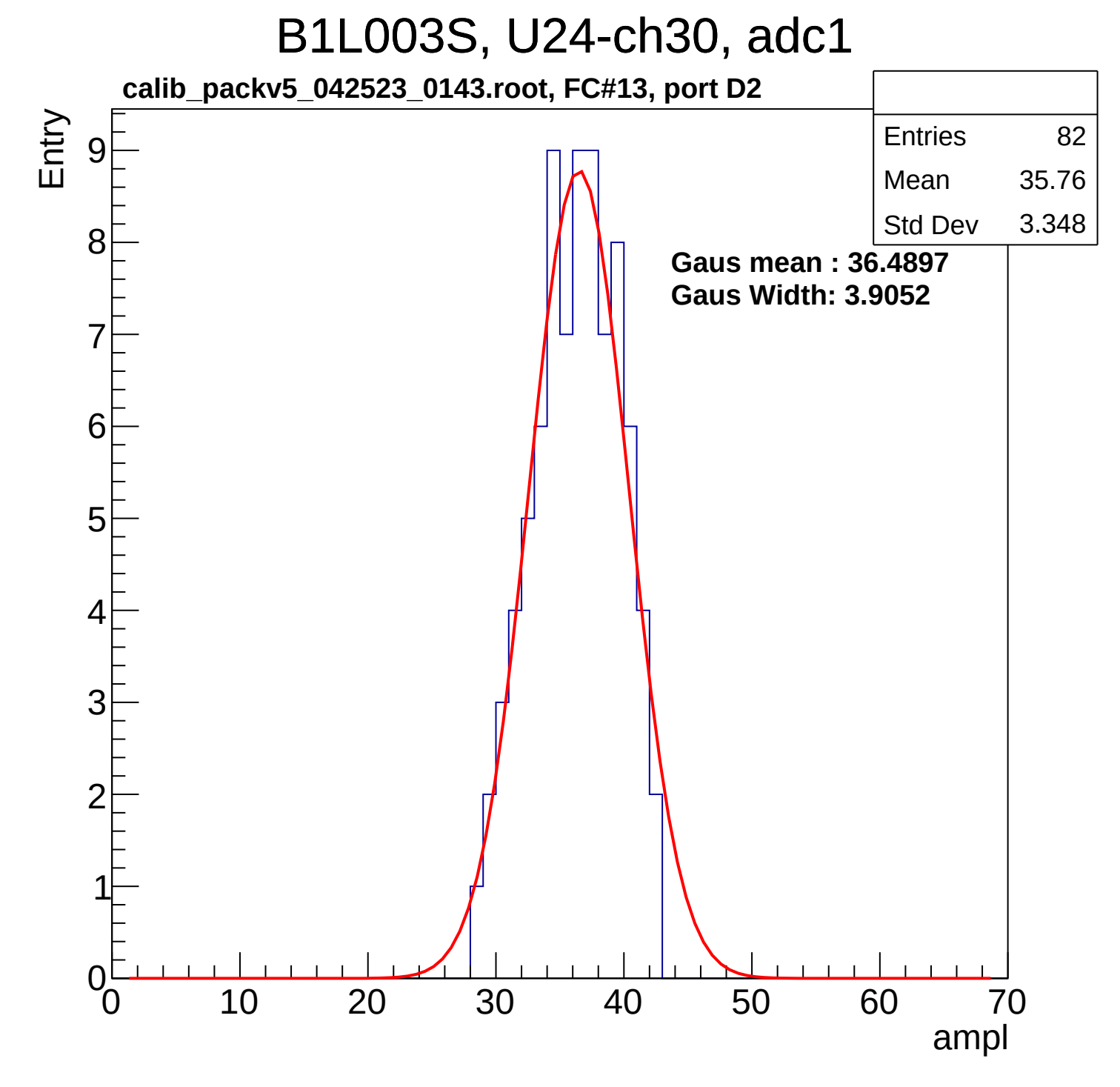
B1L003S, U24-ch30, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 35.76
Std Dev 3.348
Gaus mean : 36.4897
Gaus Width: 3.9052
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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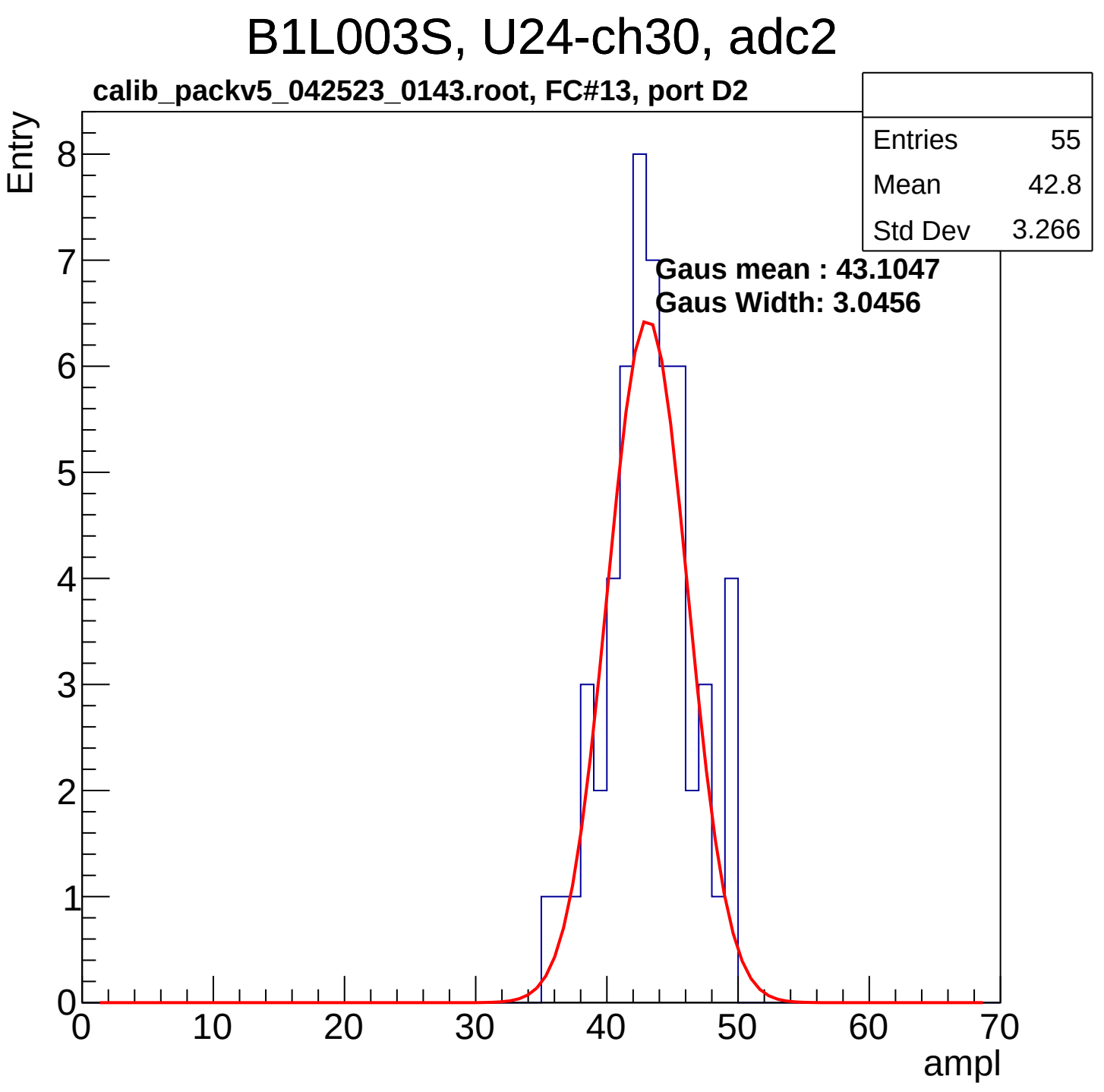
B1L003S, U24-ch30, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 42.8
Std Dev 3.266
Gaus mean : 43.1047
Gaus Width: 3.0456
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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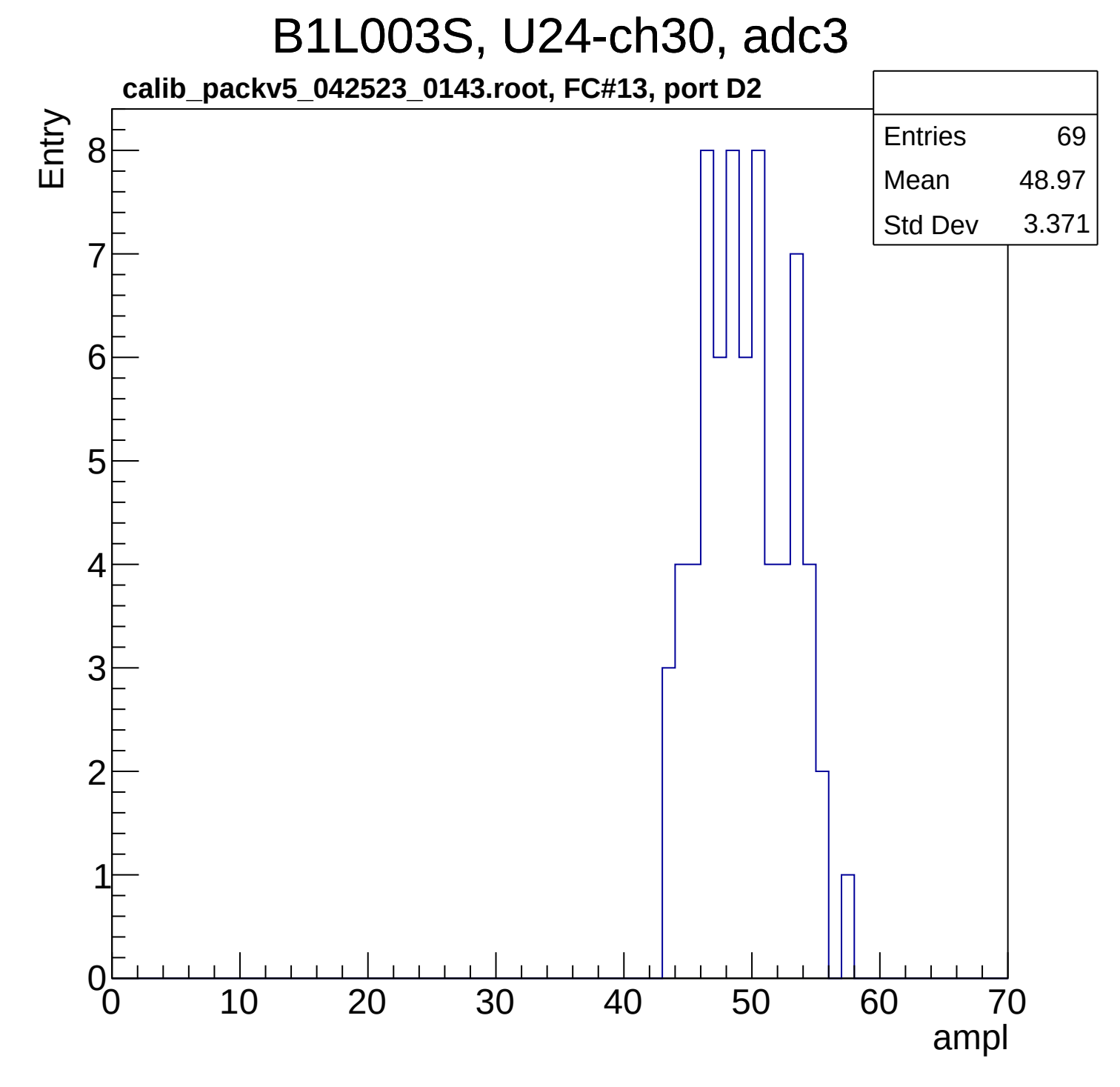
B1L003S, U24-ch30, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.97
Std Dev 3.371
Entry
ampl
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2
3
4
5
6
7
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0
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20
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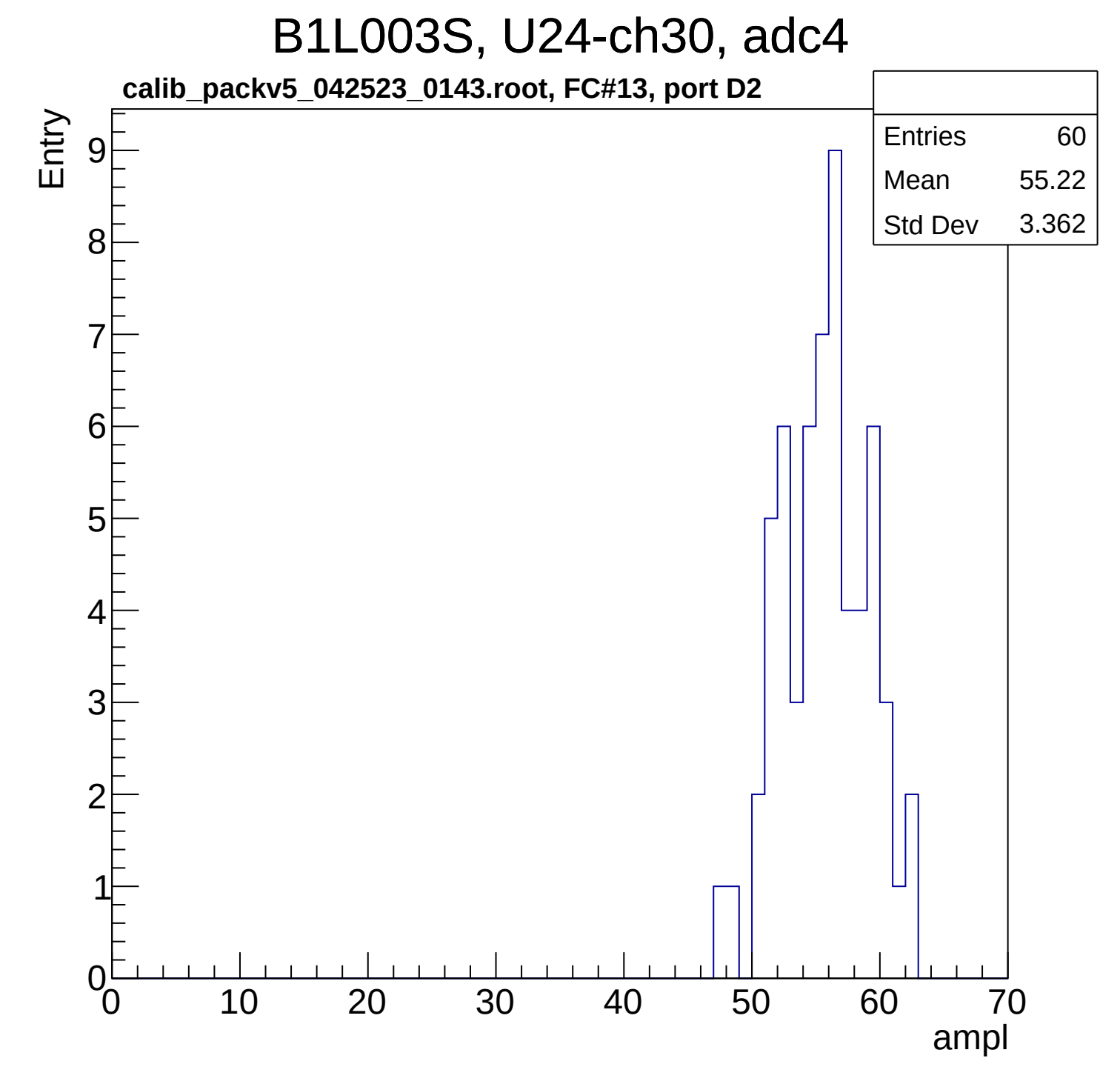
B1L003S, U24-ch30, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.22
Std Dev 3.362
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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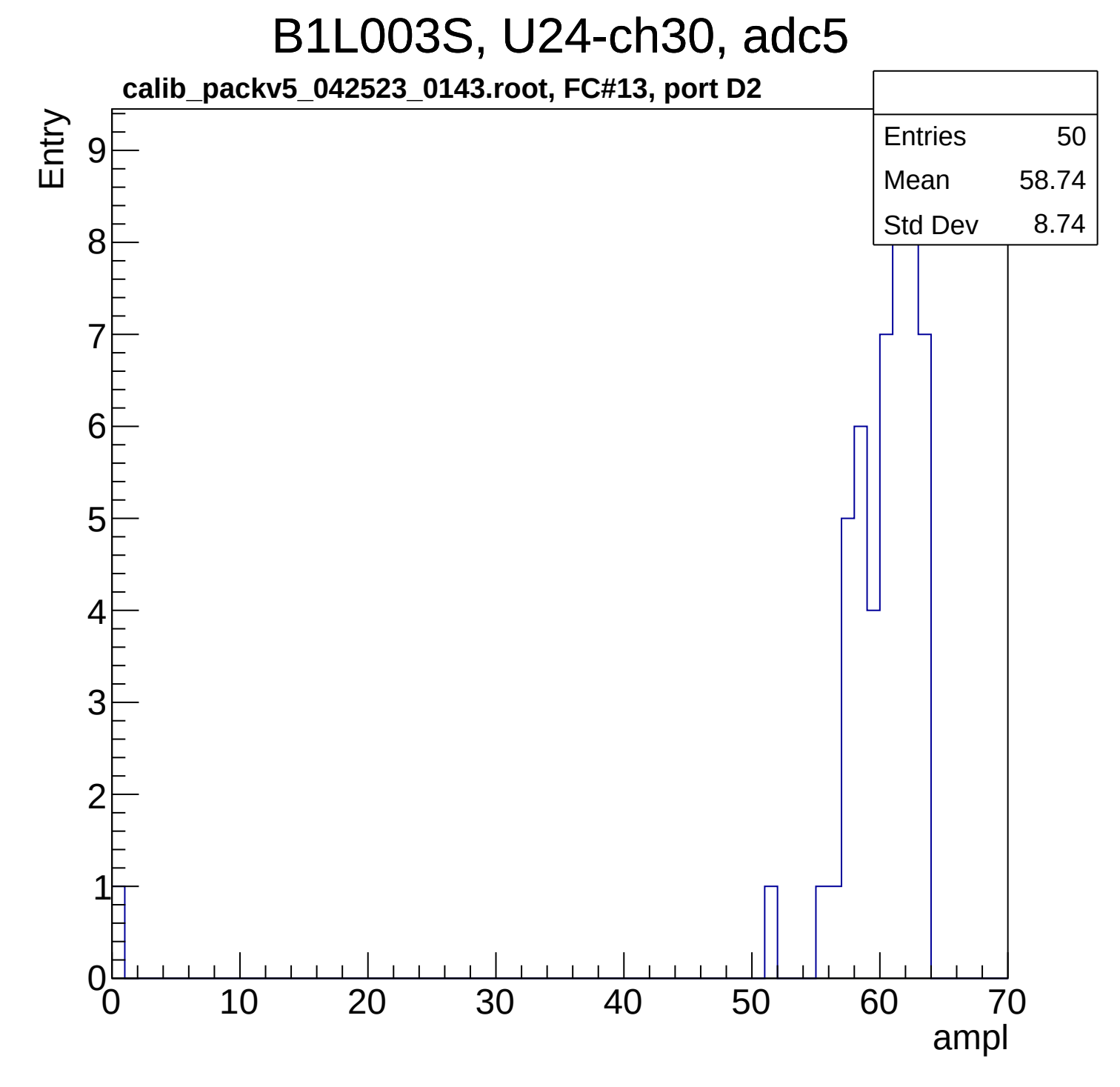
B1L003S, U24-ch30, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.74
Std Dev 8.74
Entry
ampl
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2
3
4
5
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7
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60
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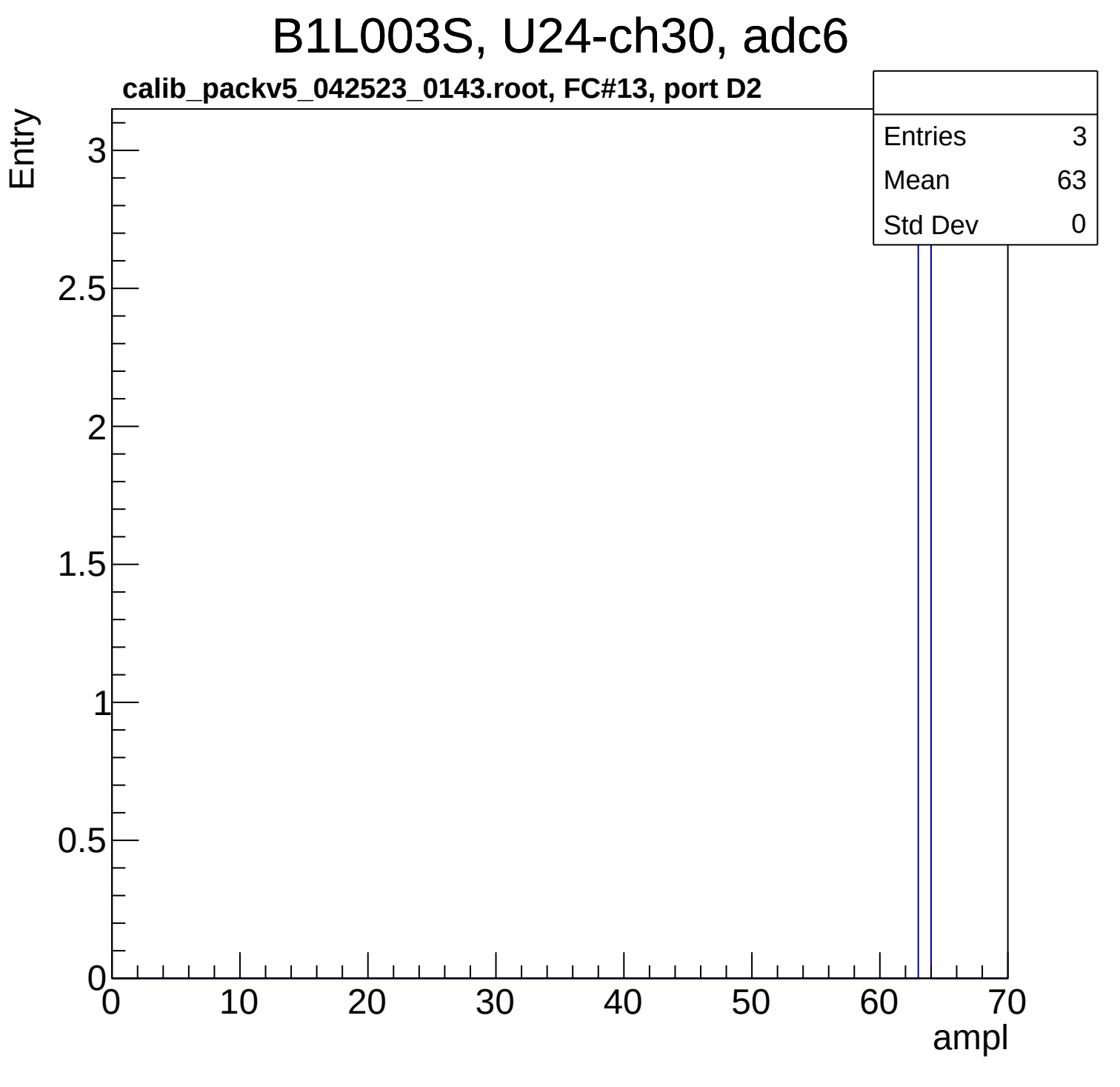
B1L003S, U24-ch30, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
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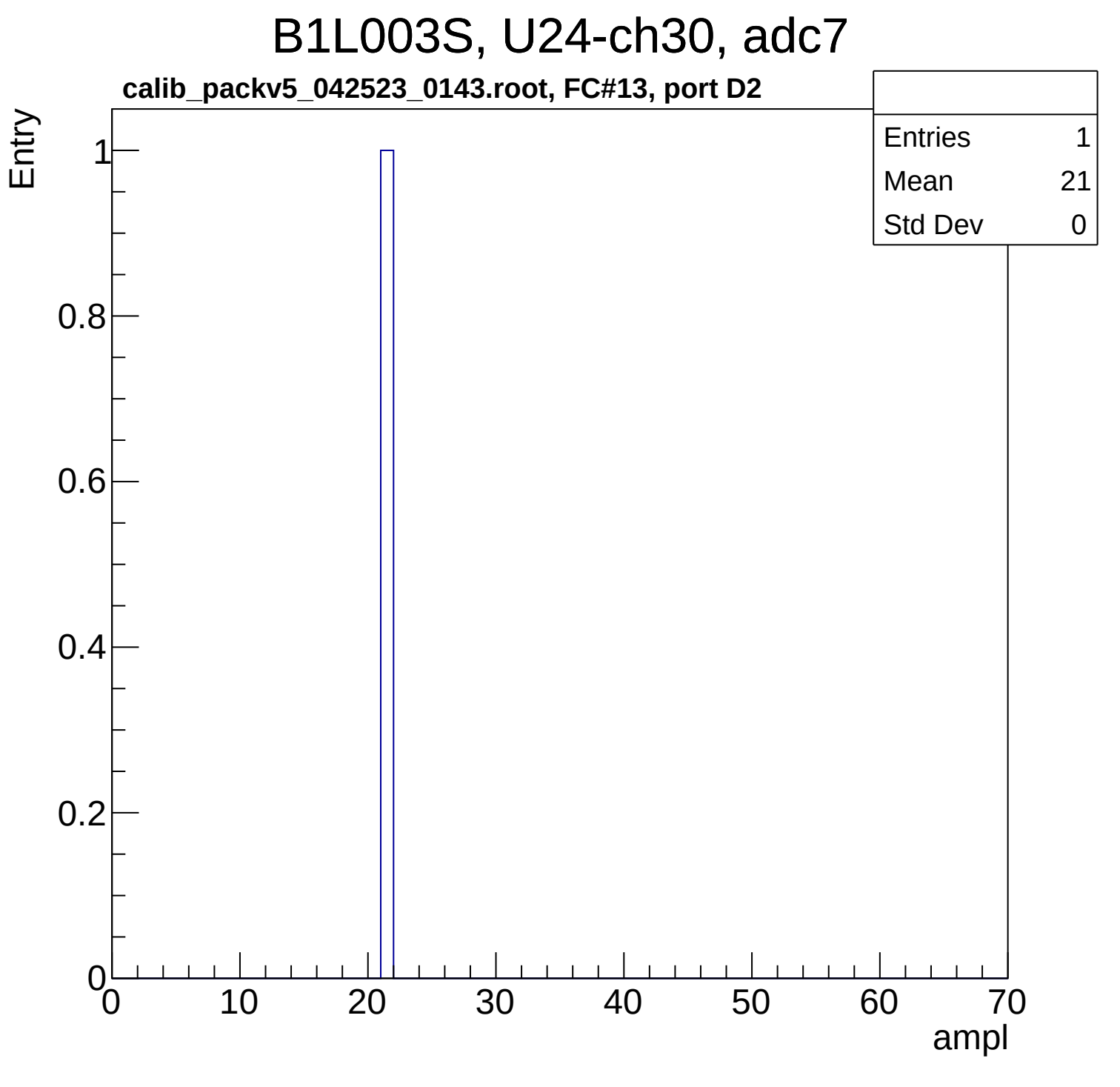
70
ampl


B1L003S, U24-ch30, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
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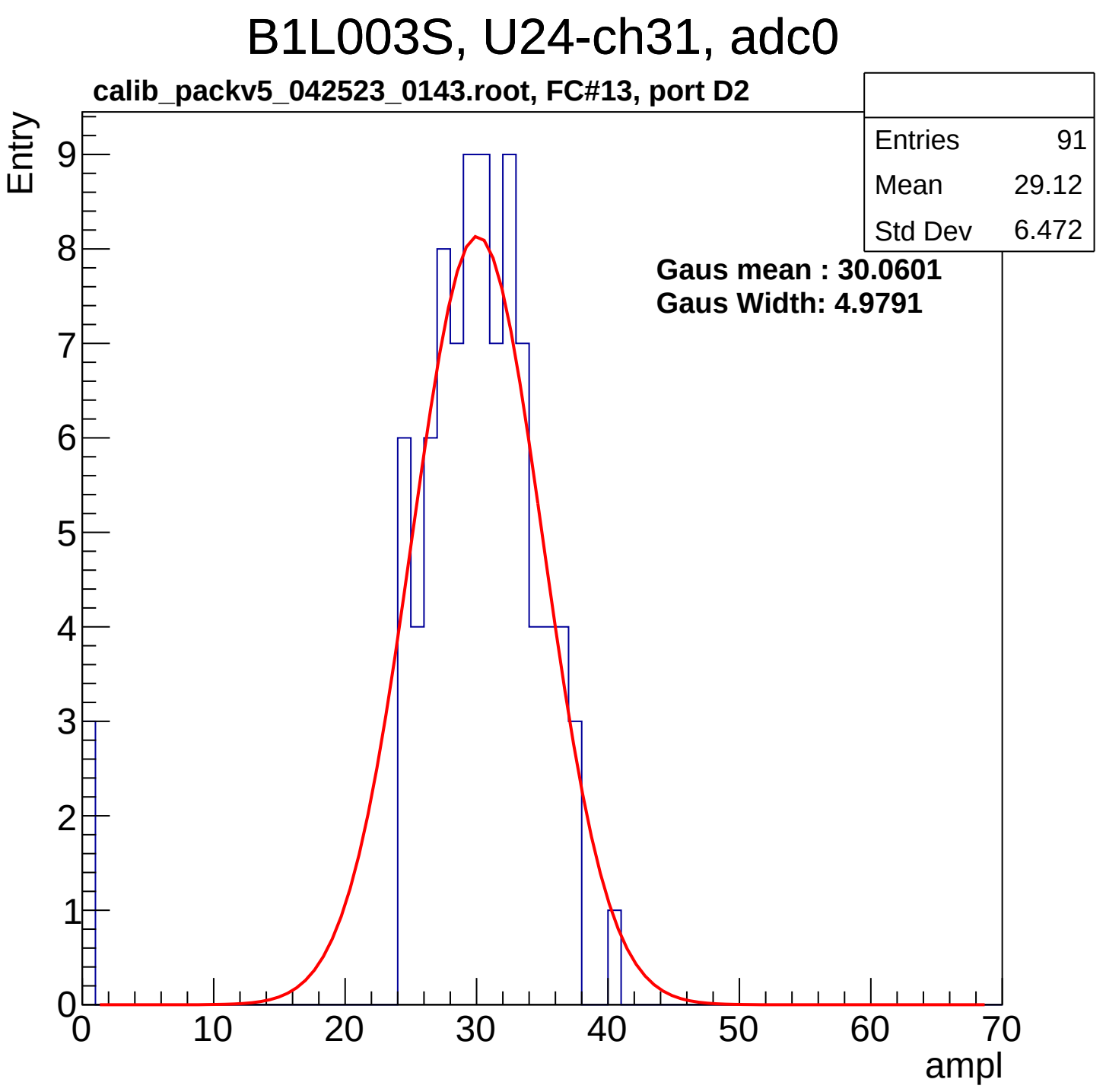
ampl


B1L003S, U24-ch31, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 91
Mean 29.12
Std Dev 6.472
Gaus mean : 30.0601
Gaus Width: 4.9791
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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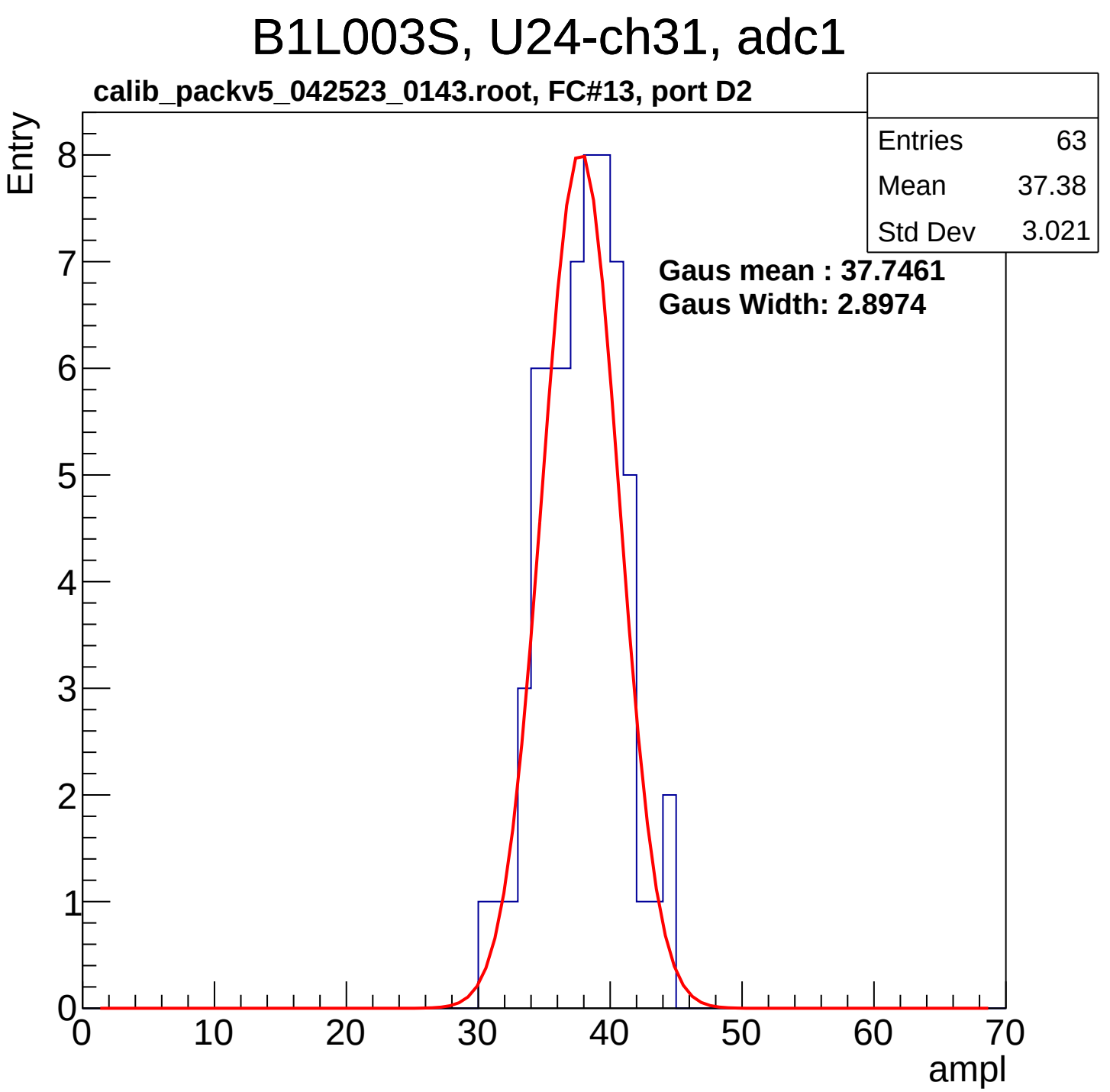
B1L003S, U24-ch31, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 37.38
Std Dev 3.021
Gaus mean : 37.7461
Gaus Width: 2.8974
Entry
ampl
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3
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5
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0
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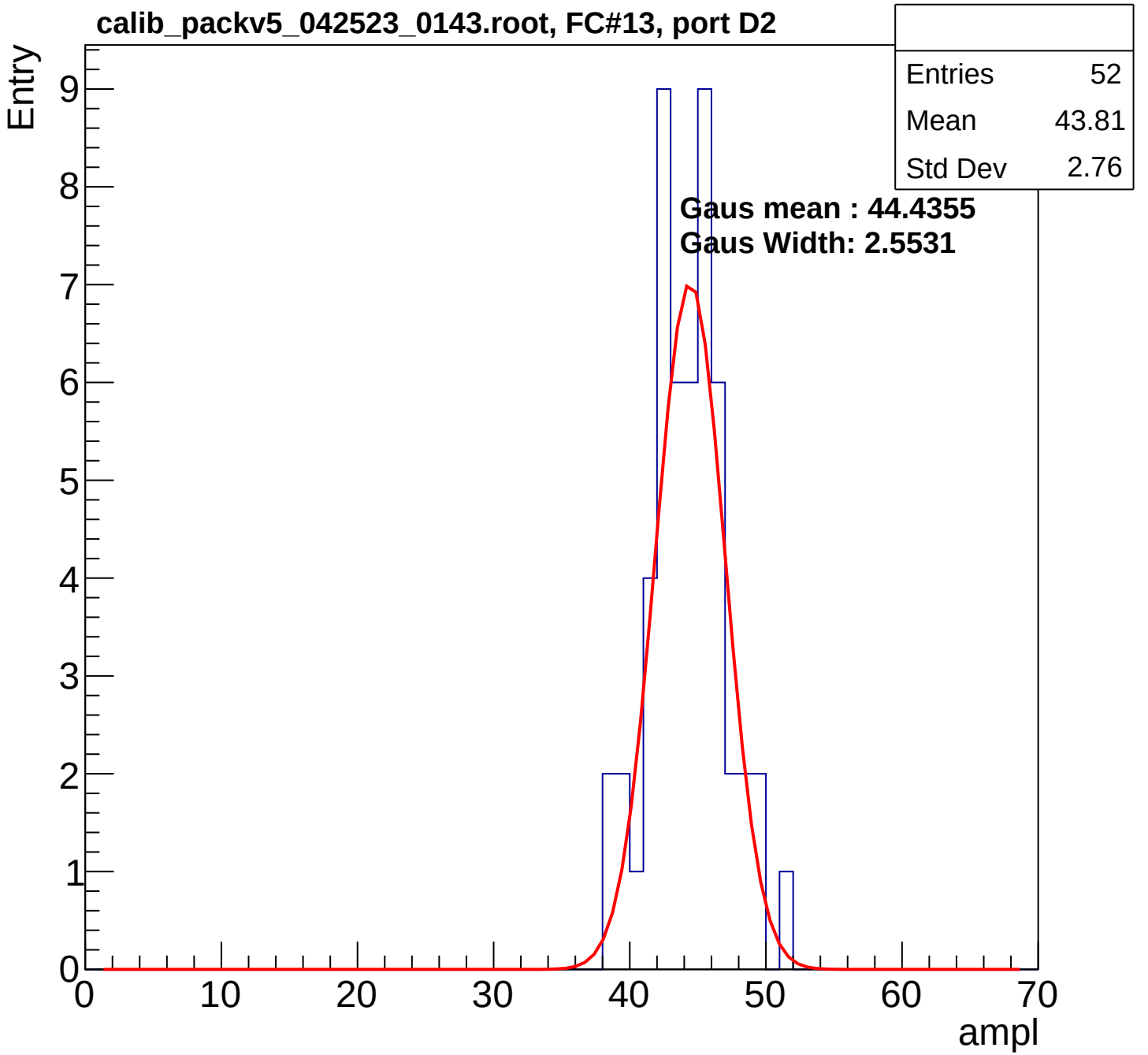
B1L003S, U24-ch31, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 43.81
Std Dev 2.76
Gaus mean : 44.4355
Gaus Width: 2.5531
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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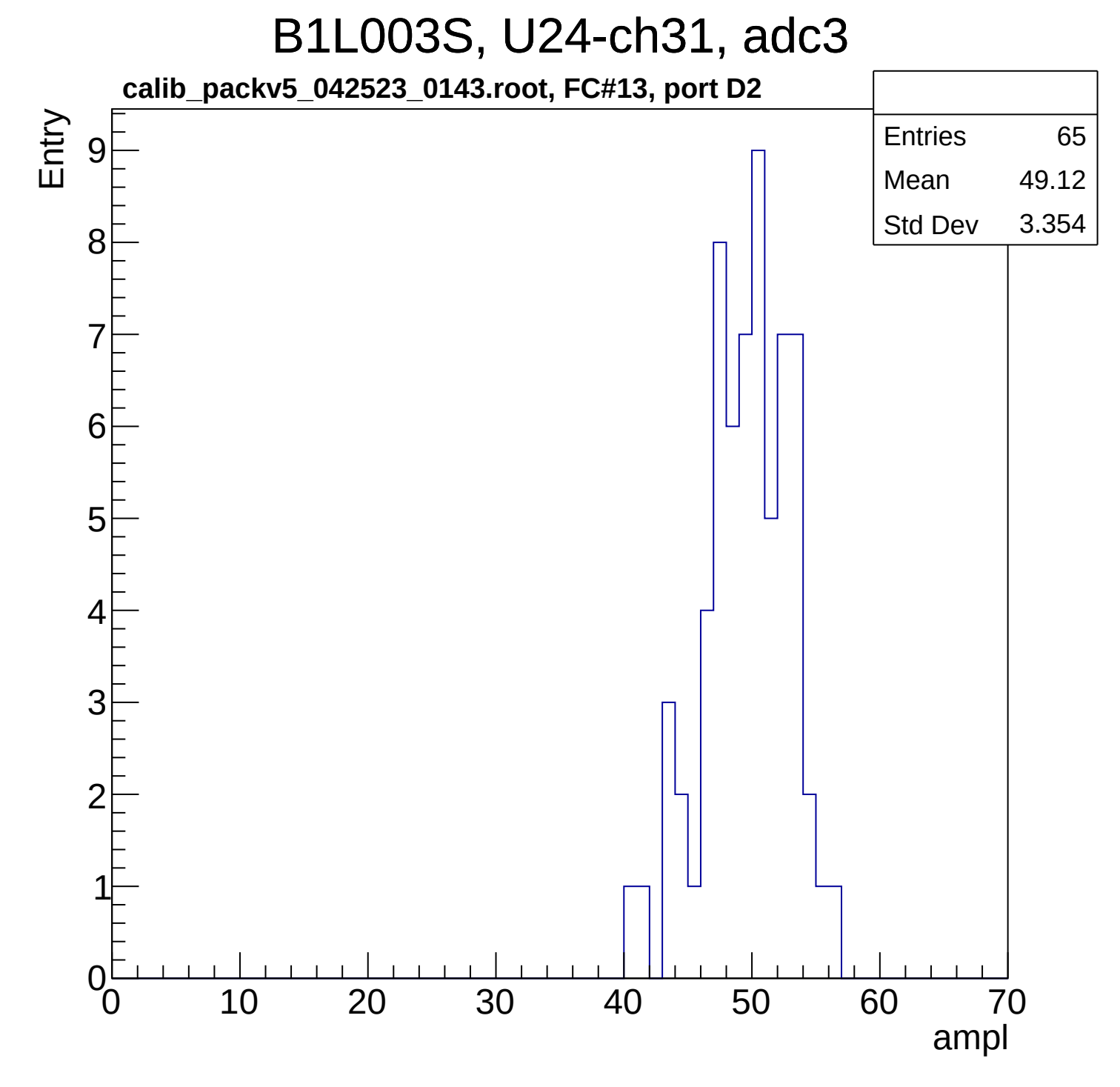
B1L003S, U24-ch31, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 49.12
Std Dev 3.354
Entry
ampl
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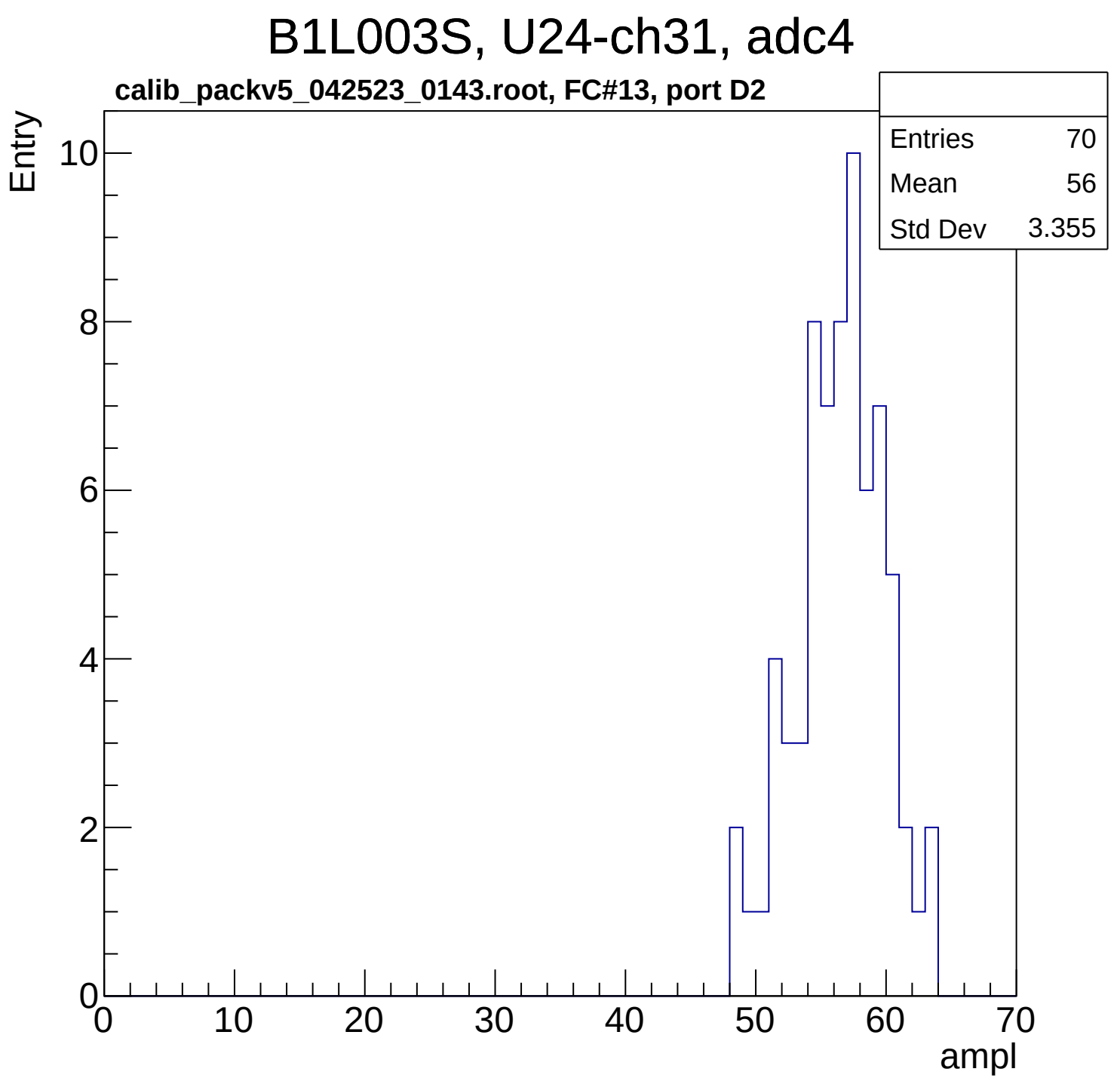
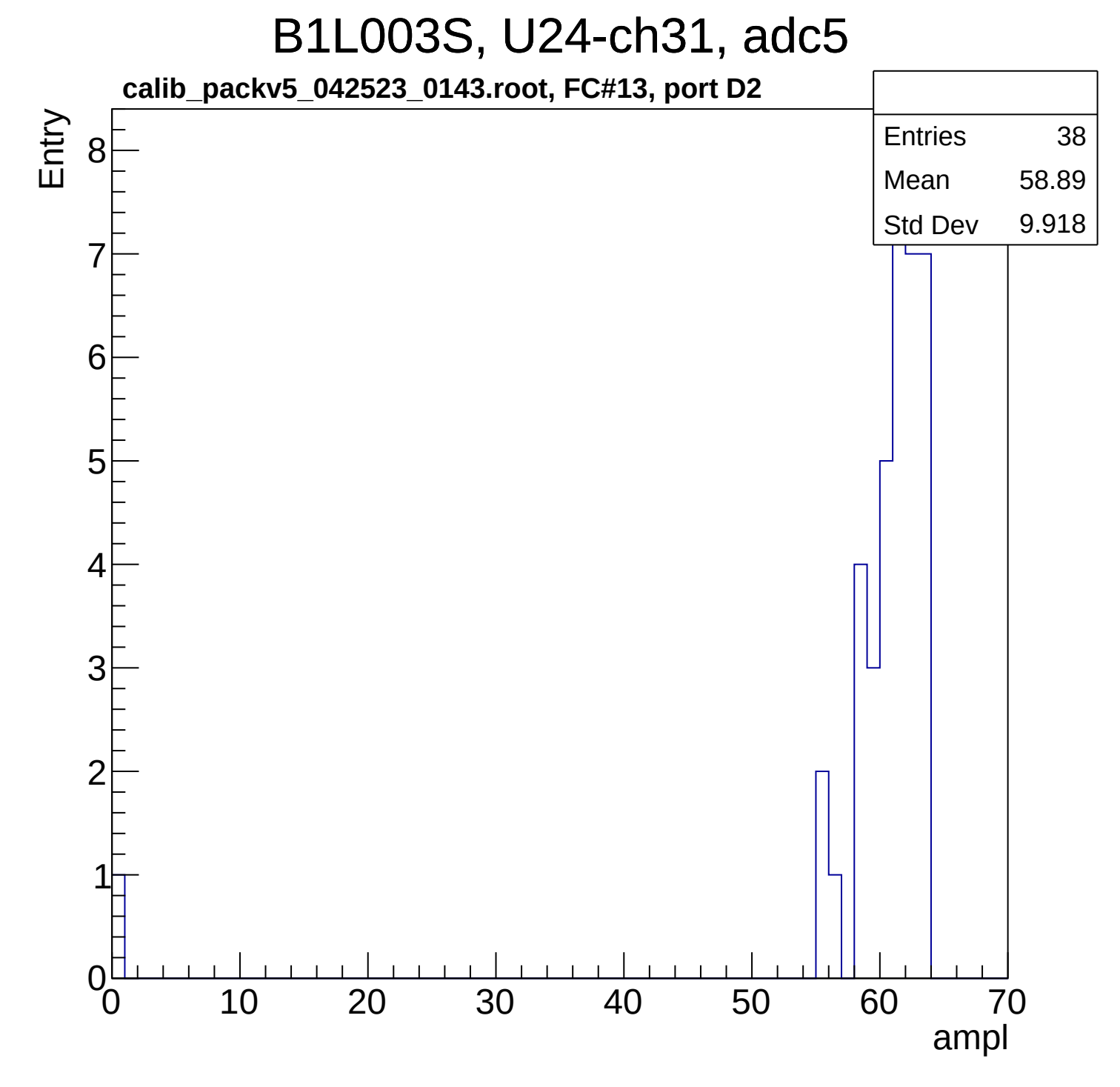

B1L003S, U24-ch31, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 56
Std Dev 3.355
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch31, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 58.89
Std Dev 9.918
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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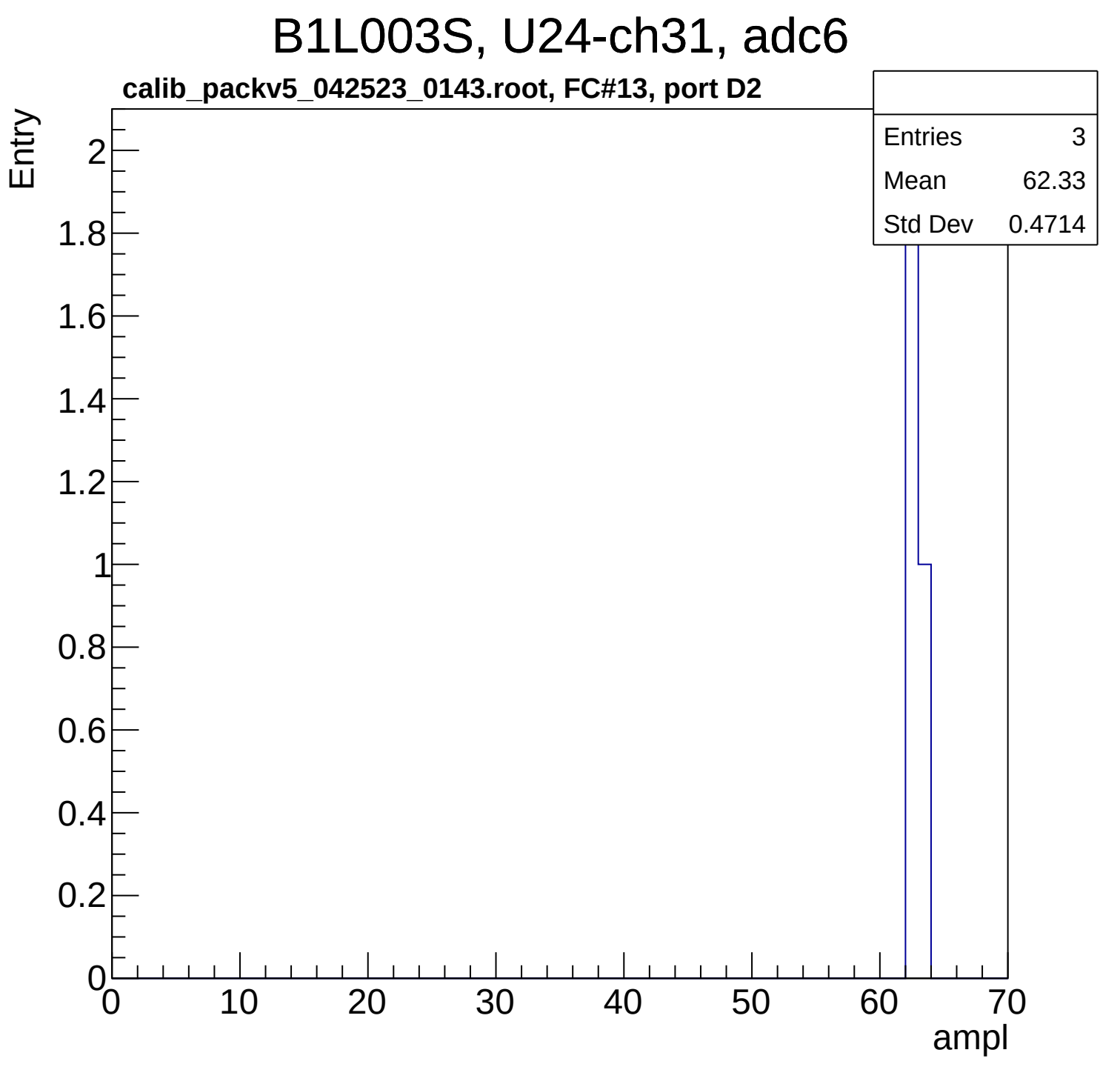
B1L003S, U24-ch31, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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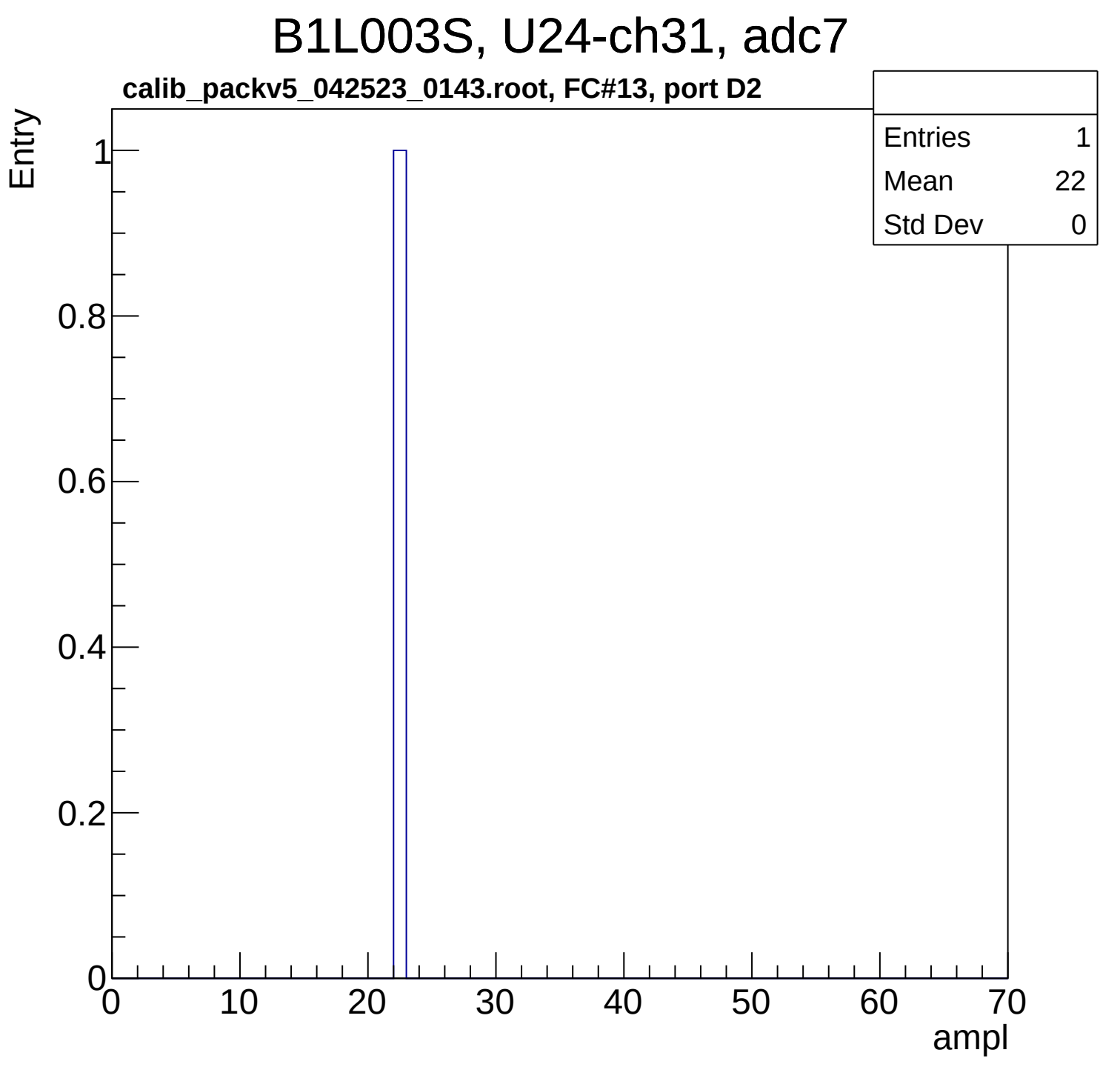
B1L003S, U24-ch31, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
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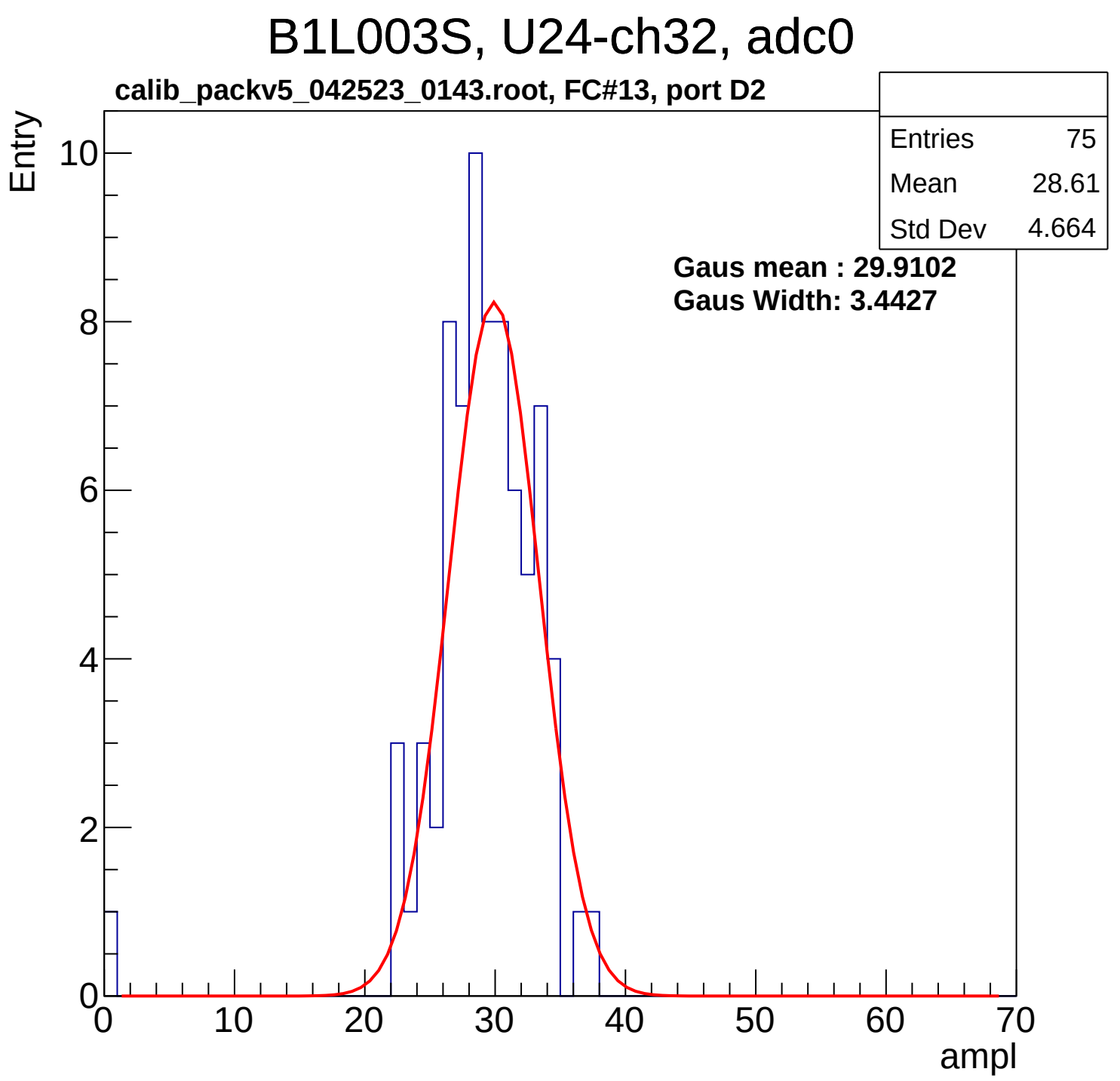
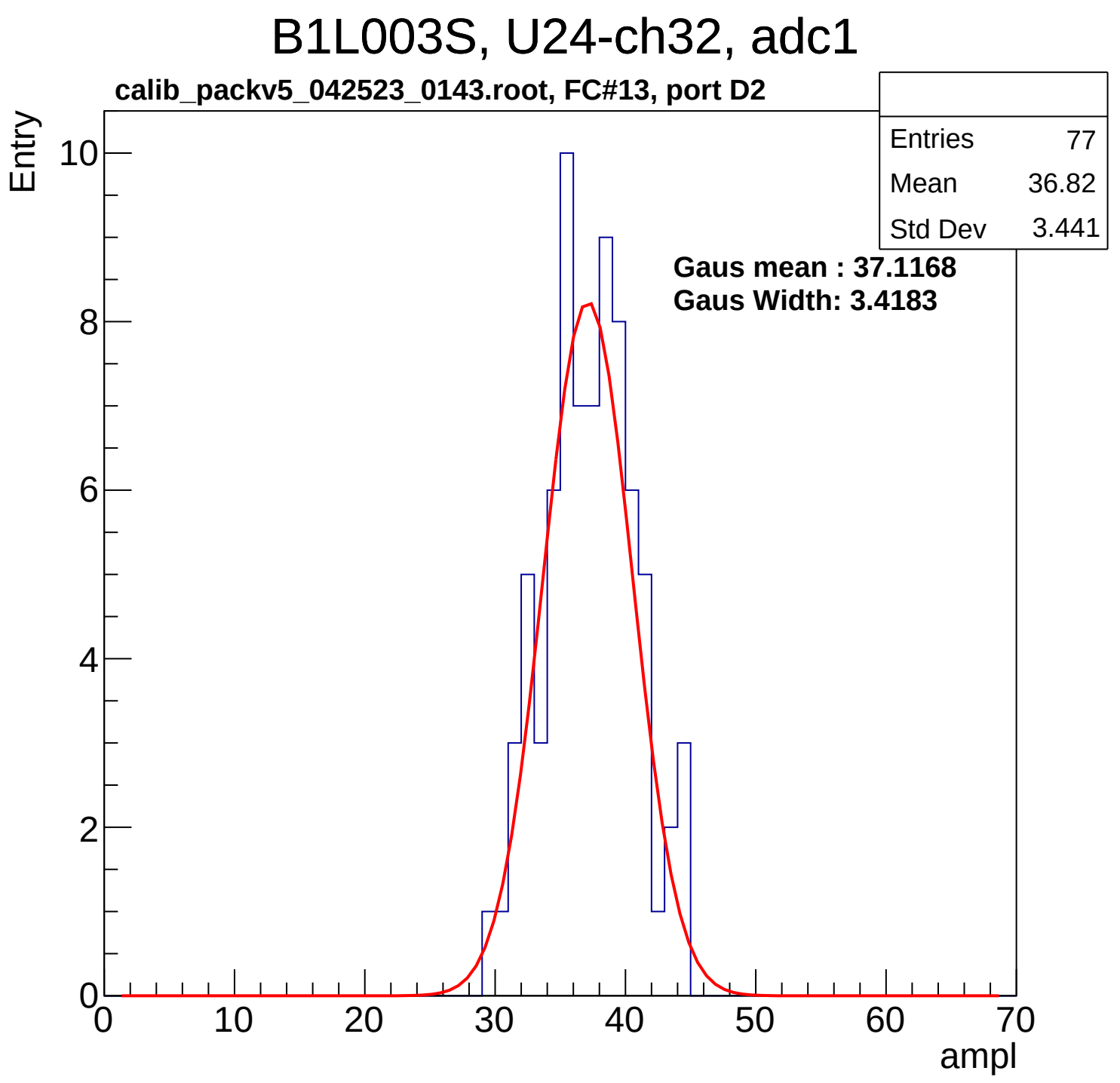
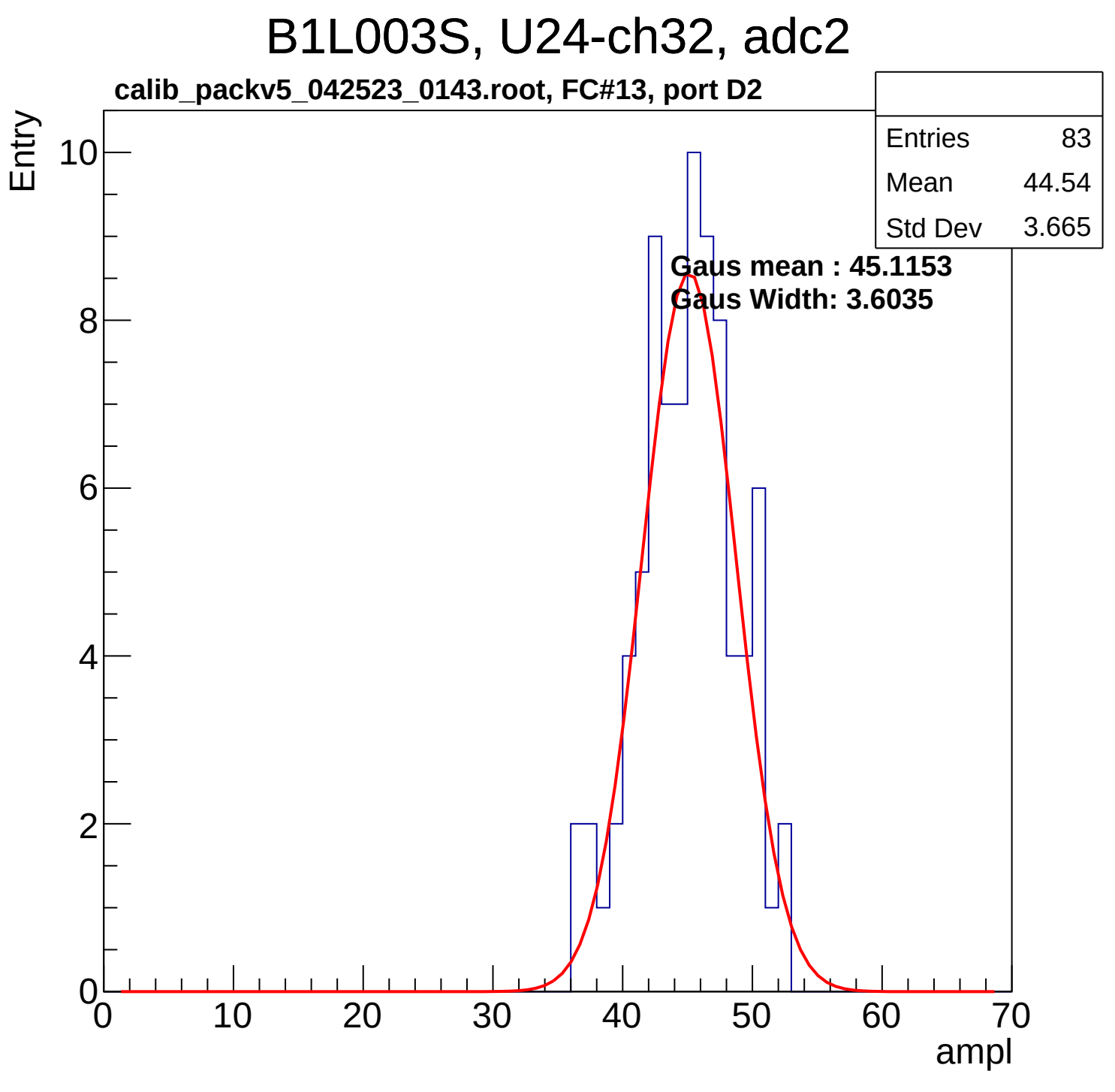
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch32, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.61
Std Dev 4.664
Gaus mean : 29.9102
Gaus Width: 3.4427
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch32, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.82
Std Dev 3.441
Gaus mean : 37.1168
Gaus Width: 3.4183
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch32, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 44.54
Std Dev 3.665
Gaus mean : 45.1153
Gaus Width: 3.6035
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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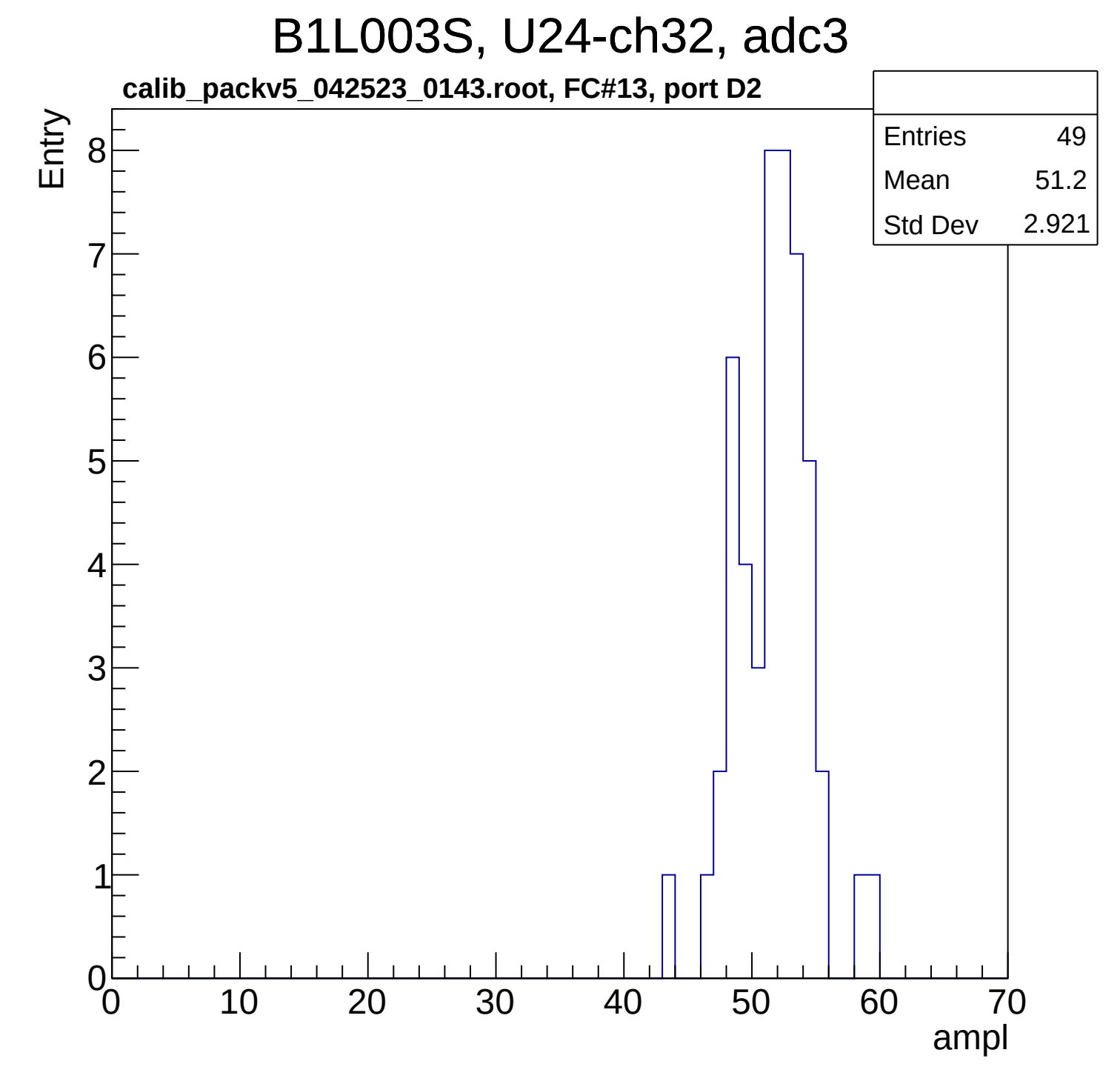
B1L003S, U24-ch32, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 51.2
Std Dev 2.921
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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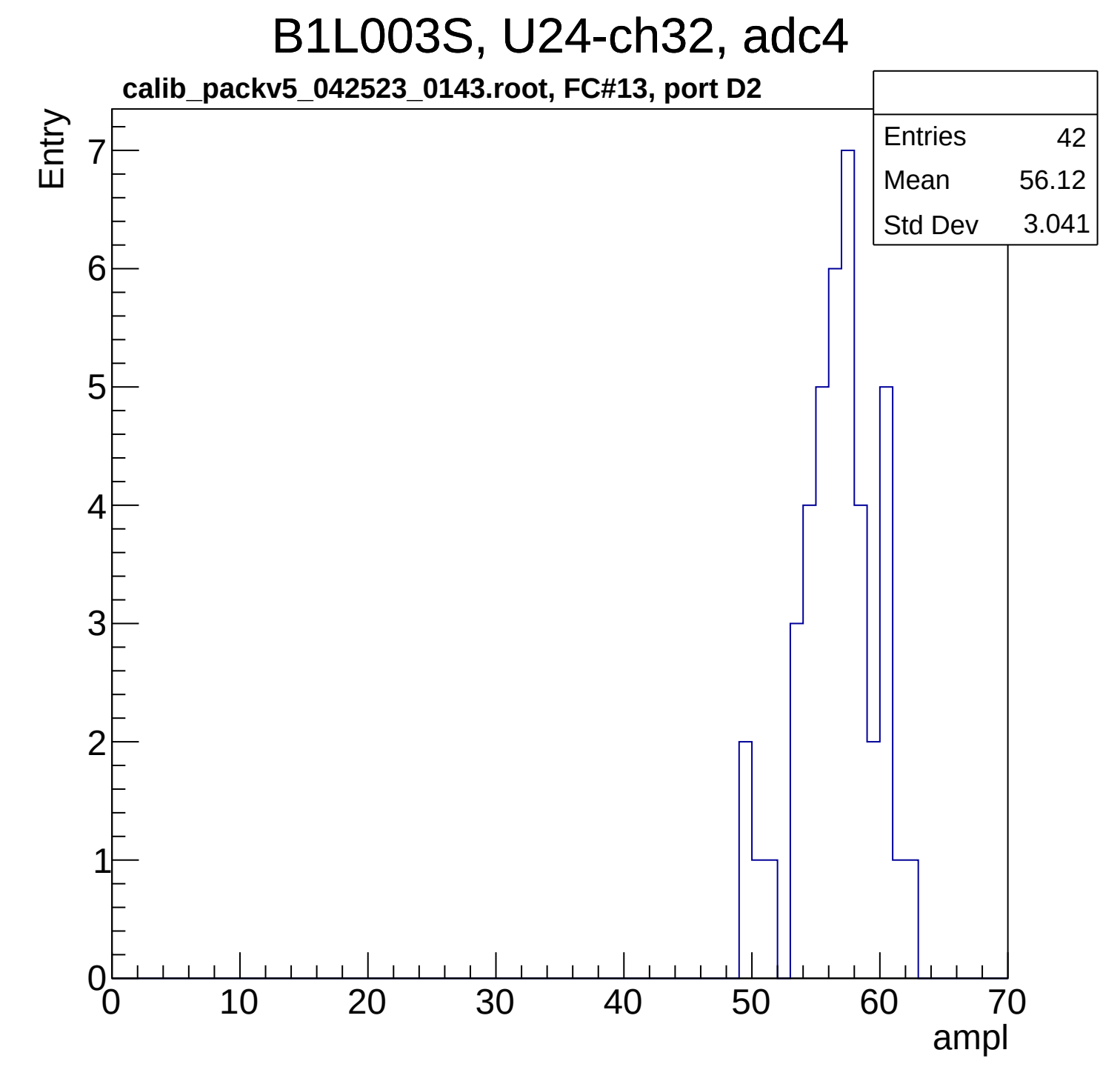
B1L003S, U24-ch32, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 56.12
Std Dev 3.041
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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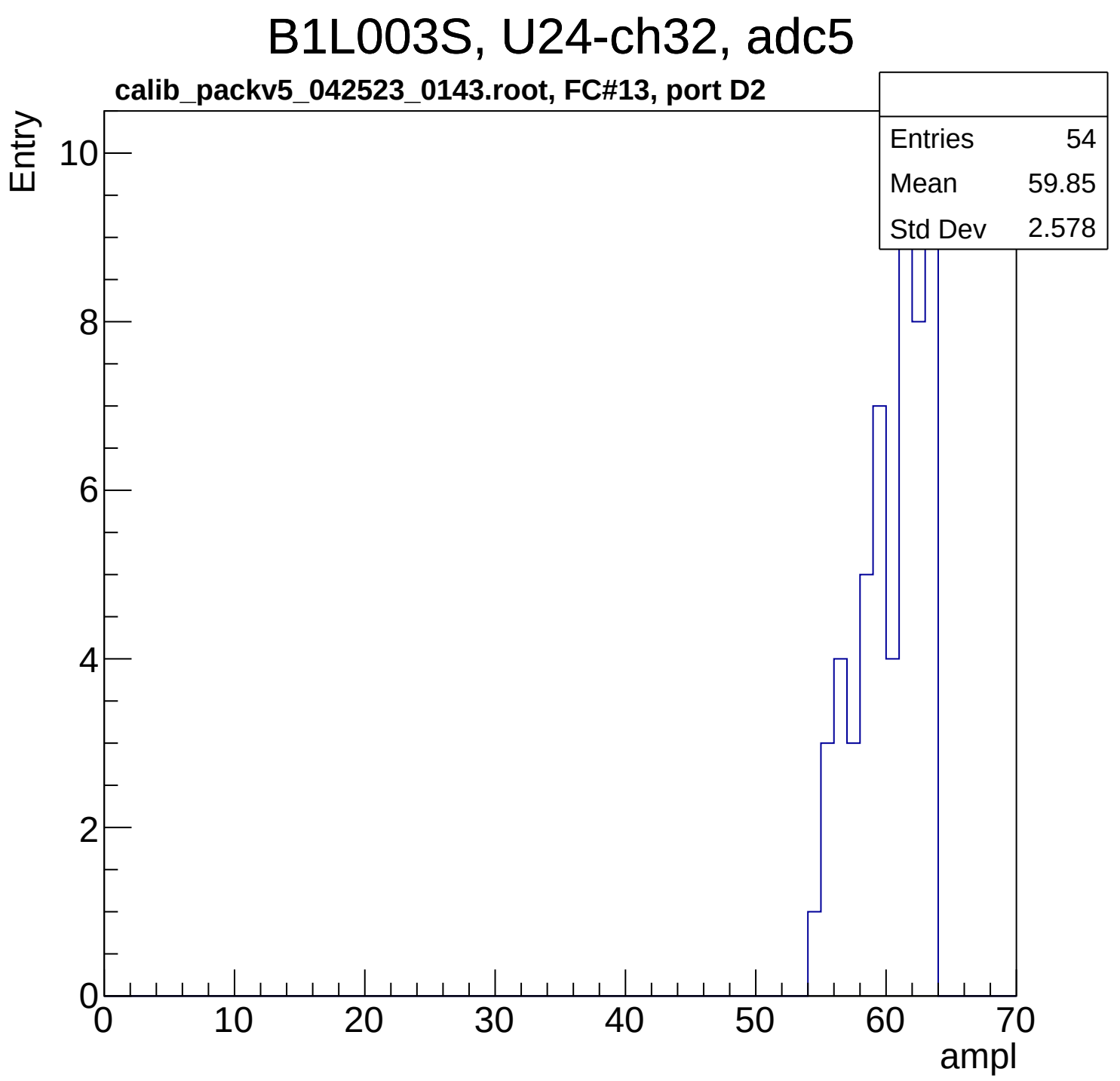
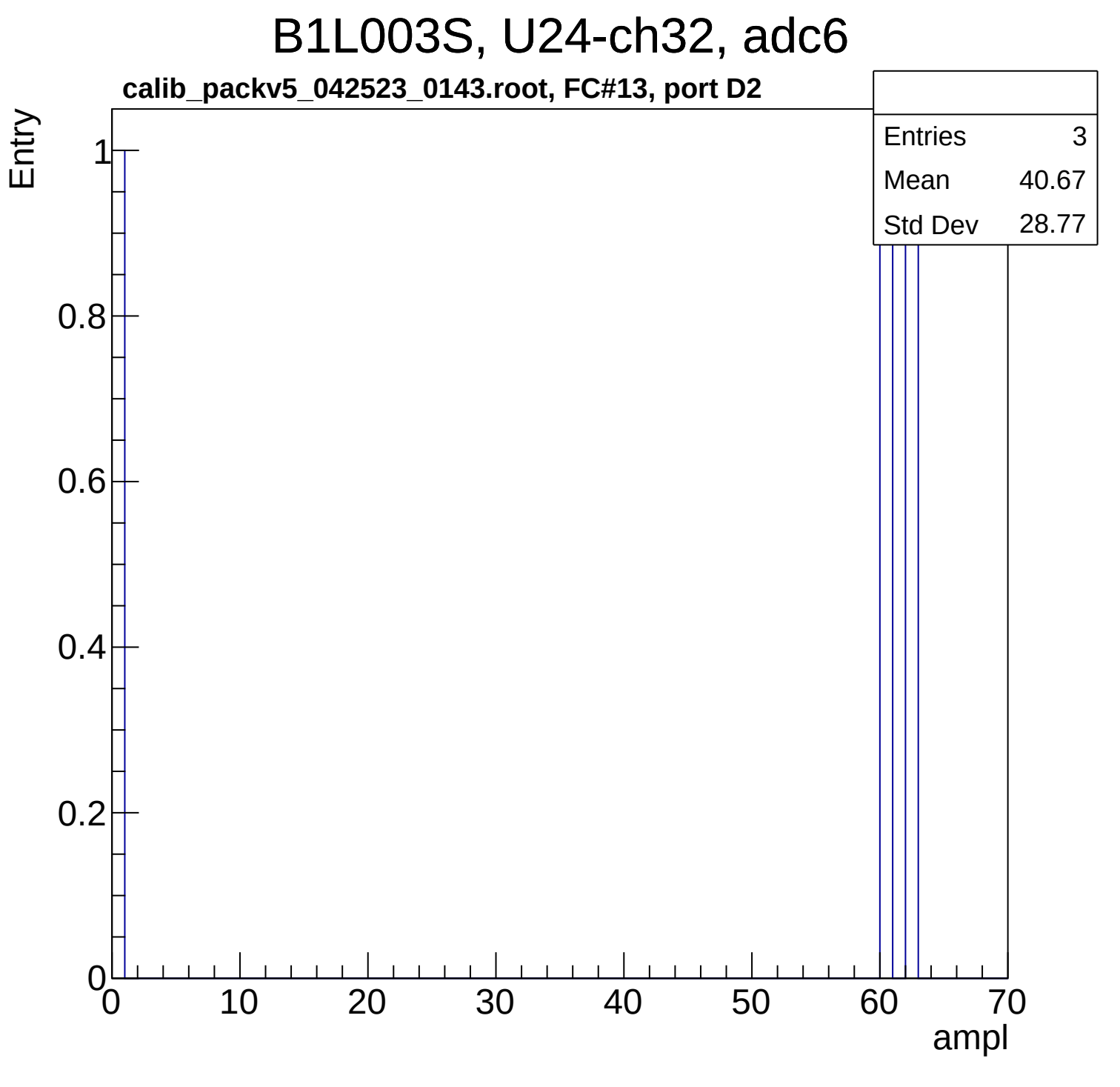
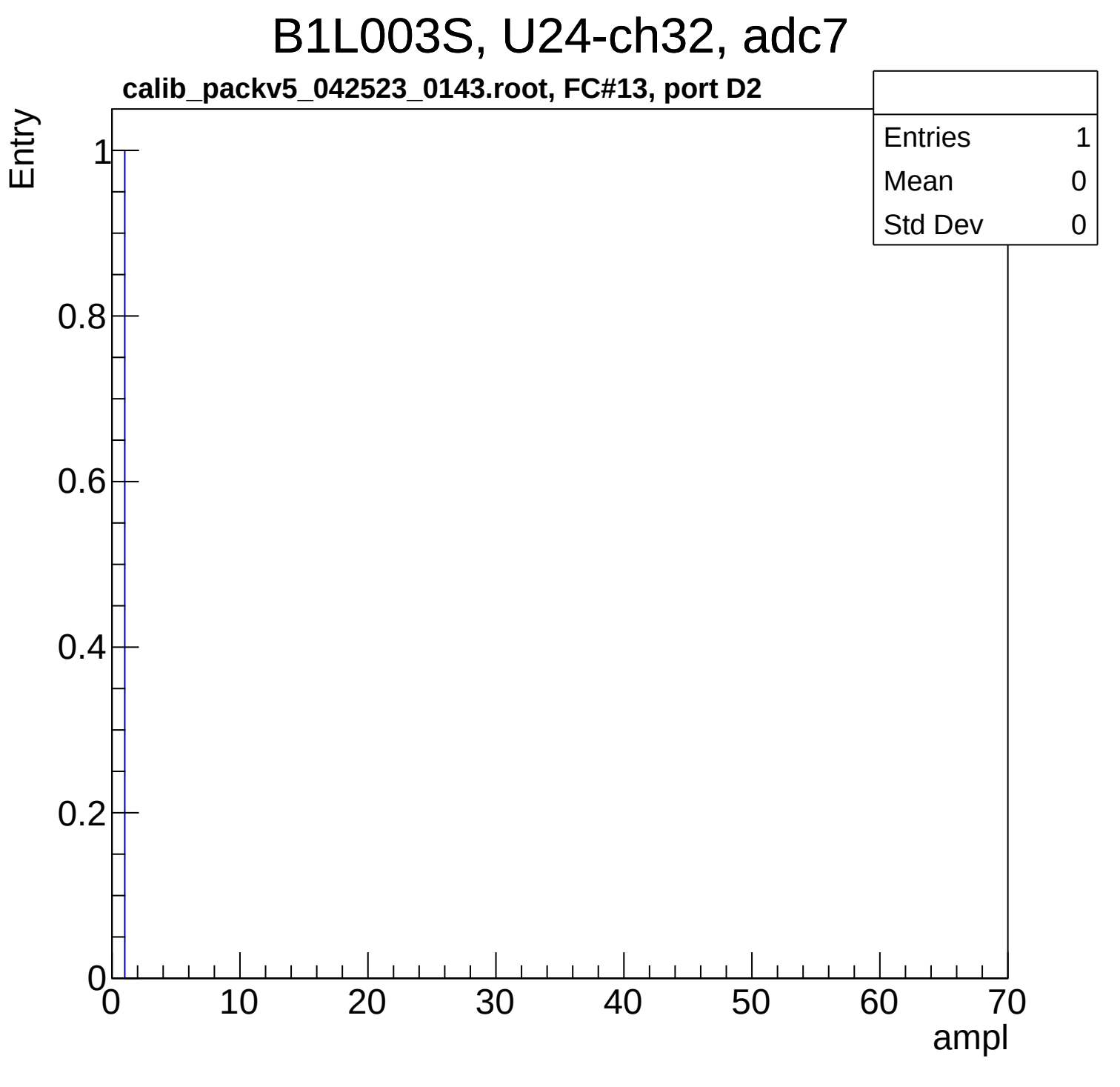
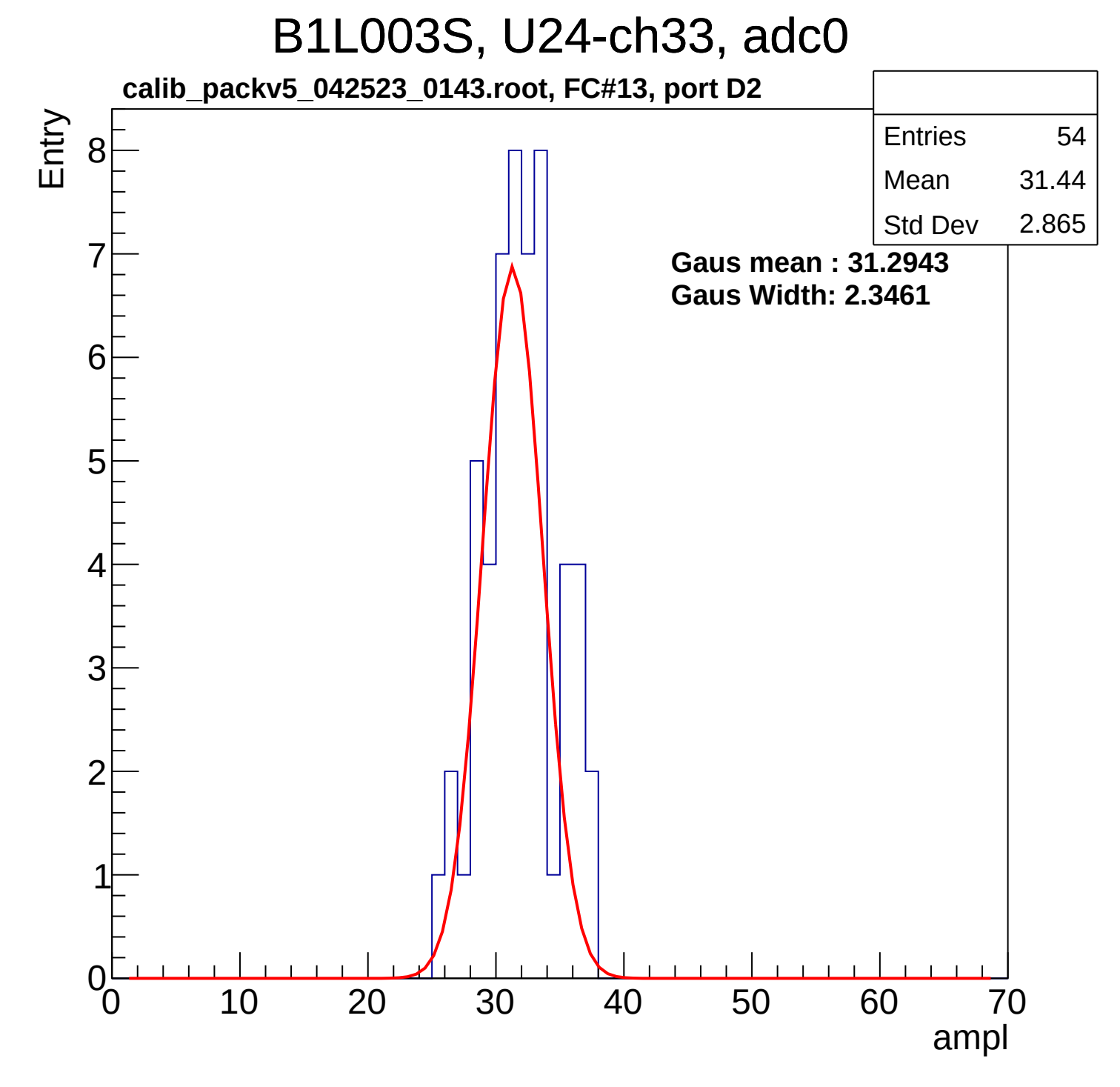

B1L003S, U24-ch32, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.85
Std Dev 2.578
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch32, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 40.67
Std Dev 28.77
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch32, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch33, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 31.44
Std Dev 2.865
Gaus mean : 31.2943
Gaus Width: 2.3461
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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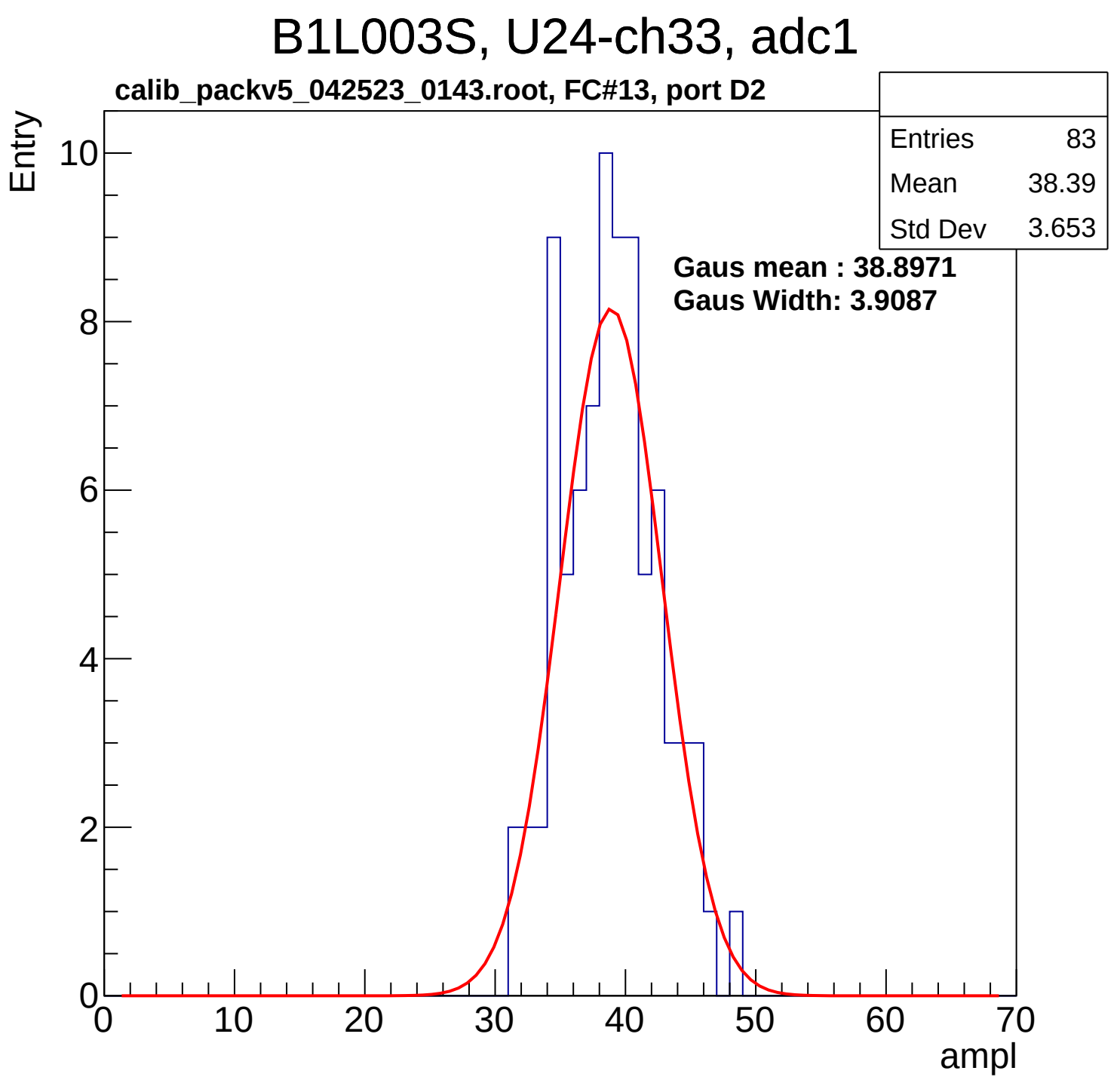
B1L003S, U24-ch33, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 38.39
Std Dev 3.653
Gaus mean : 38.8971
Gaus Width: 3.9087
Entry
ampl
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4
6
8
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0
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40
50
60
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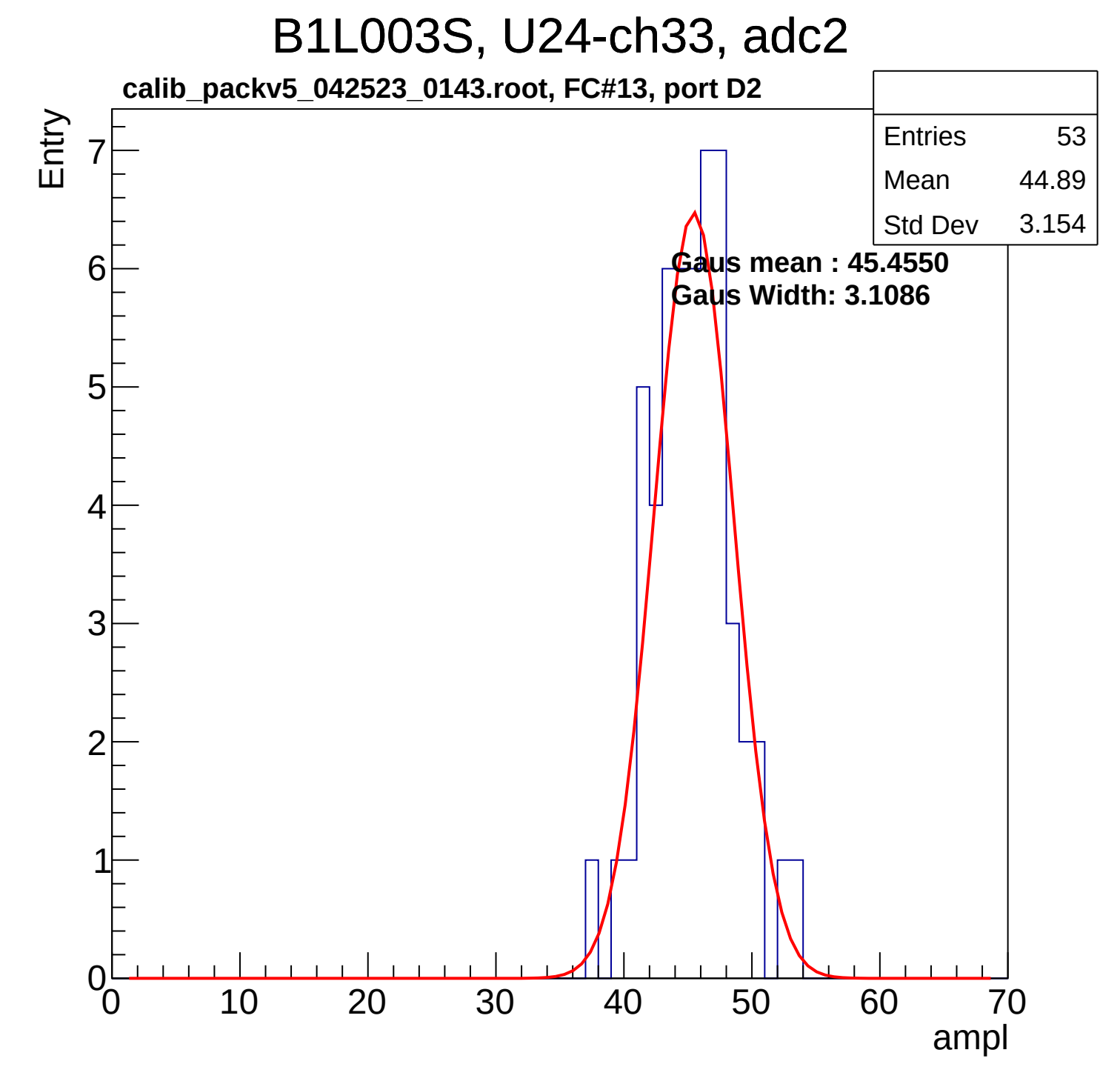
B1L003S, U24-ch33, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 44.89
Std Dev 3.154
Gaus mean : 45.4550
Gaus Width: 3.1086
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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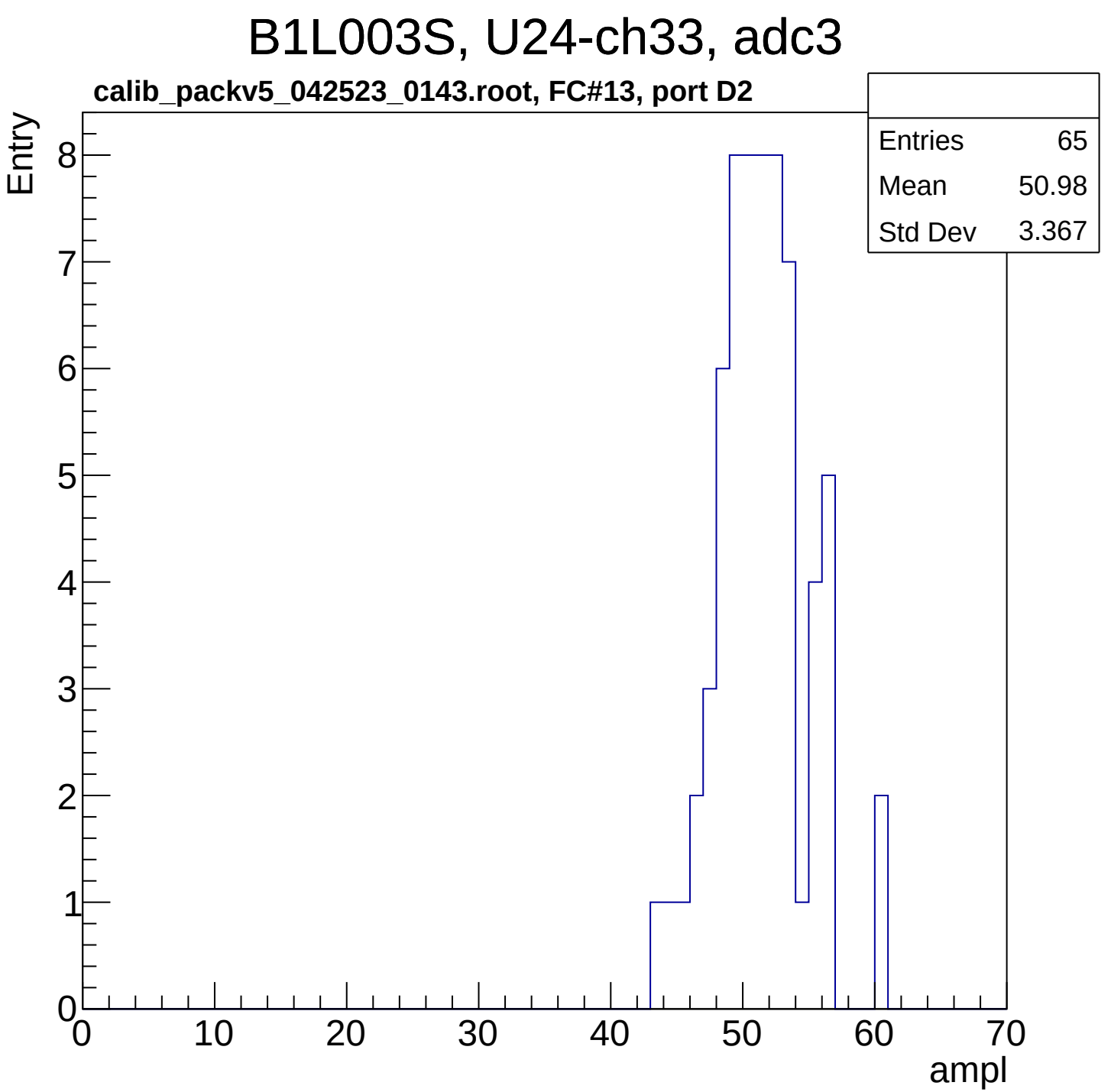
B1L003S, U24-ch33, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 50.98
Std Dev 3.367
Entry
ampl
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5
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0
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60
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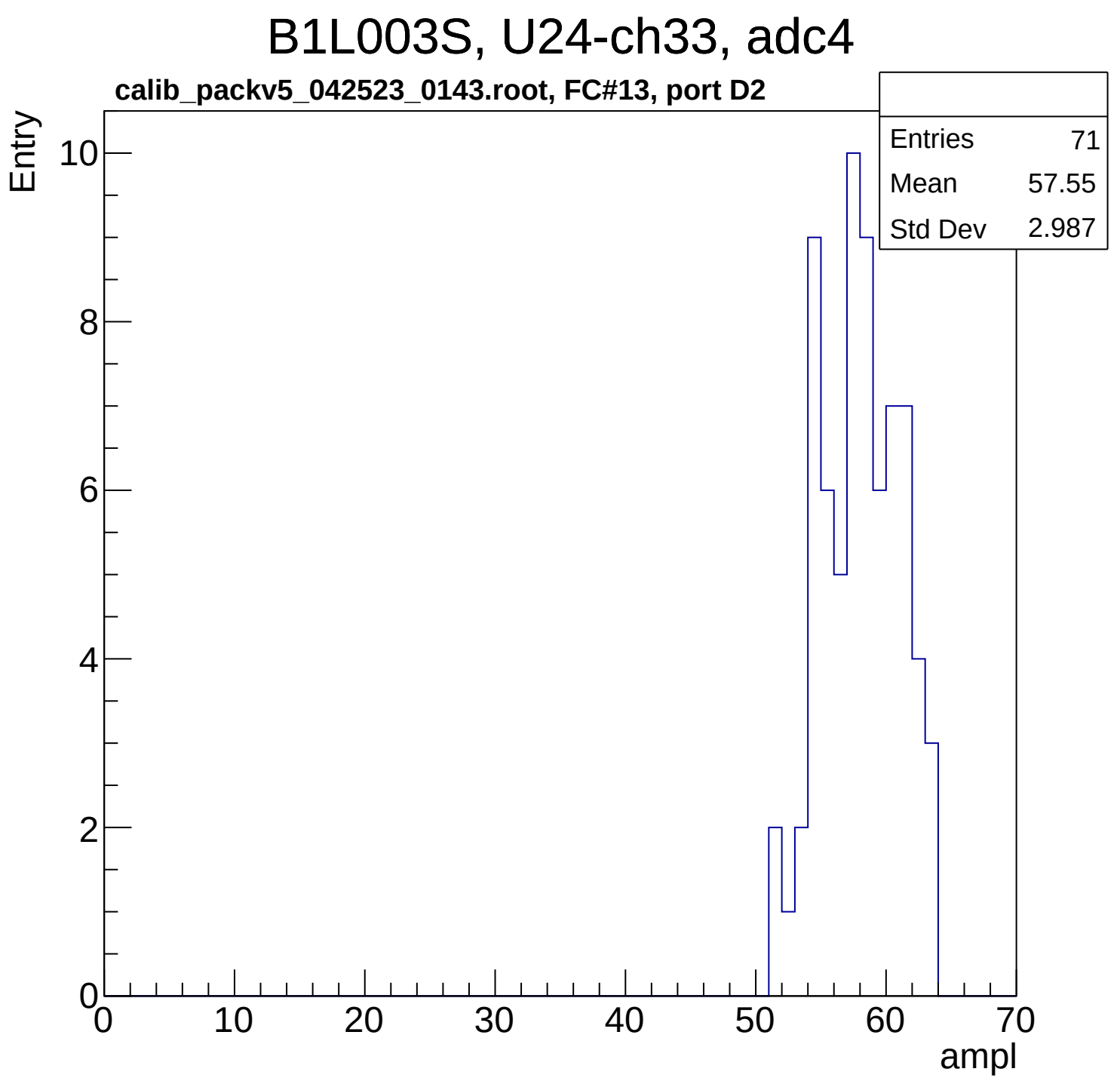
B1L003S, U24-ch33, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 57.55
Std Dev 2.987
Entry
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6
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10
0
10
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50
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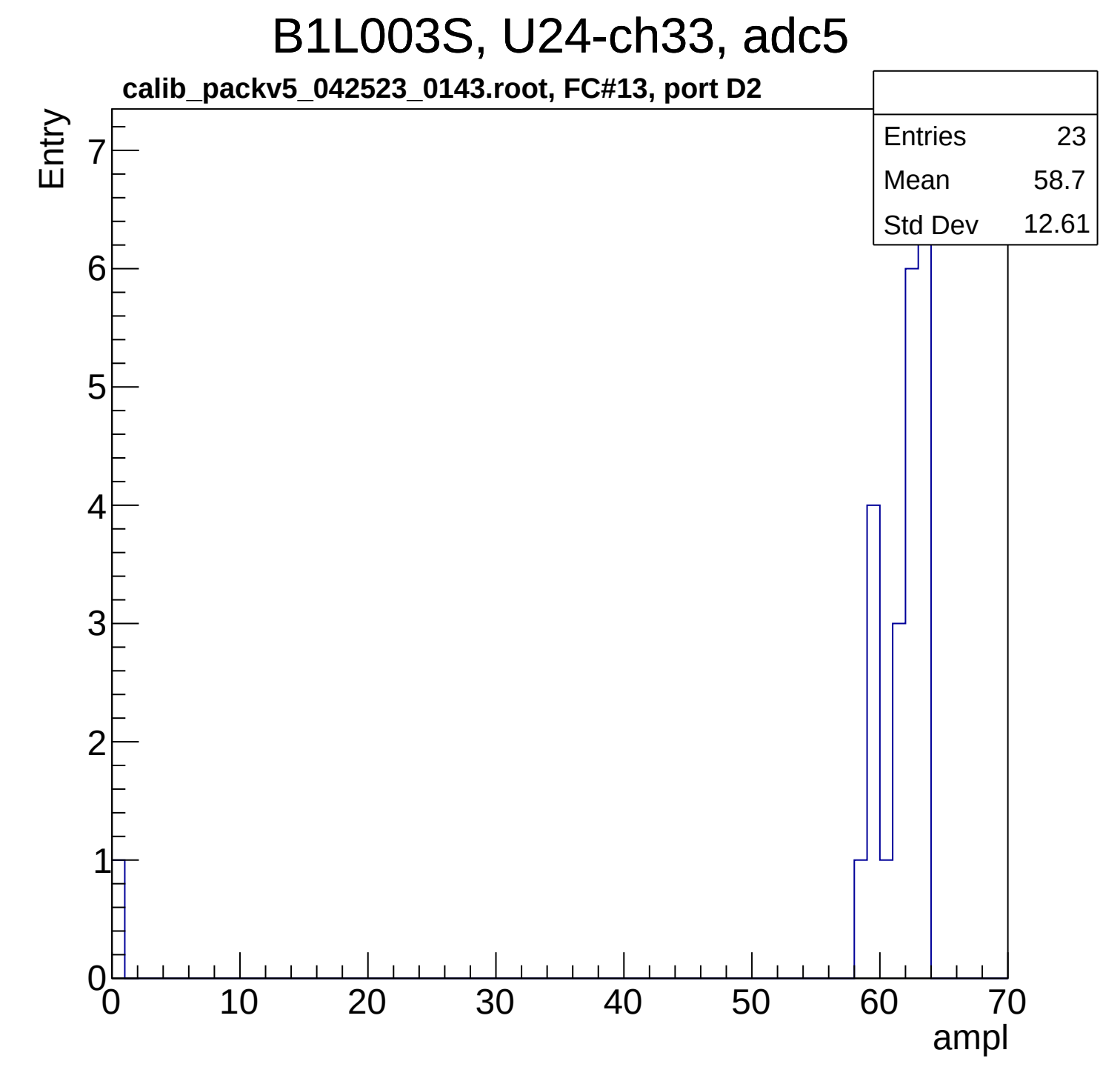
70
ampl


B1L003S, U24-ch33, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 23
Mean 58.7
Std Dev 12.61
Entry
ampl
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3
4
5
6
7
0
10
20
30
40
50
60
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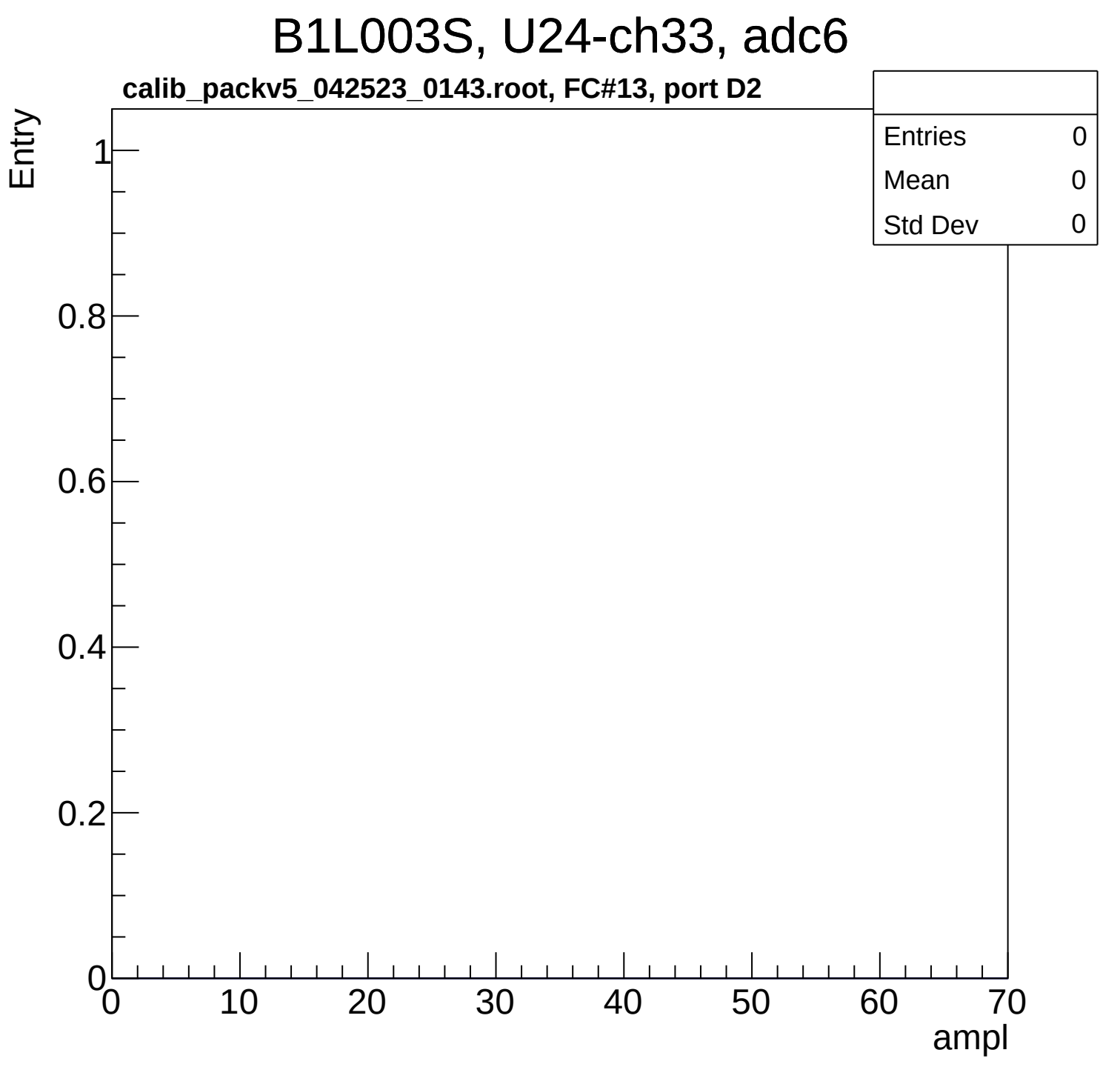
B1L003S, U24-ch33, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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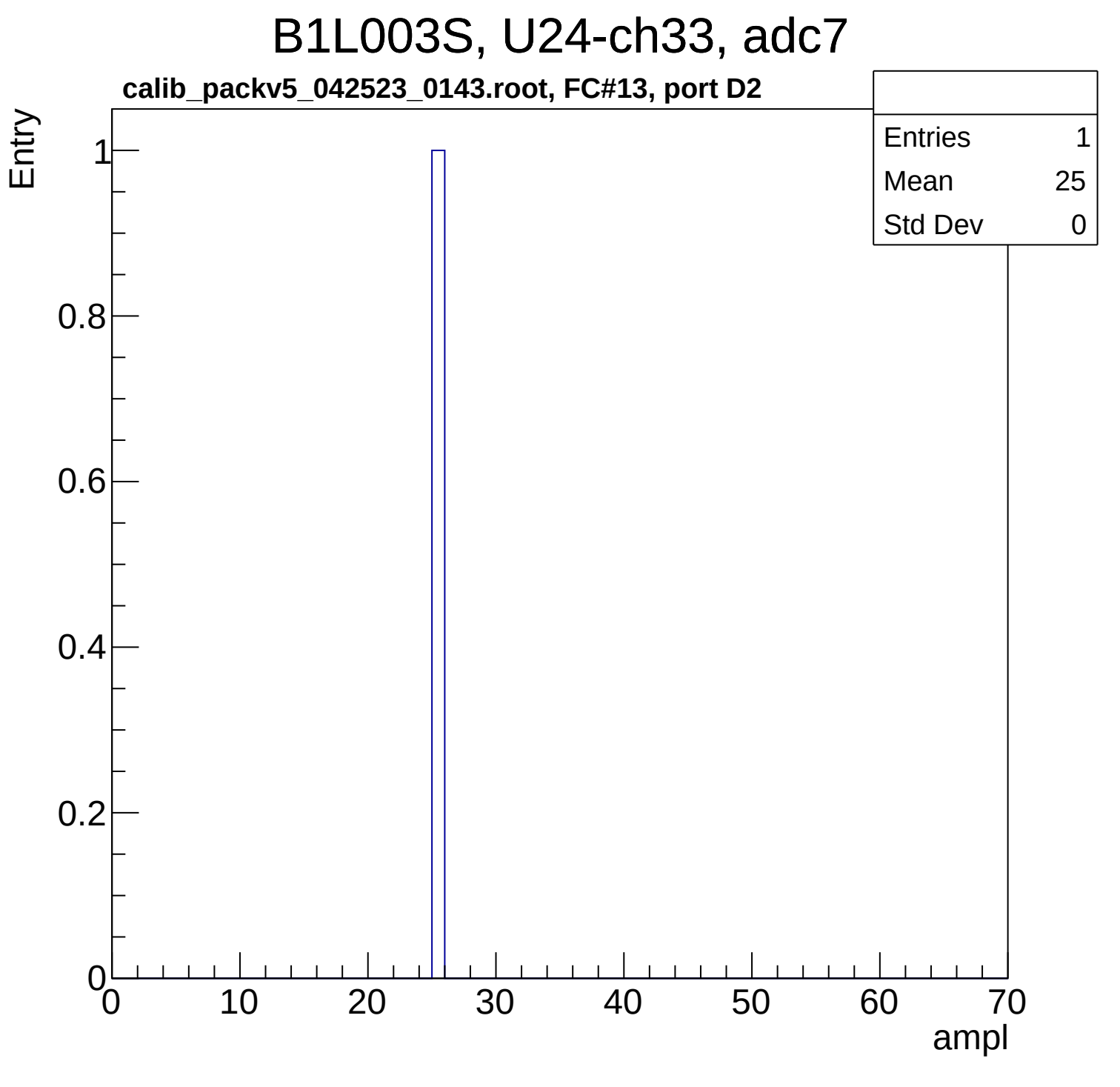
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch33, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 25
Std Dev 0
Entry
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0.4
0.2
0
0
10
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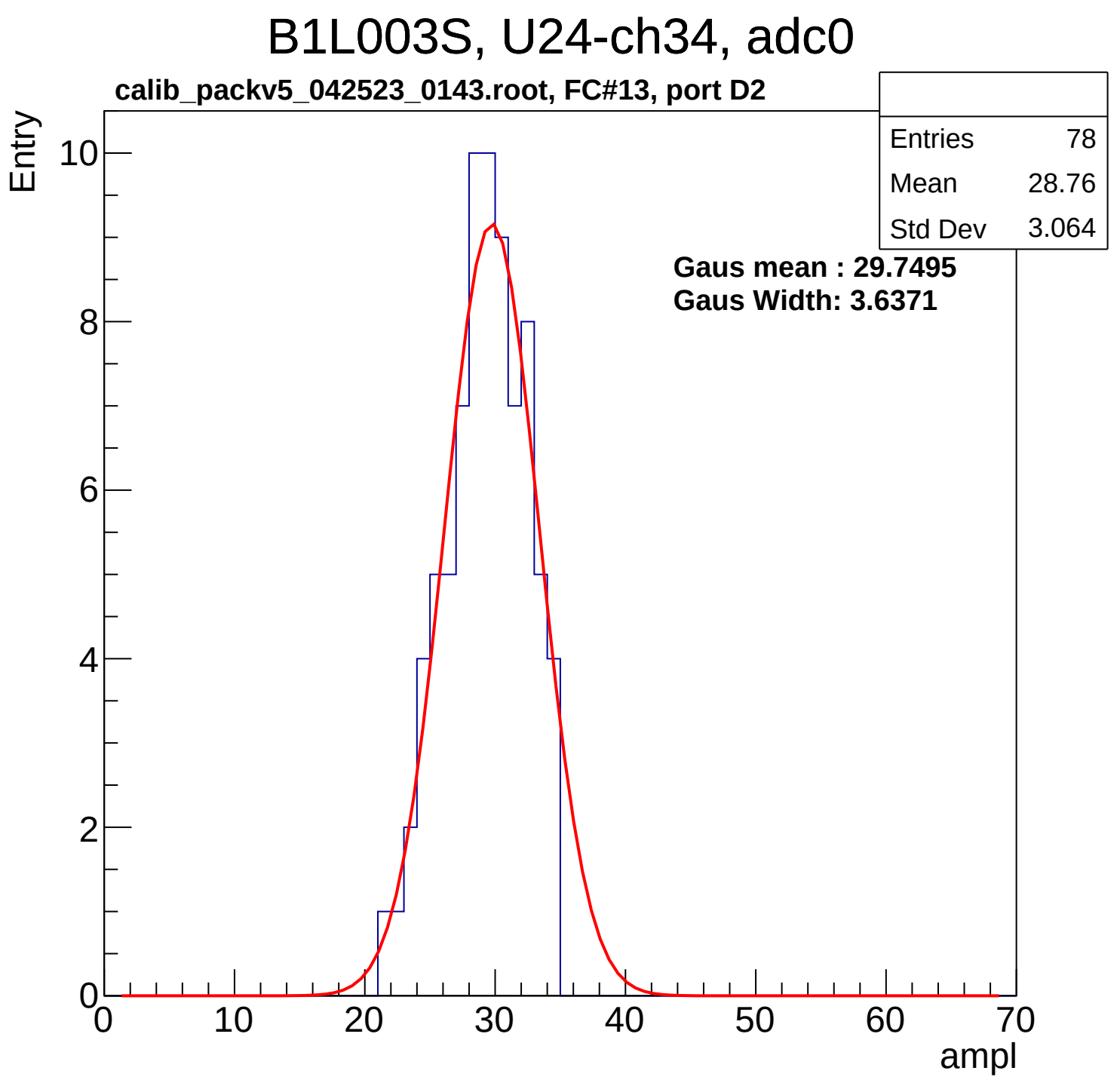
30
40
50
60
70
ampl


B1L003S, U24-ch34, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 28.76
Std Dev 3.064
Gaus mean : 29.7495
Gaus Width: 3.6371
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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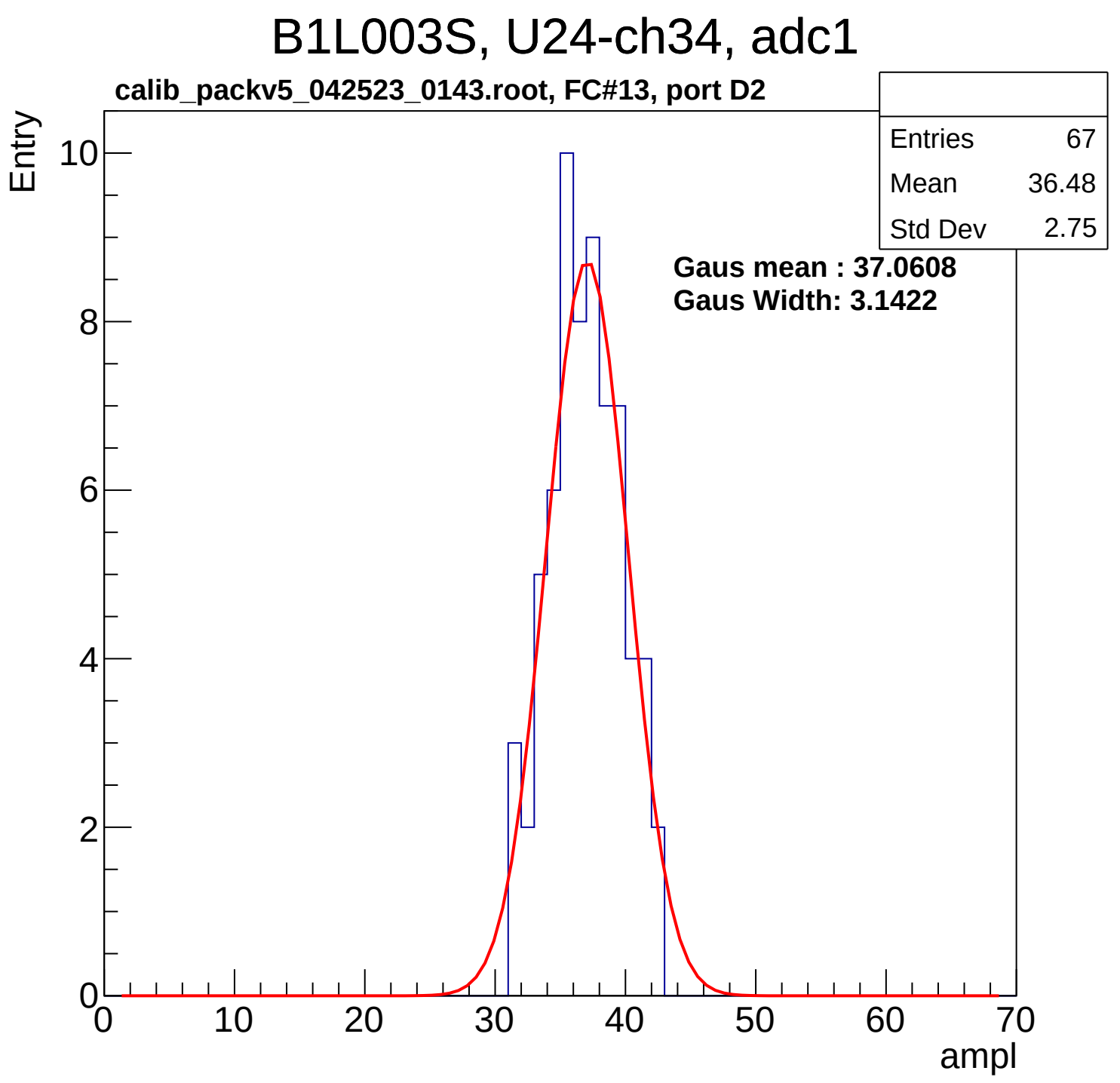
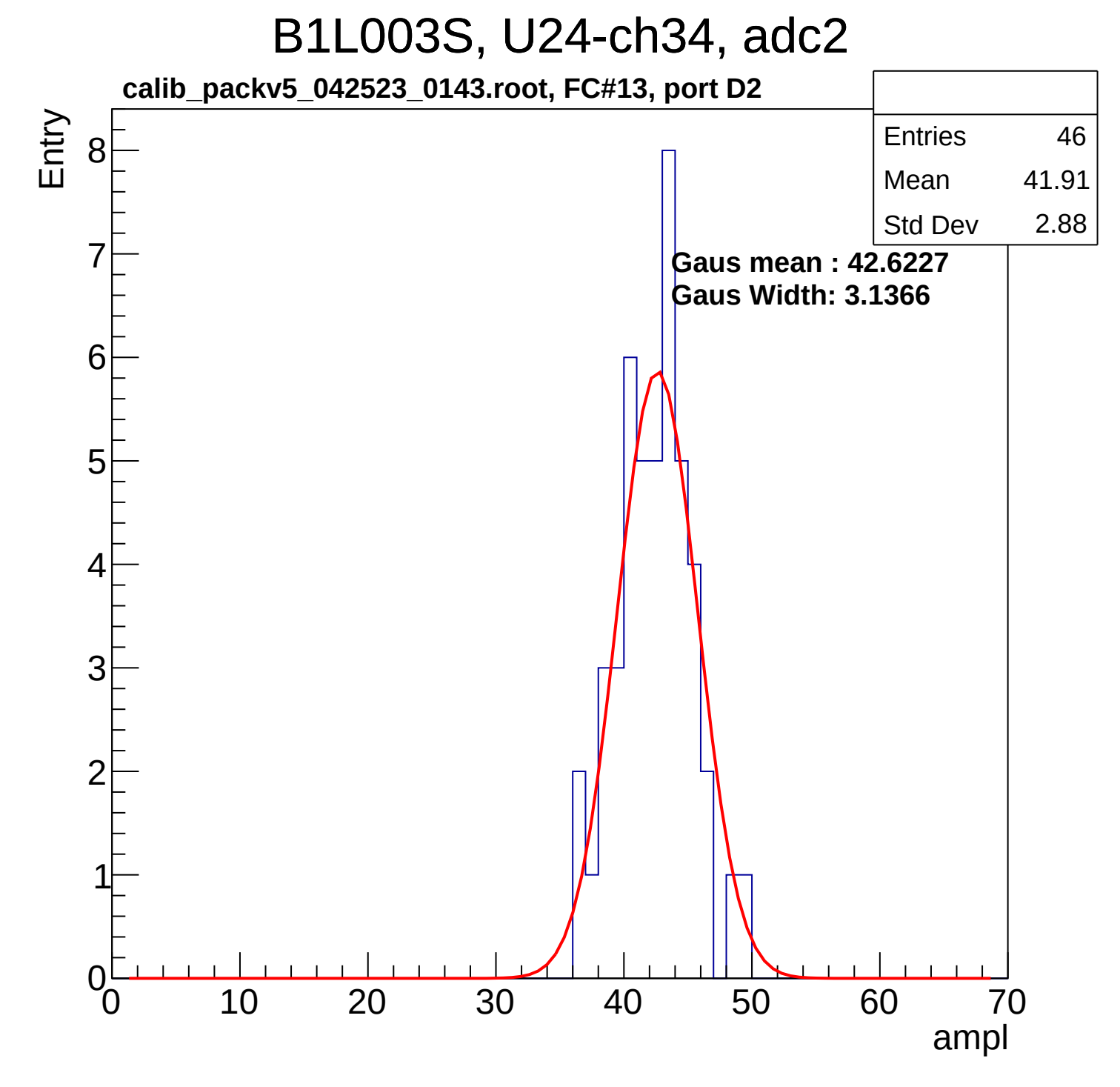

B1L003S, U24-ch34, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.48
Std Dev 2.75
Gaus mean : 37.0608
Gaus Width: 3.1422
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch34, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 41.91
Std Dev 2.88
Gaus mean : 42.6227
Gaus Width: 3.1366
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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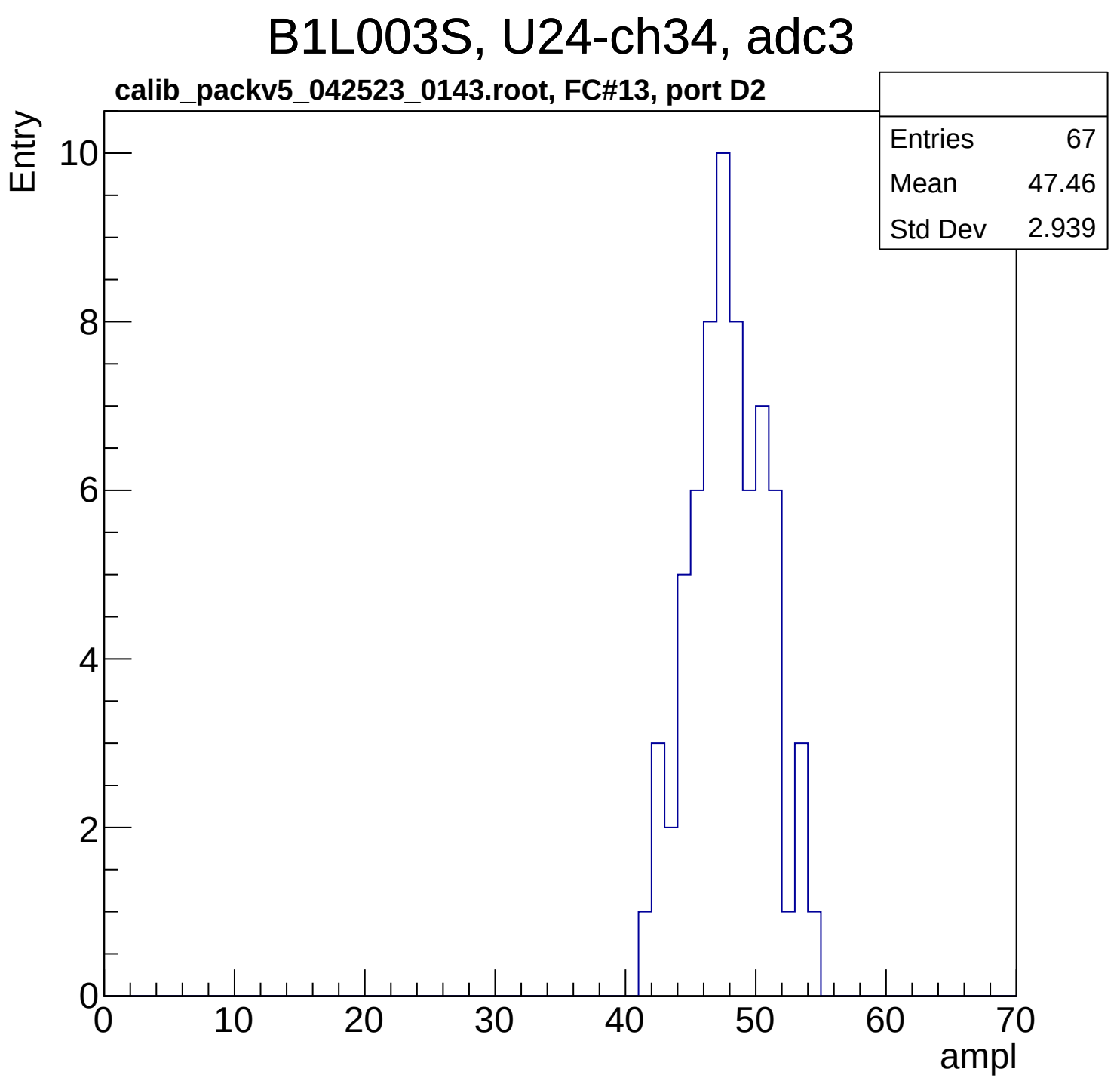
B1L003S, U24-ch34, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 47.46
Std Dev 2.939
Entry
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50
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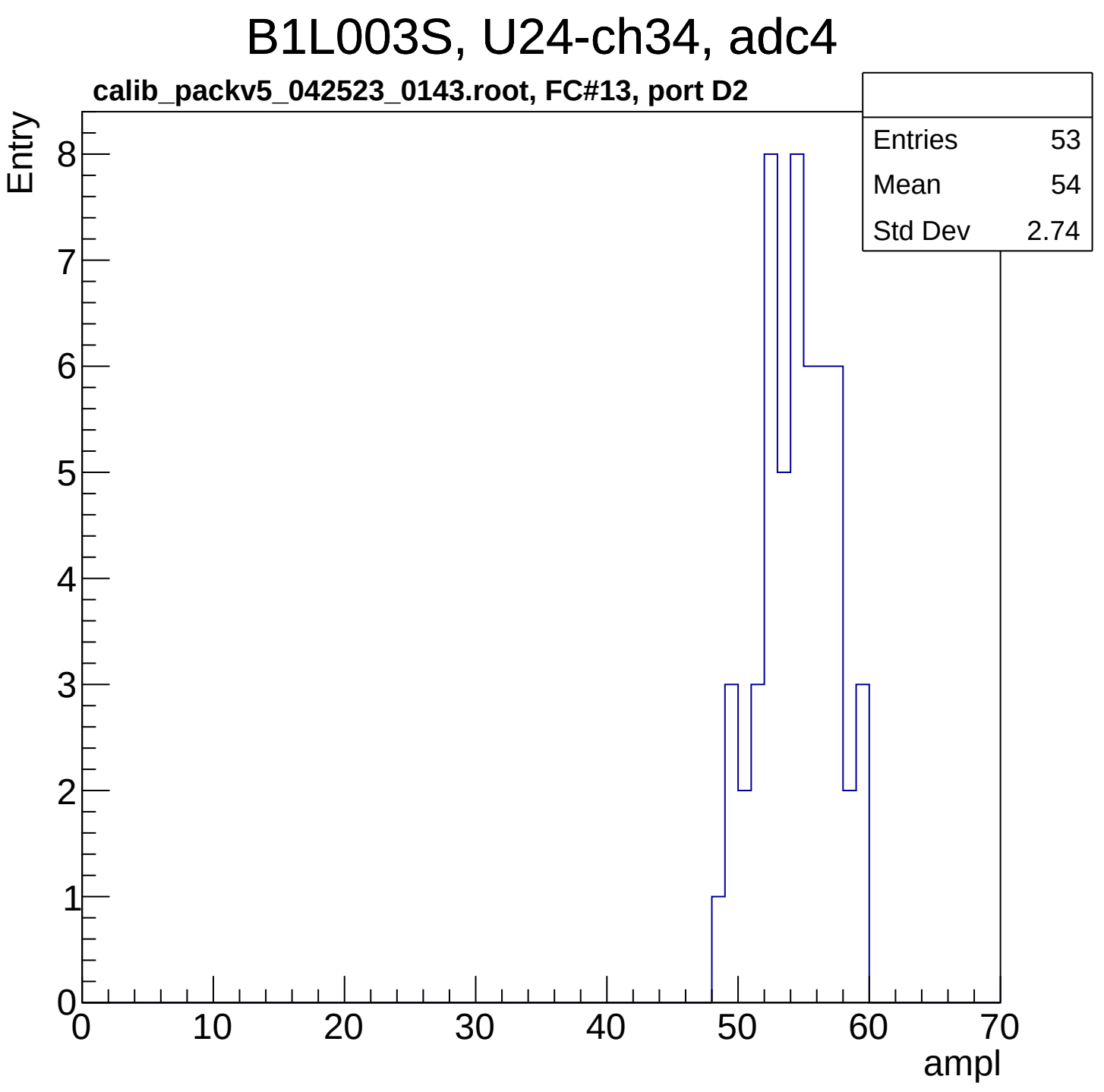
70
ampl


B1L003S, U24-ch34, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54
Std Dev 2.74
Entry
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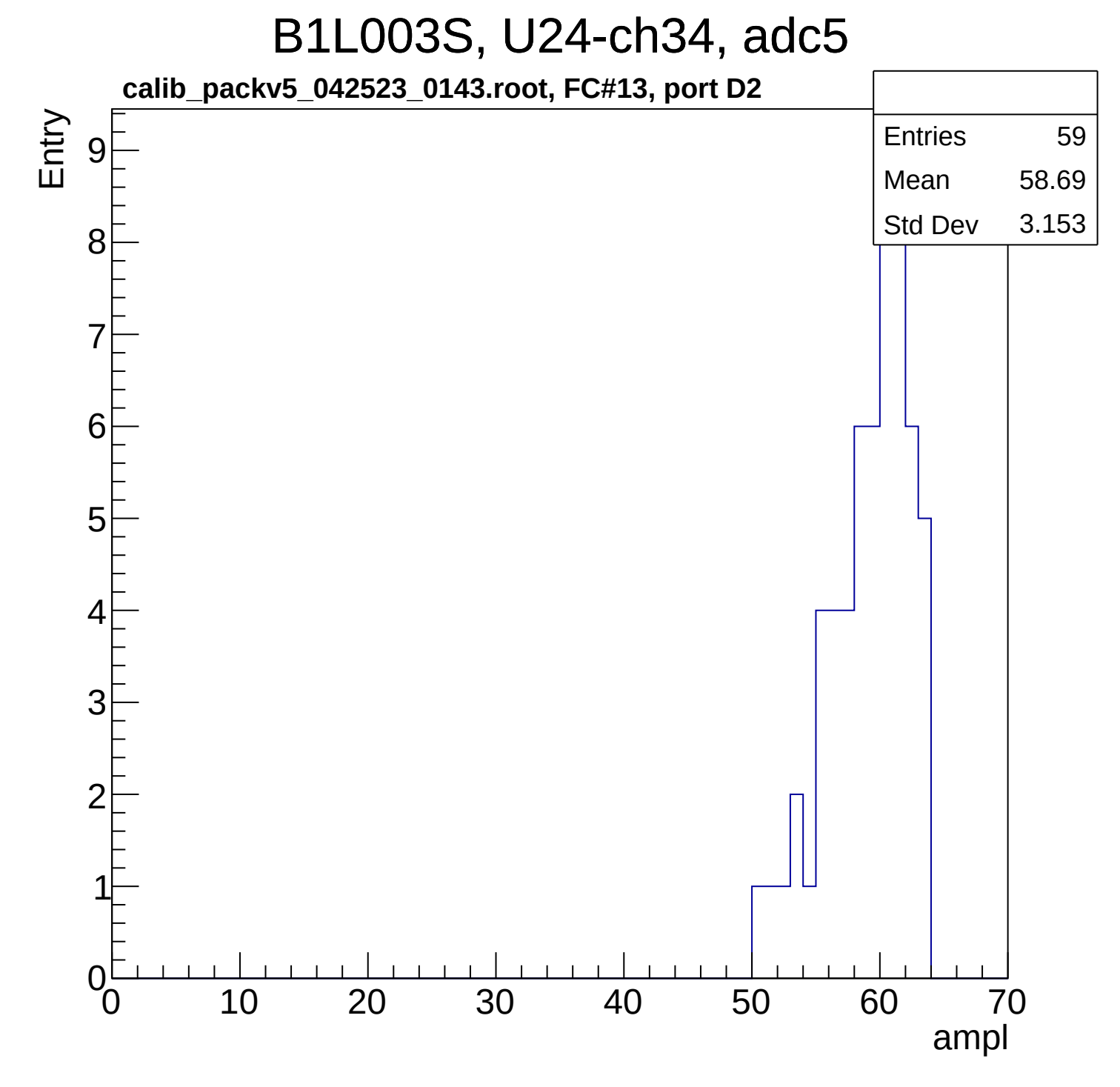
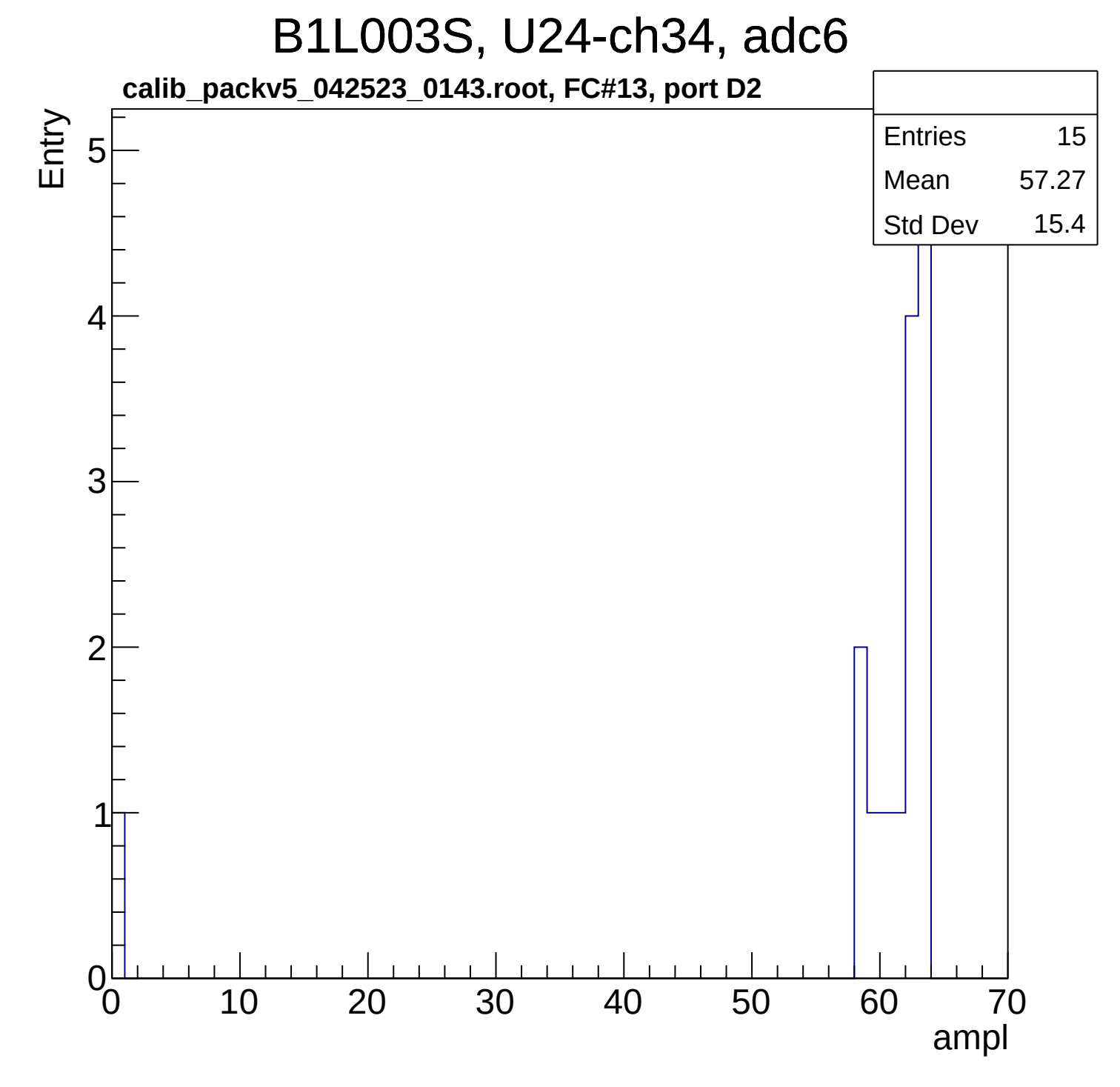
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch34, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.69
Std Dev 3.153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch34, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.27
Std Dev 15.4
Entry
0
1
2
3
4
5
0
10
20
30
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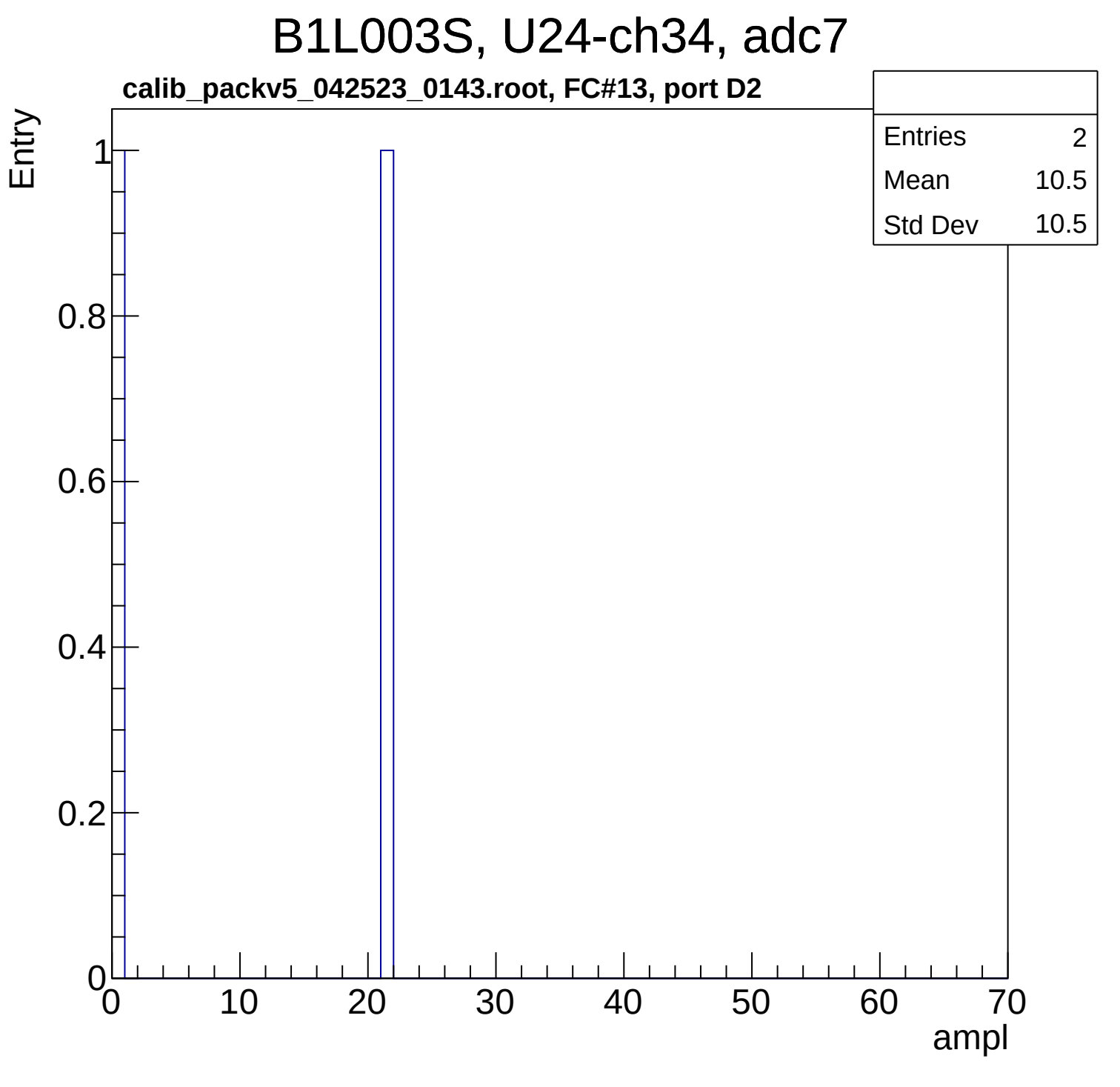
50
60
70
ampl


B1L003S, U24-ch34, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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20
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40
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60
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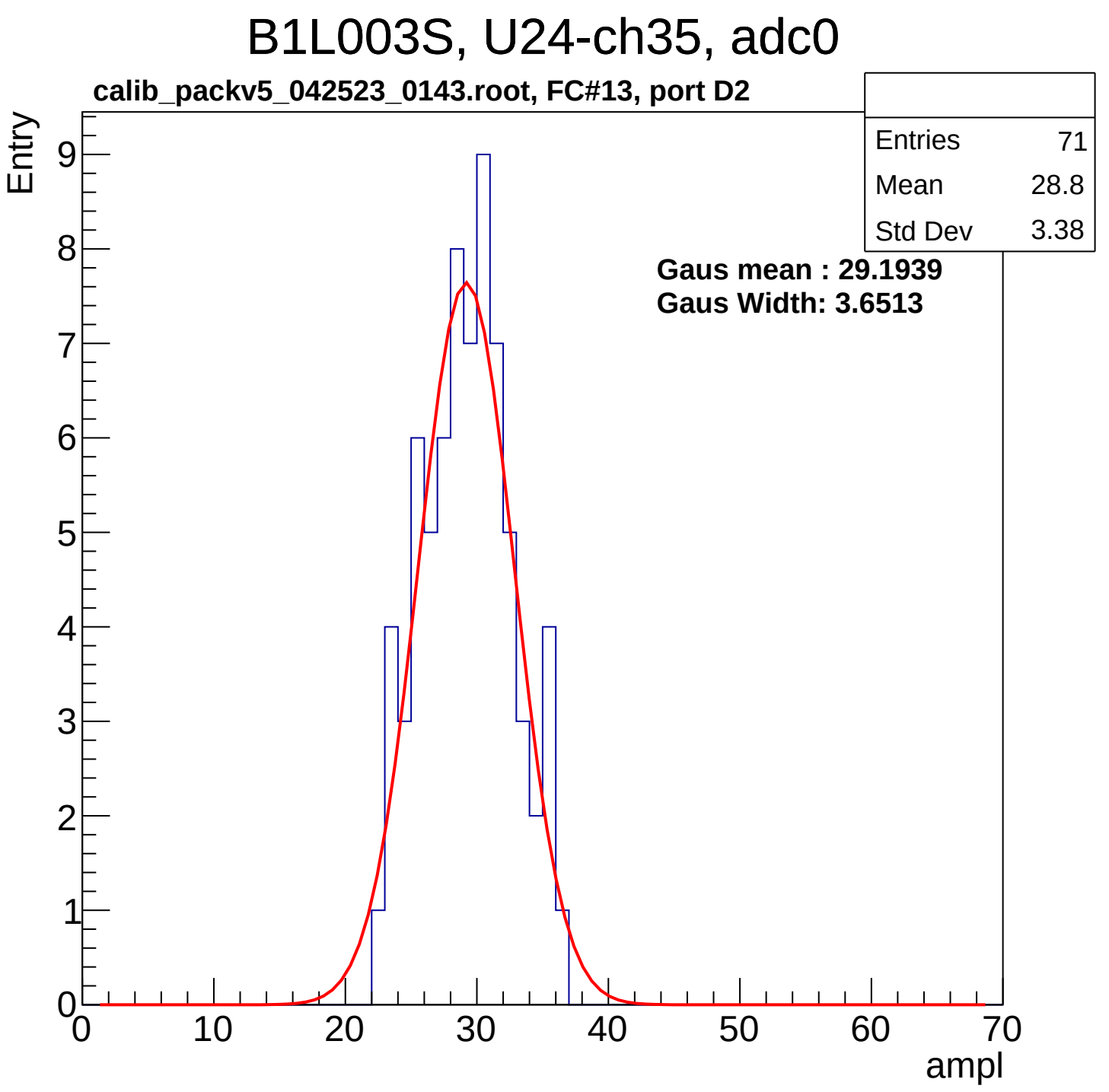
B1L003S, U24-ch35, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 28.8
Std Dev 3.38
Gaus mean : 29.1939
Gaus Width: 3.6513
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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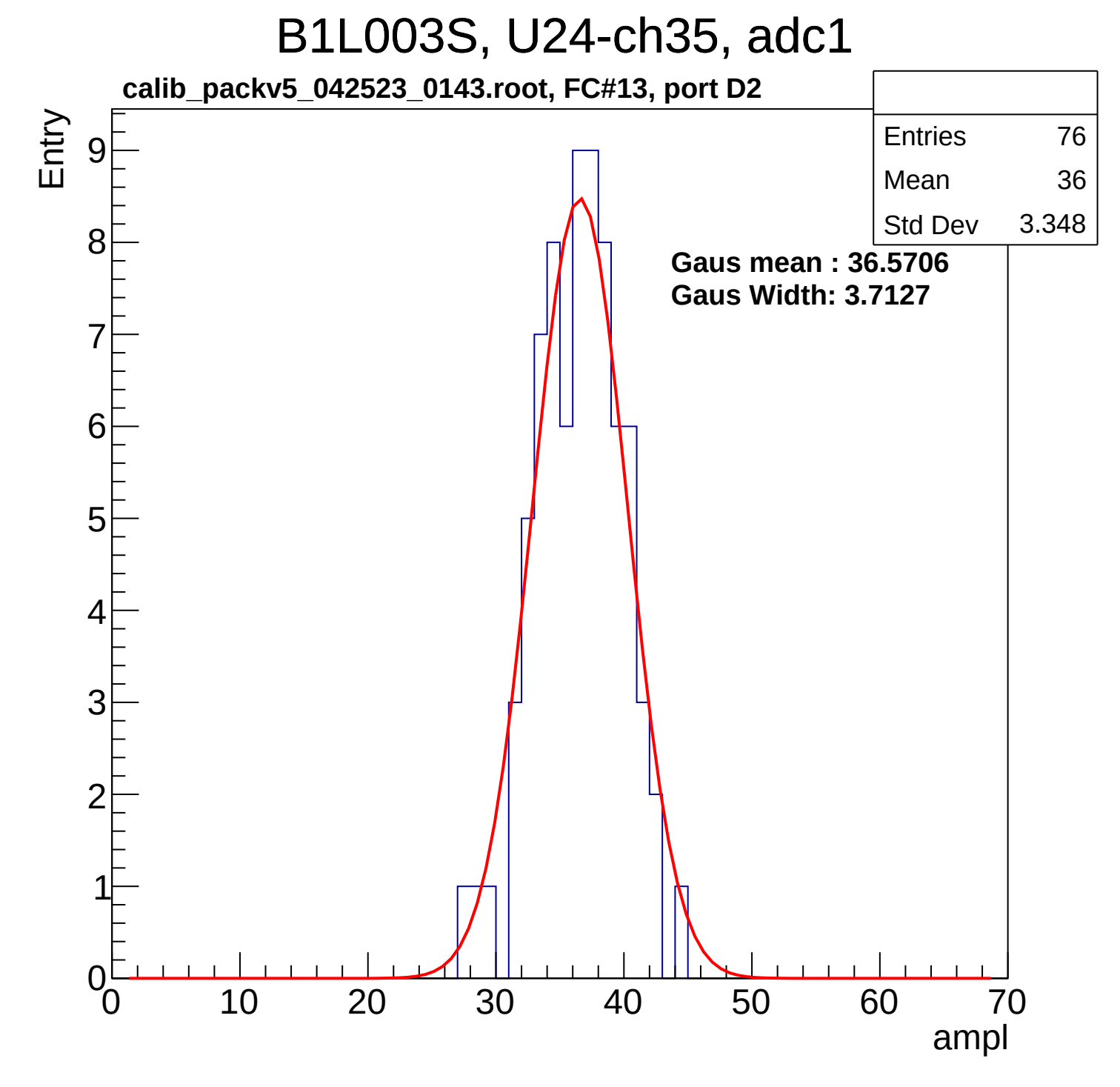
B1L003S, U24-ch35, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36
Std Dev 3.348
Gaus mean : 36.5706
Gaus Width: 3.7127
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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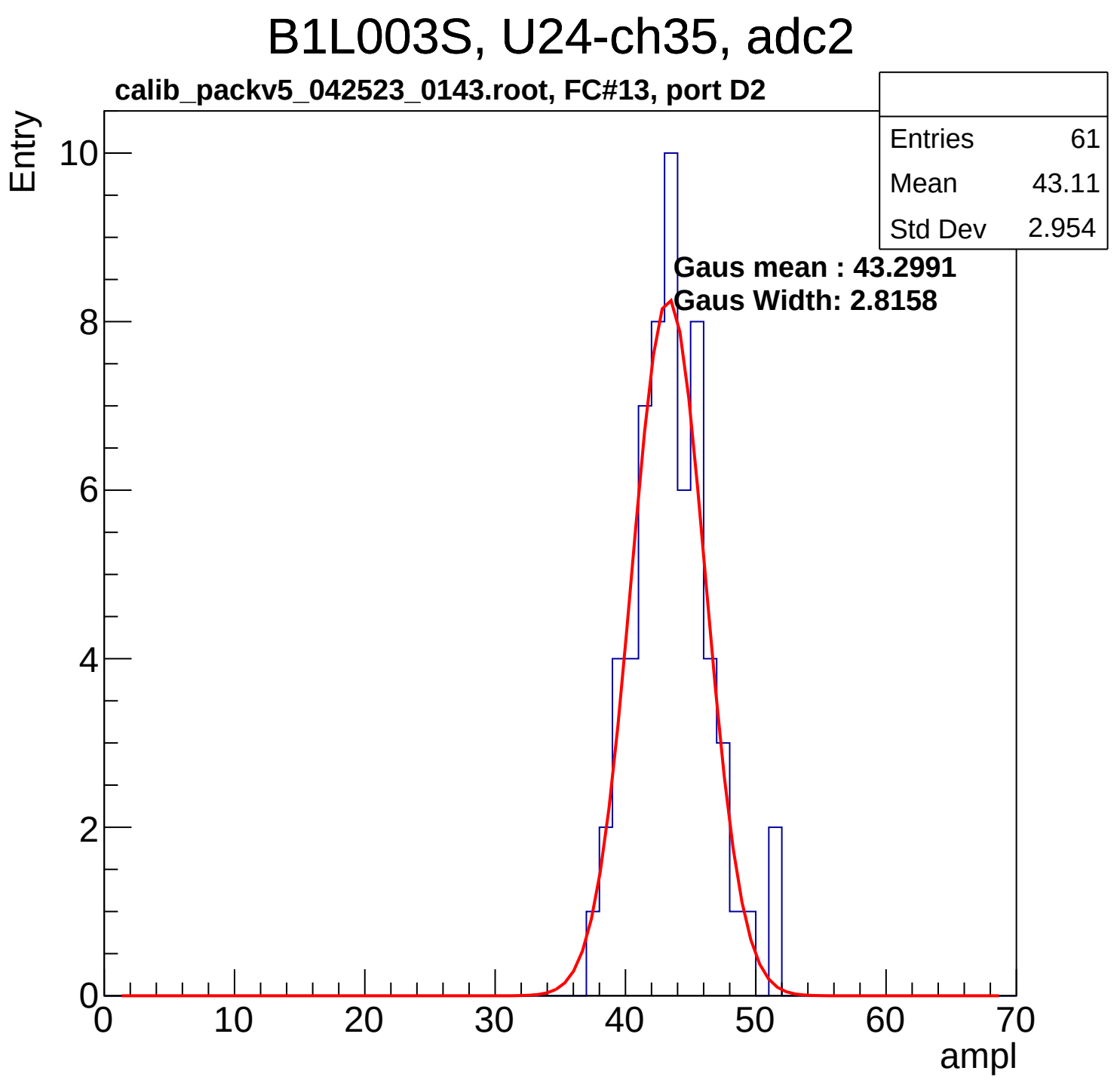
B1L003S, U24-ch35, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 43.11
Std Dev 2.954
Gaus mean : 43.2991
Gaus Width: 2.8158
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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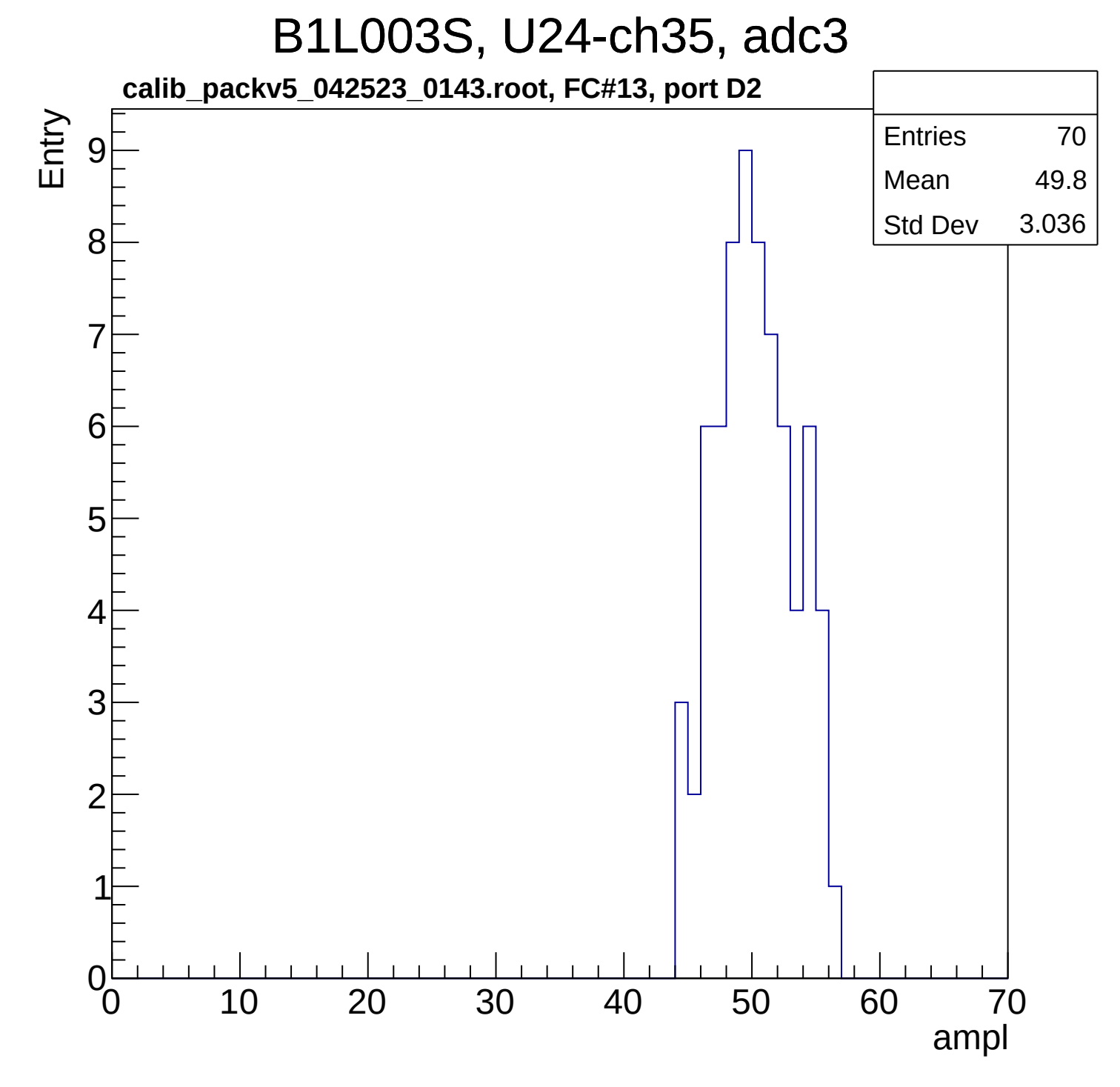
B1L003S, U24-ch35, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 49.8
Std Dev 3.036
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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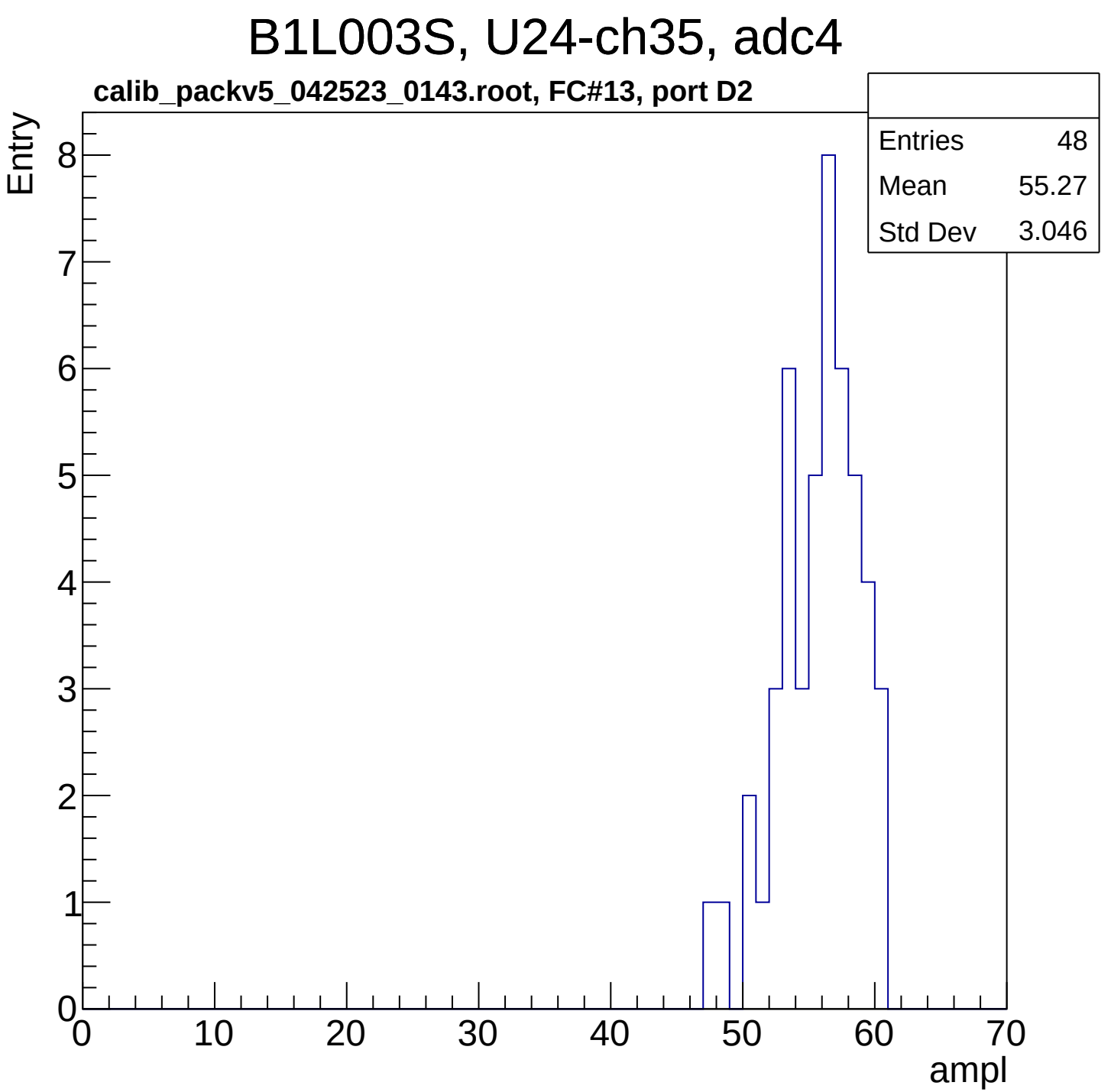
B1L003S, U24-ch35, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.27
Std Dev 3.046
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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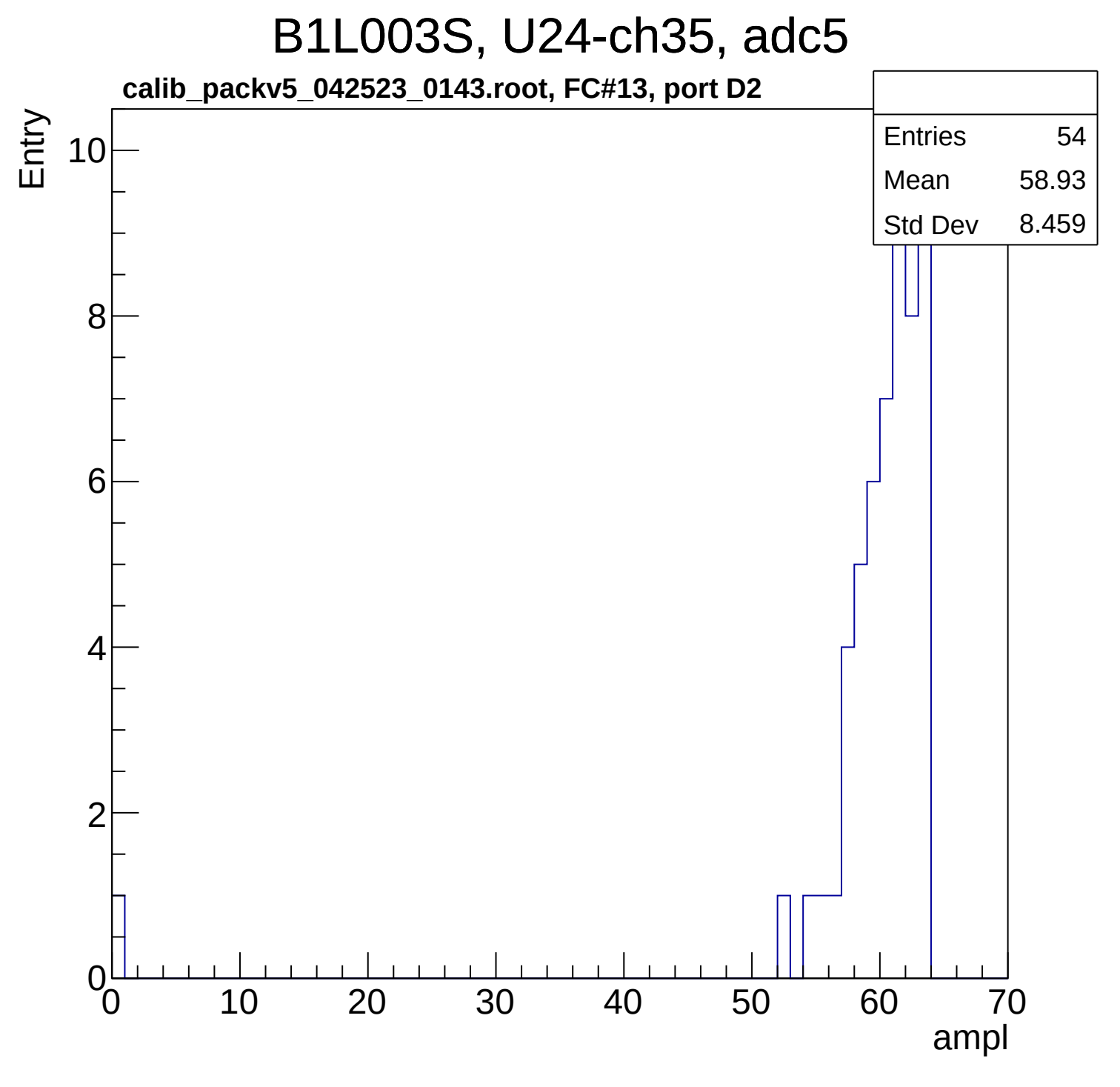
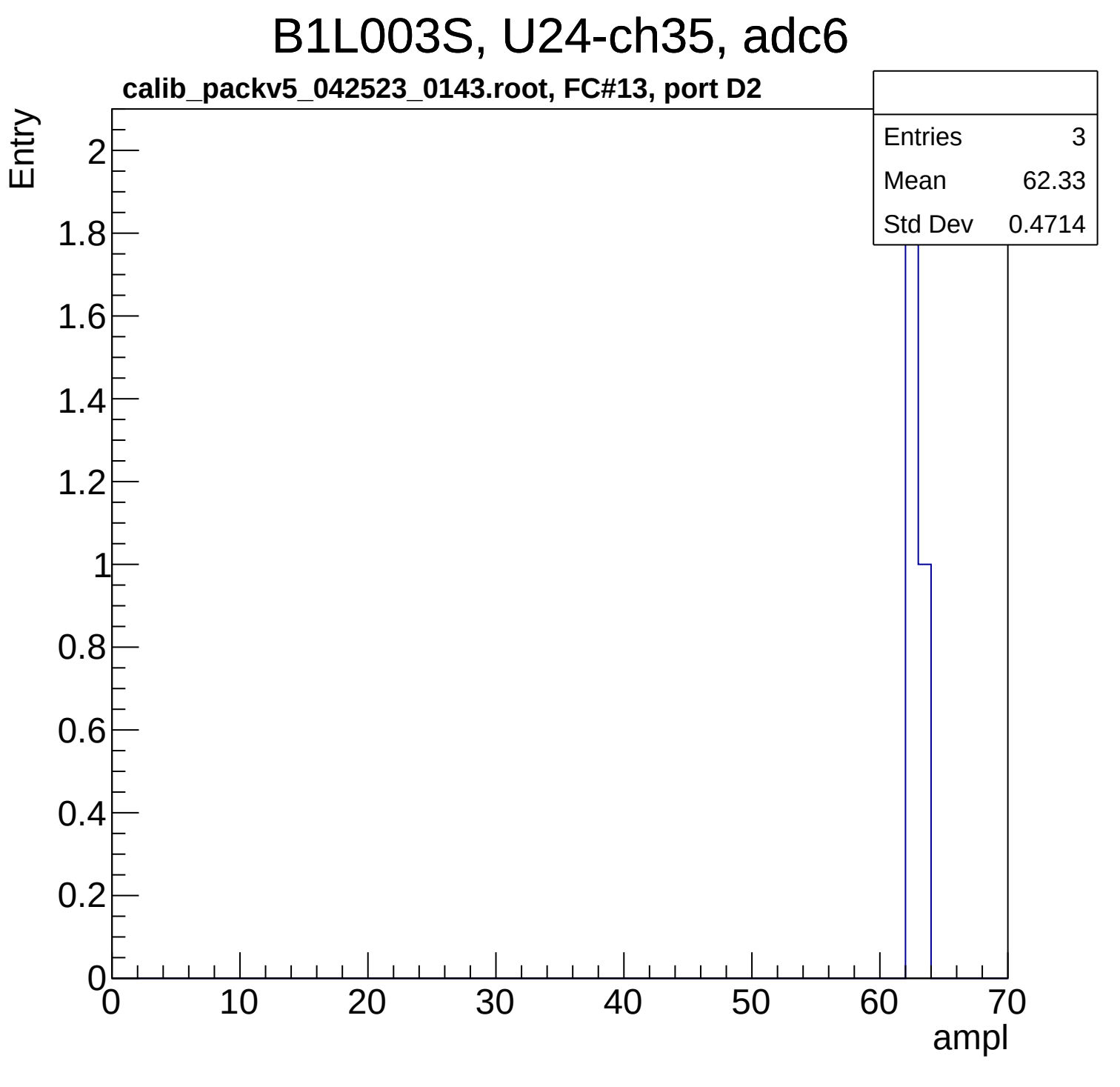
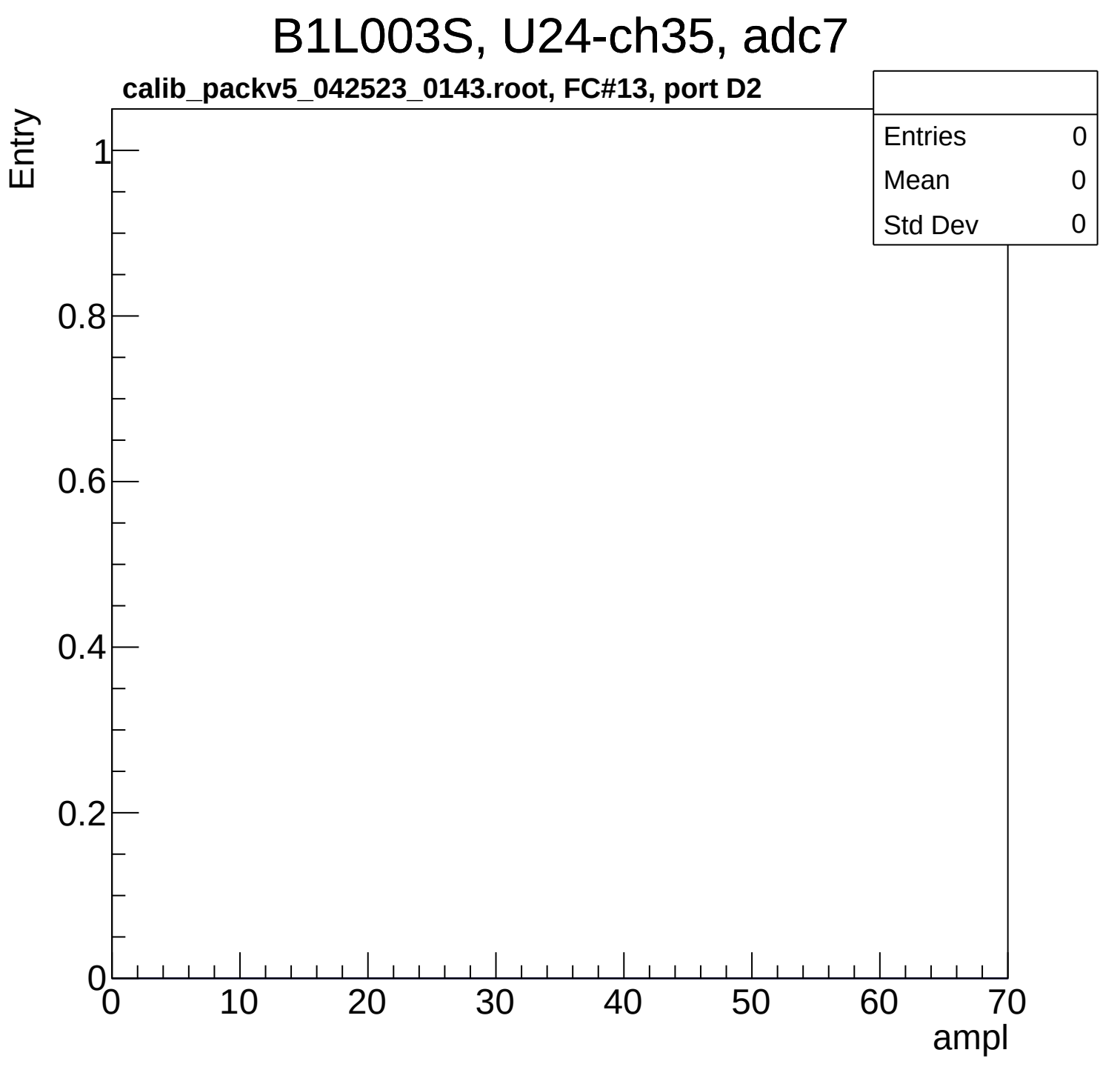
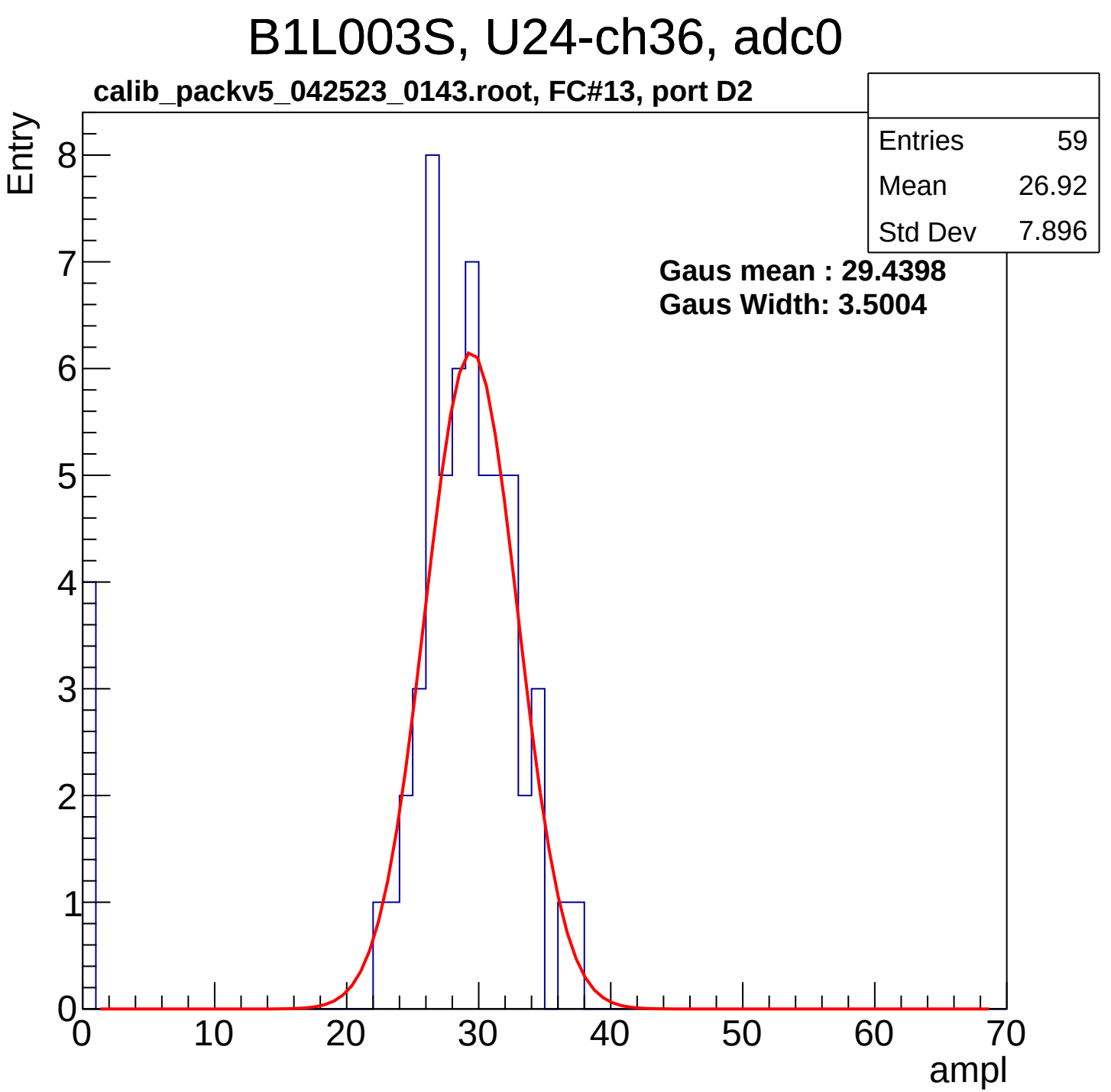

B1L003S, U24-ch35, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.93
Std Dev 8.459
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch35, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch35, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch36, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 26.92
Std Dev 7.896
Gaus mean : 29.4398
Gaus Width: 3.5004
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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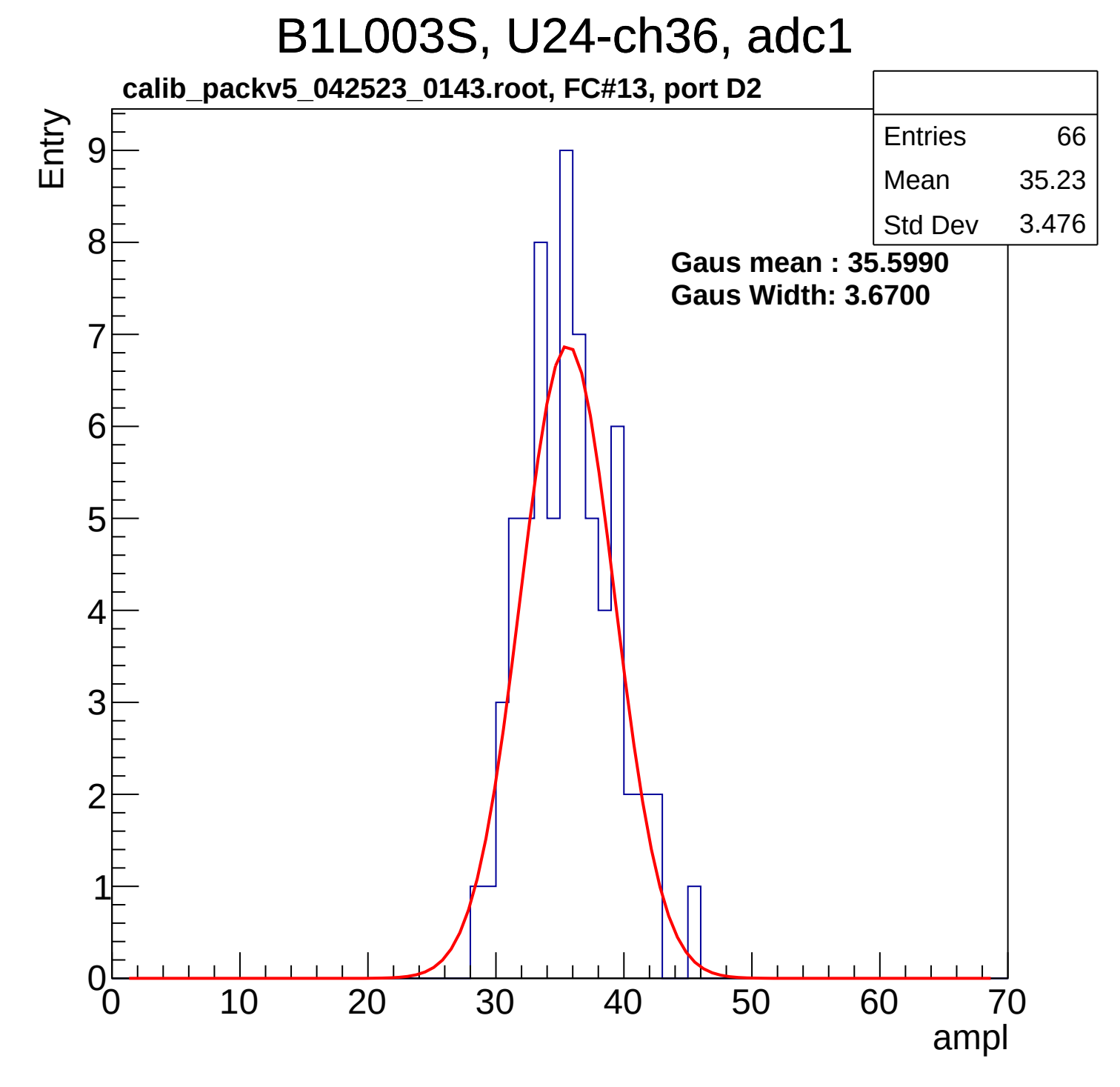
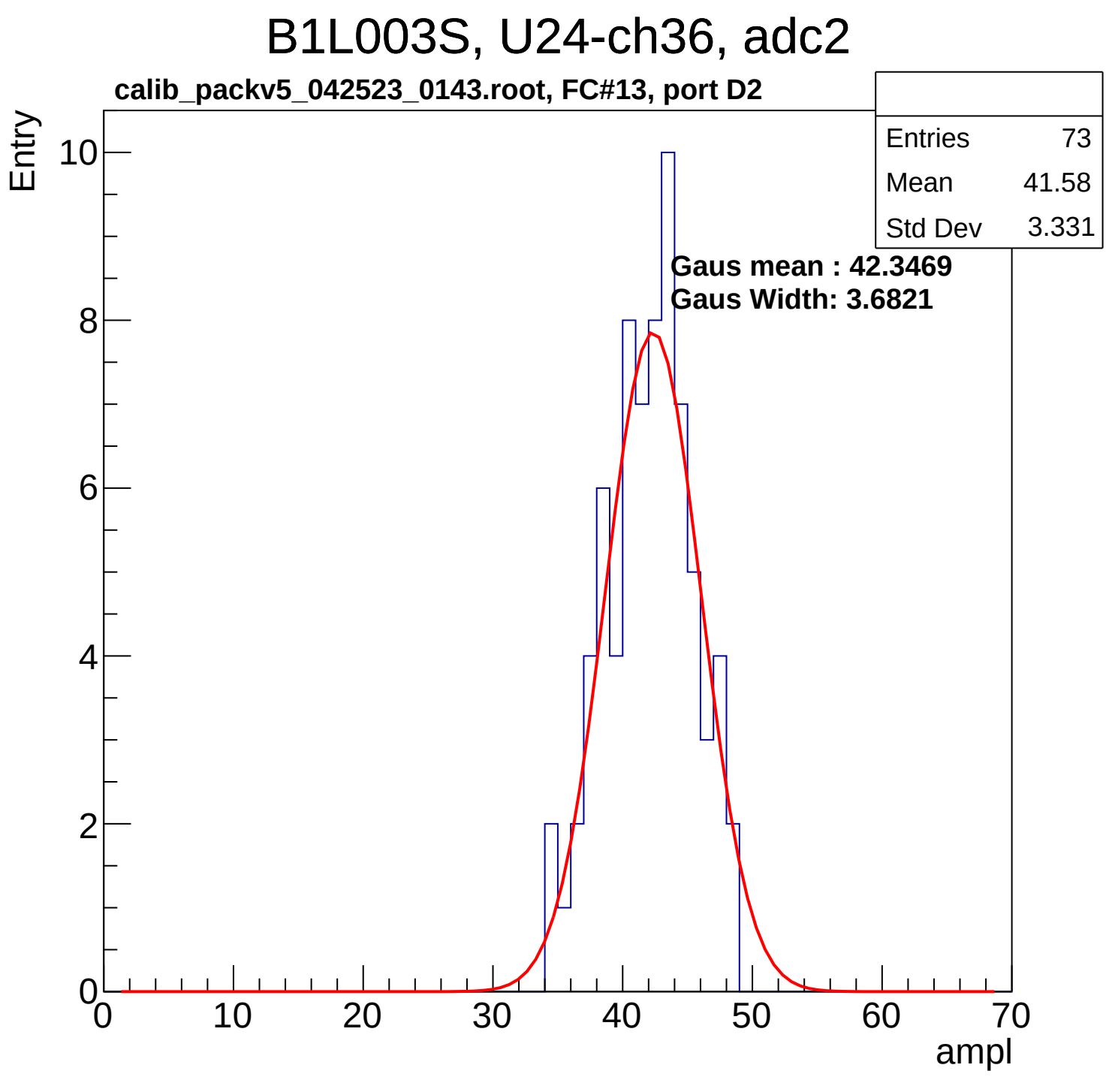

B1L003S, U24-ch36, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.23
Std Dev 3.476
Gaus mean : 35.5990
Gaus Width: 3.6700
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch36, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.58
Std Dev 3.331
Gaus mean : 42.3469
Gaus Width: 3.6821
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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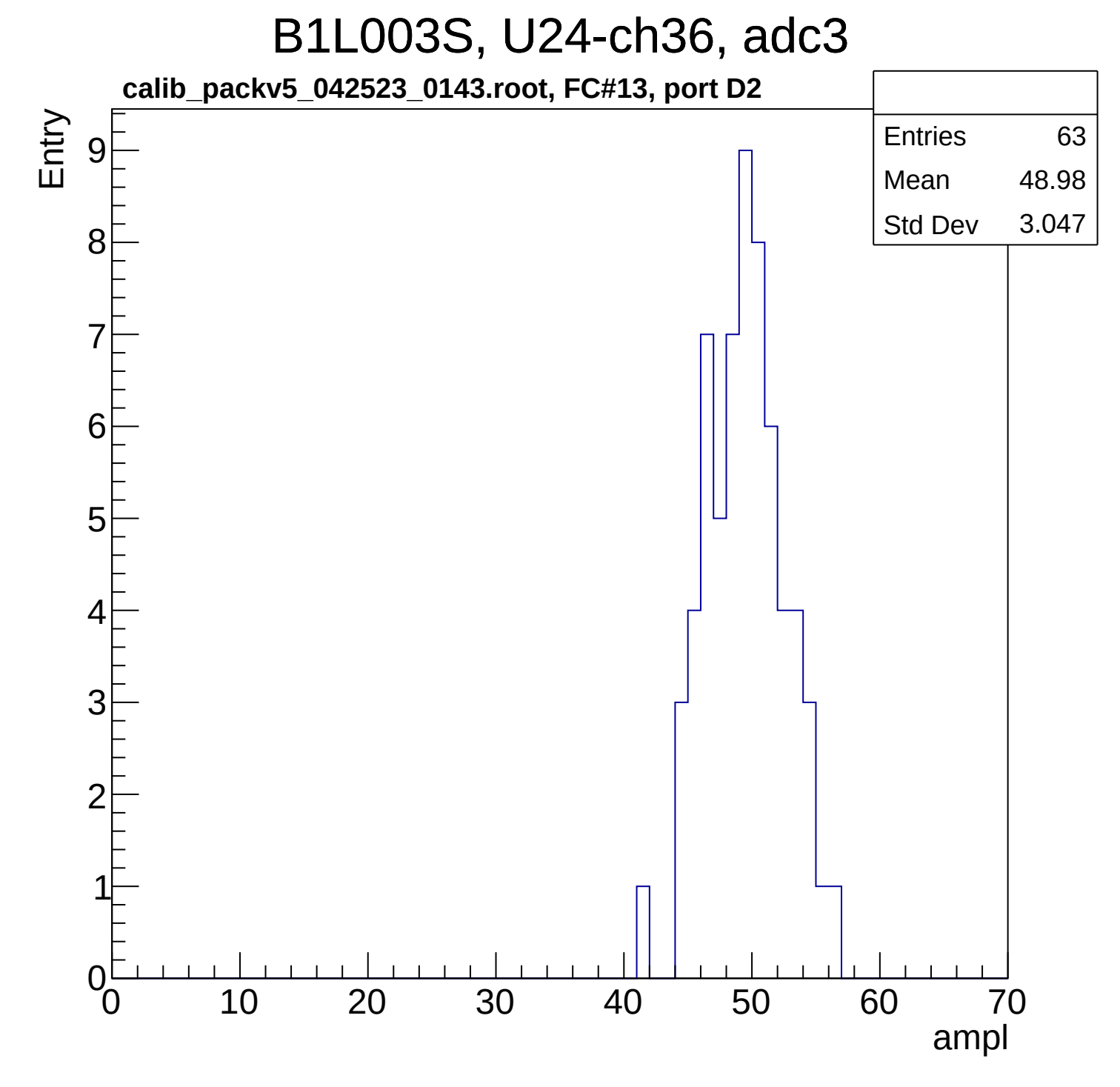
B1L003S, U24-ch36, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.98
Std Dev 3.047
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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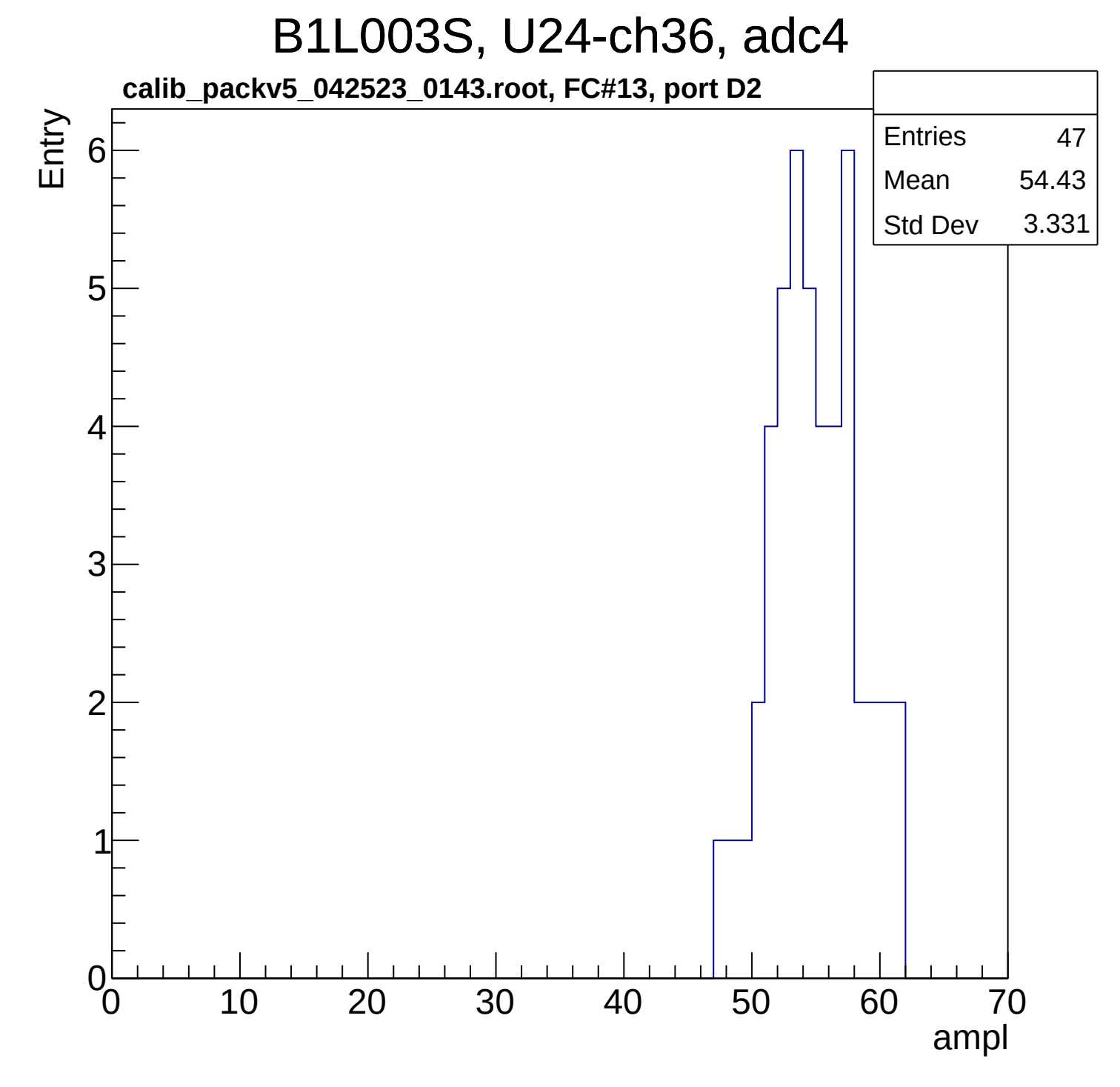
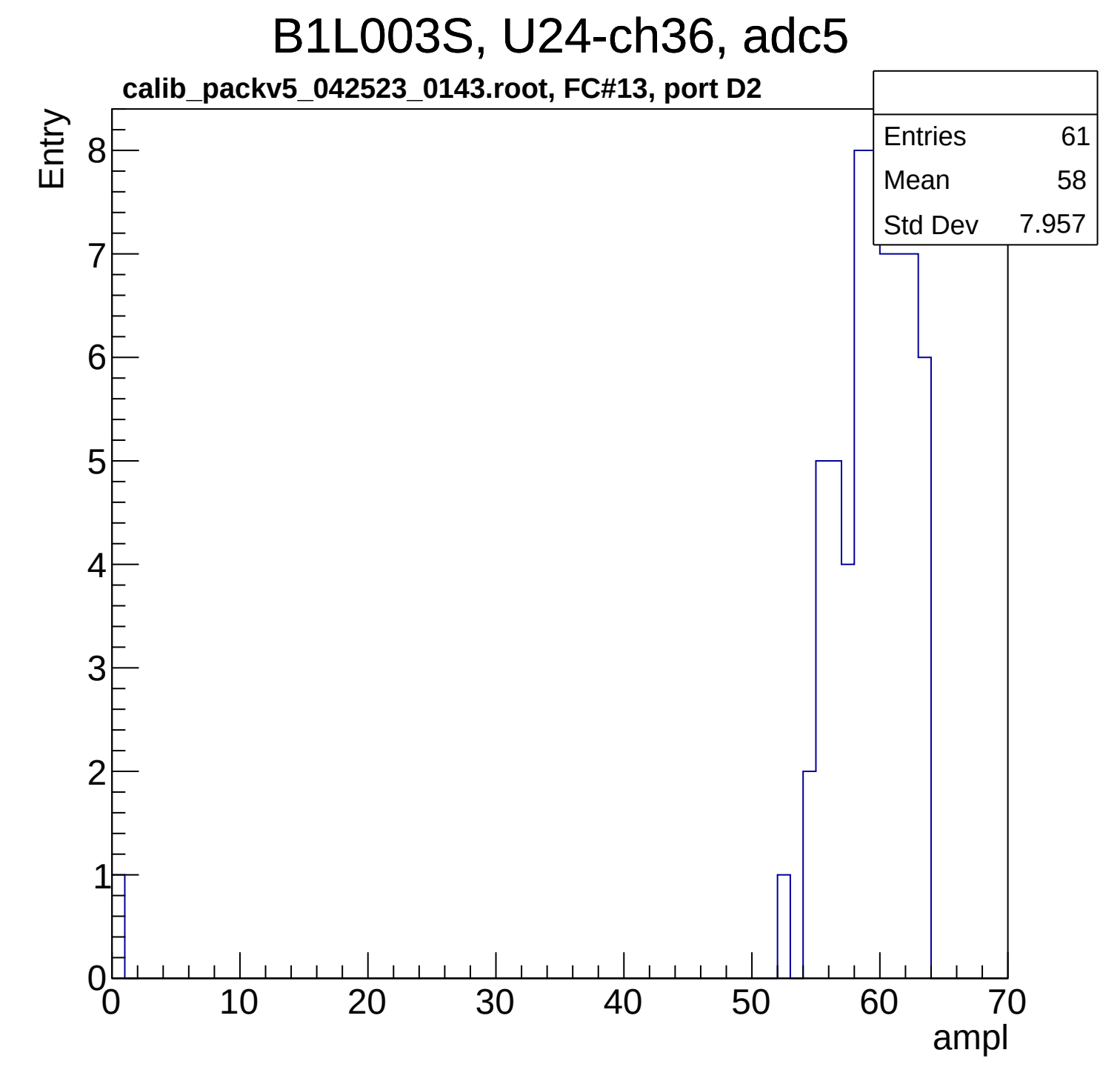

B1L003S, U24-ch36, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.43
Std Dev 3.331
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch36, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58
Std Dev 7.957
Entry
ampl
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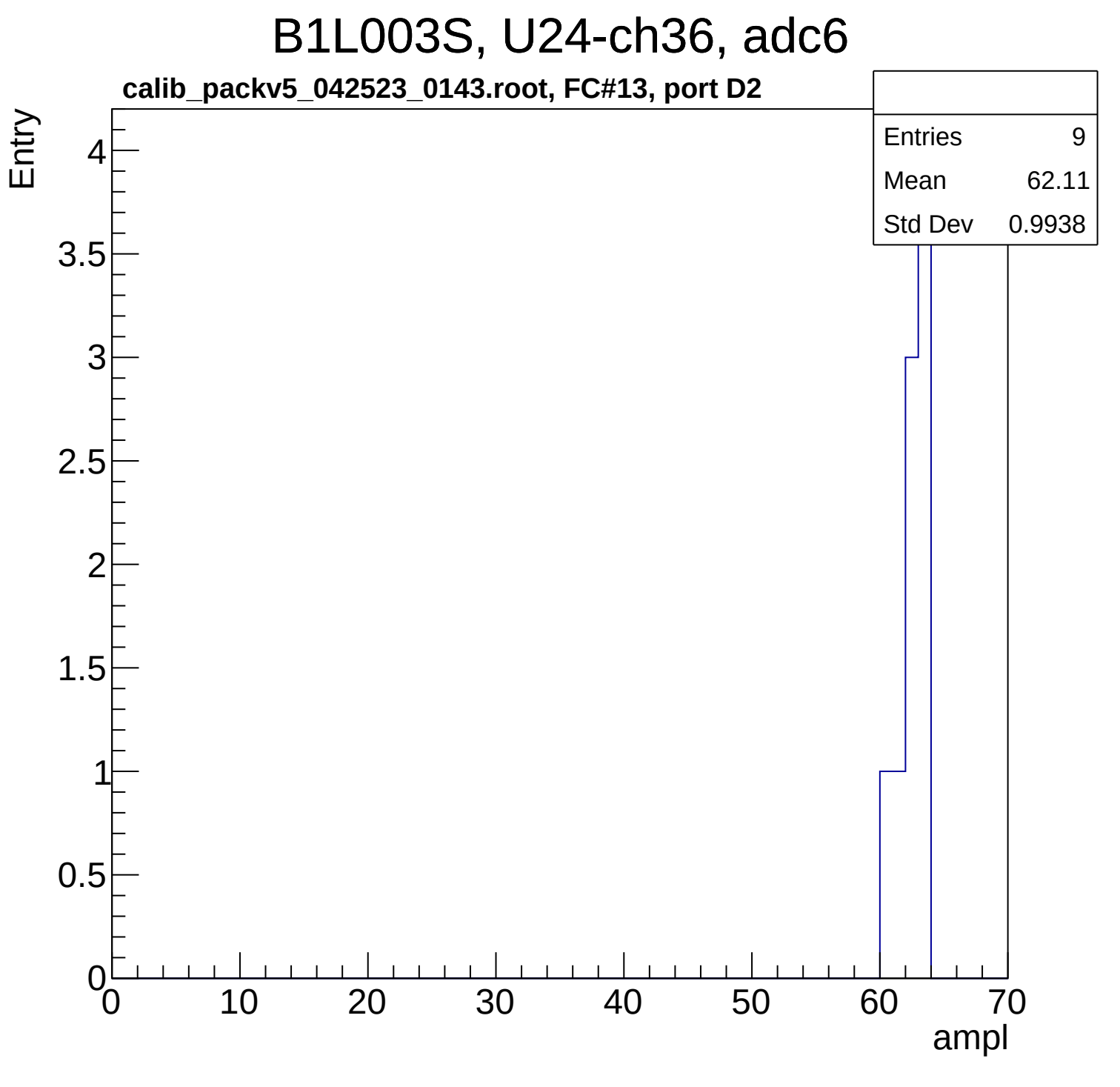
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch36, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 0.9938
Entry
ampl
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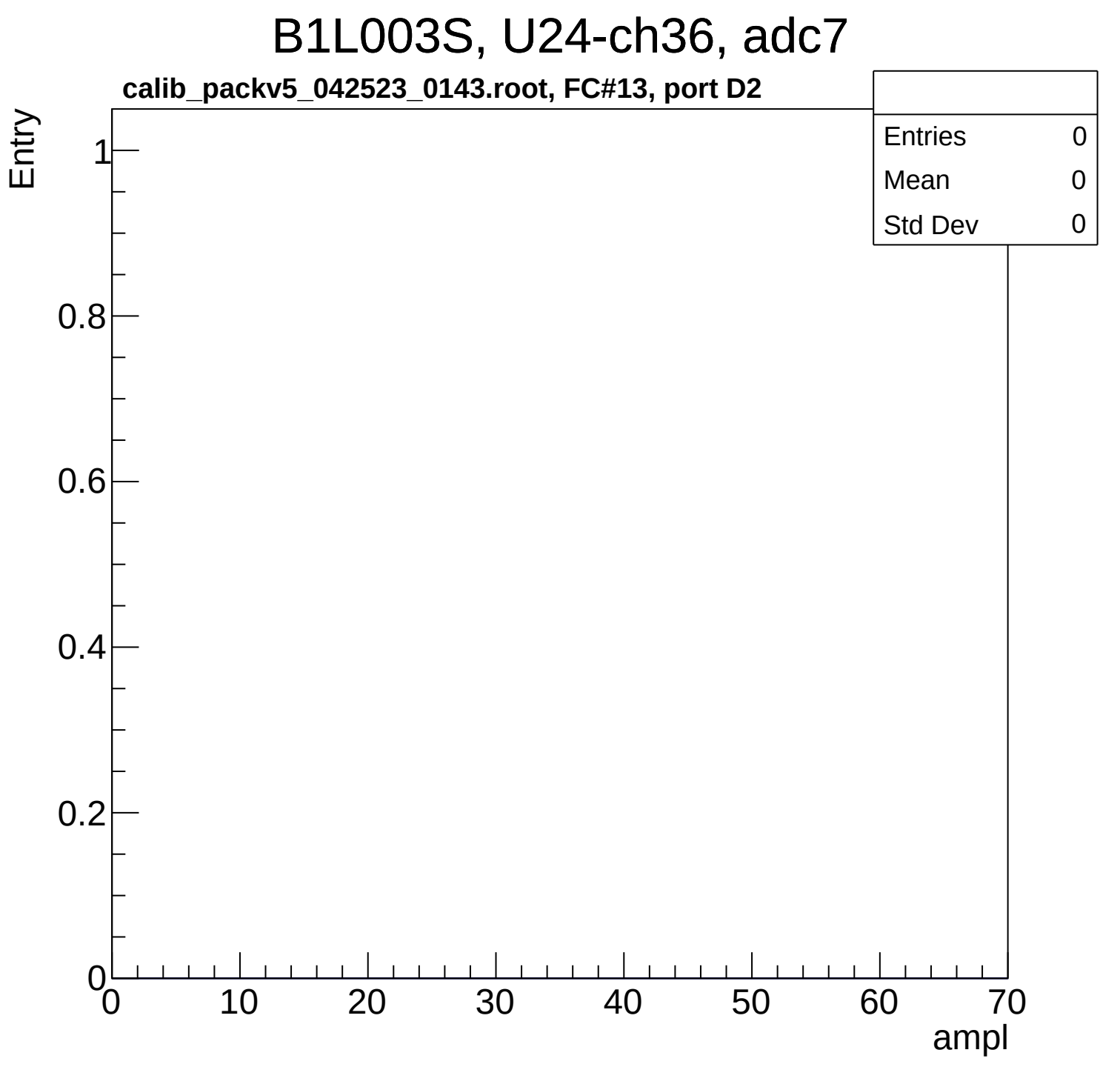
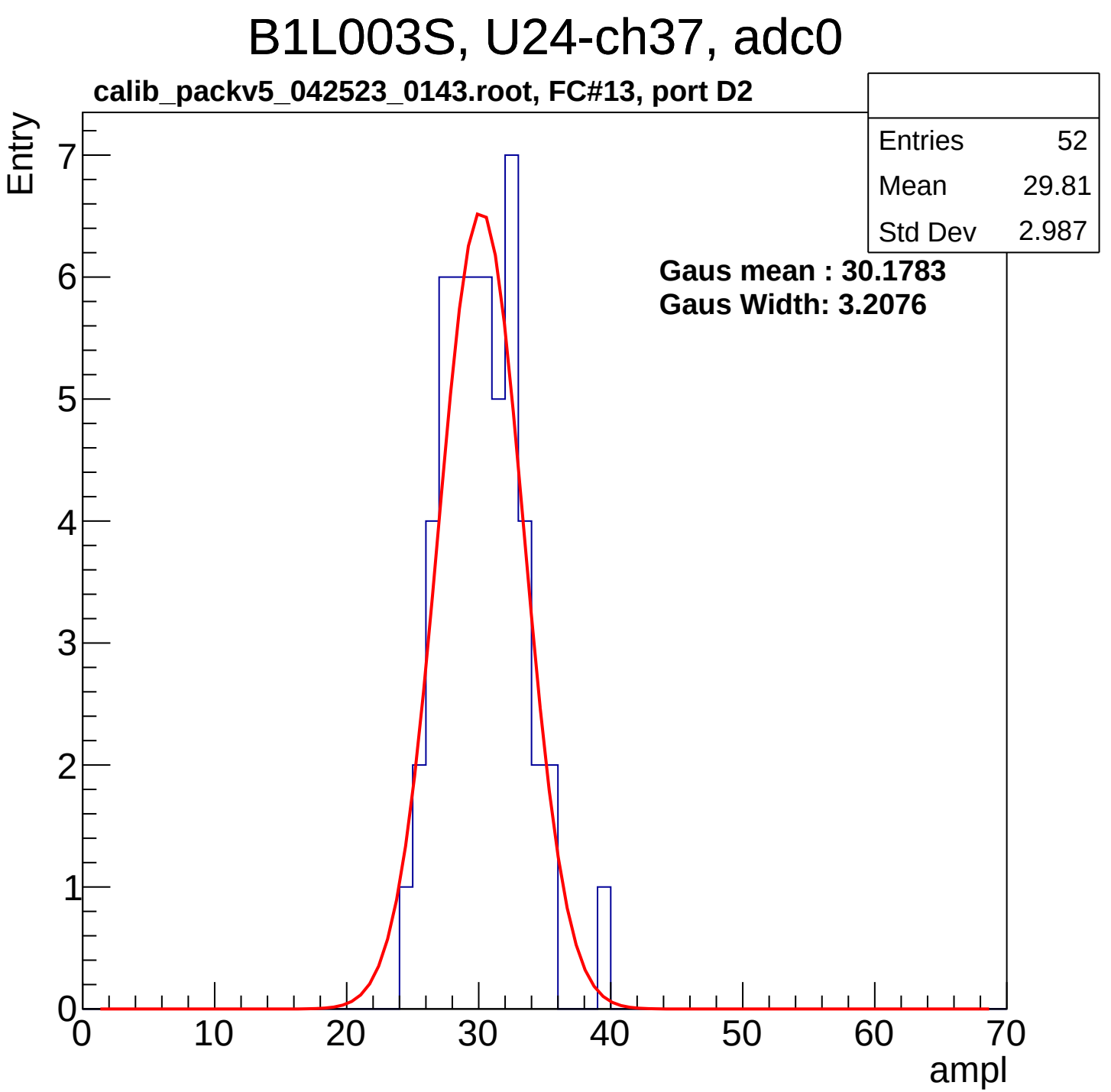
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U24-ch36, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch37, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 29.81
Std Dev 2.987
Gaus mean : 30.1783
Gaus Width: 3.2076
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
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60
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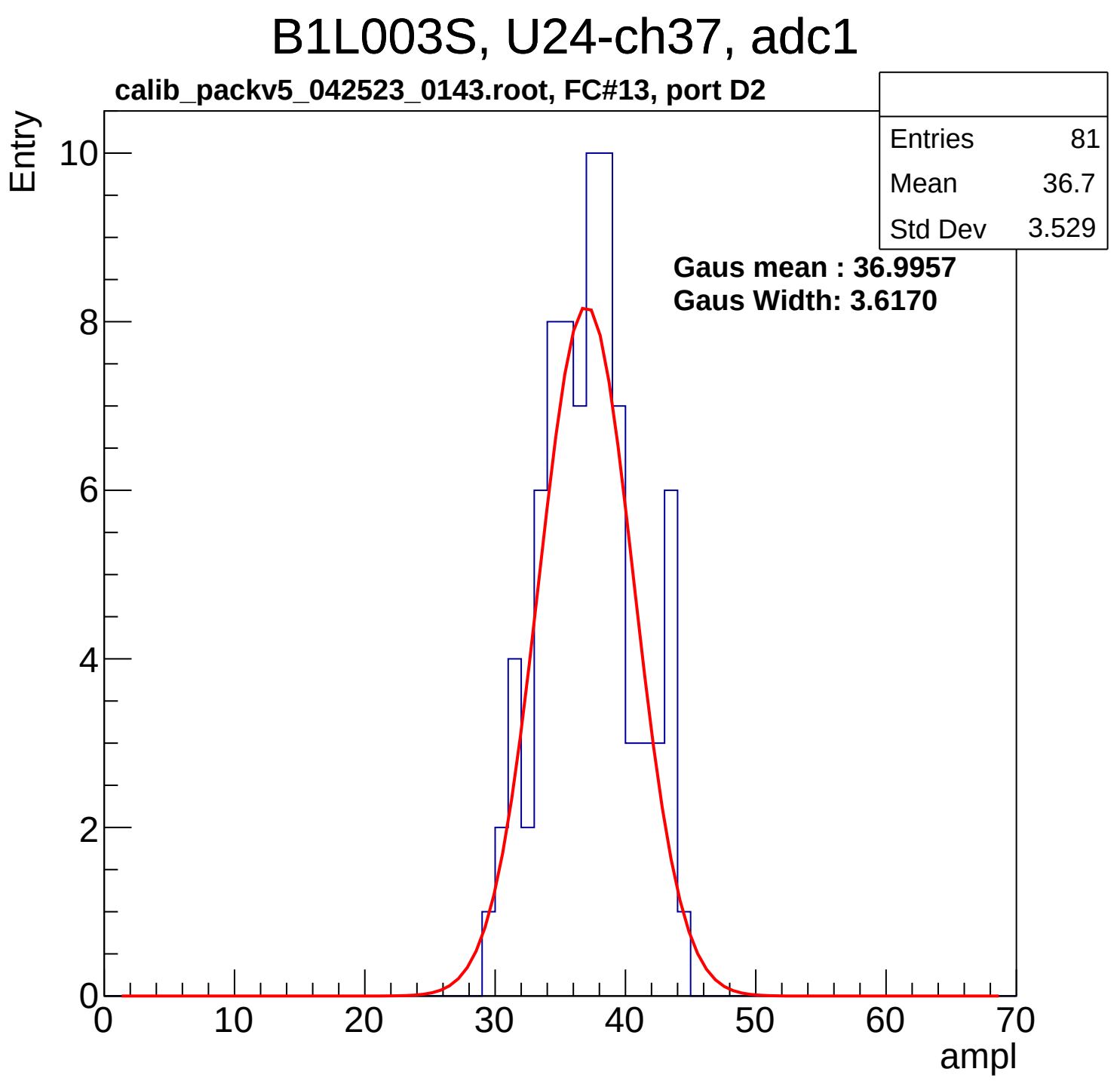
B1L003S, U24-ch37, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 36.7
Std Dev 3.529
Gaus mean : 36.9957
Gaus Width: 3.6170
Entry
ampl
0
2
4
6
8
10
0
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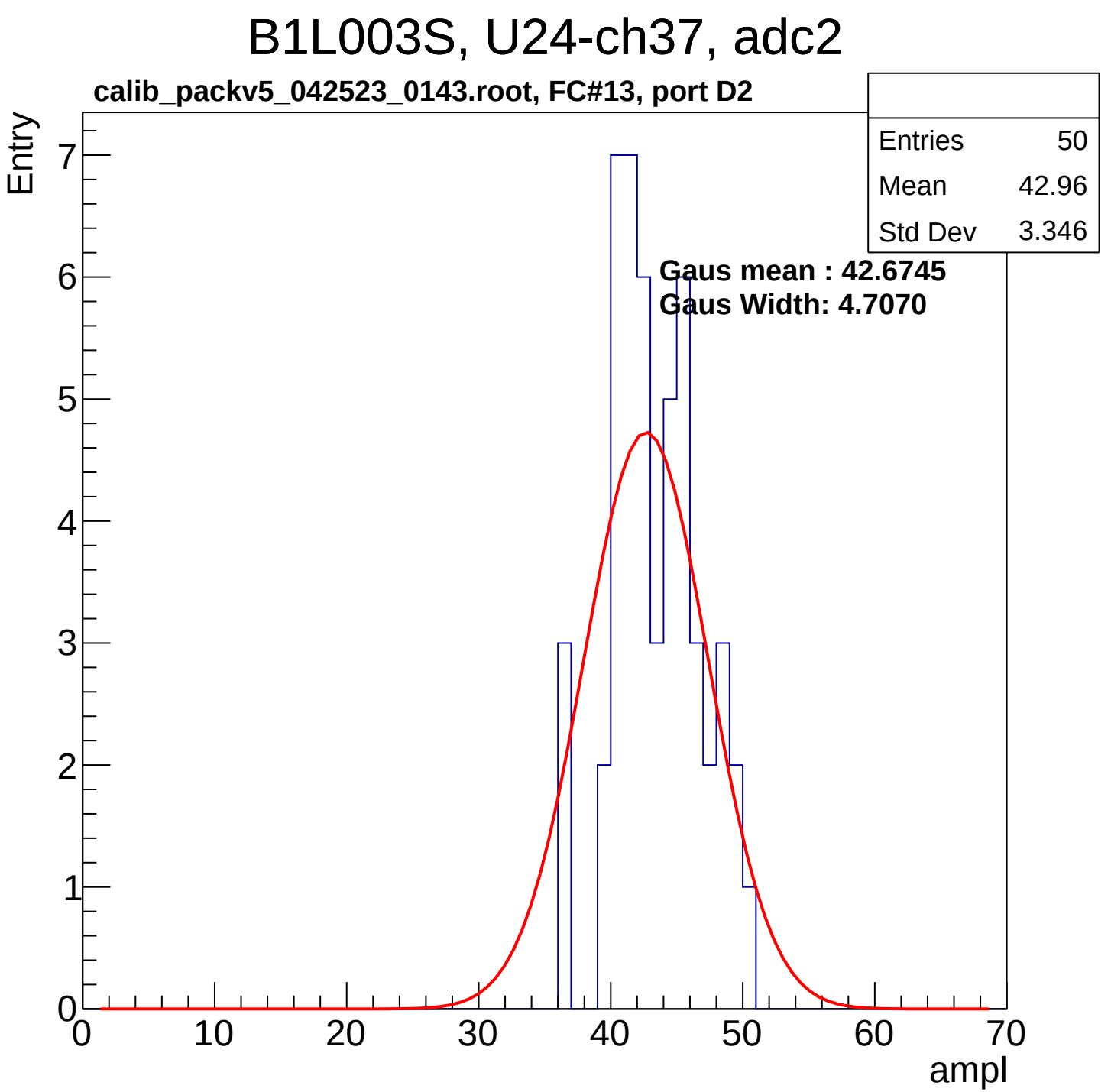
20
30
40
50
60
70


B1L003S, U24-ch37, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 42.96
Std Dev 3.346
Gaus mean : 42.6745
Gaus Width: 4.7070
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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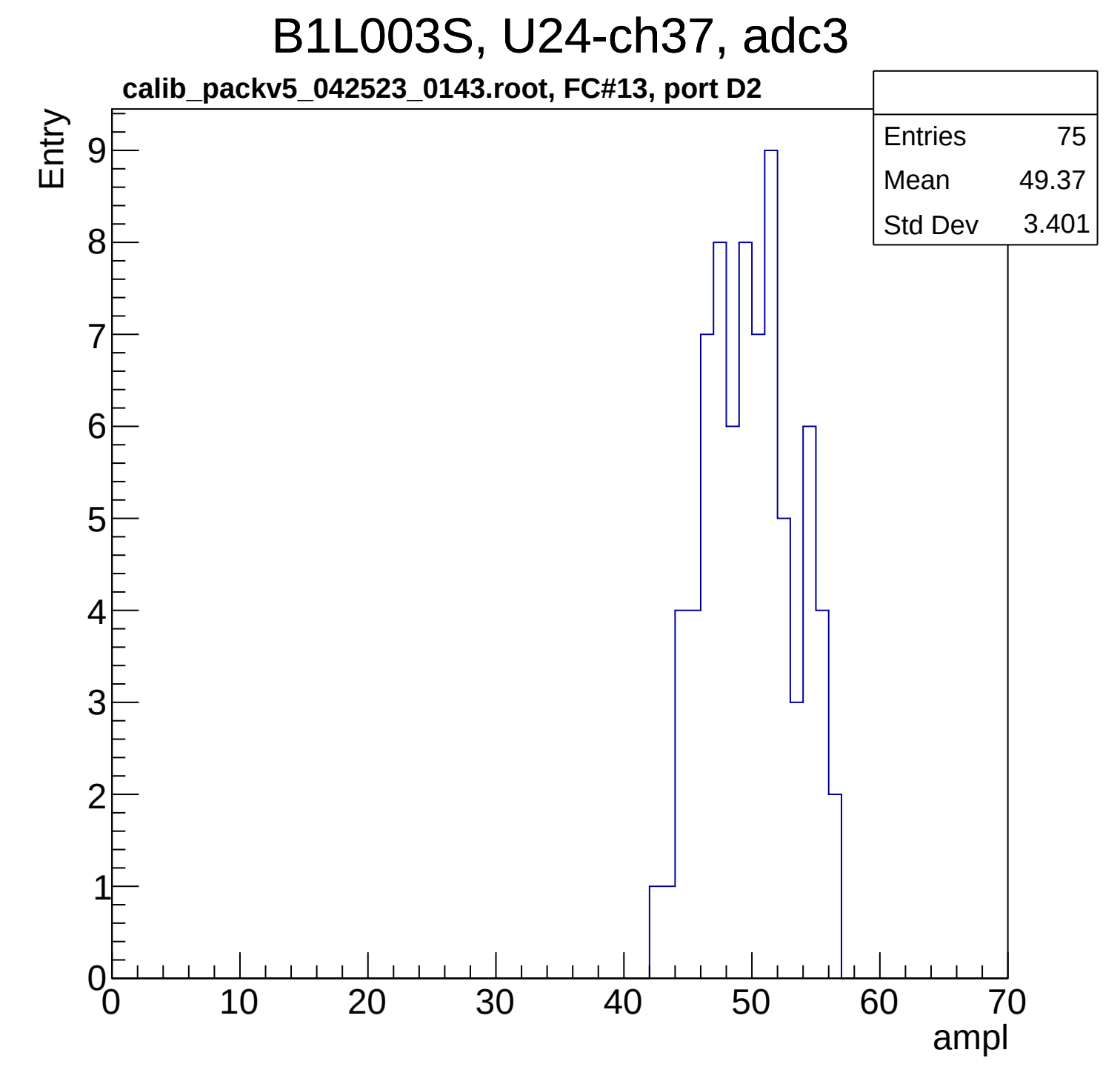
B1L003S, U24-ch37, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 49.37
Std Dev 3.401
Entry
ampl
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3
4
5
6
7
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0
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60
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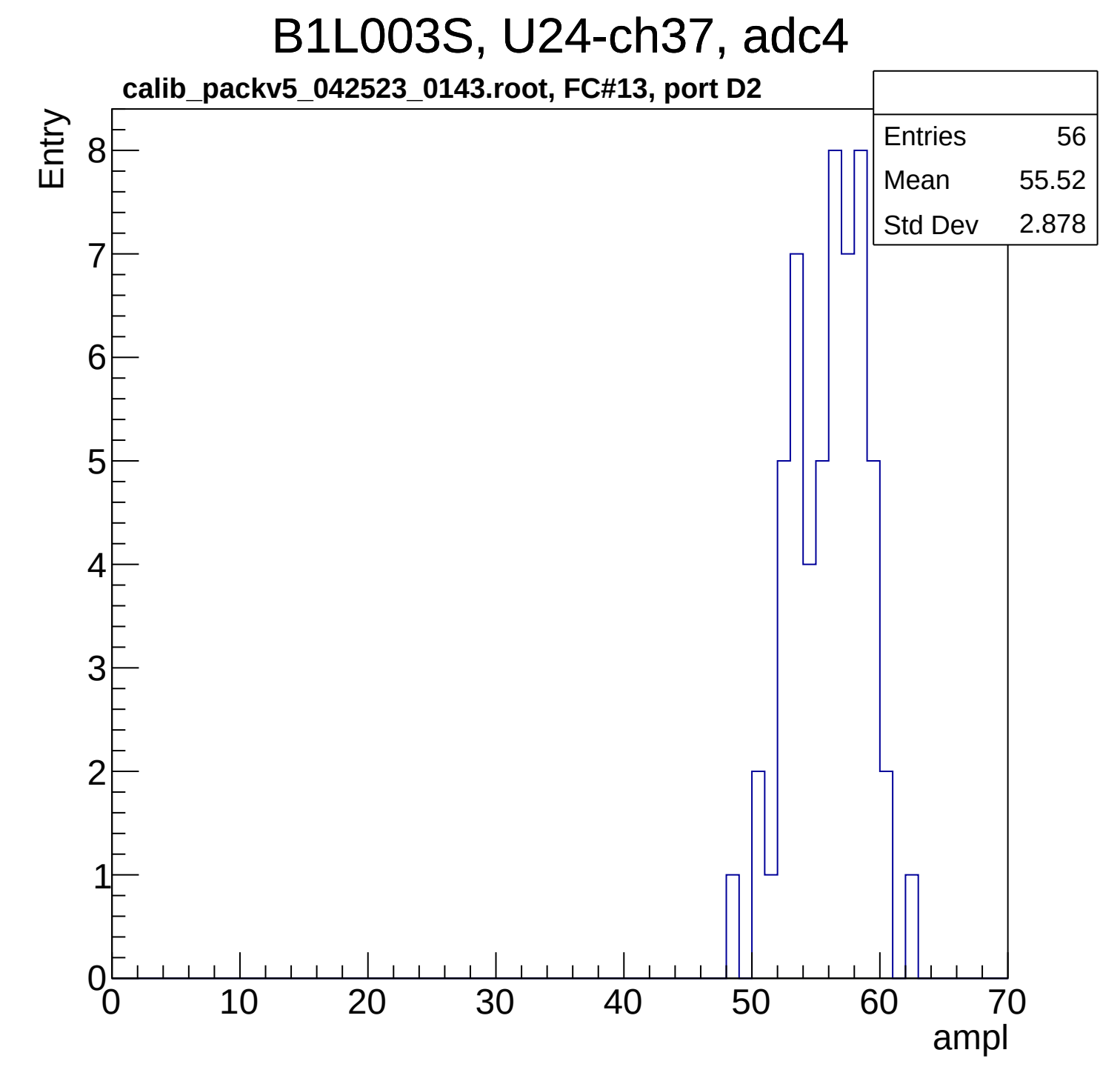
B1L003S, U24-ch37, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.52
Std Dev 2.878
Entry
ampl
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1
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4
5
6
7
8
0
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20
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60
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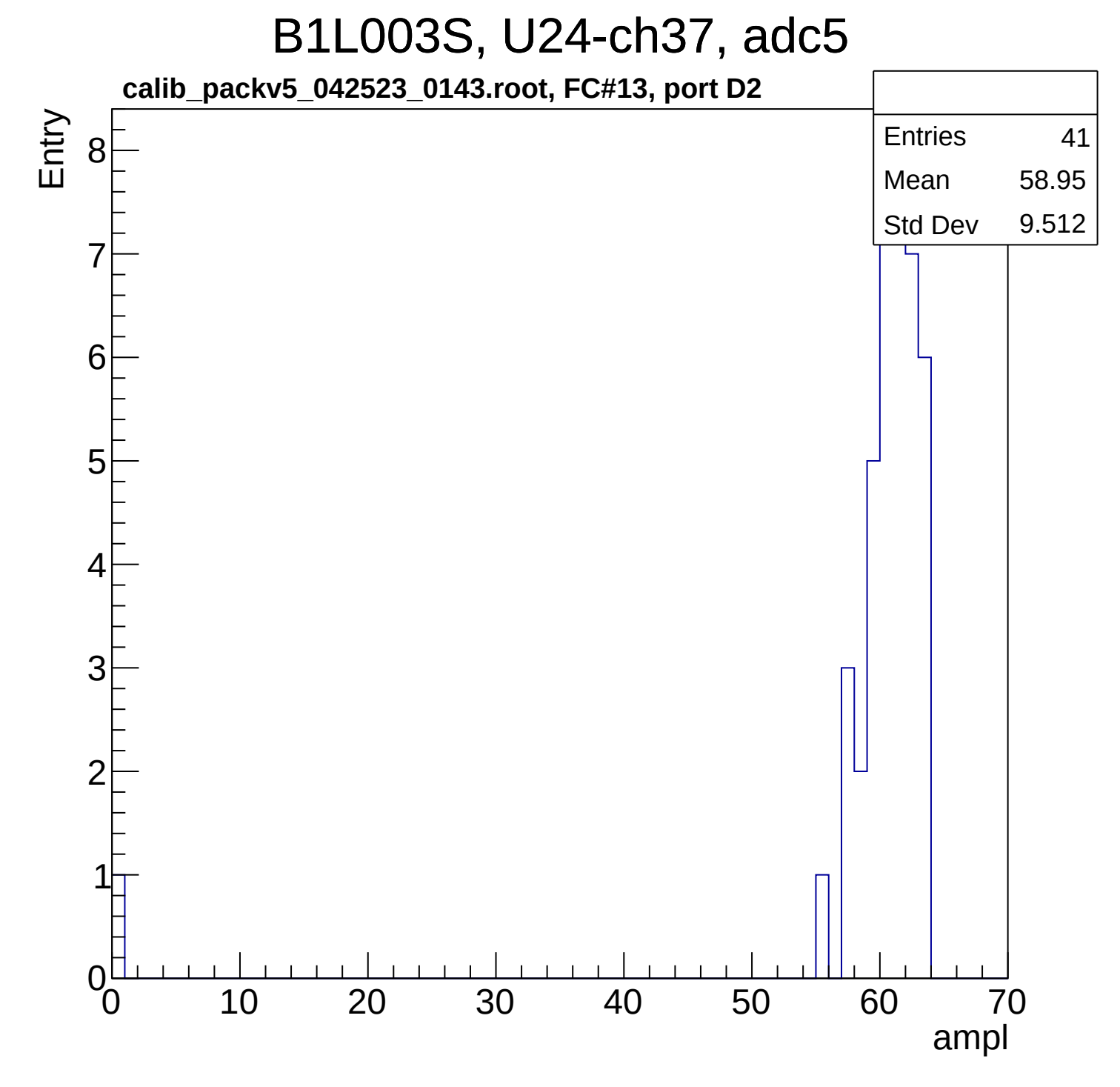
B1L003S, U24-ch37, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.95
Std Dev 9.512
Entry
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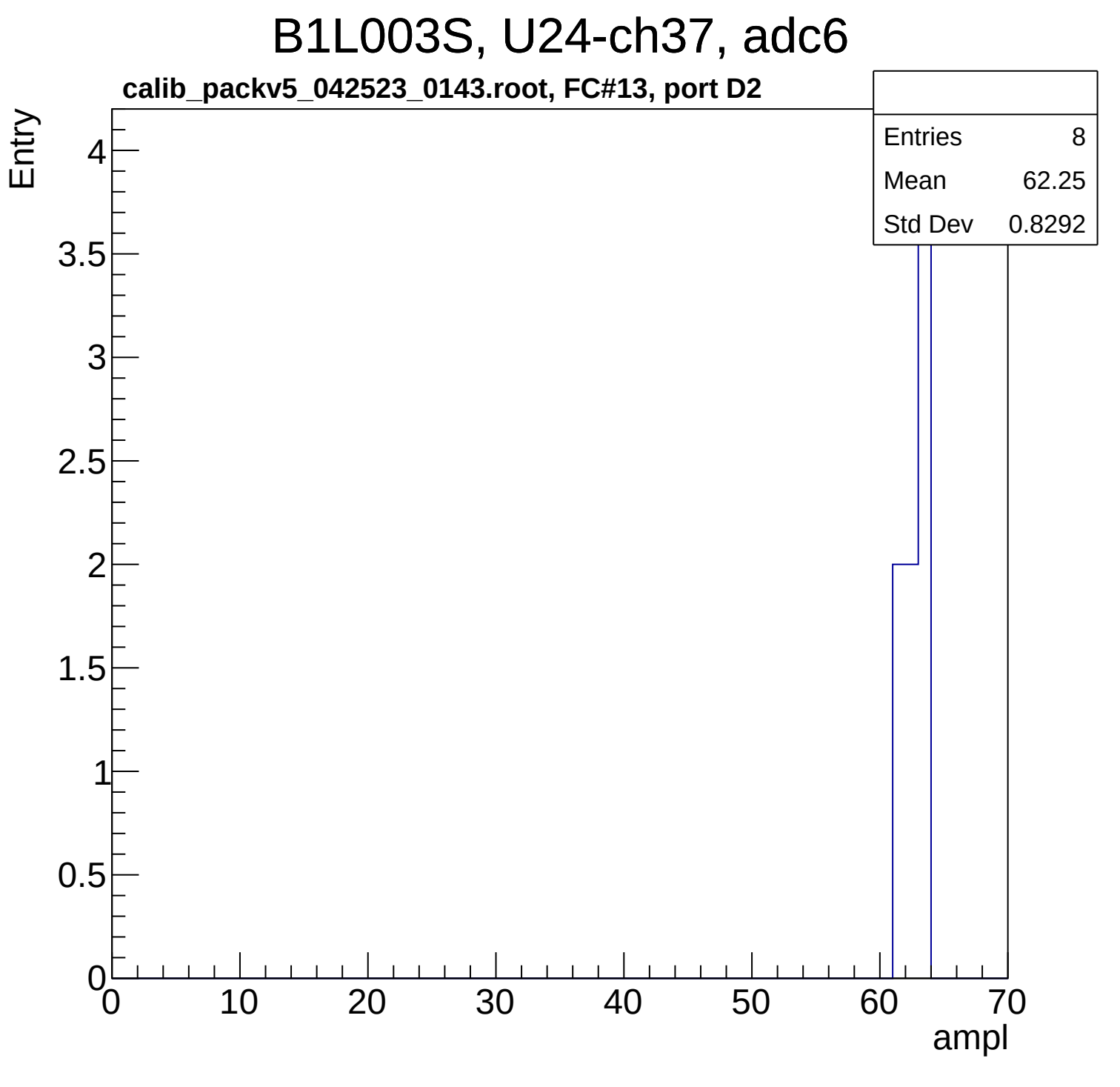
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch37, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.25
Std Dev 0.8292
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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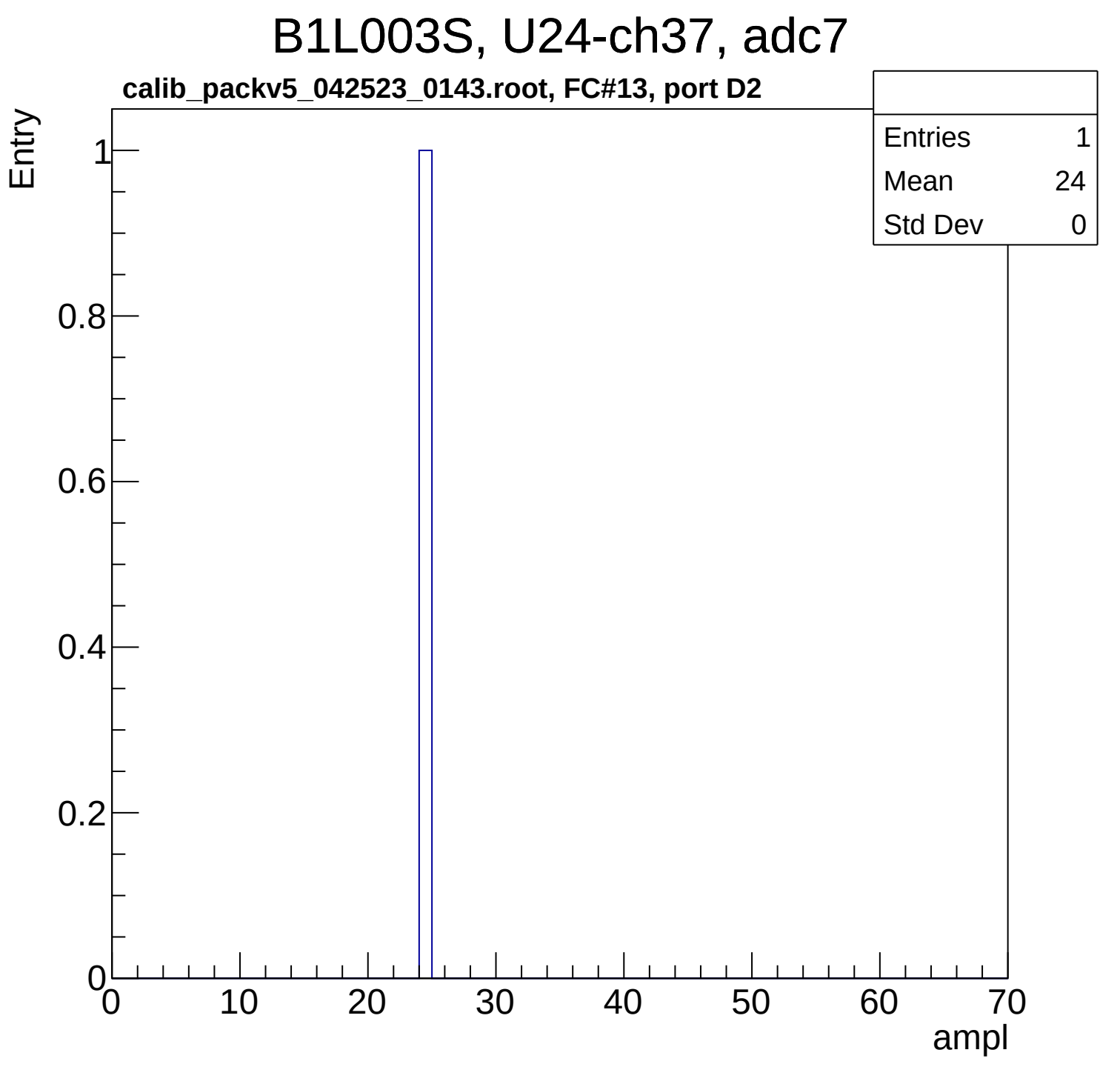
B1L003S, U24-ch37, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
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10
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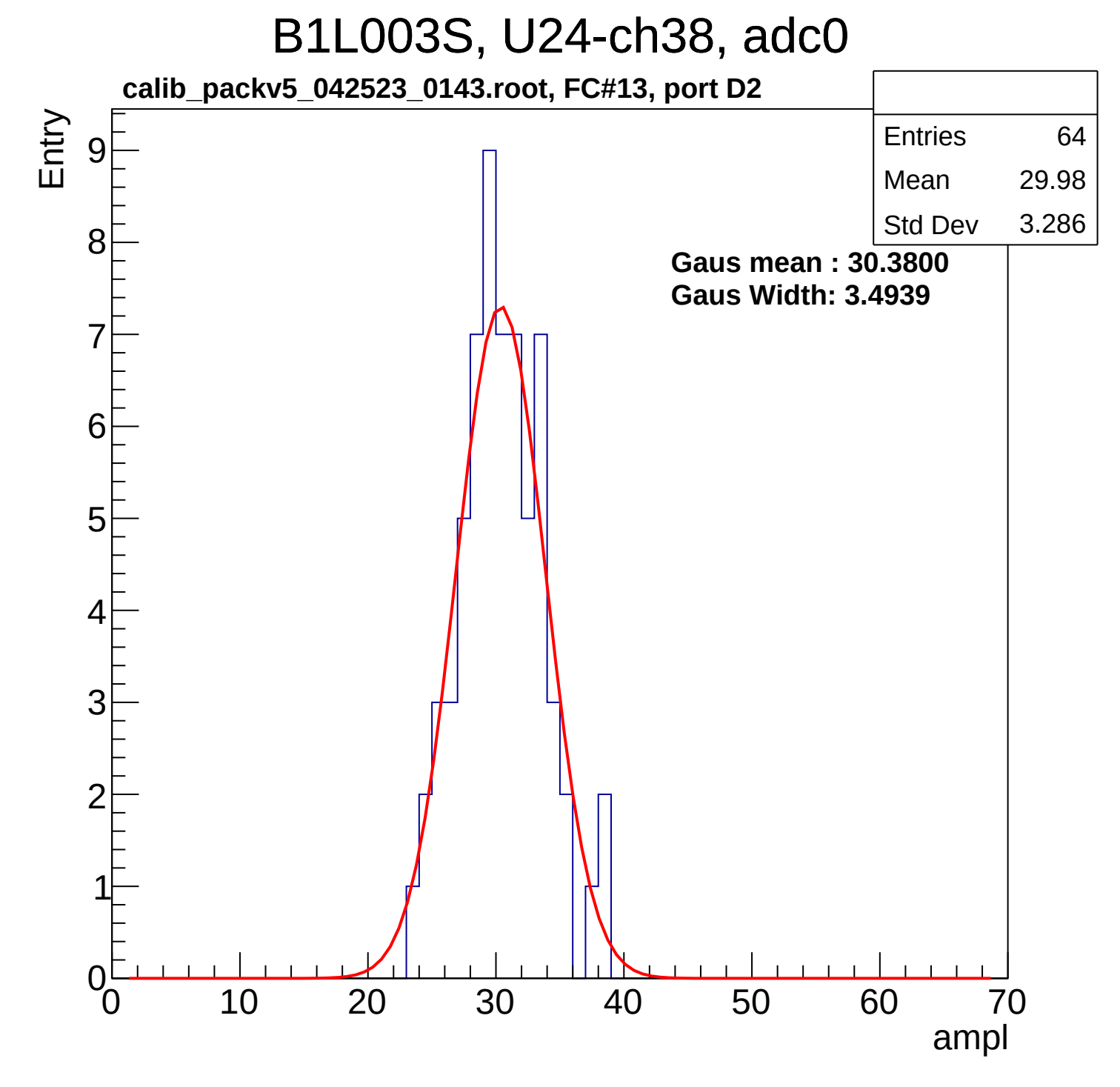
40
50
60
70
ampl


B1L003S, U24-ch38, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.98
Std Dev 3.286
Gaus mean : 30.3800
Gaus Width: 3.4939
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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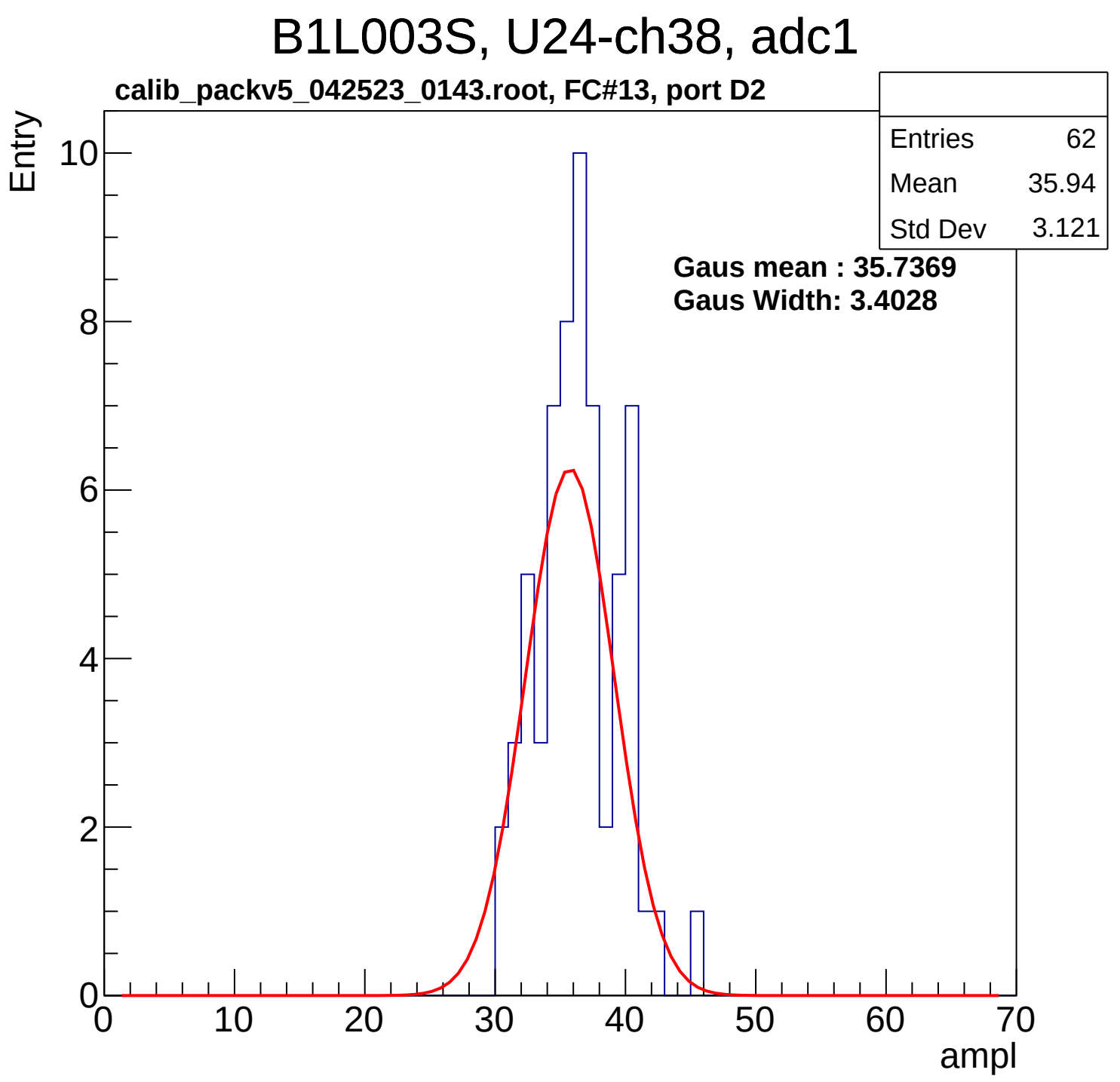
B1L003S, U24-ch38, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.94
Std Dev 3.121
Gaus mean : 35.7369
Gaus Width: 3.4028
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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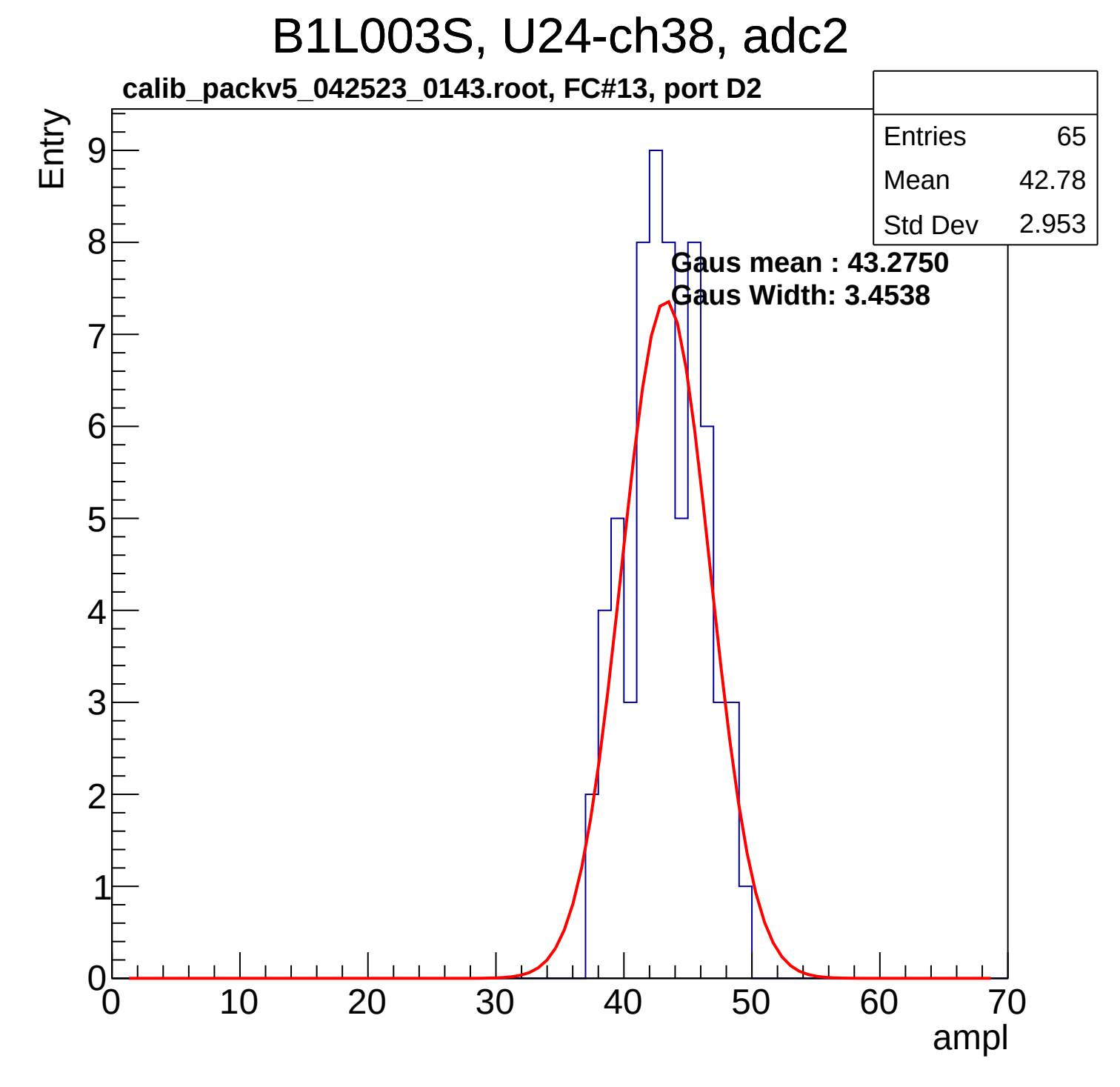
B1L003S, U24-ch38, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.78
Std Dev 2.953
Gaus mean : 43.2750
Gaus Width: 3.4538
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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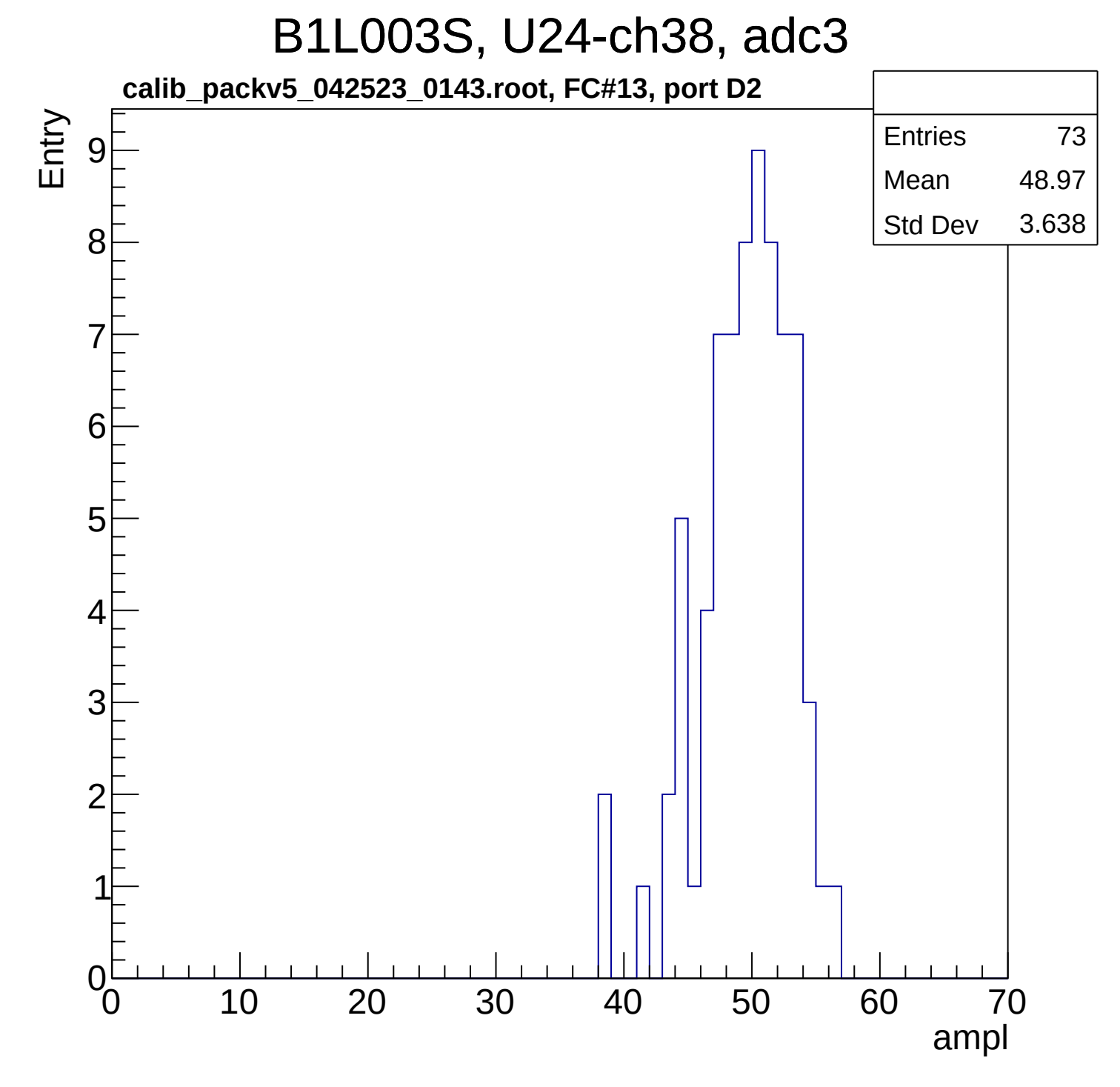
B1L003S, U24-ch38, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 48.97
Std Dev 3.638
Entry
ampl
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4
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6
7
8
9
0
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20
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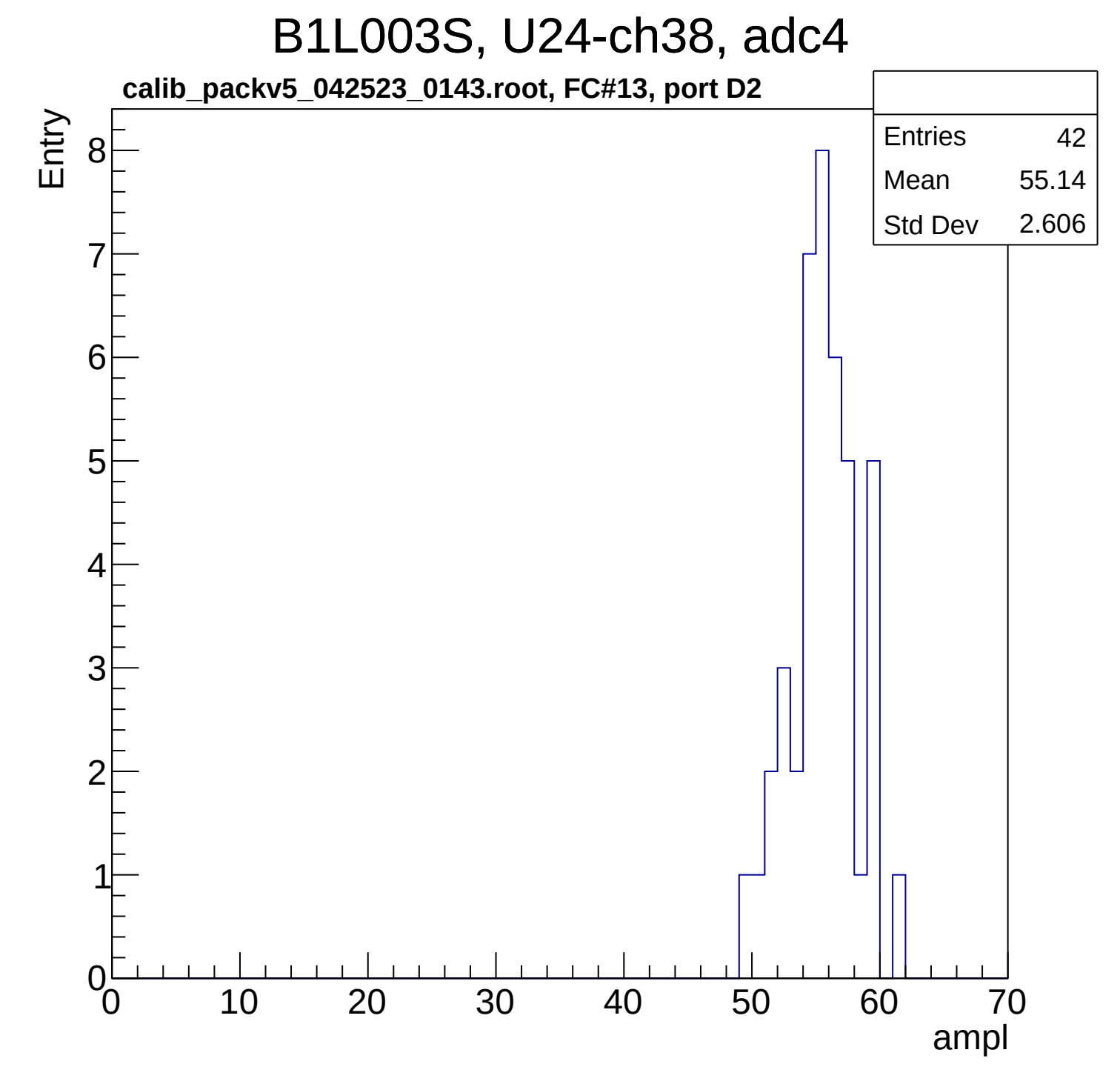
B1L003S, U24-ch38, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 55.14
Std Dev 2.606
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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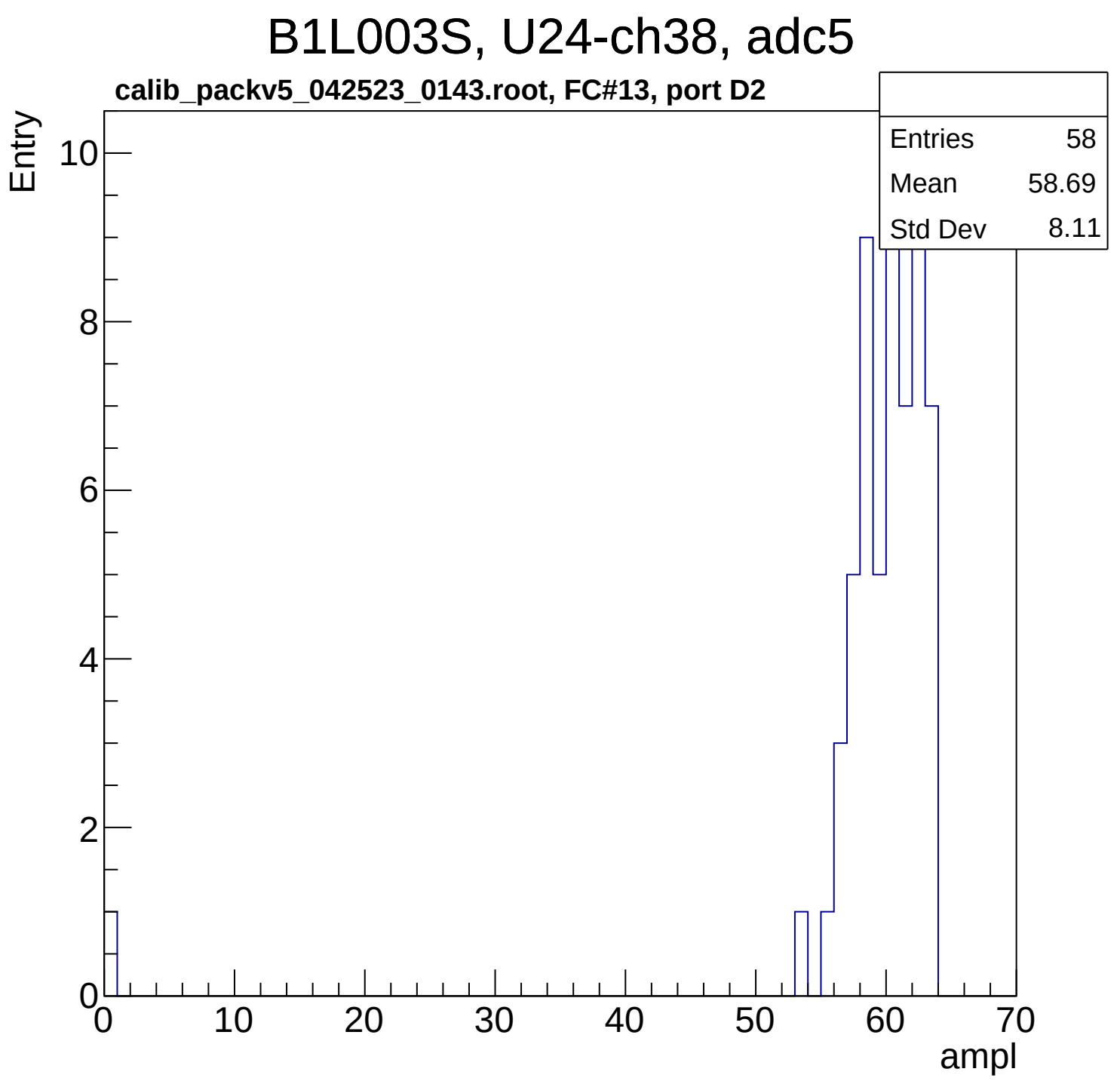
B1L003S, U24-ch38, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 58.69
Std Dev 8.11
Entry
0
2
4
6
8
10
0
10
20
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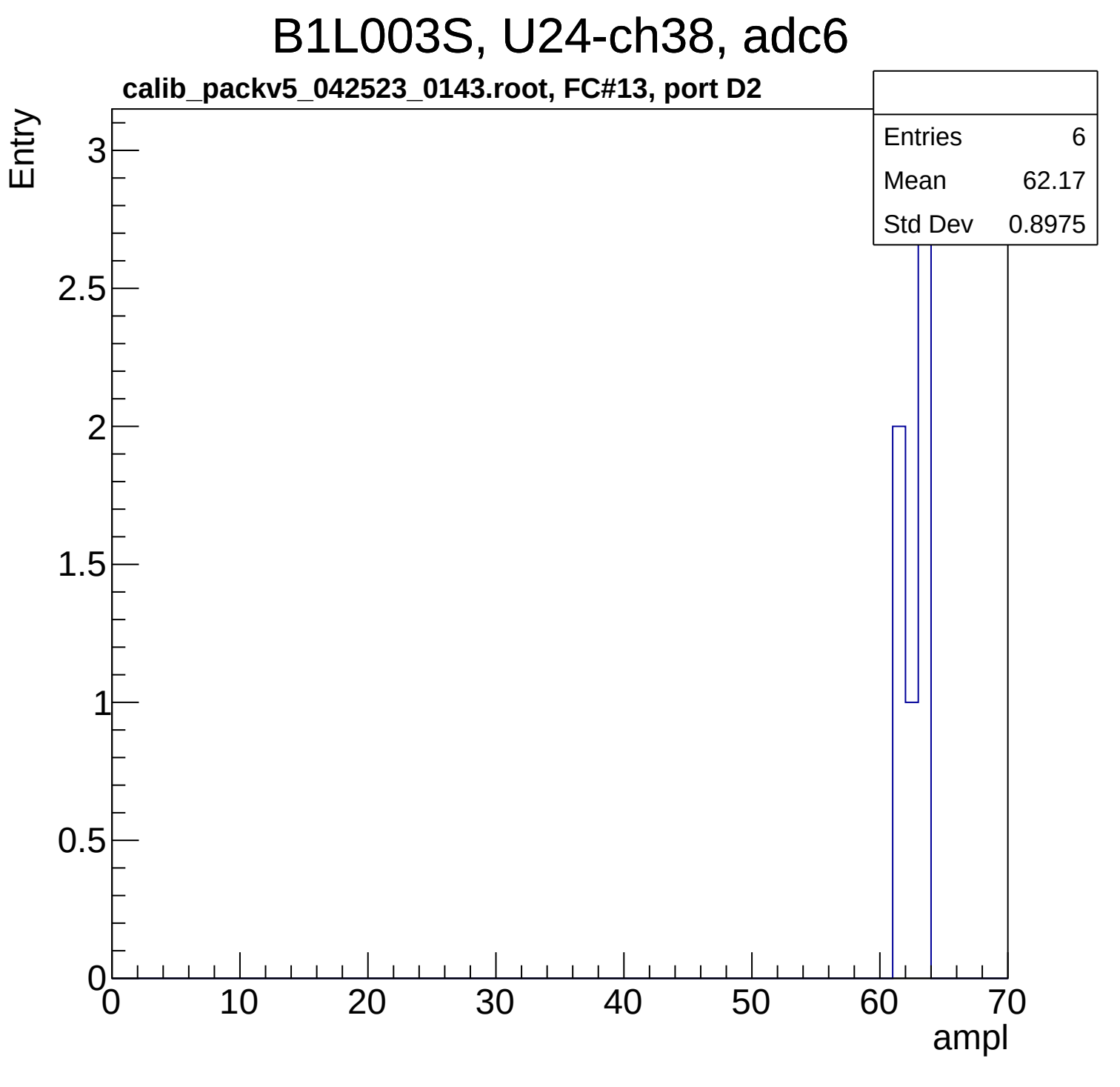
50
60
70
ampl


B1L003S, U24-ch38, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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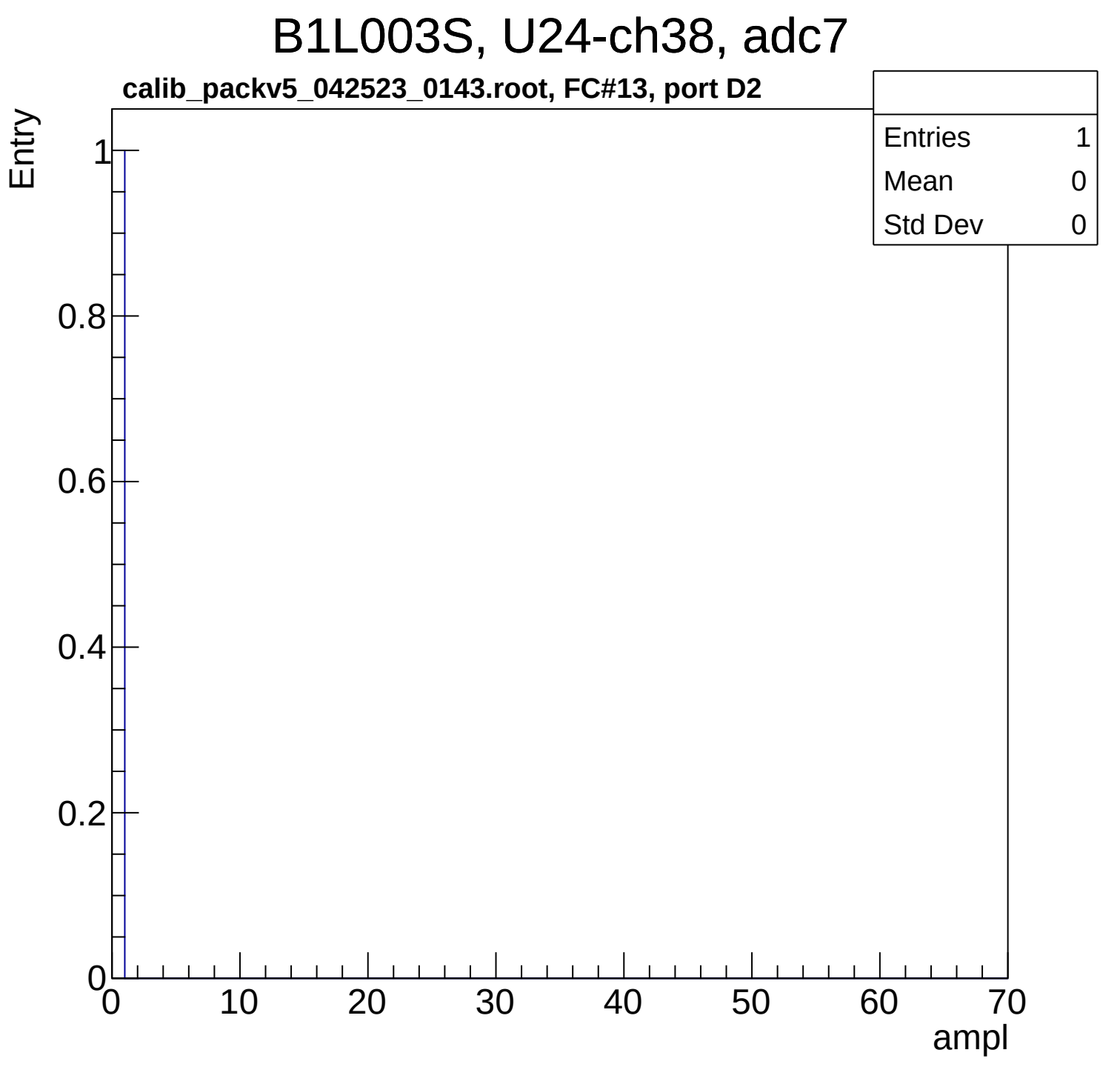
B1L003S, U24-ch38, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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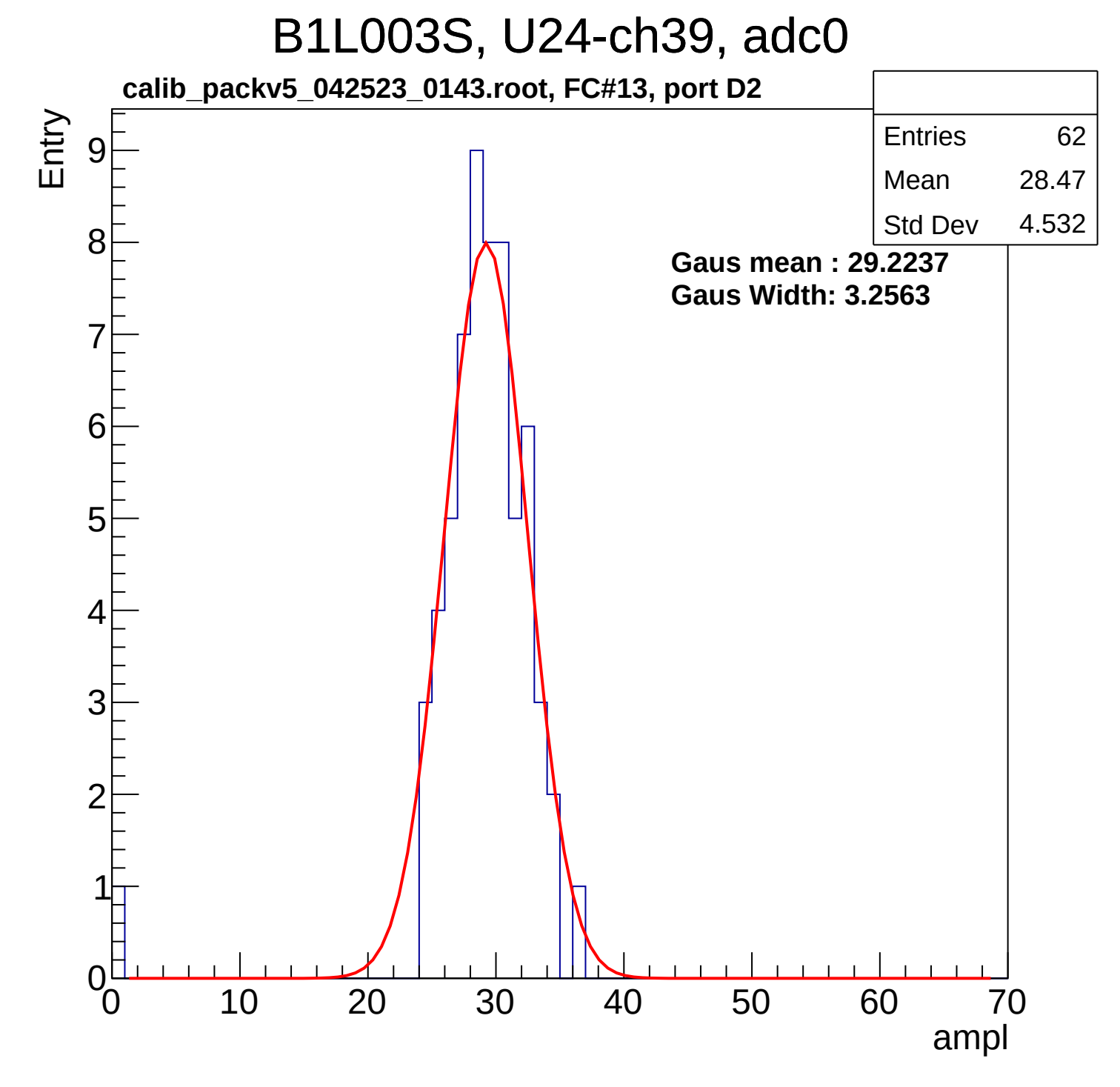
40
50
60
70
ampl


B1L003S, U24-ch39, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.47
Std Dev 4.532
Gaus mean : 29.2237
Gaus Width: 3.2563
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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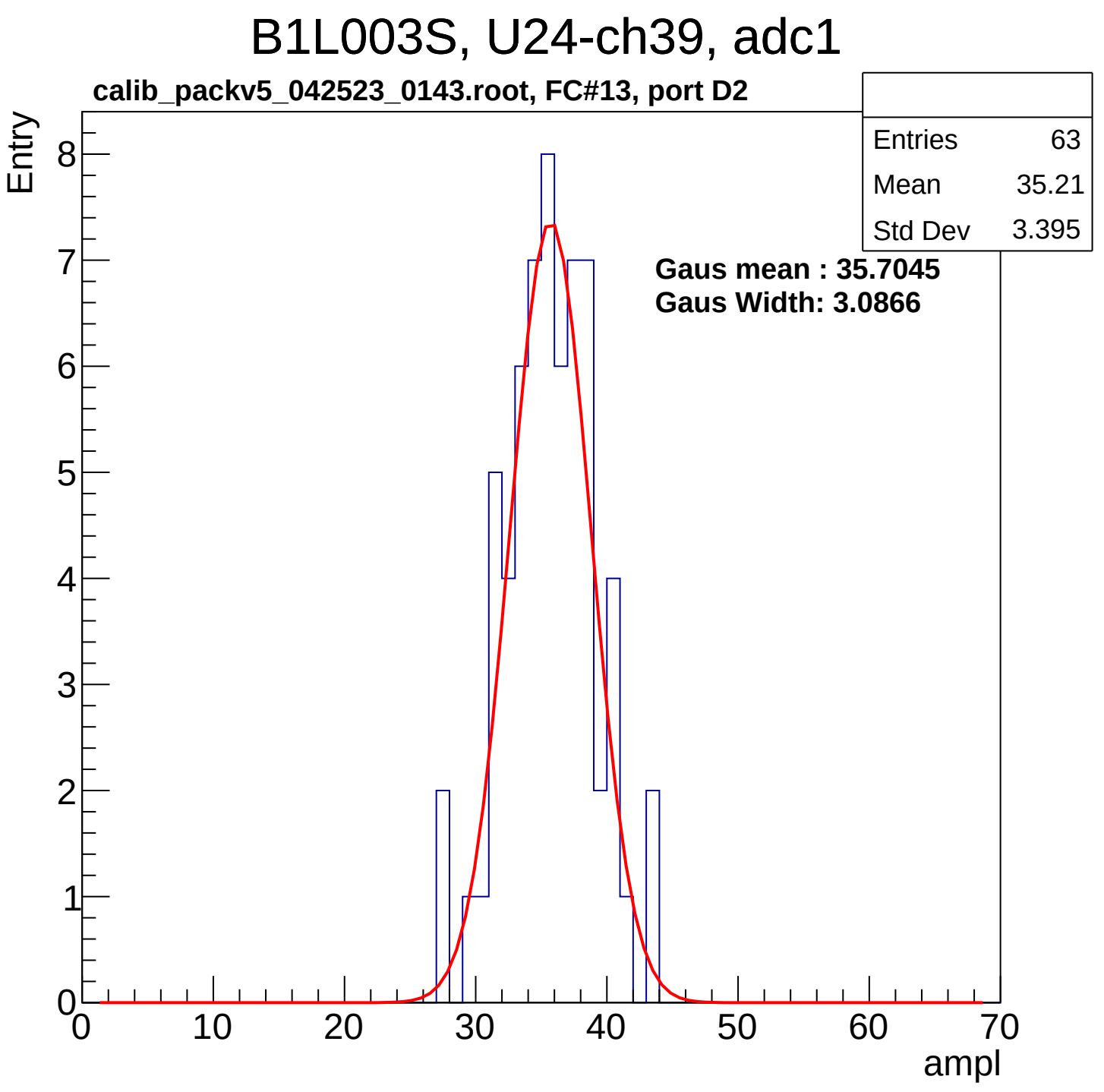
B1L003S, U24-ch39, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 35.21
Std Dev 3.395
Gaus mean : 35.7045
Gaus Width: 3.0866
Entry
ampl
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1
2
3
4
5
6
7
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0
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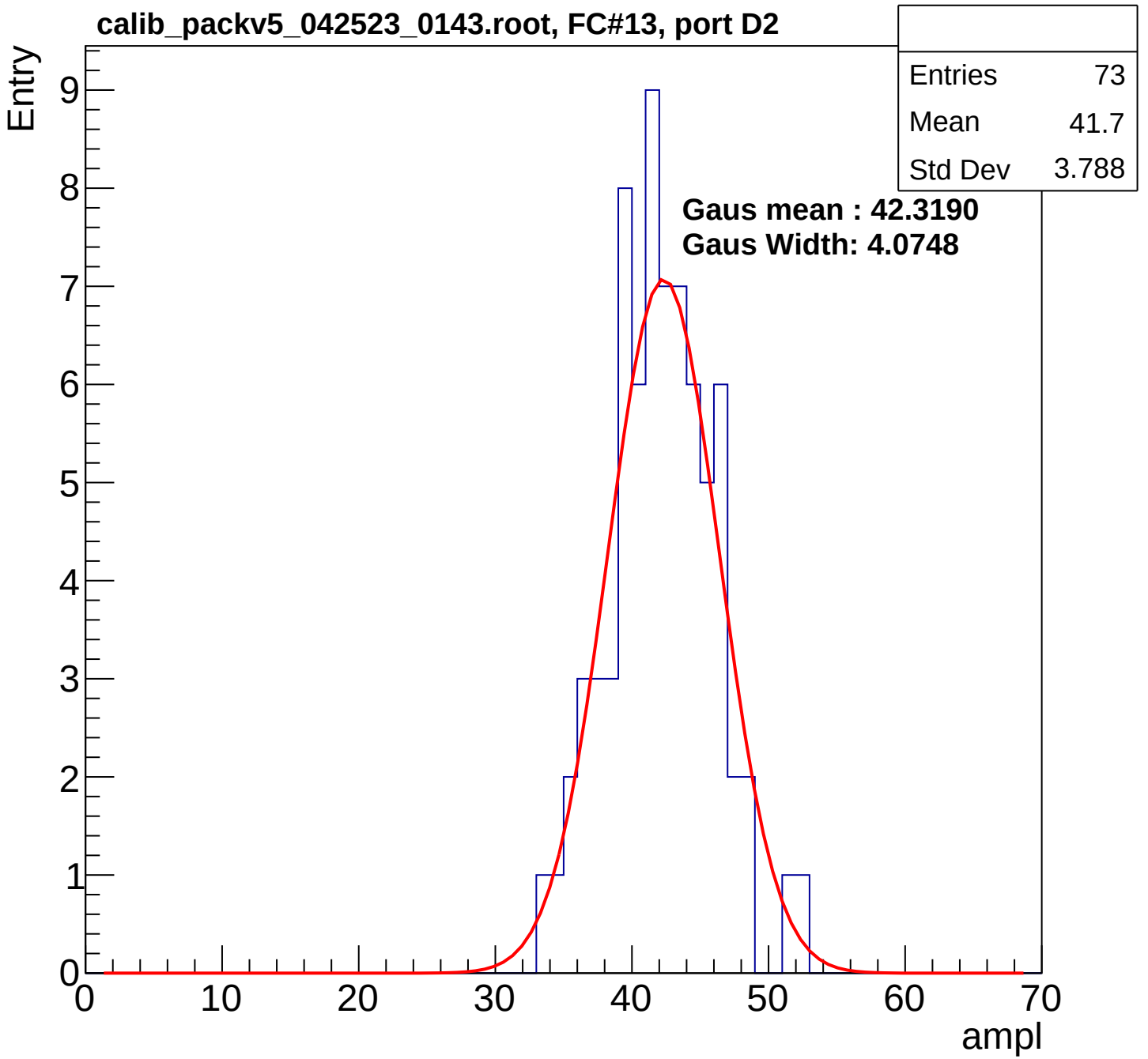
B1L003S, U24-ch39, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.7
Std Dev 3.788
Gaus mean : 42.3190
Gaus Width: 4.0748
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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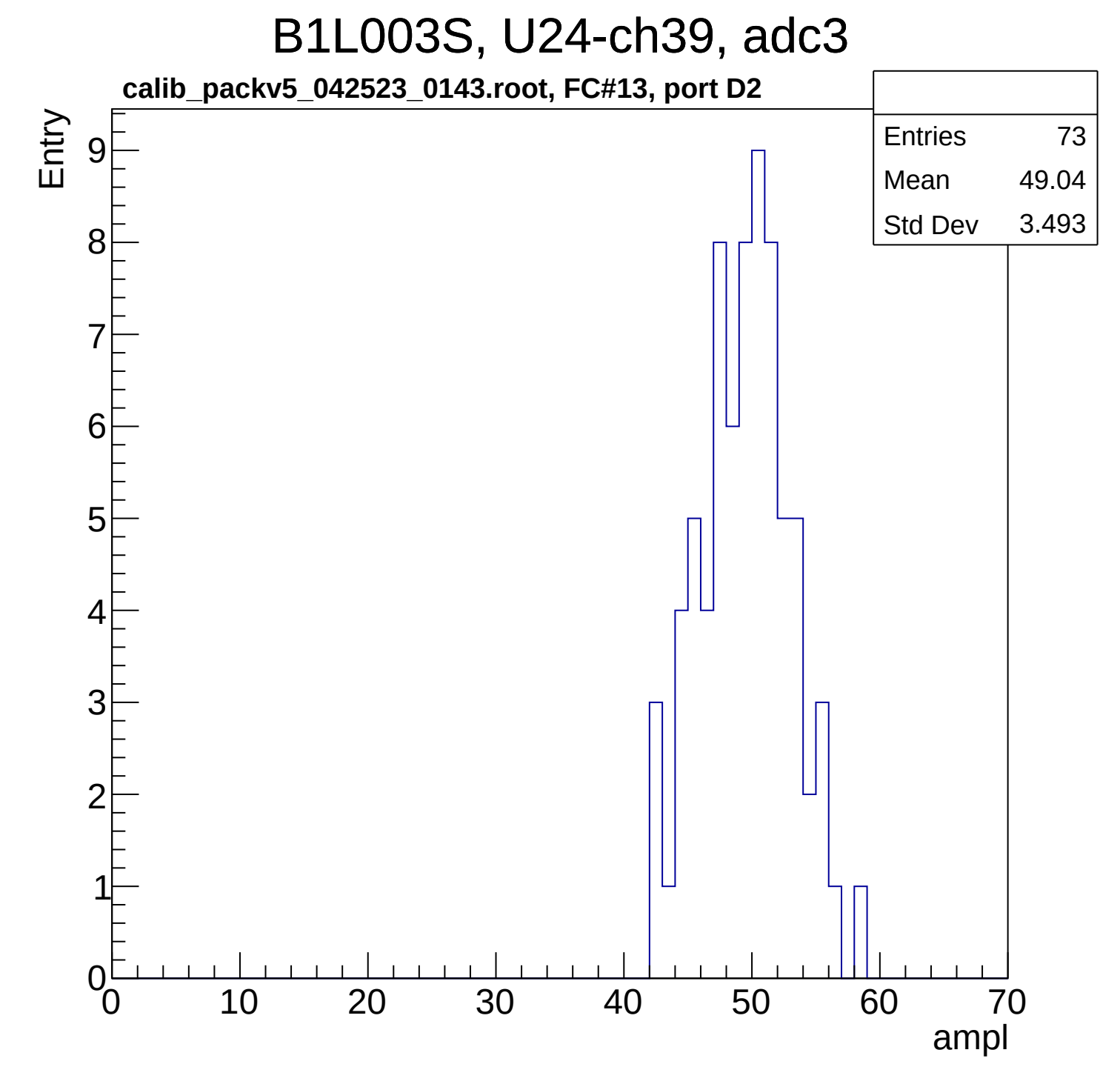
B1L003S, U24-ch39, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.04
Std Dev 3.493
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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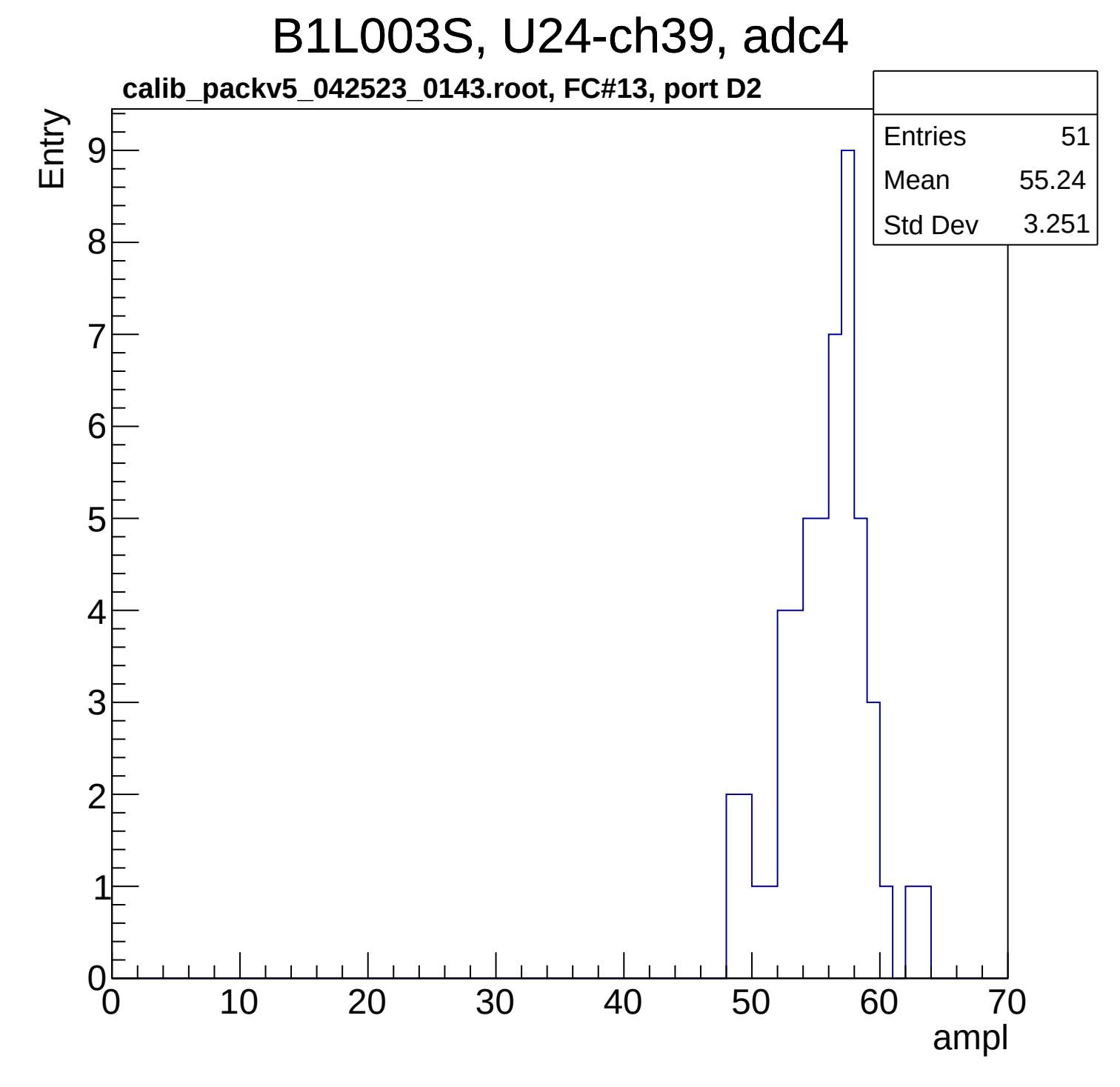
B1L003S, U24-ch39, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.24
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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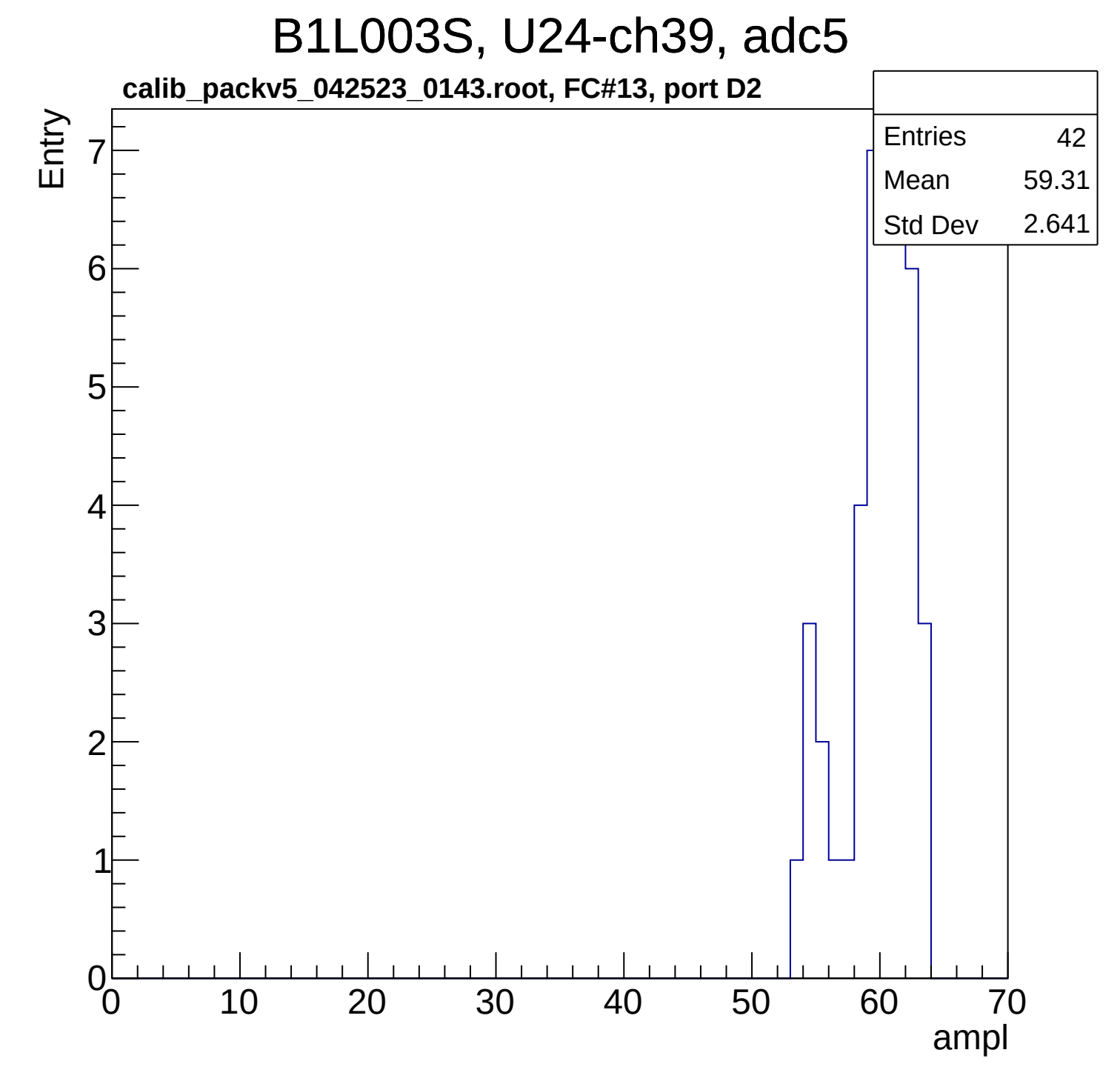
B1L003S, U24-ch39, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 59.31
Std Dev 2.641
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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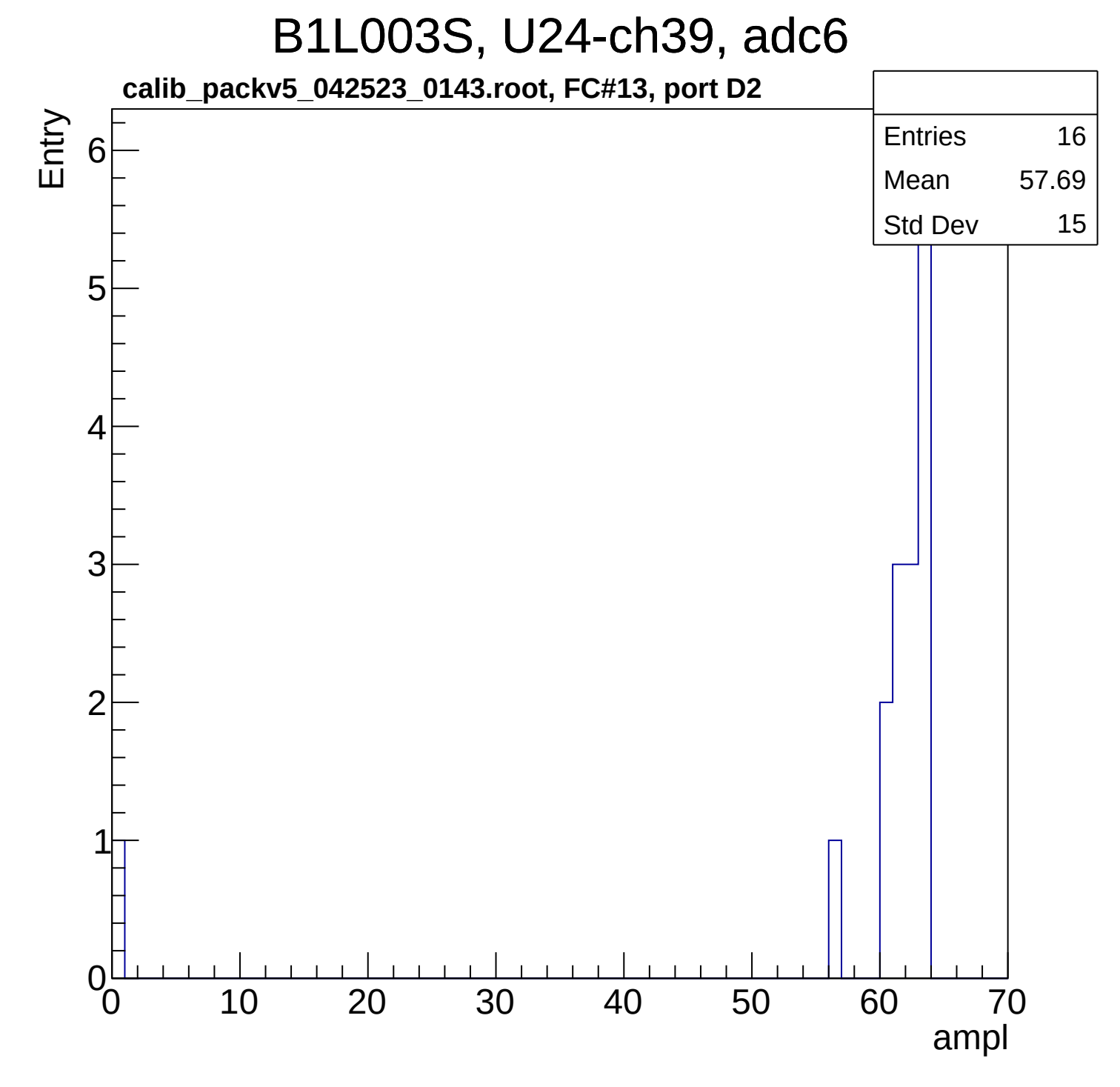
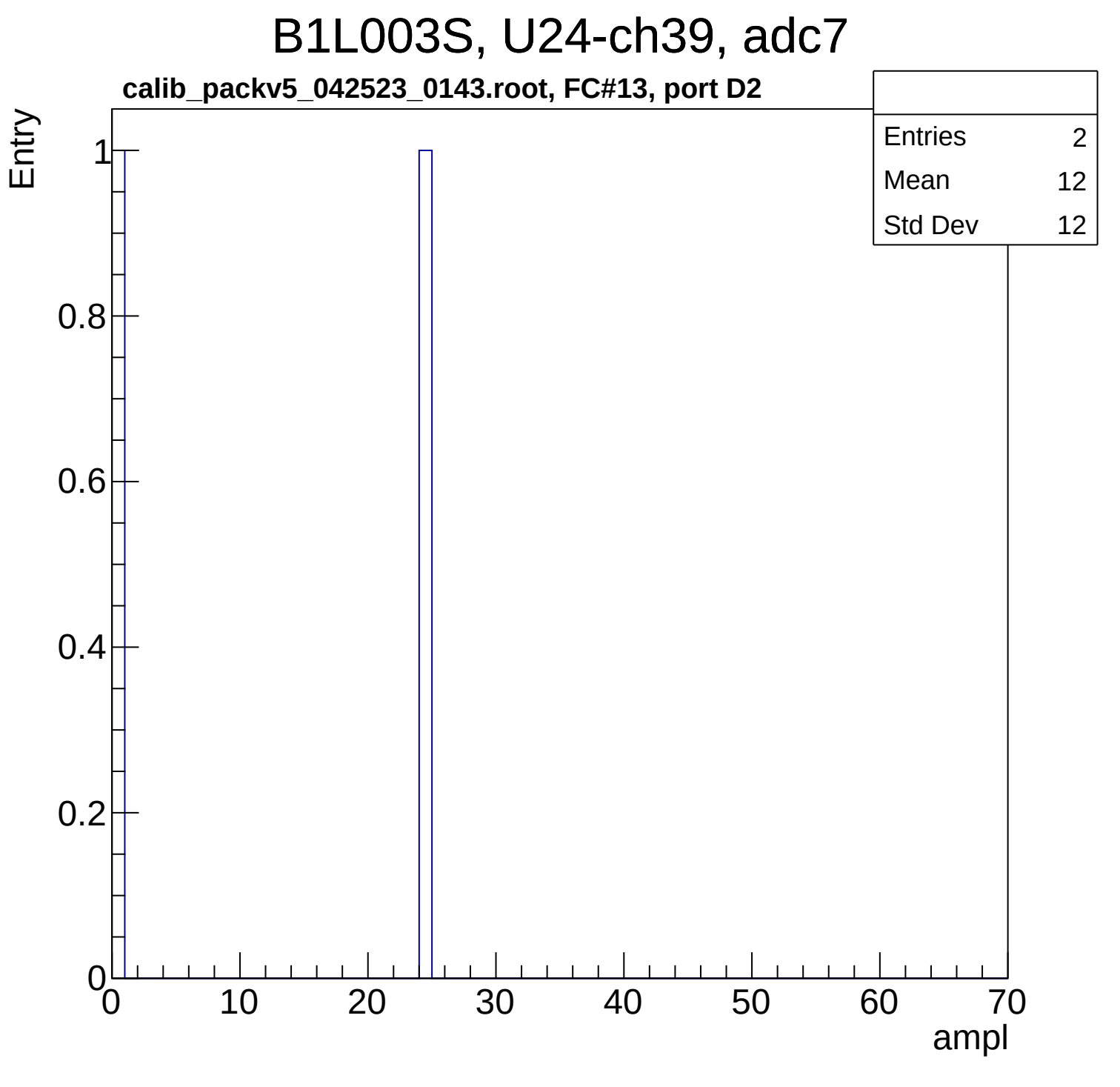
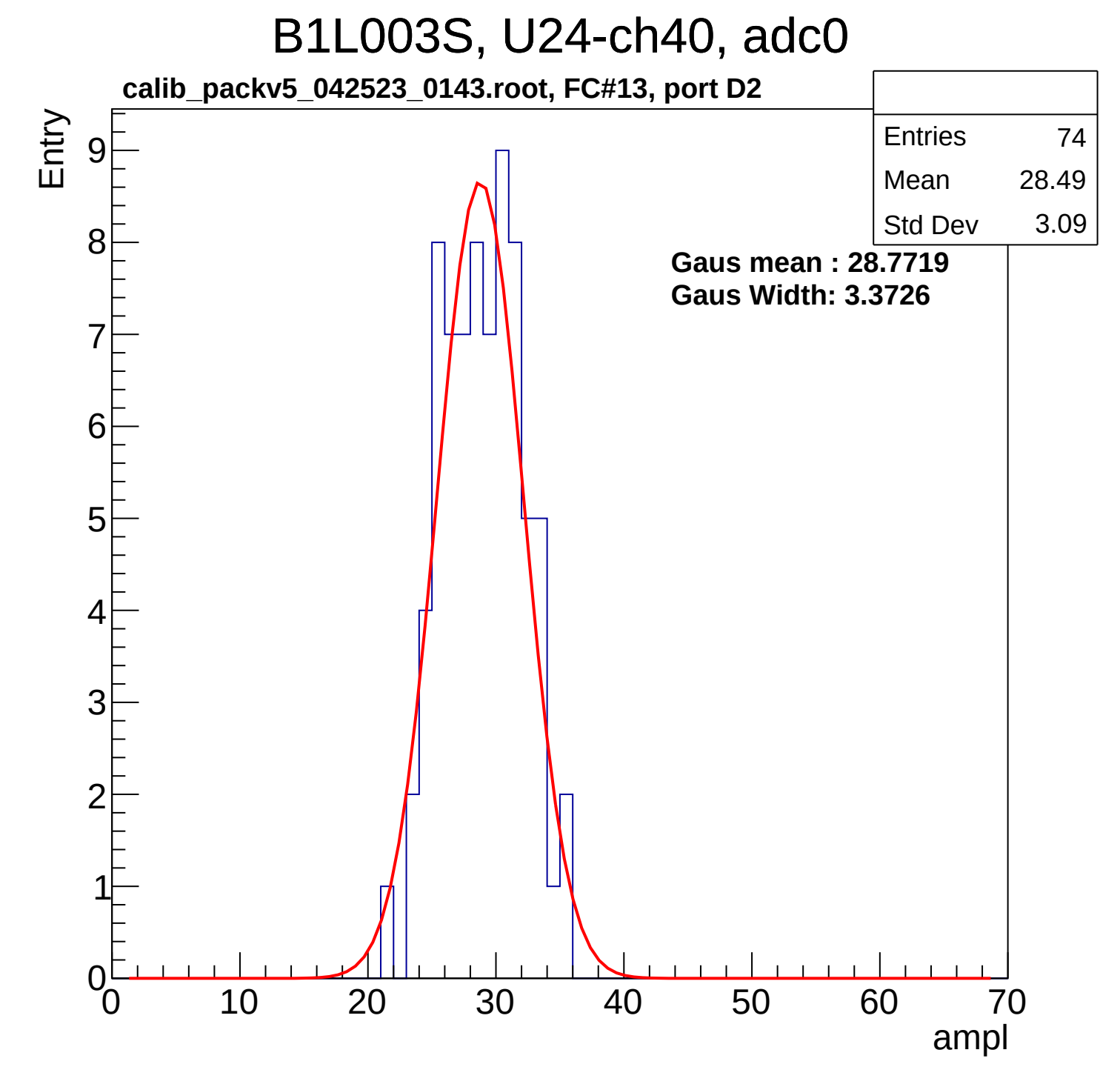
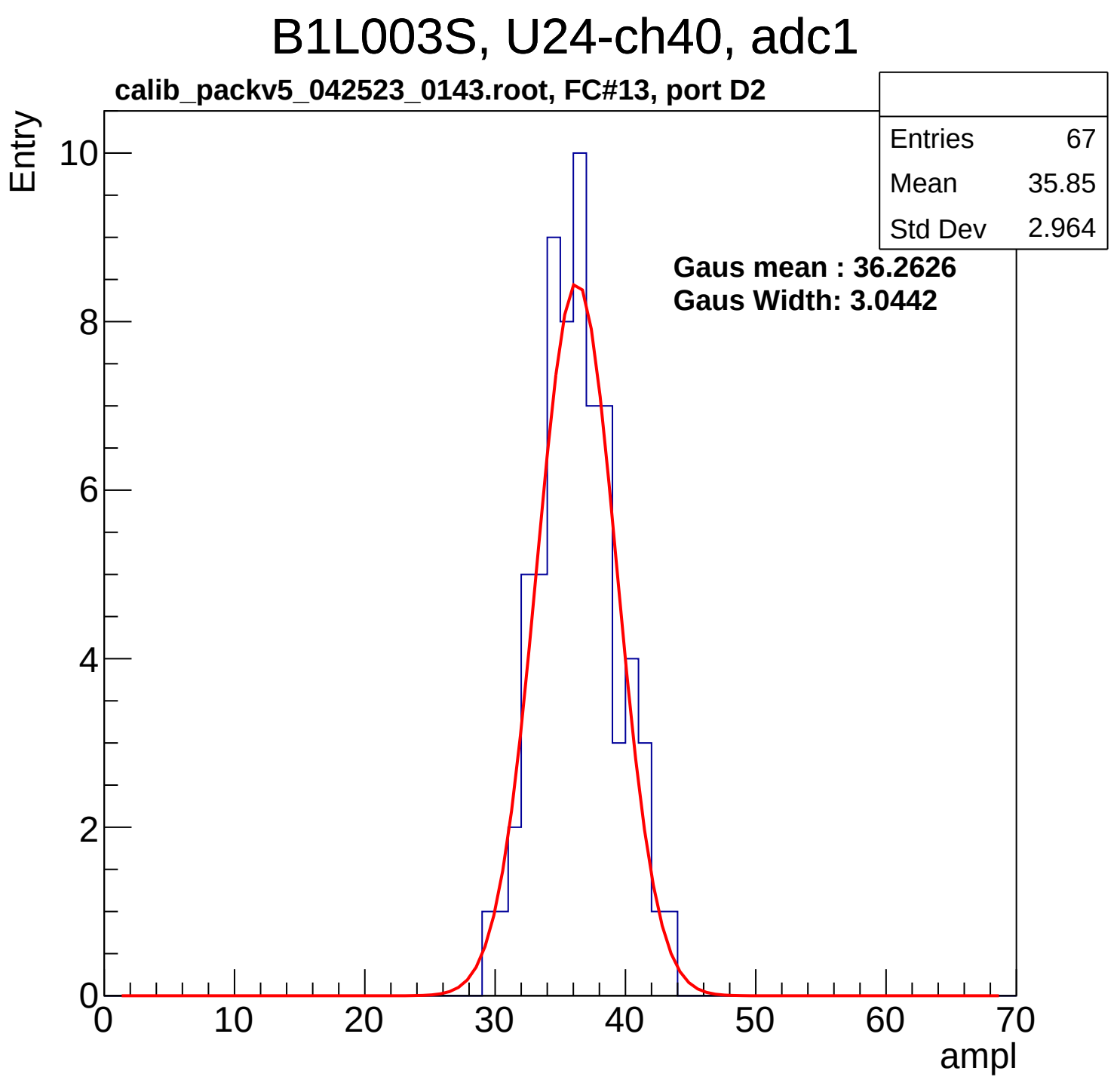
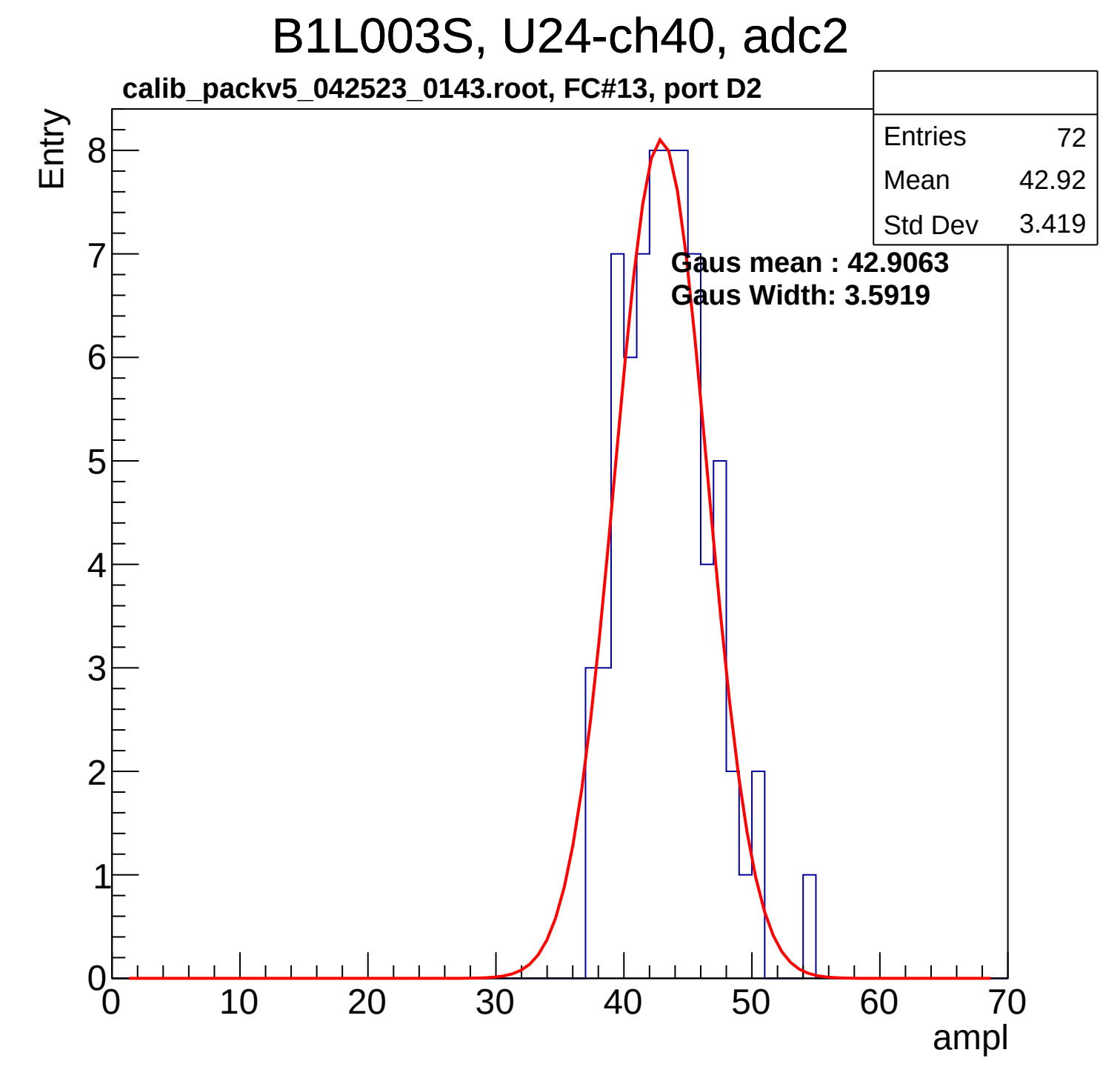

B1L003S, U24-ch39, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 57.69
Std Dev 15
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U24-ch39, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch40, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 28.49
Std Dev 3.09
Gaus mean : 28.7719
Gaus Width: 3.3726
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch40, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 35.85
Std Dev 2.964
Gaus mean : 36.2626
Gaus Width: 3.0442
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch40, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.92
Std Dev 3.419
Gaus mean : 42.9063
Gaus Width: 3.5919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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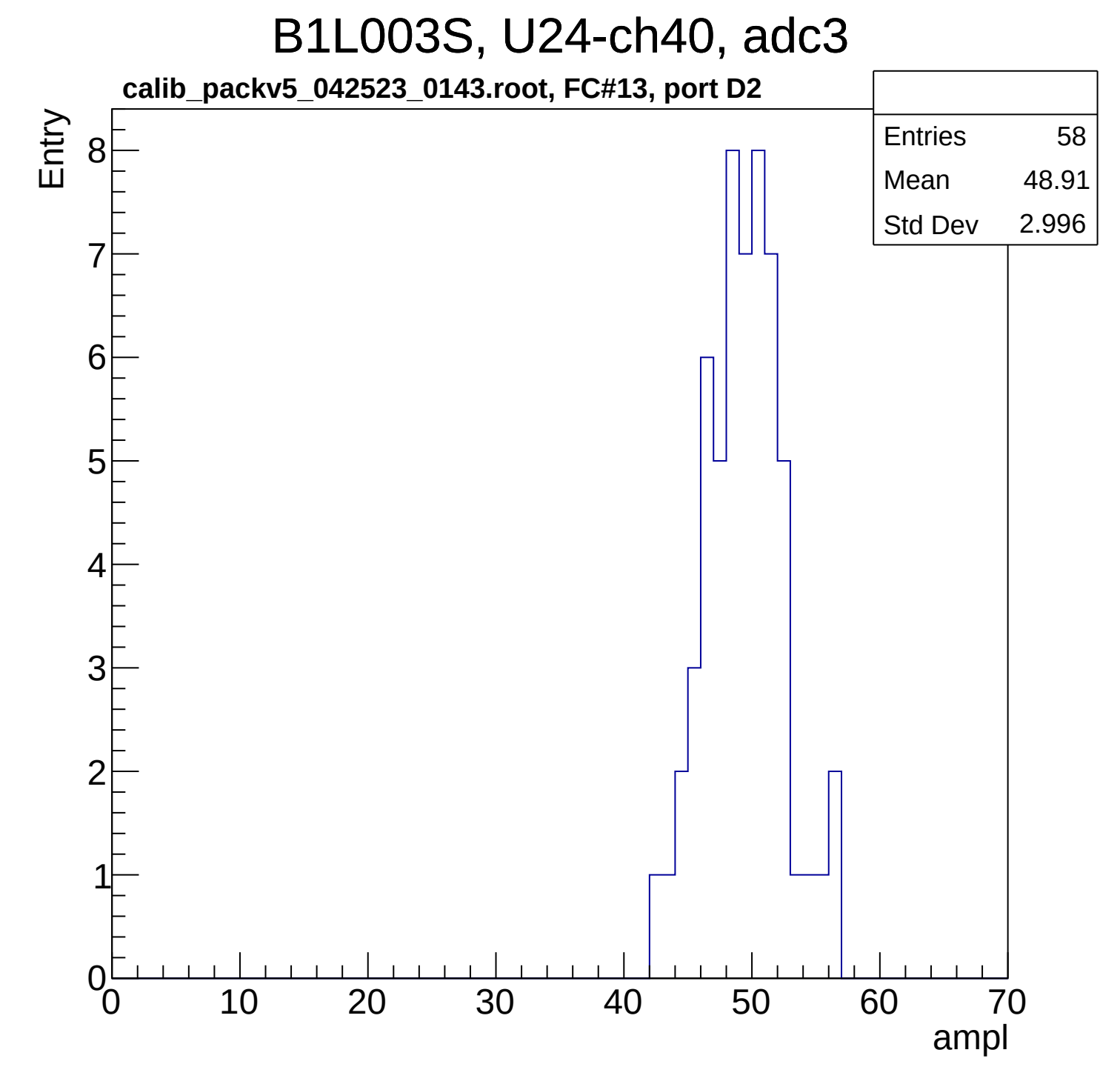
B1L003S, U24-ch40, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.91
Std Dev 2.996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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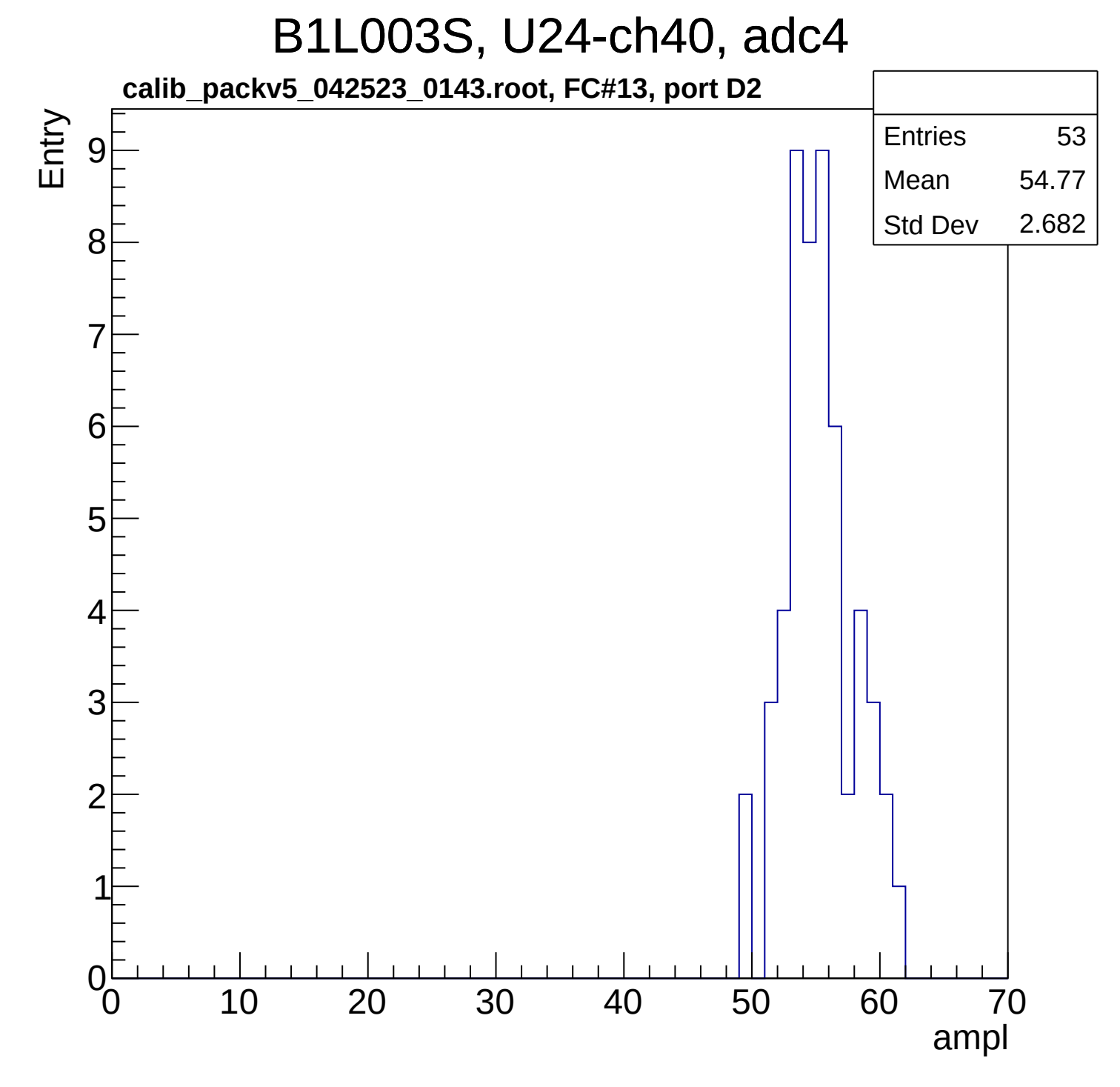
B1L003S, U24-ch40, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.77
Std Dev 2.682
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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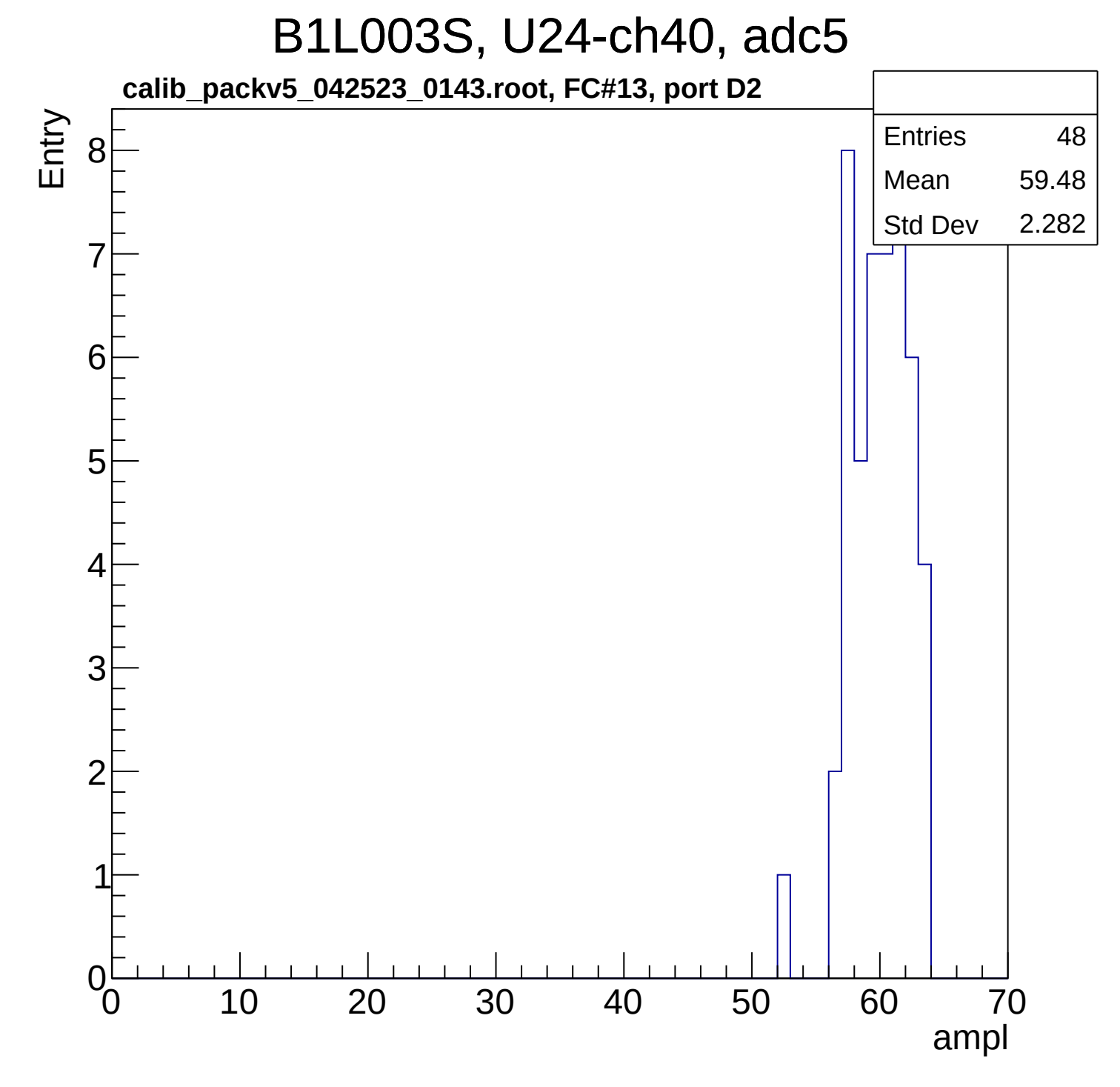
B1L003S, U24-ch40, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.48
Std Dev 2.282
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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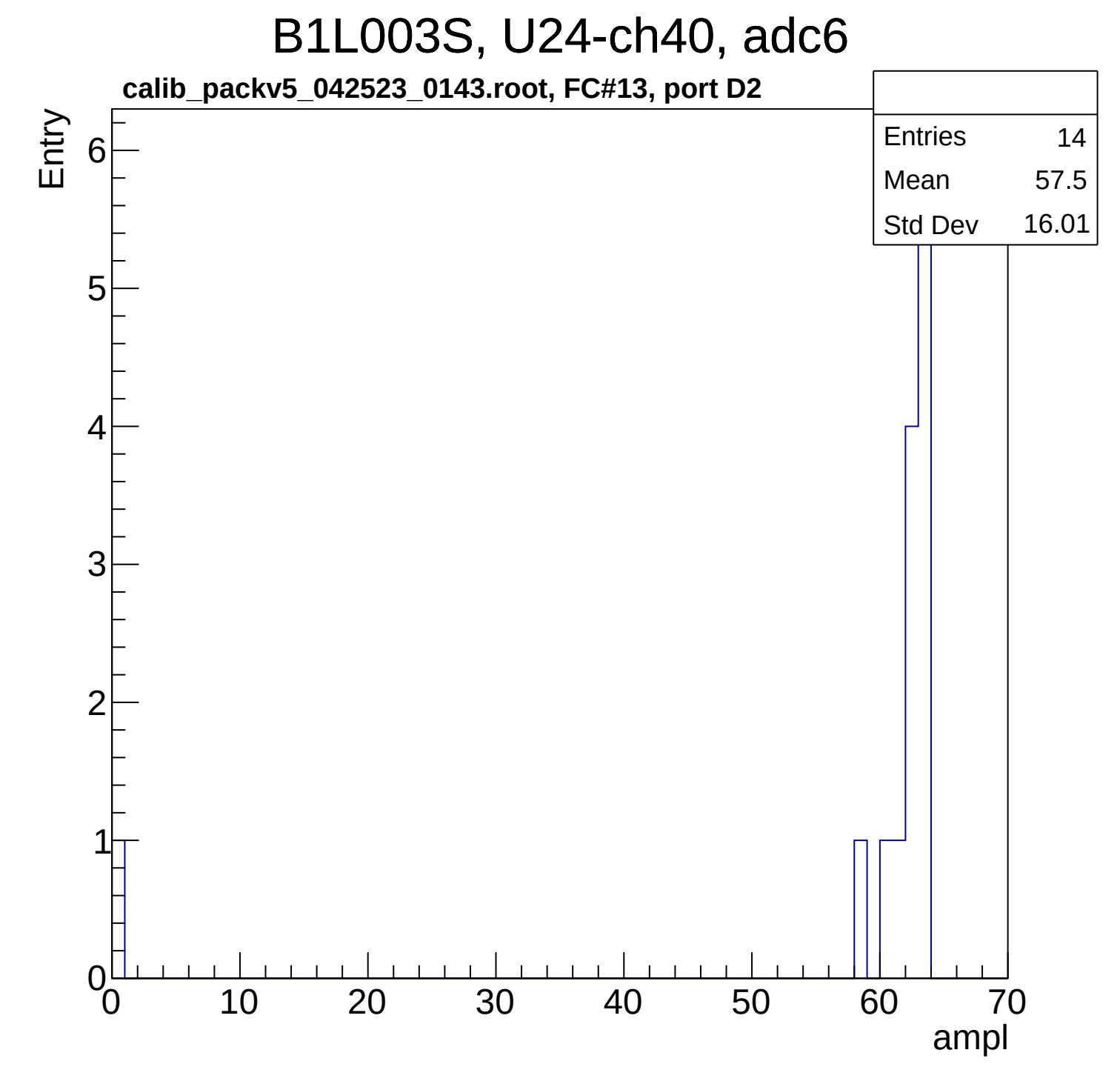
60
70


B1L003S, U24-ch40, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.5
Std Dev 16.01
Entry
0
1
2
3
4
5
6
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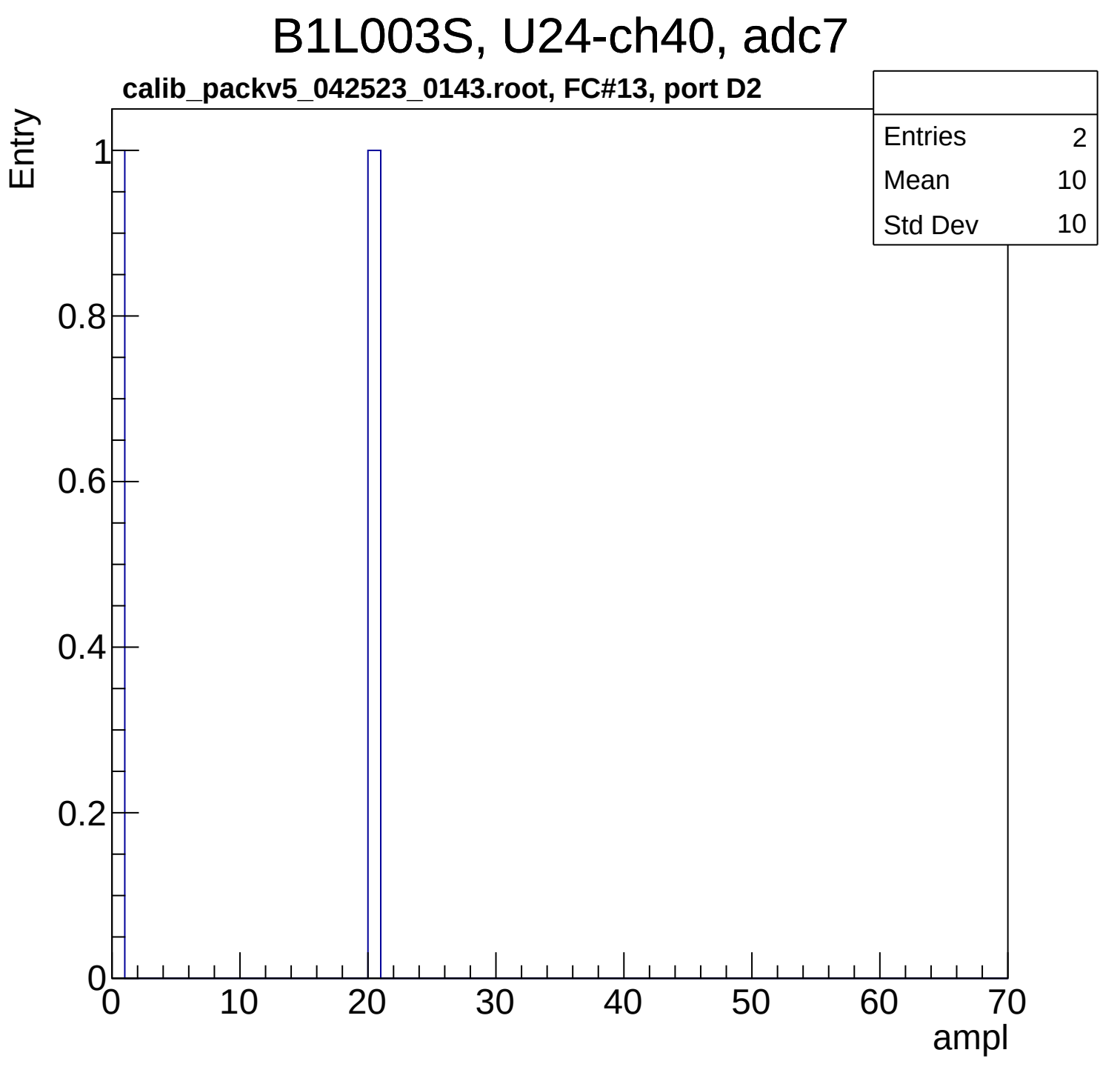
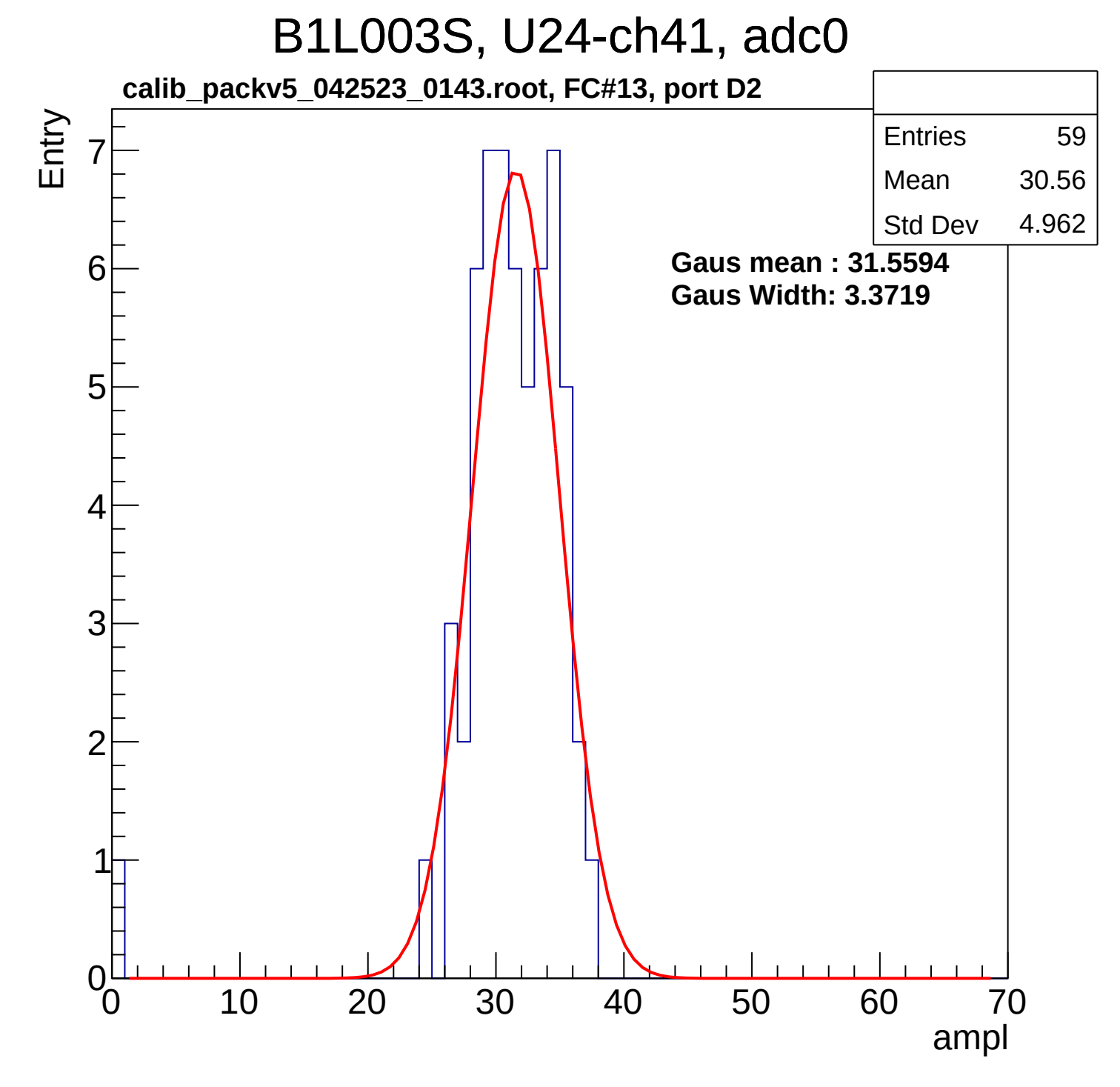
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch40, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch41, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 30.56
Std Dev 4.962
Gaus mean : 31.5594
Gaus Width: 3.3719
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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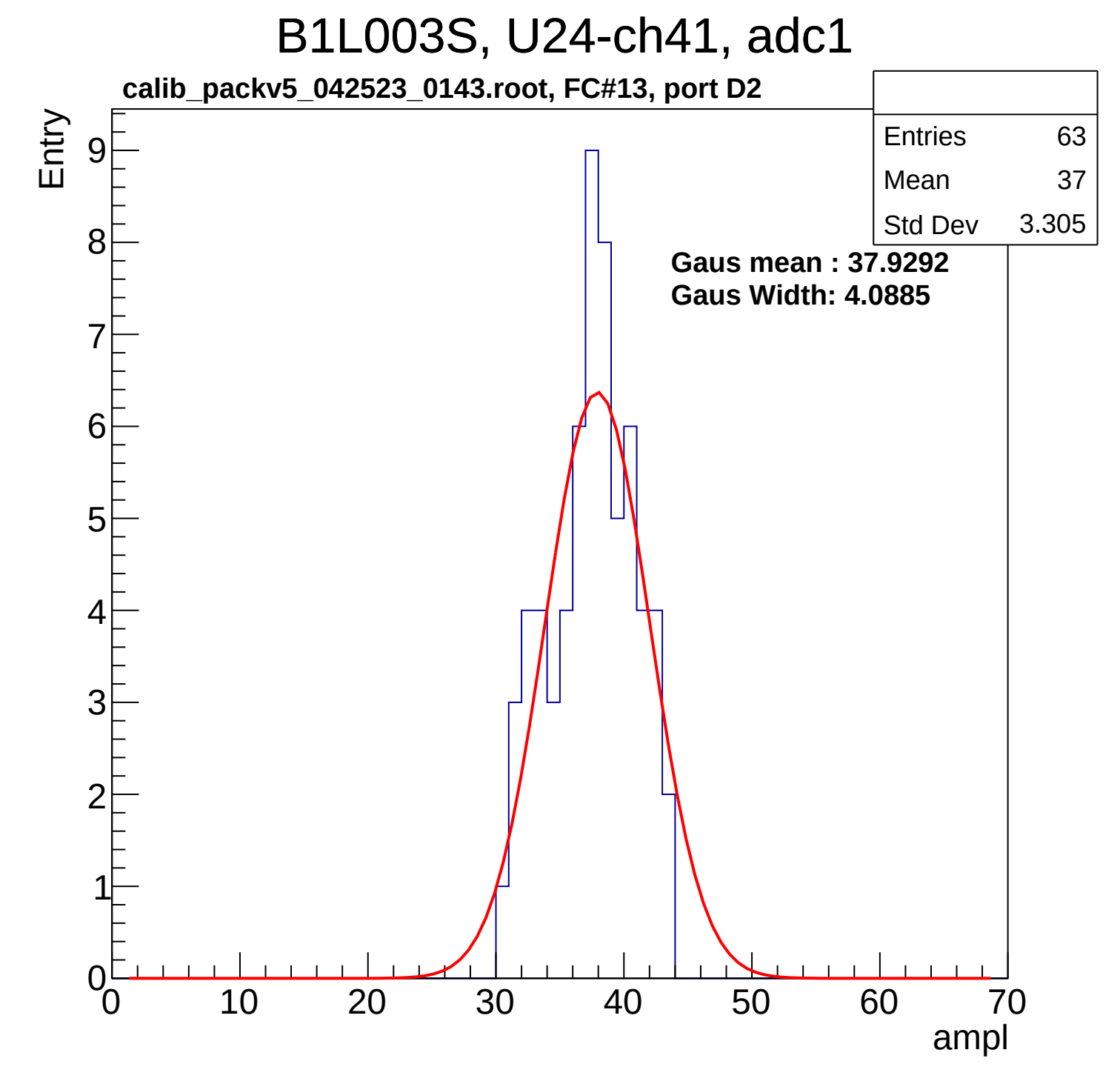
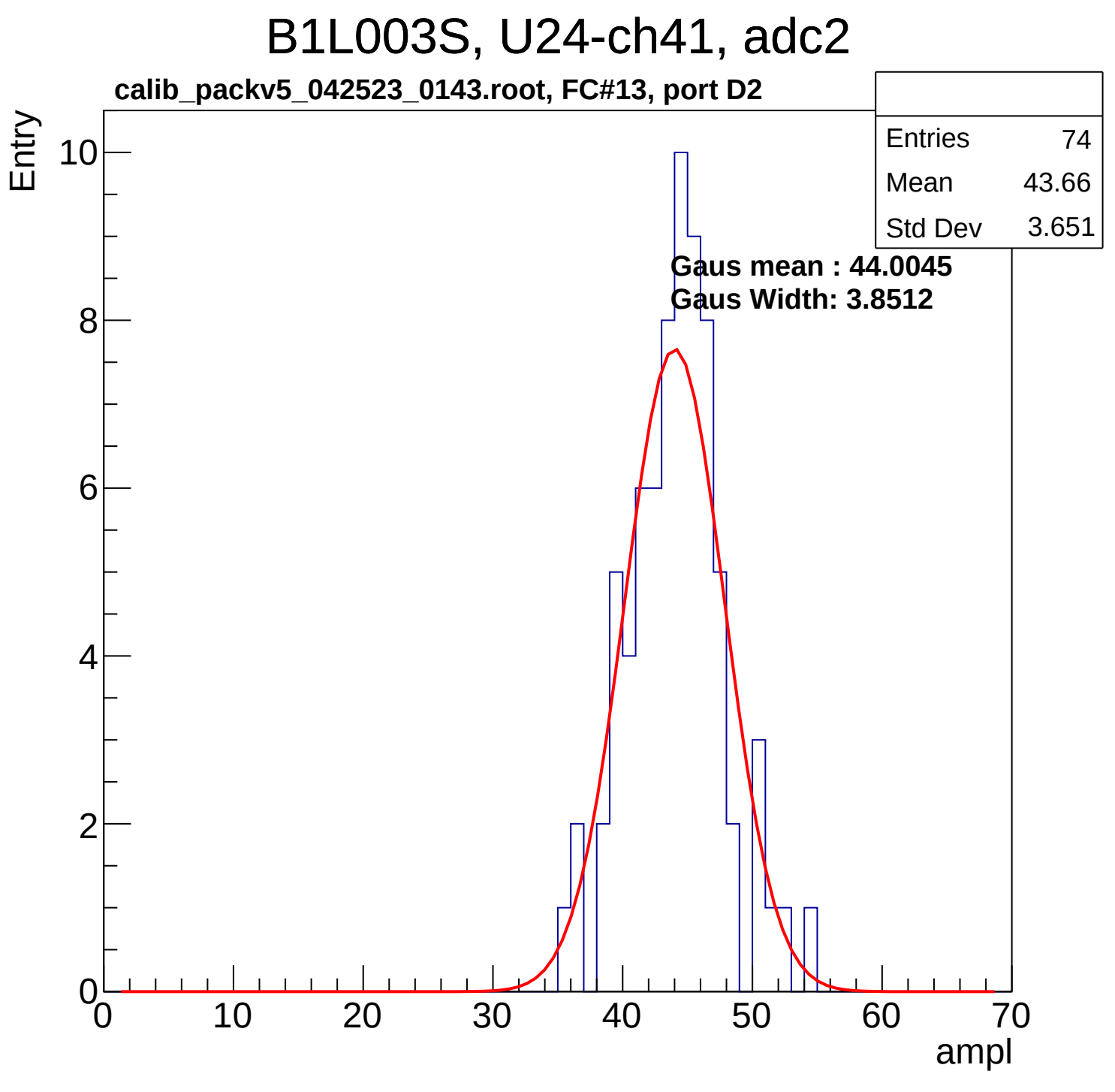
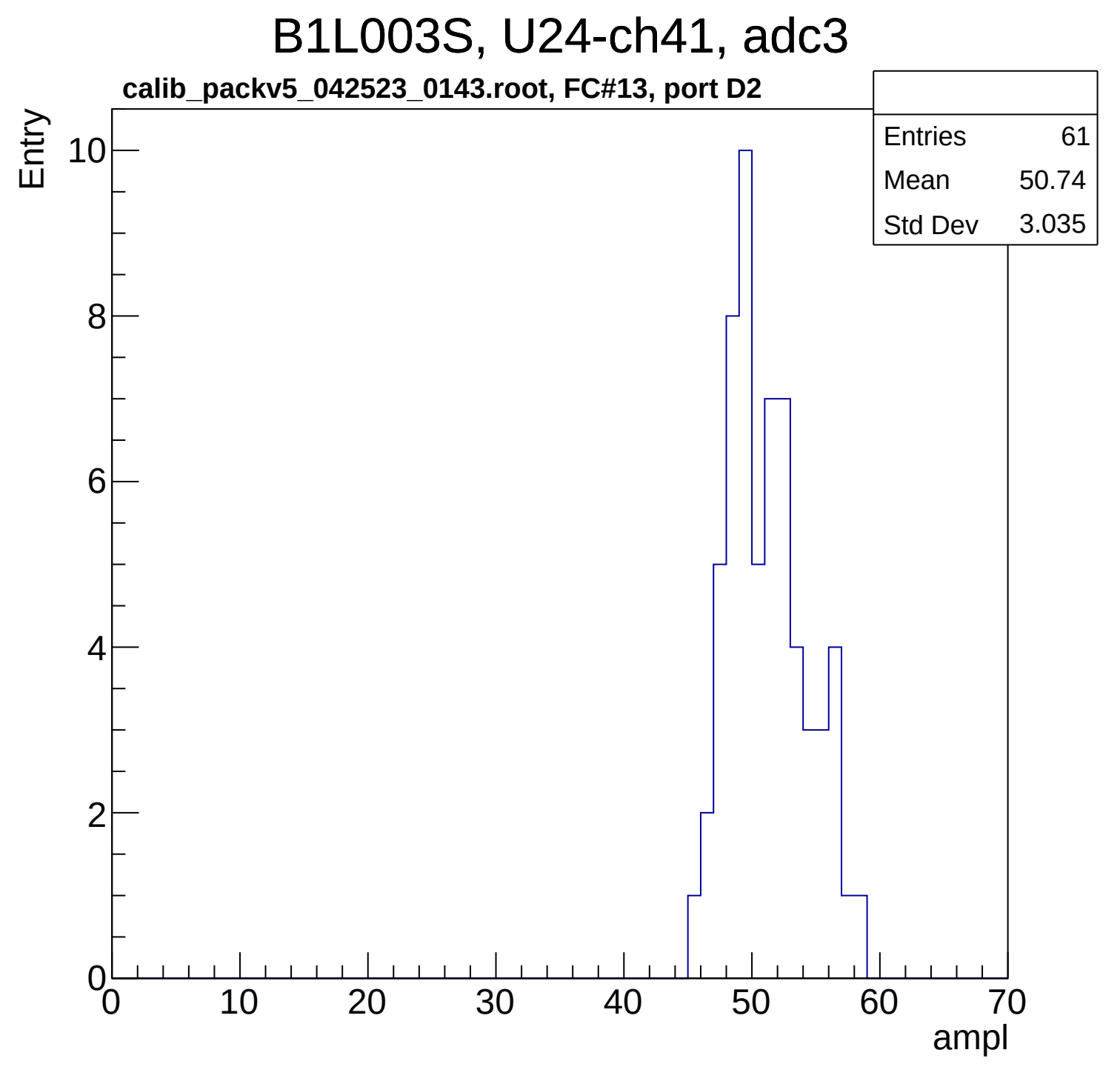
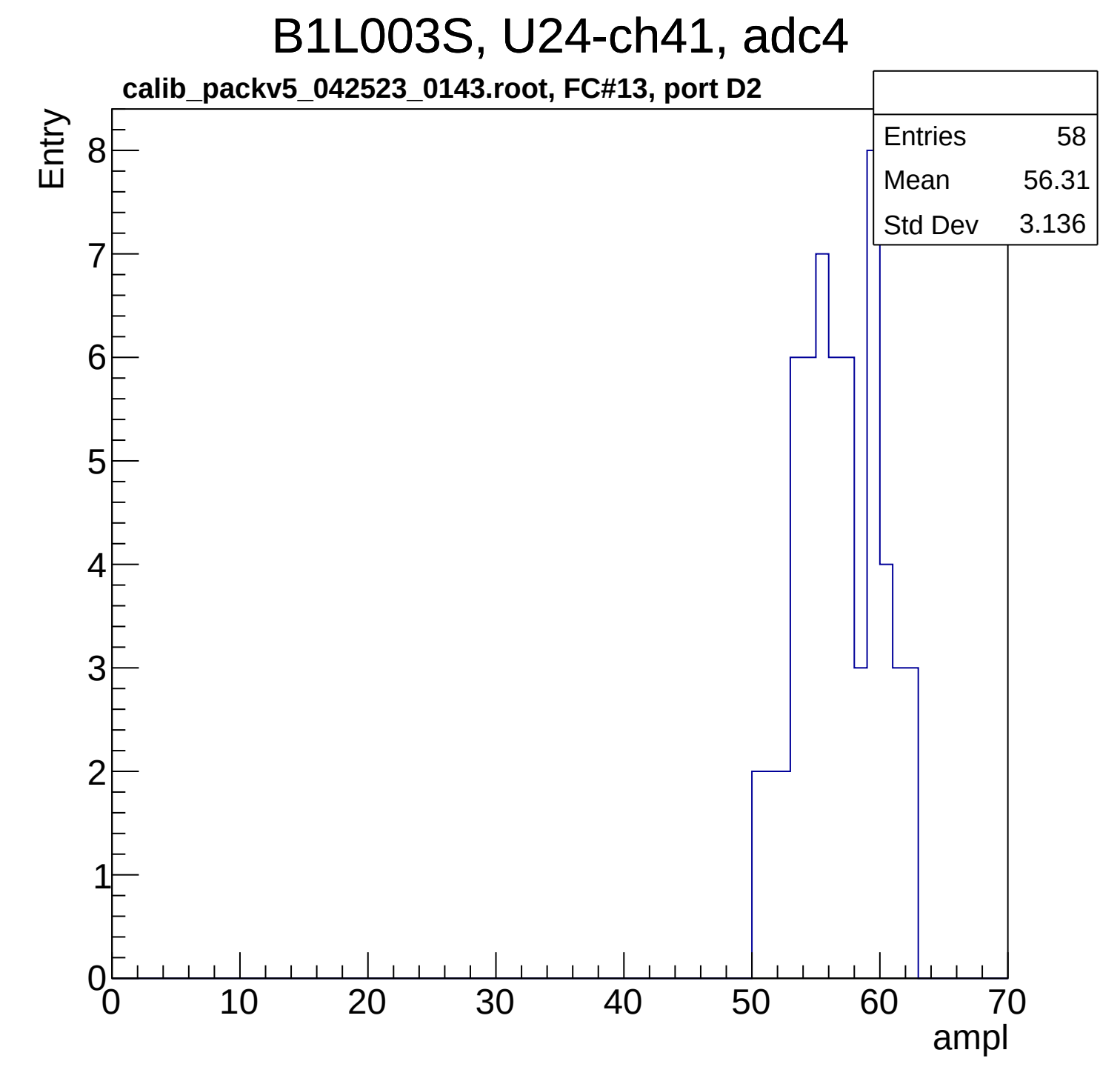

B1L003S, U24-ch41, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 37
Std Dev 3.305
Gaus mean : 37.9292
Gaus Width: 4.0885
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch41, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 43.66
Std Dev 3.651
Gaus mean : 44.0045
Gaus Width: 3.8512
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch41, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.74
Std Dev 3.035
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch41, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 56.31
Std Dev 3.136
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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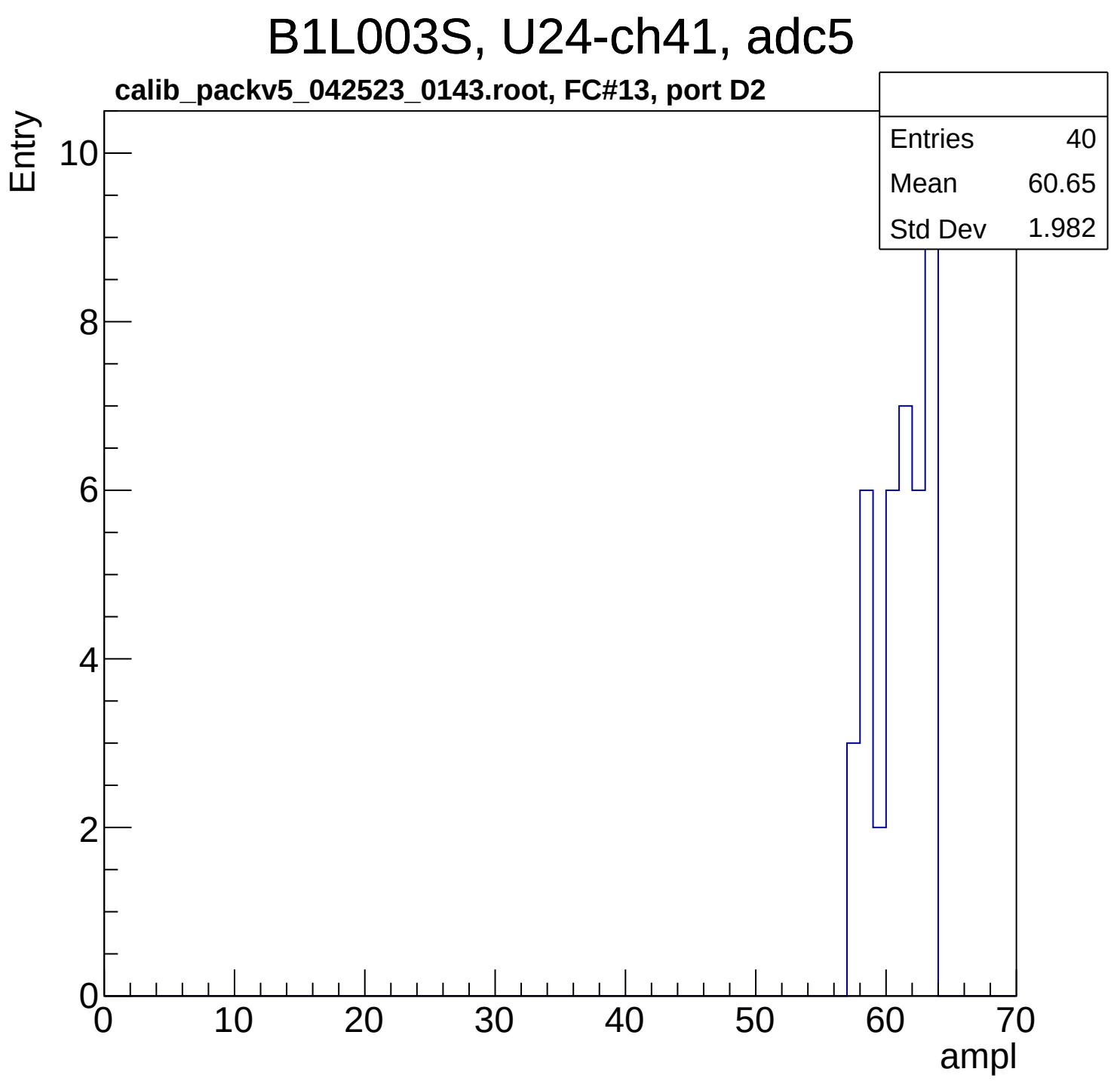
B1L003S, U24-ch41, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 60.65
Std Dev 1.982
Entry
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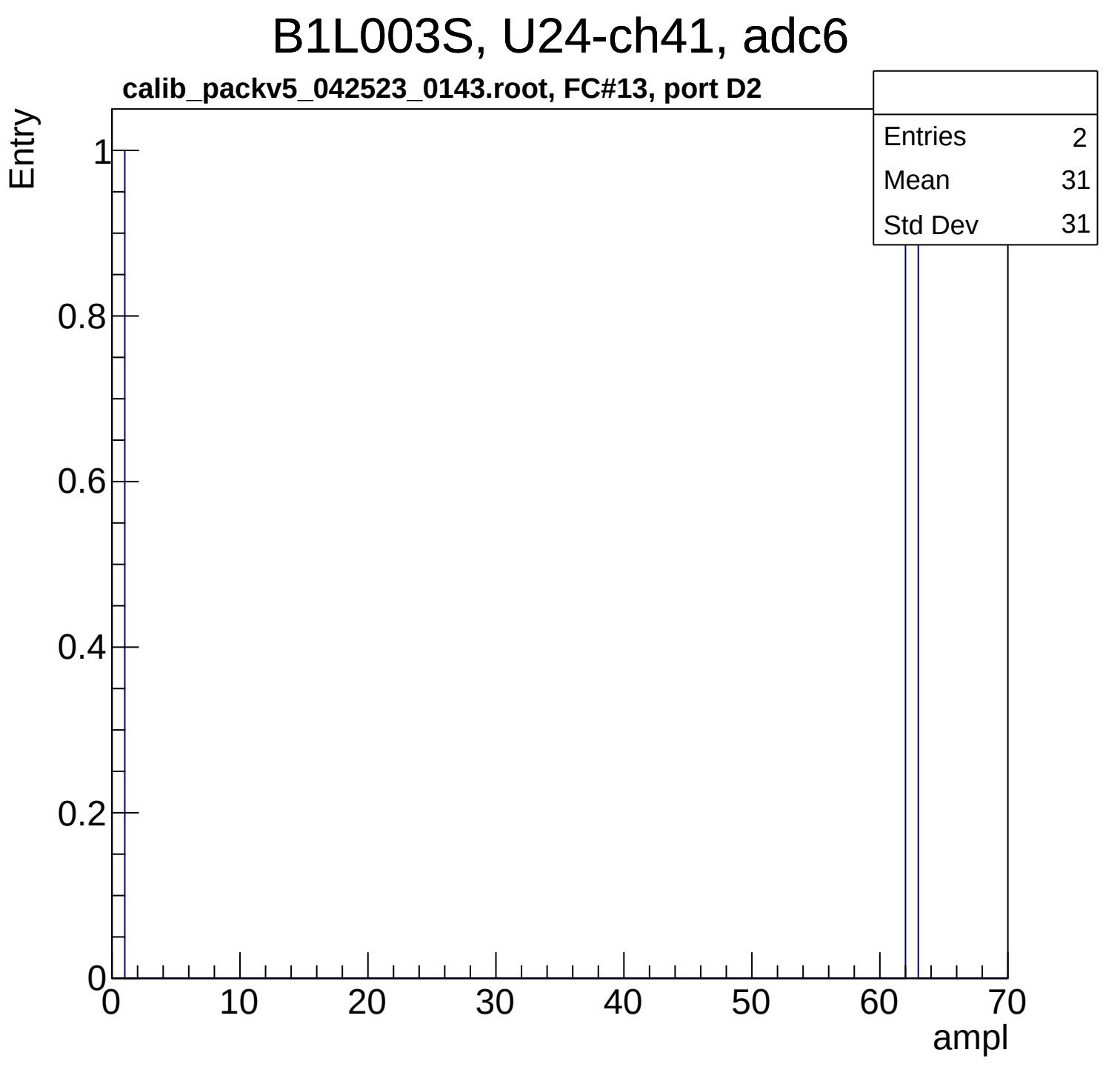
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch41, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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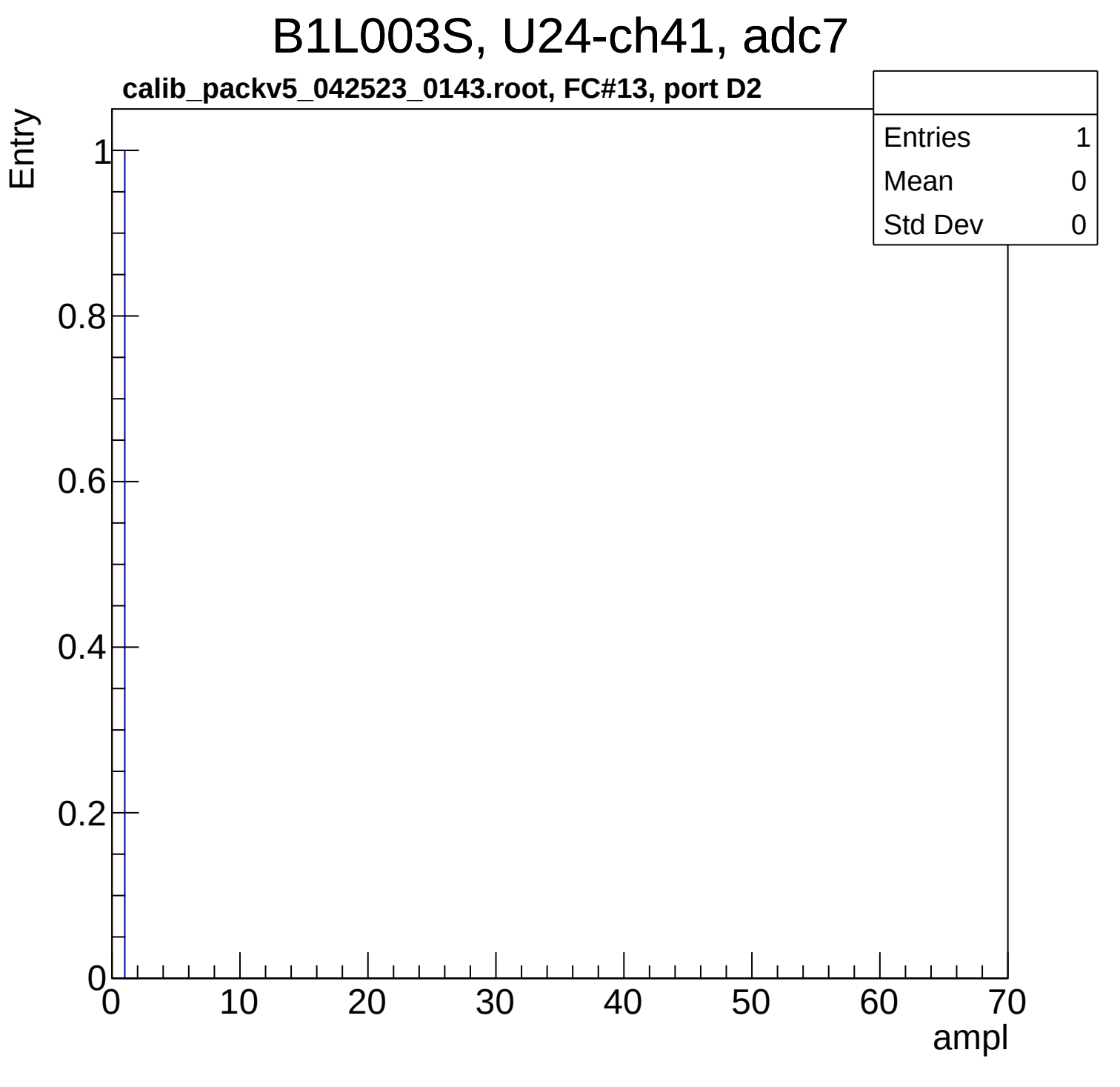
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch41, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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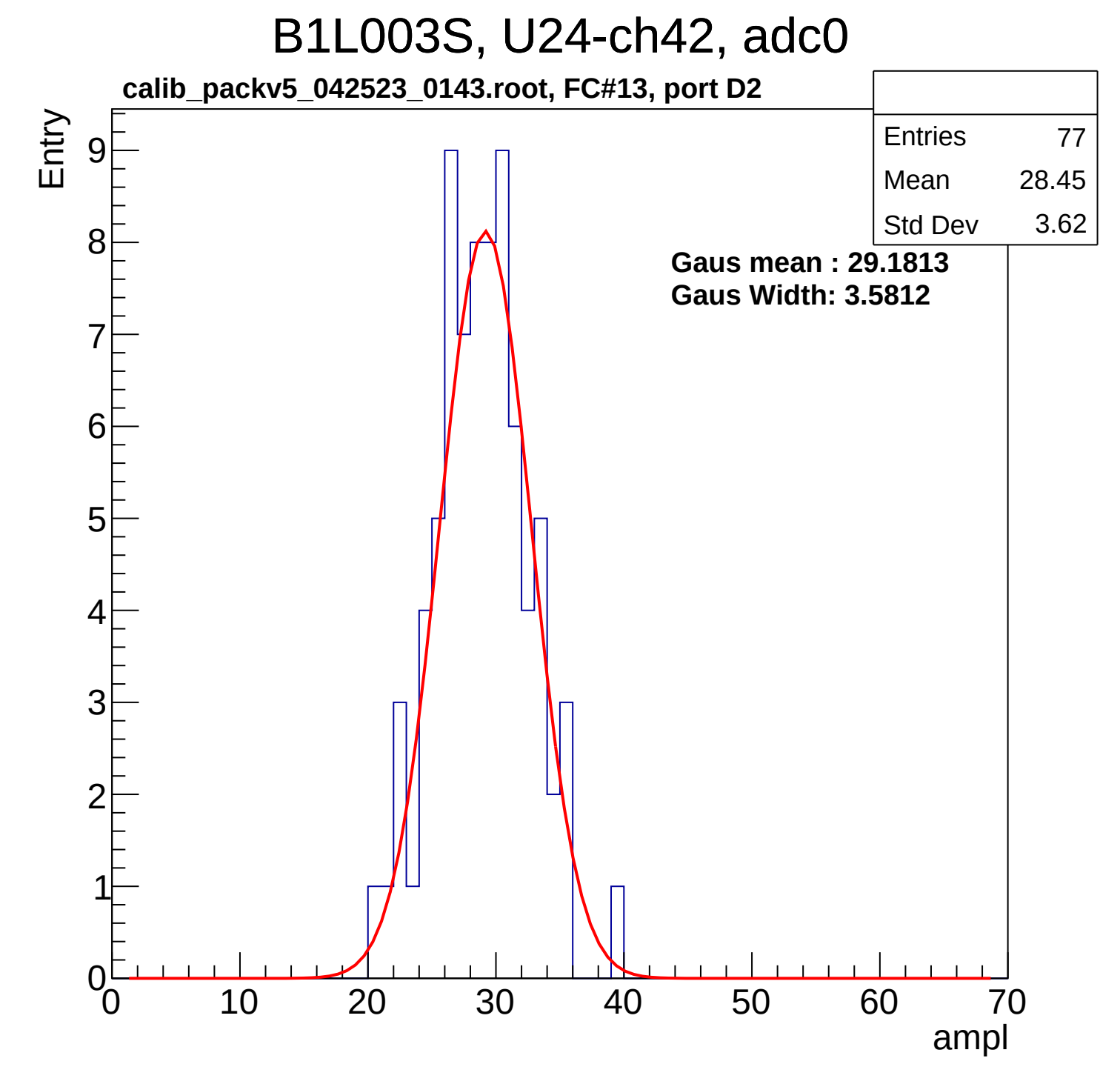
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch42, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.45
Std Dev 3.62
Gaus mean : 29.1813
Gaus Width: 3.5812
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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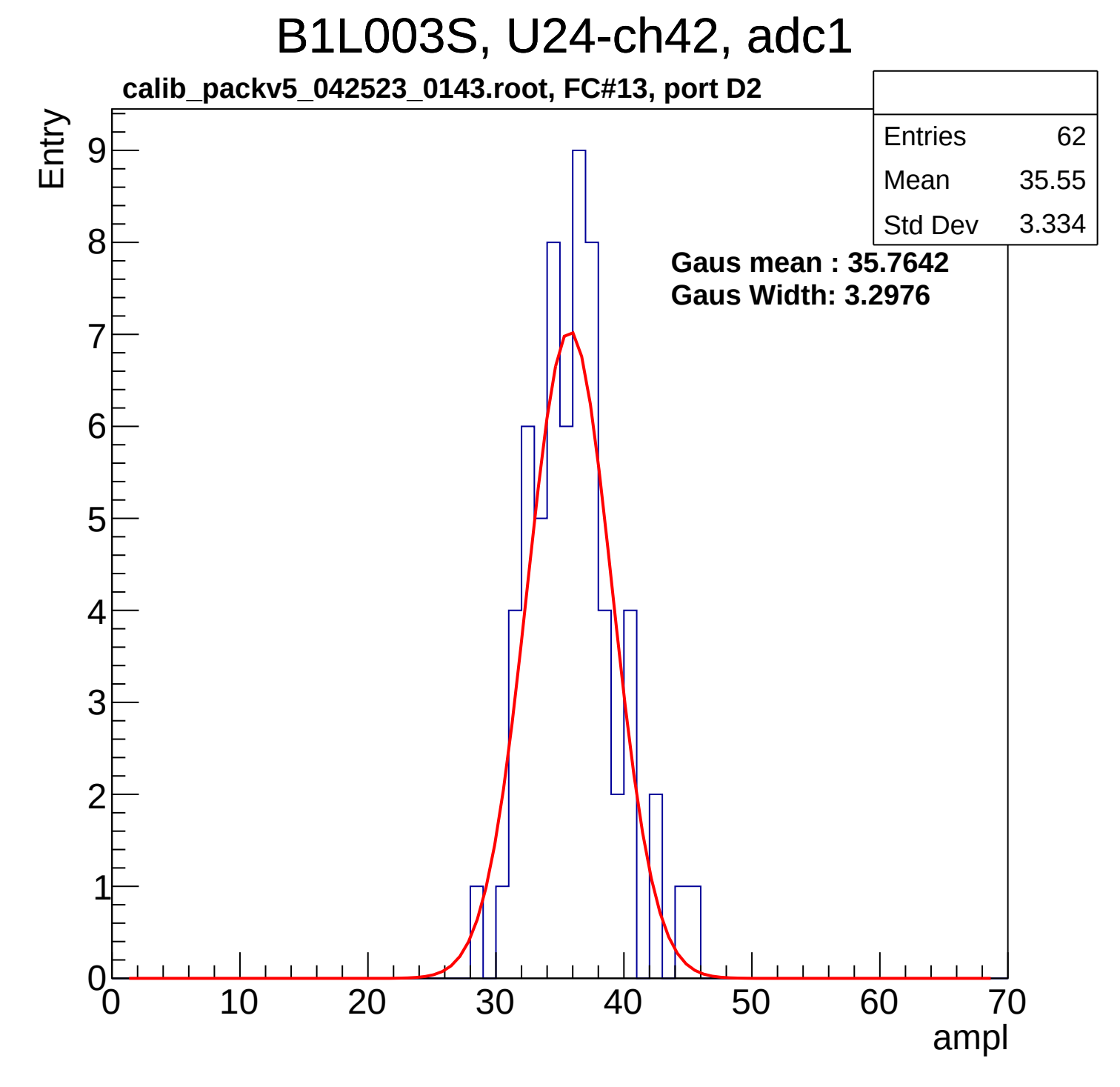
B1L003S, U24-ch42, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.55
Std Dev 3.334
Gaus mean : 35.7642
Gaus Width: 3.2976
Entry
ampl
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20
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40
50
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1
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4
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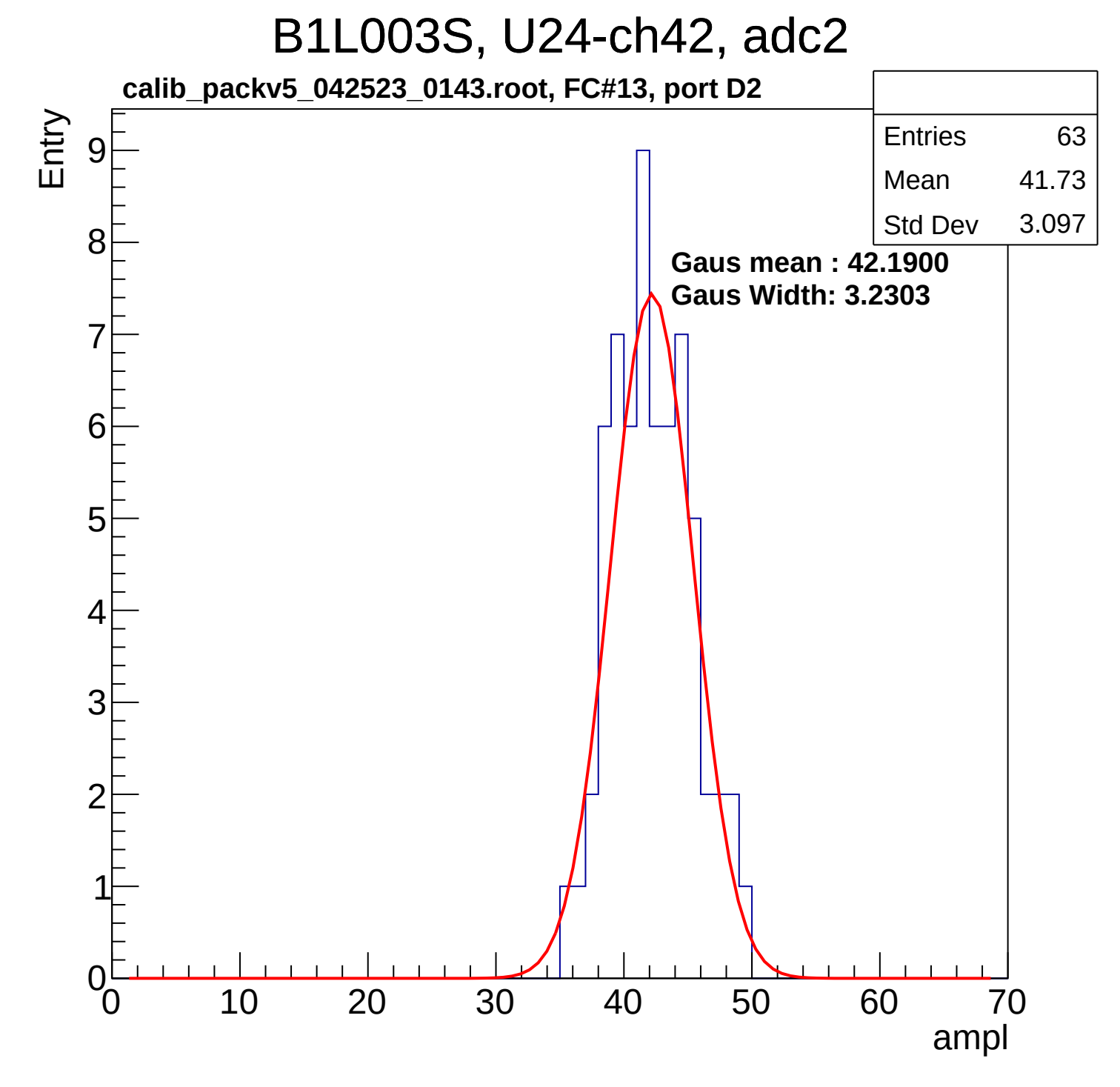
6
7
8
9


B1L003S, U24-ch42, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 41.73
Std Dev 3.097
Gaus mean : 42.1900
Gaus Width: 3.2303
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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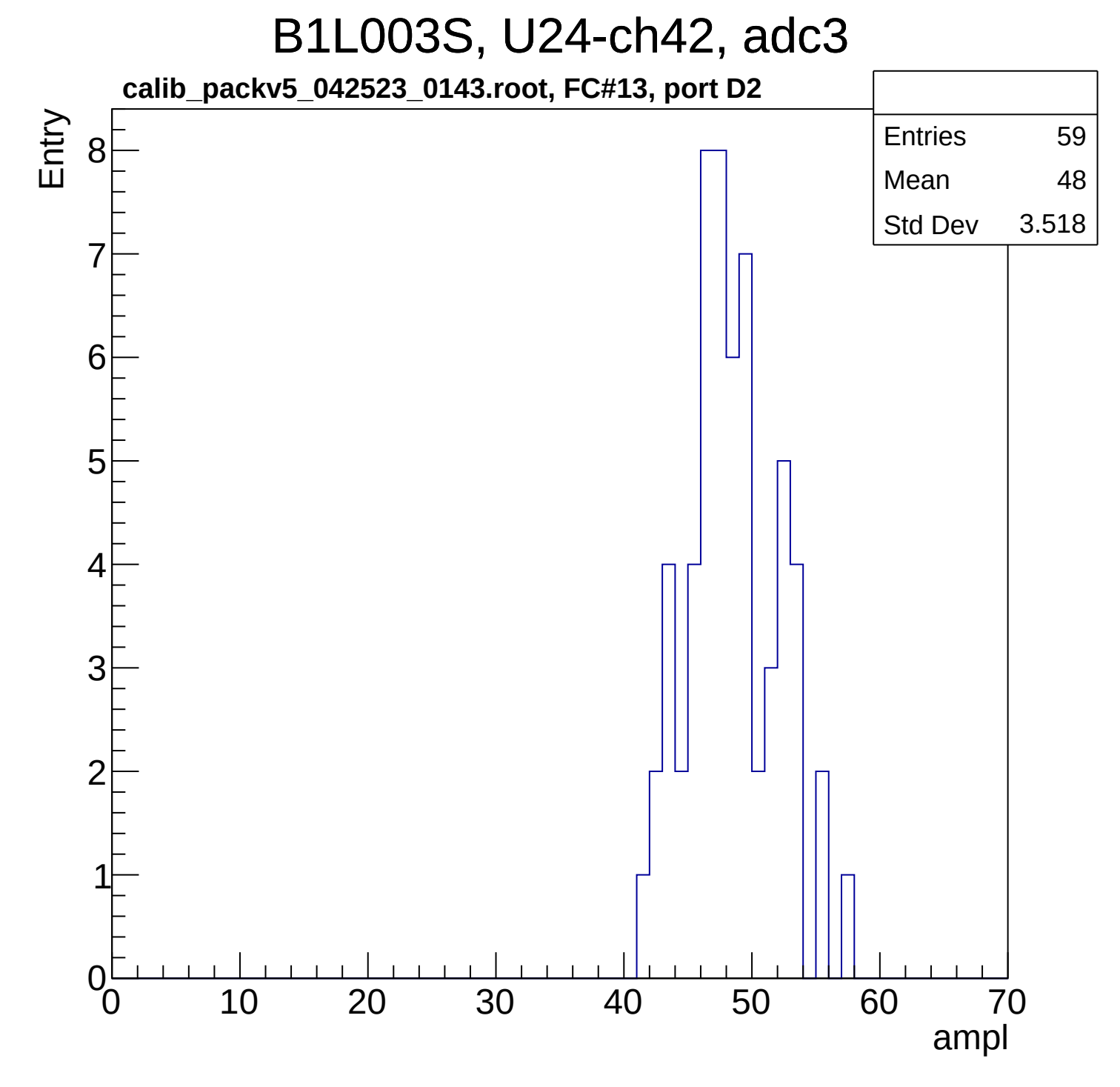
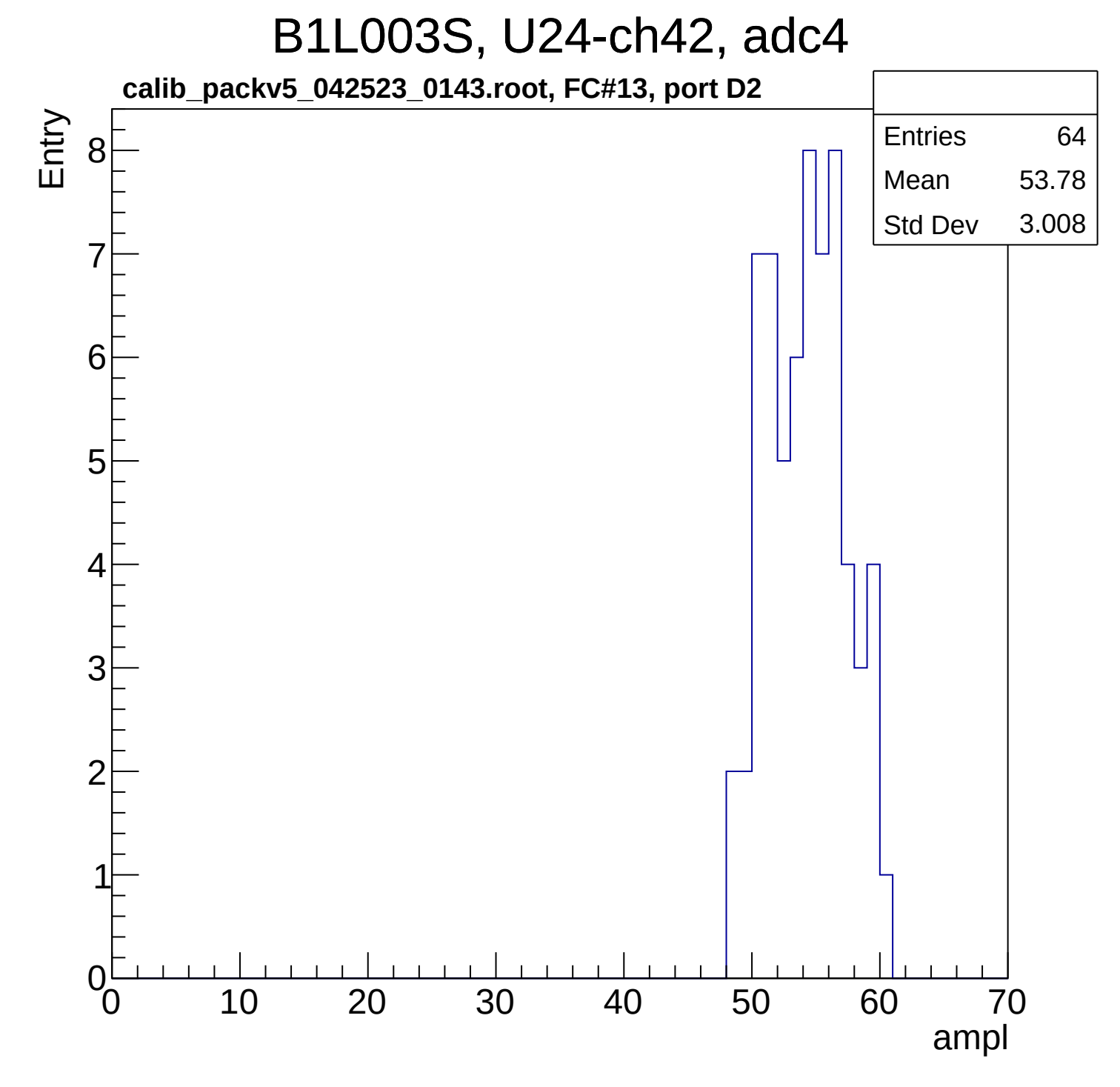

B1L003S, U24-ch42, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48
Std Dev 3.518
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch42, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 53.78
Std Dev 3.008
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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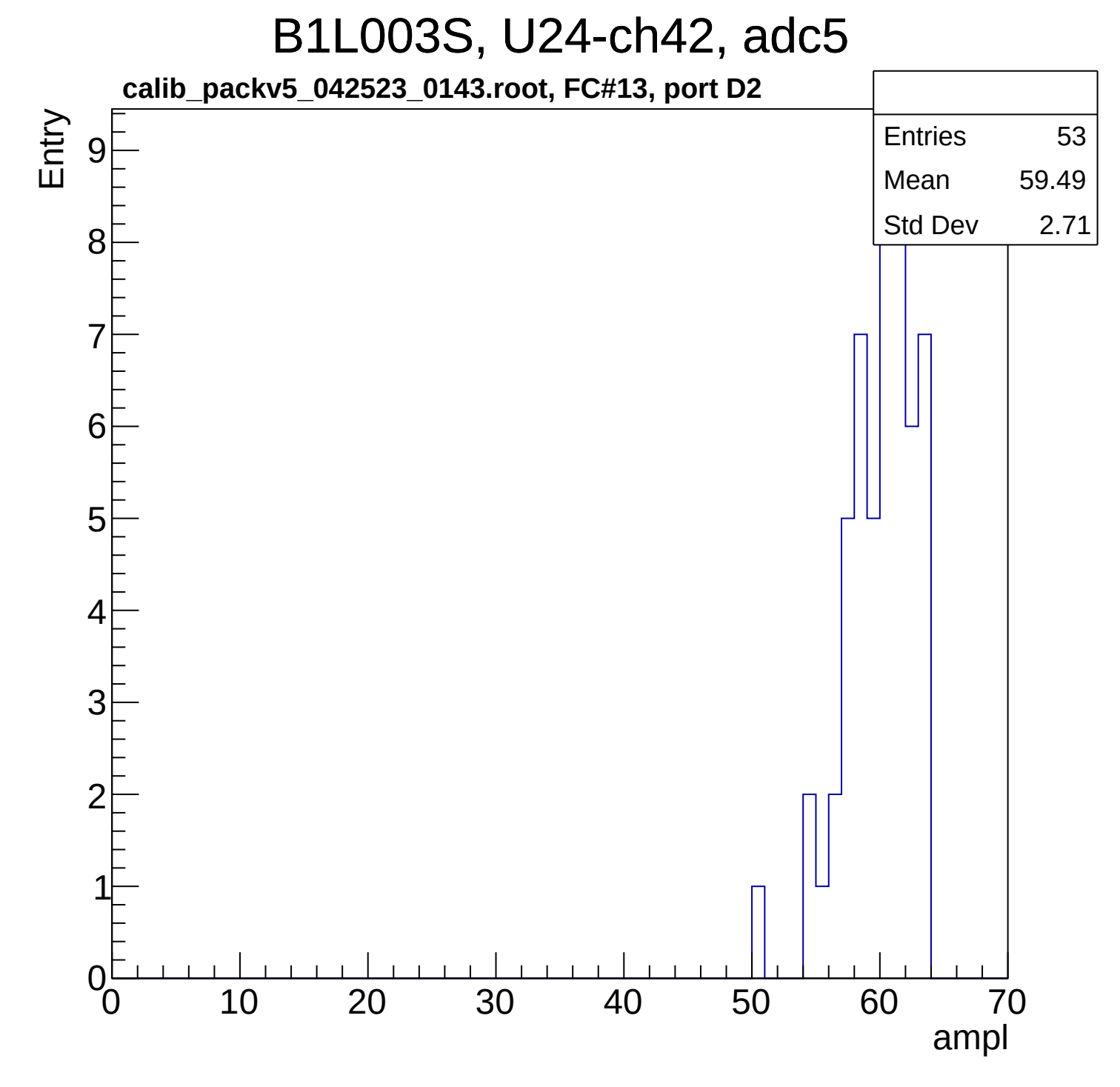
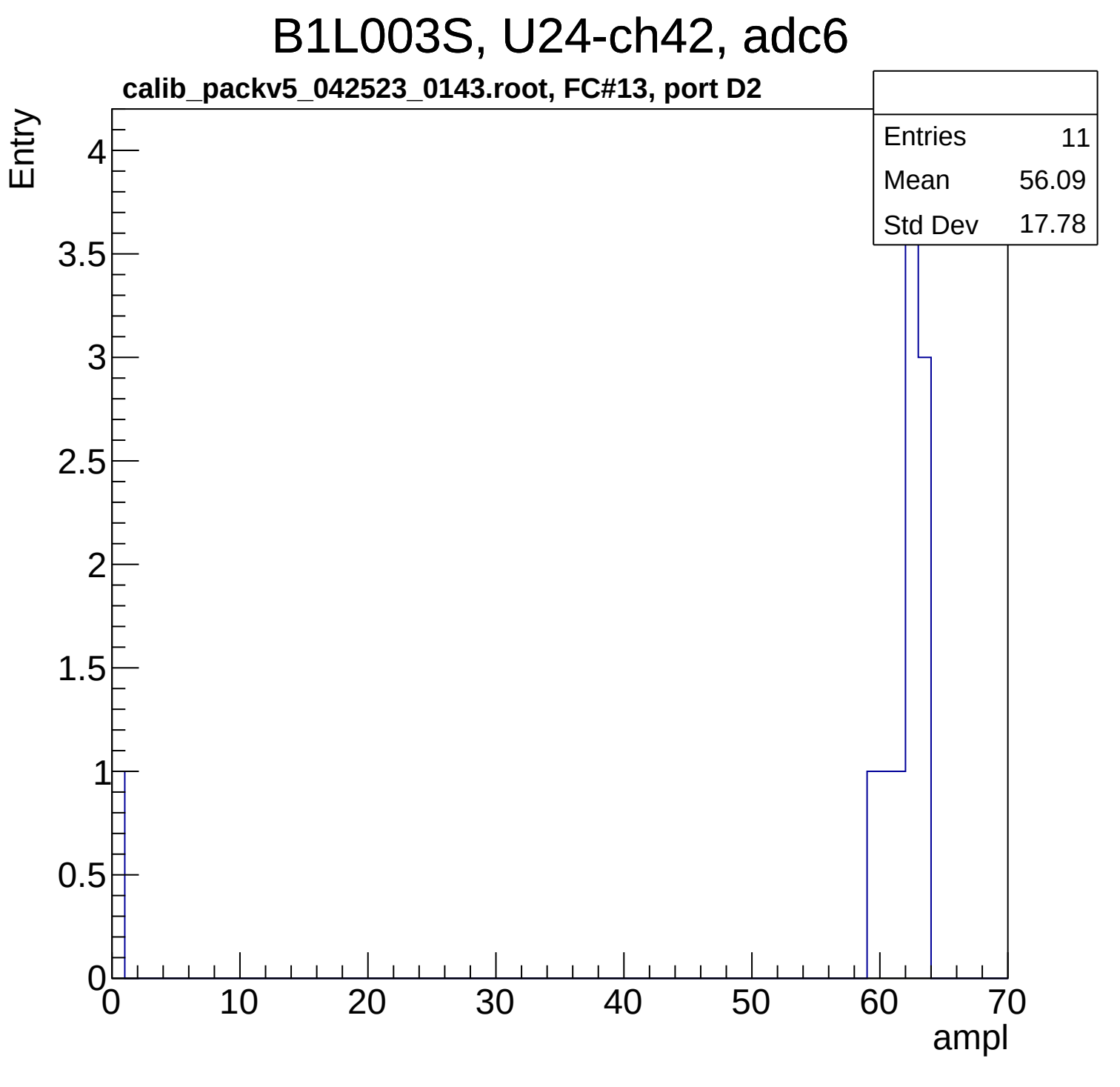
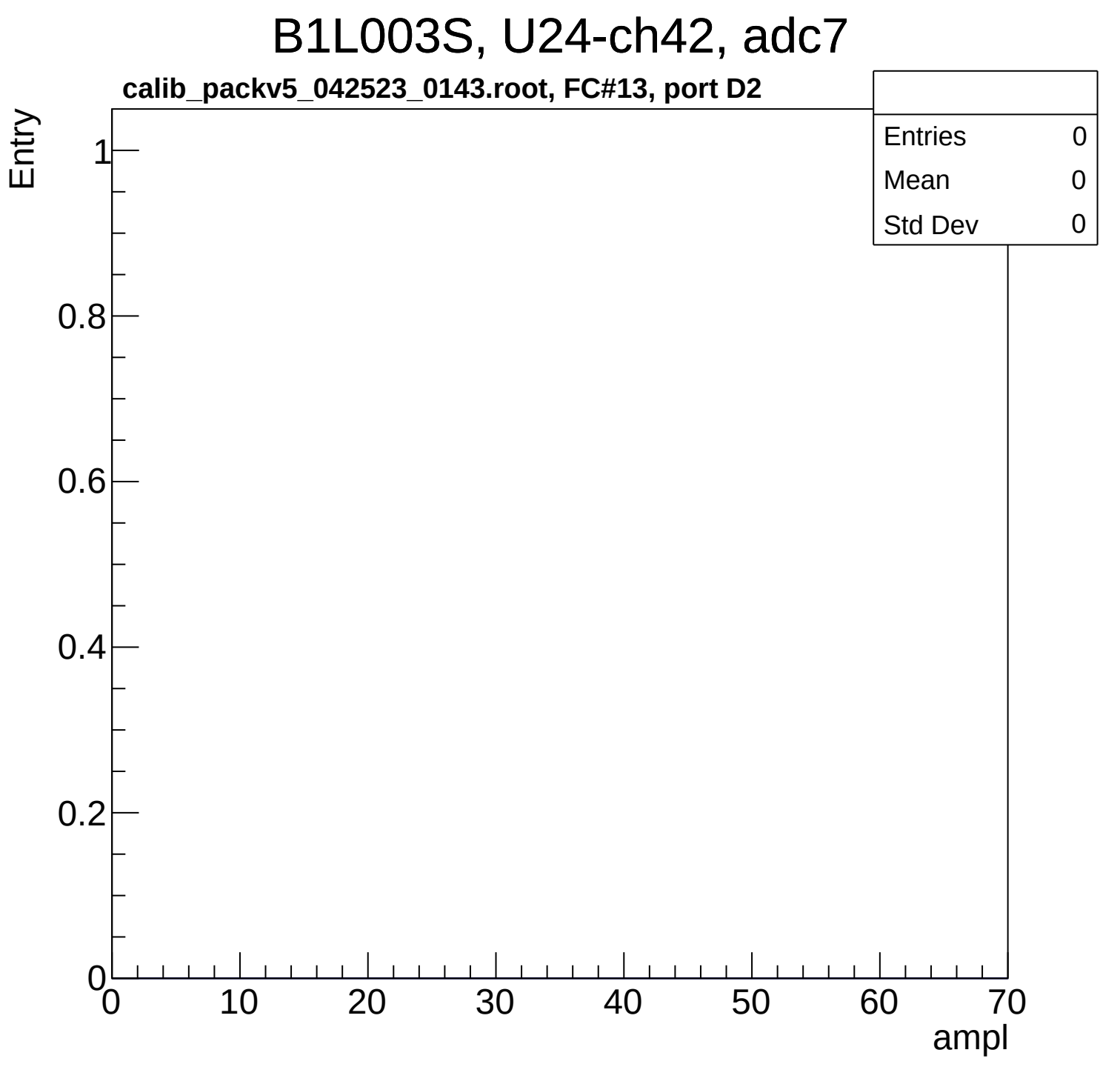
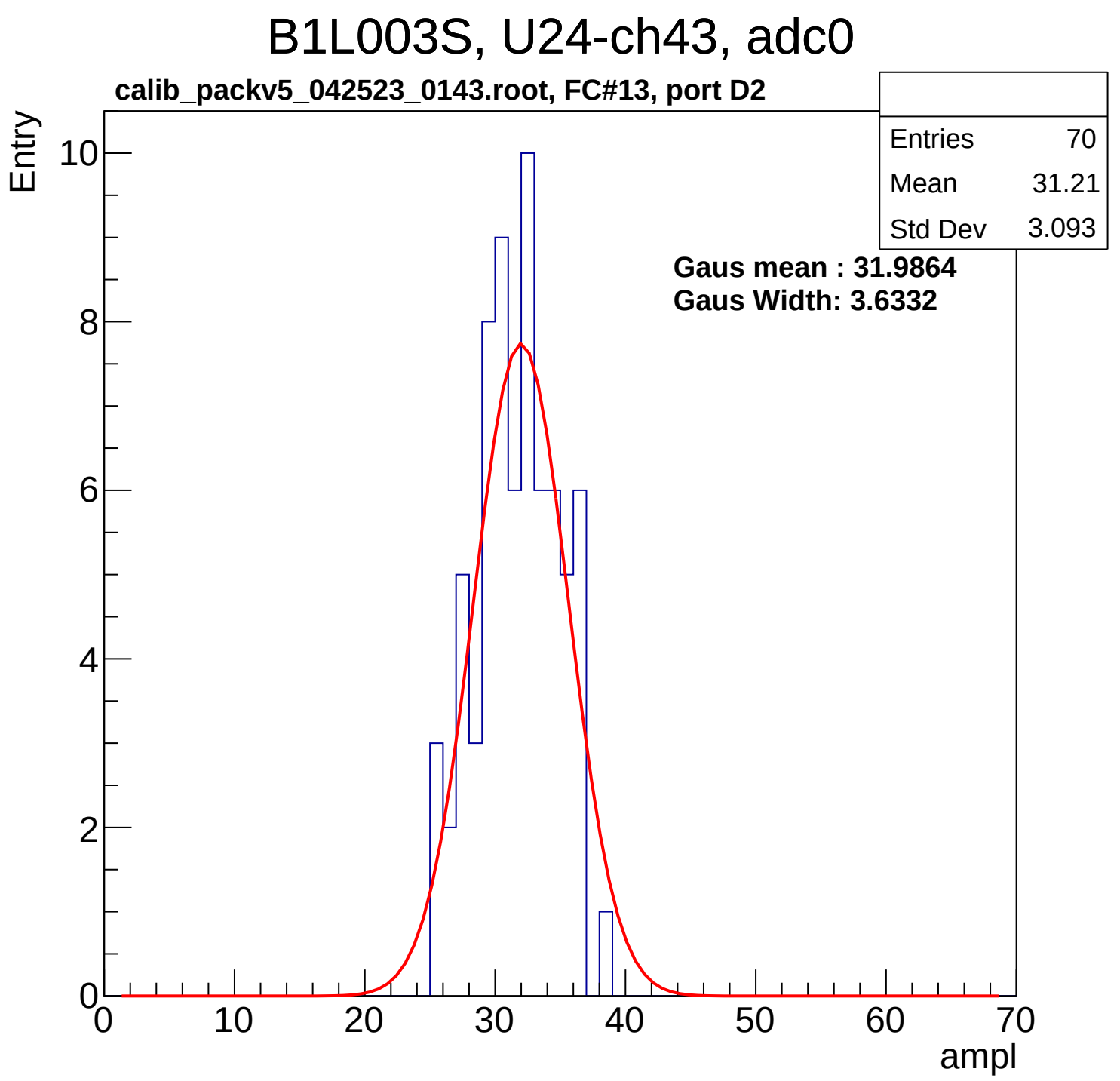
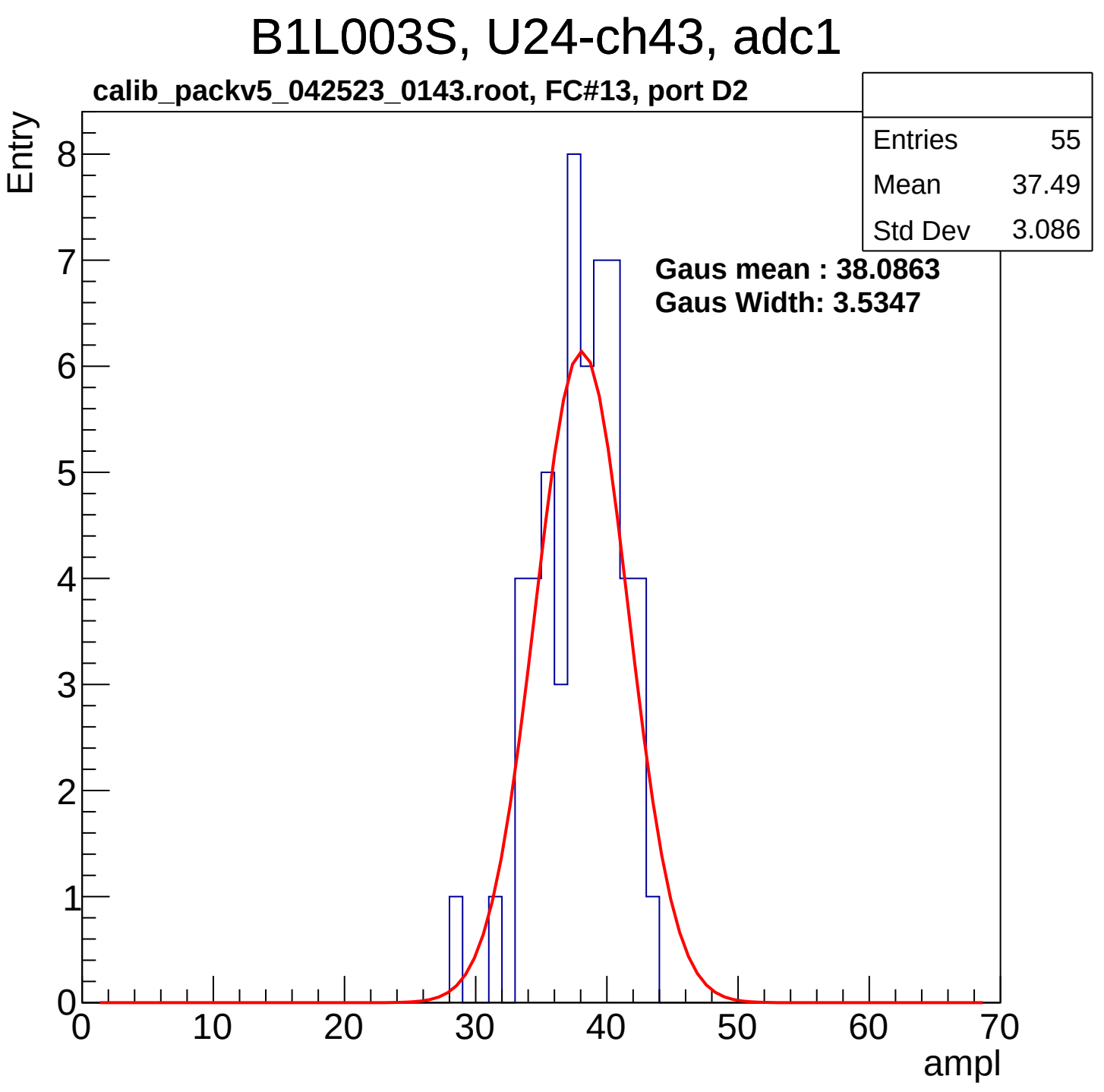

B1L003S, U24-ch42, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.49
Std Dev 2.71
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch42, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.09
Std Dev 17.78
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U24-ch42, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch43, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 31.21
Std Dev 3.093
Gaus mean : 31.9864
Gaus Width: 3.6332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch43, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 37.49
Std Dev 3.086
Gaus mean : 38.0863
Gaus Width: 3.5347
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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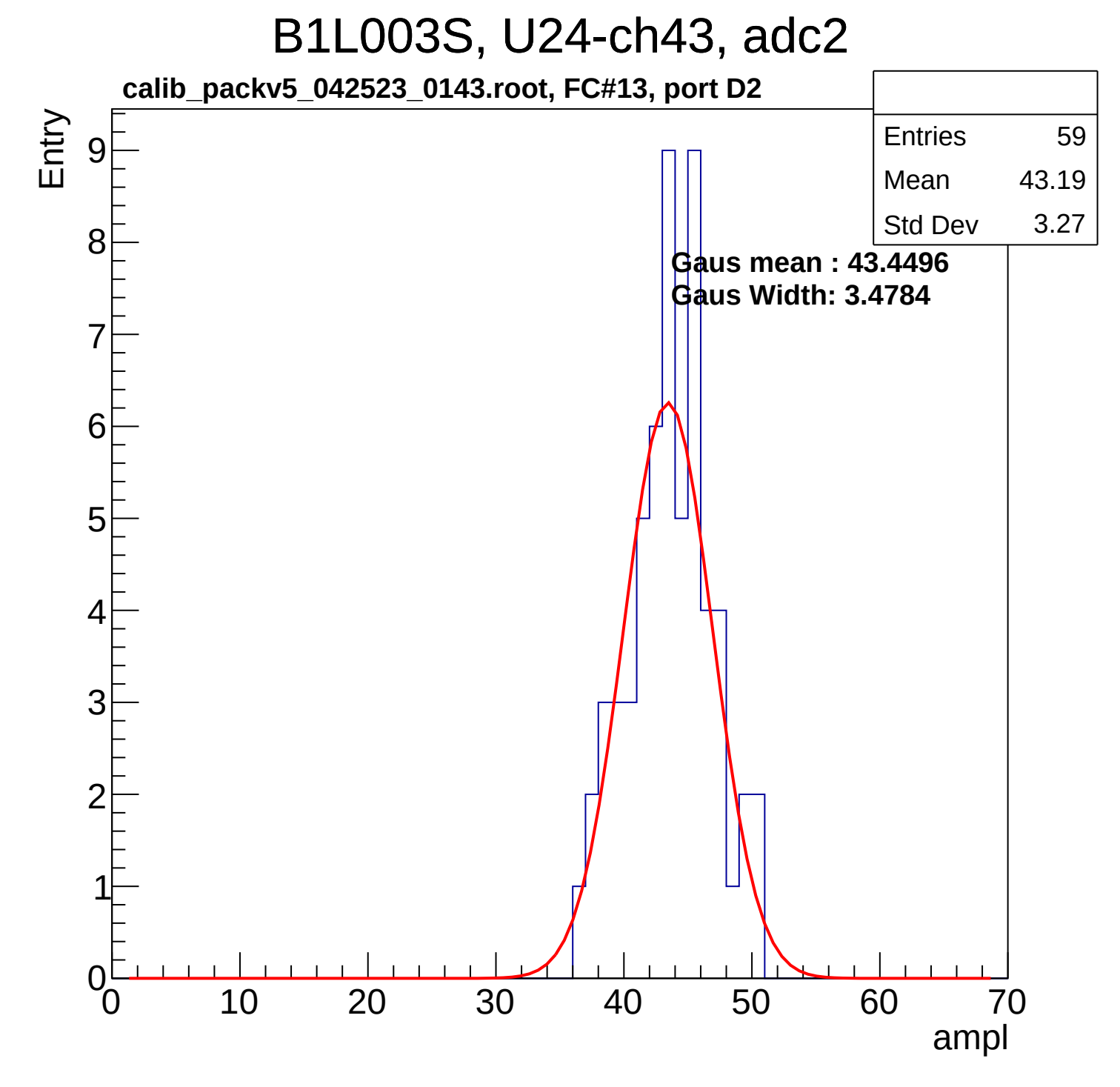
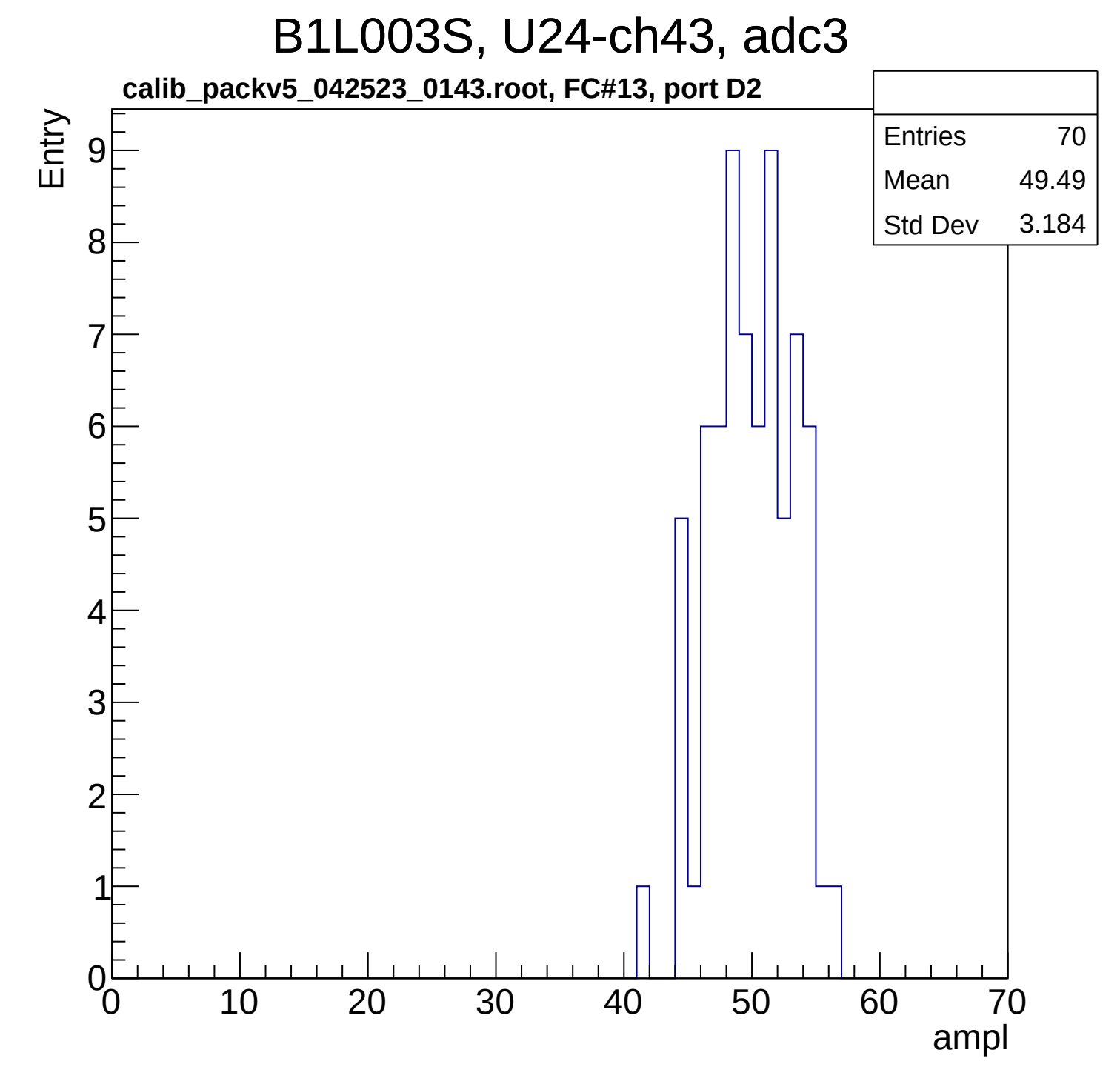
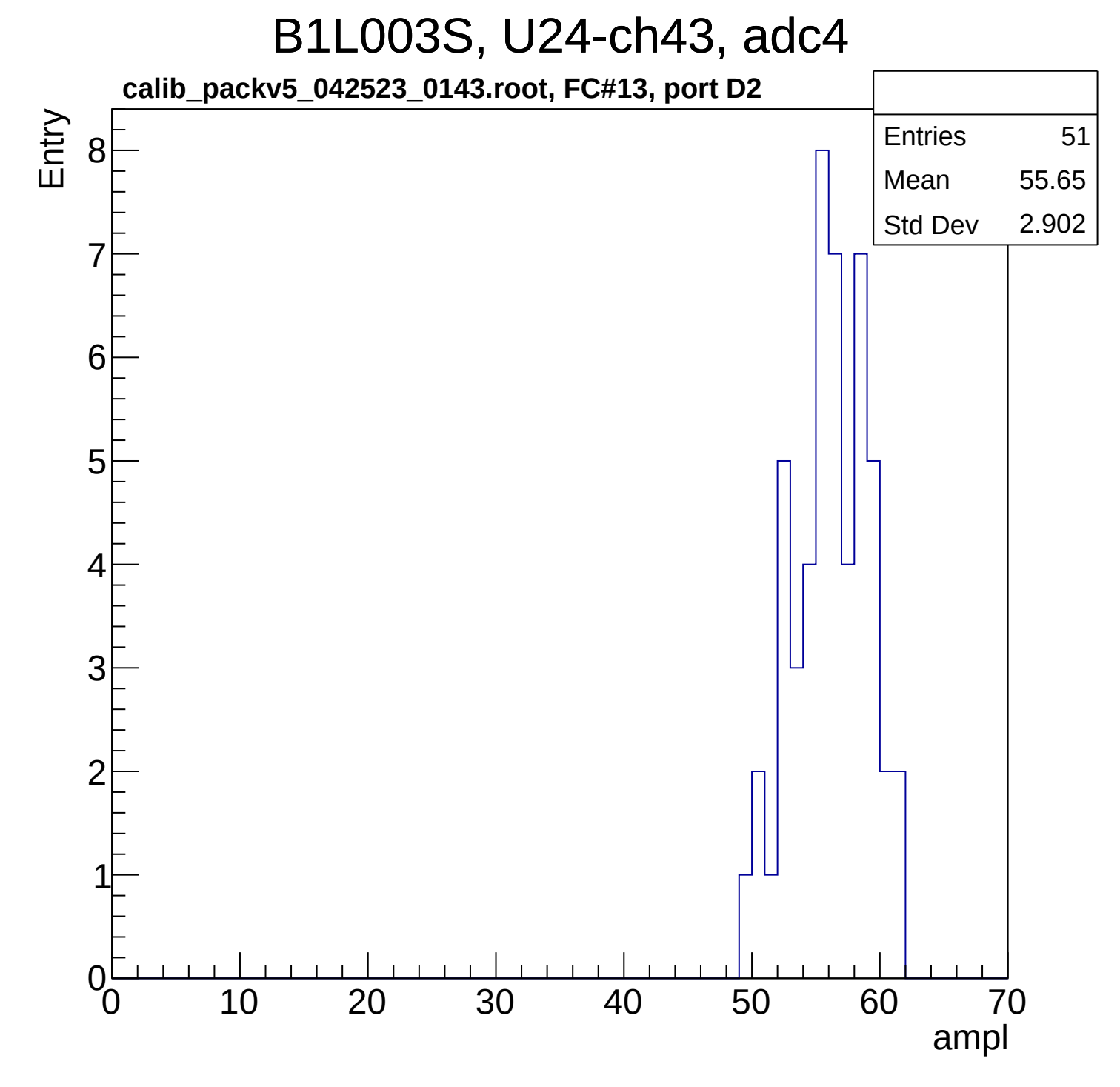
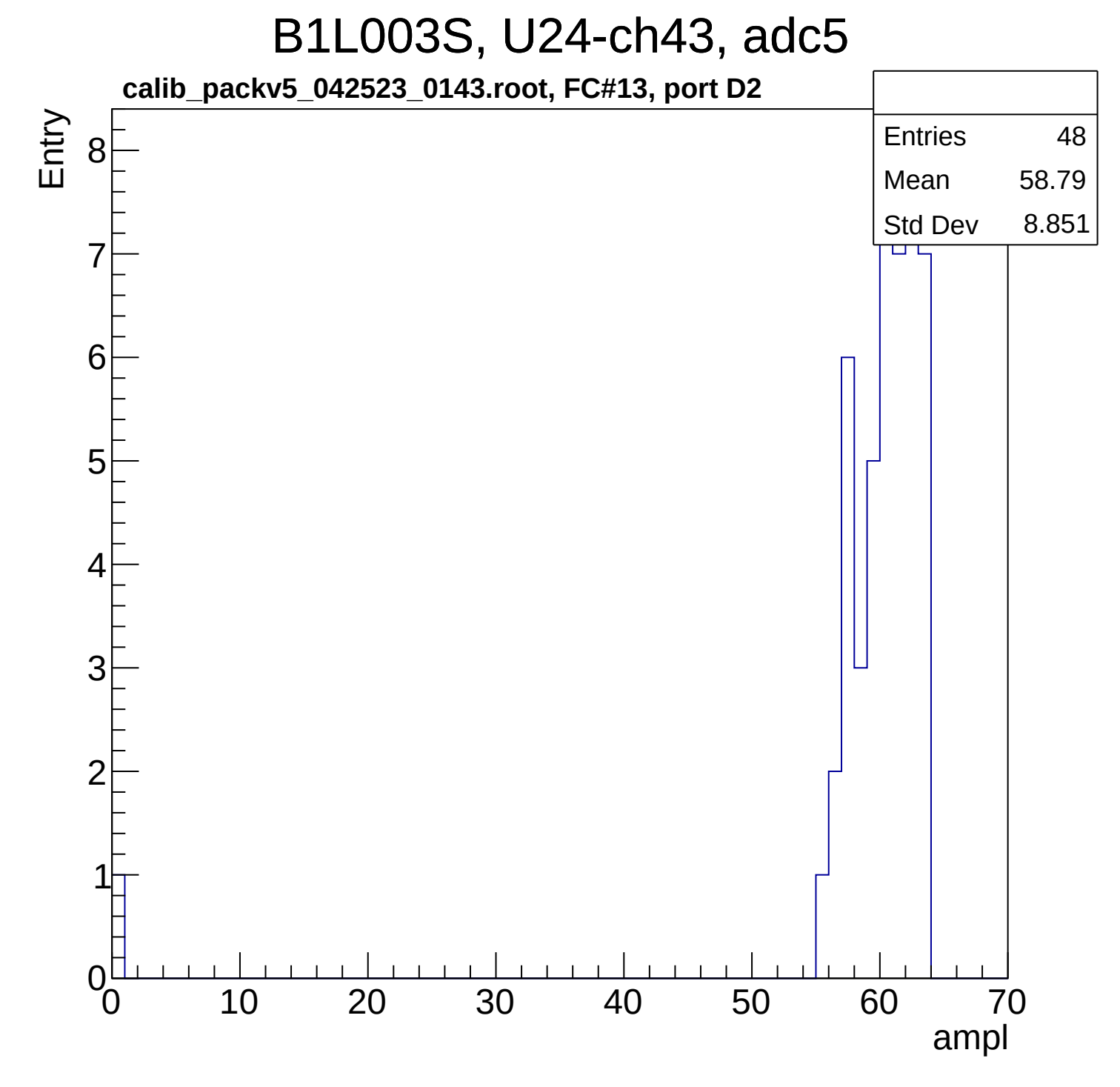
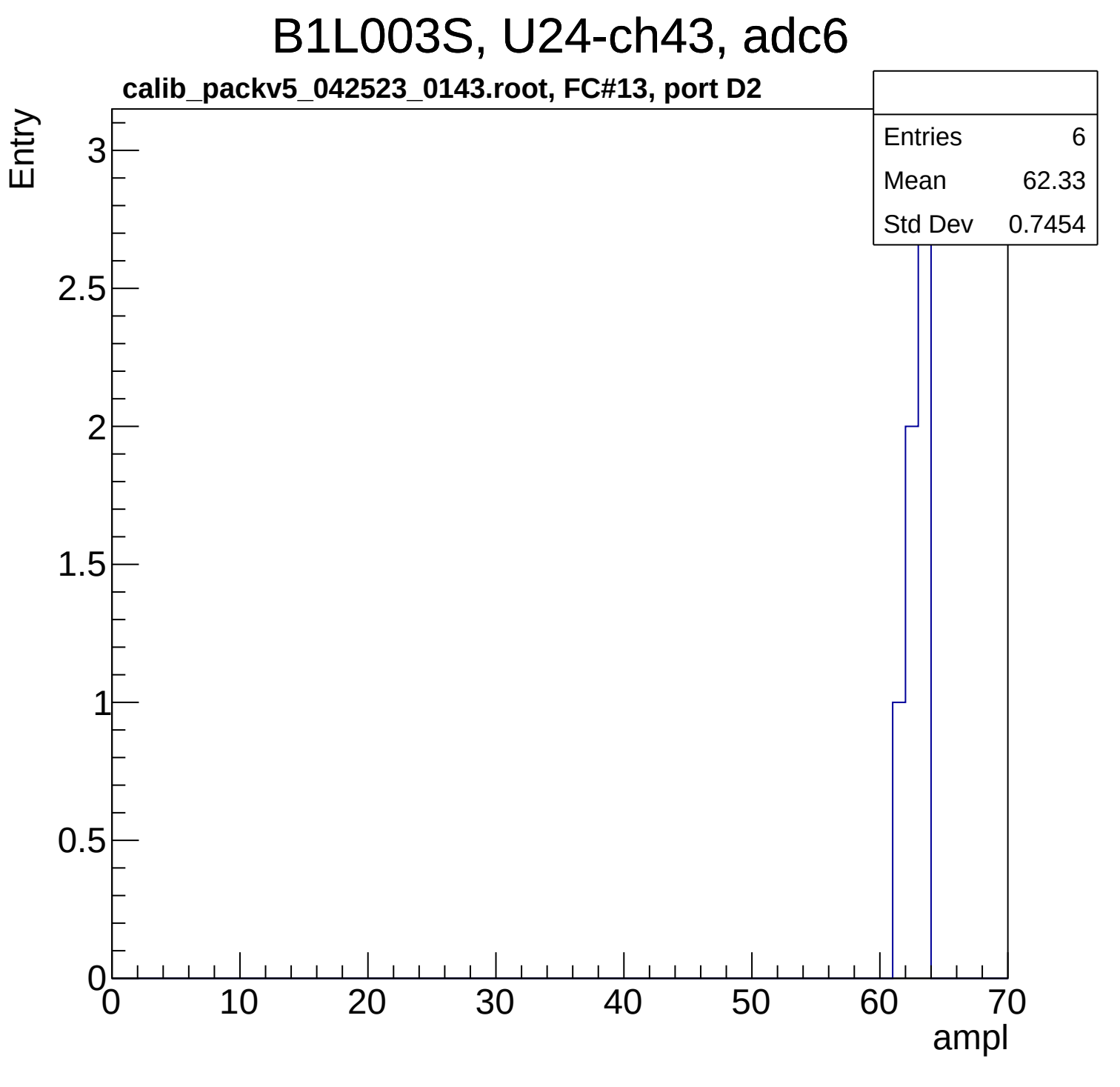
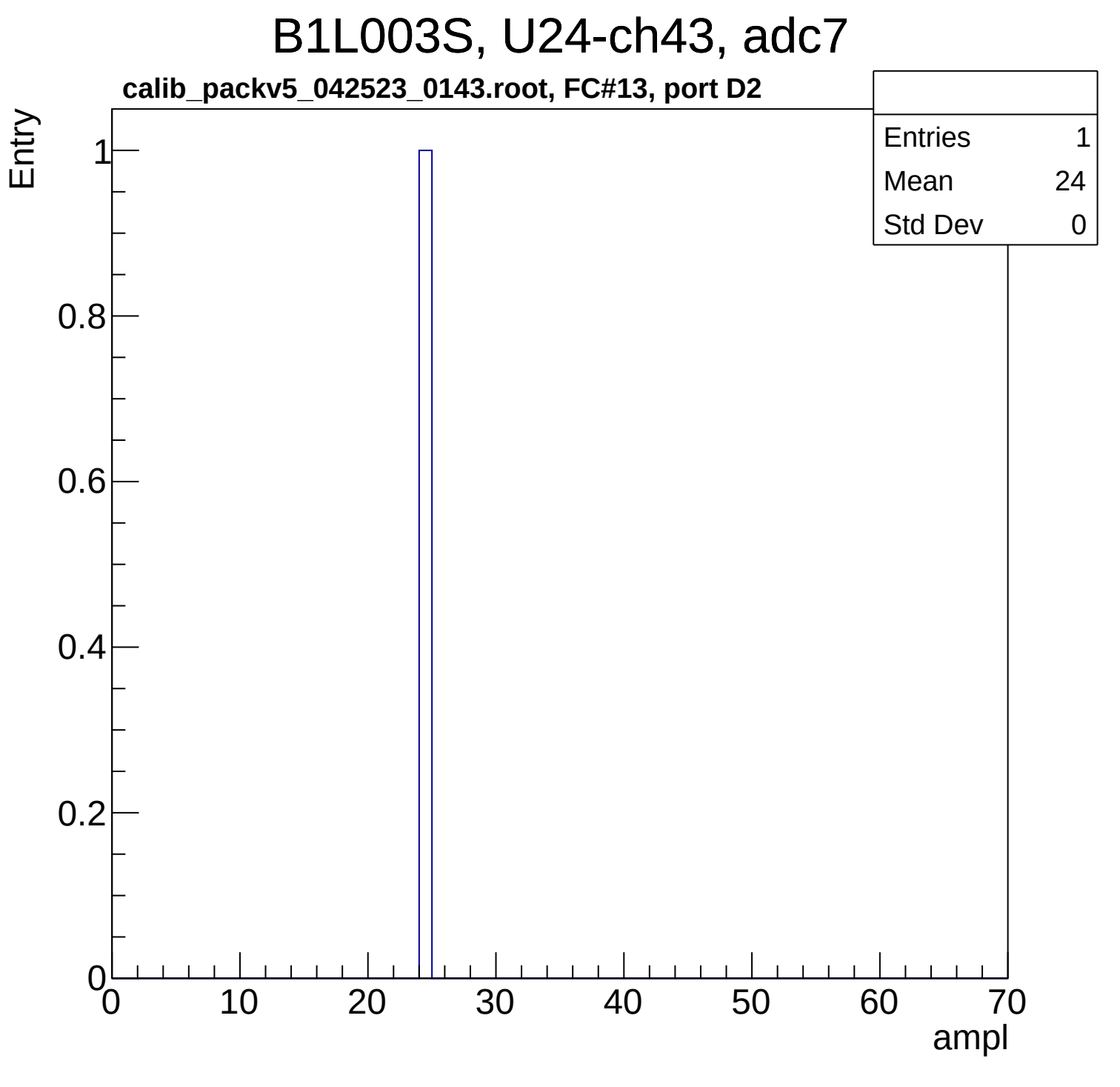

B1L003S, U24-ch43, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.19
Std Dev 3.27
Gaus mean : 43.4496
Gaus Width: 3.4784
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

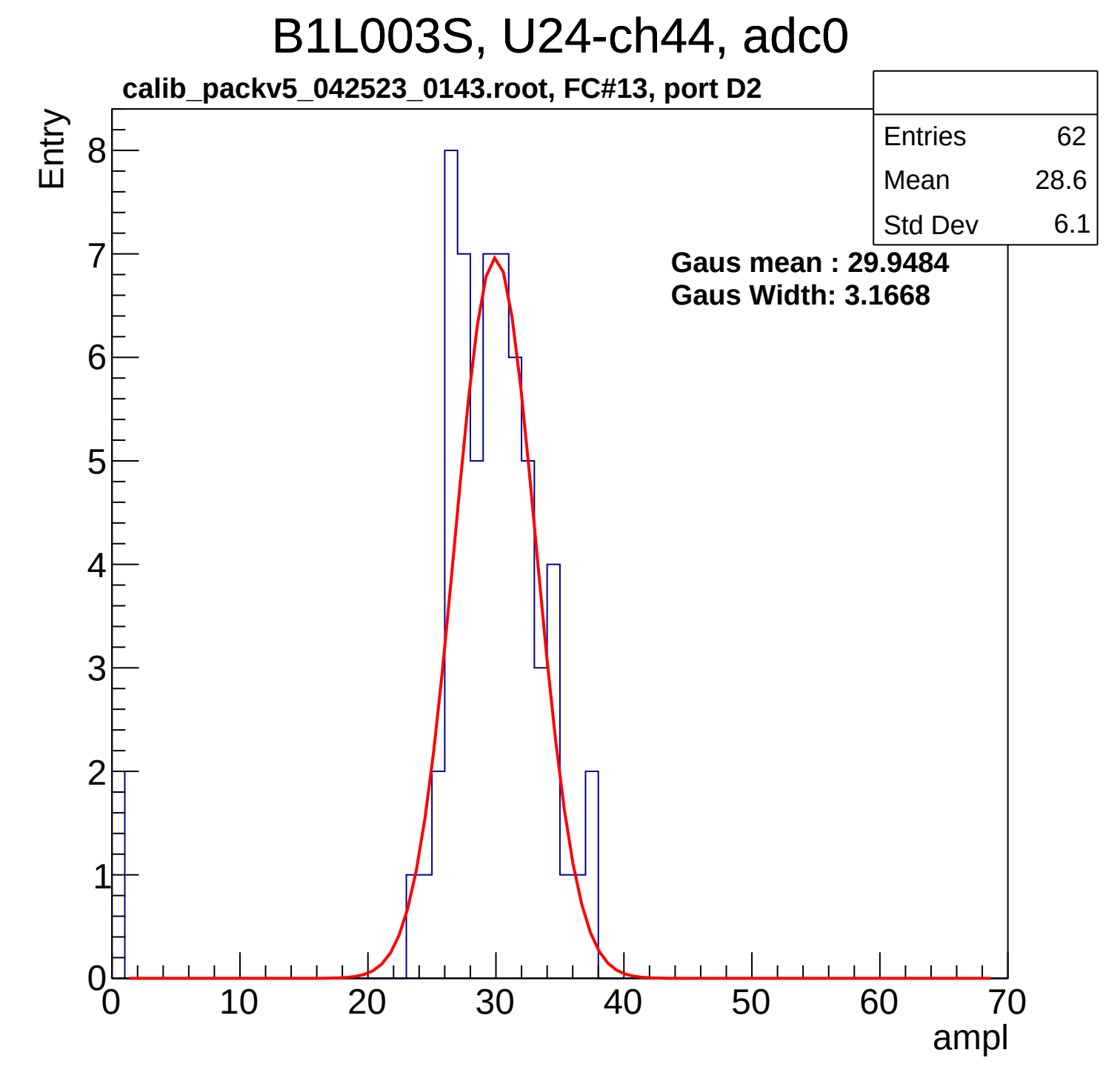

B1L003S, U24-ch43, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 49.49
Std Dev 3.184
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

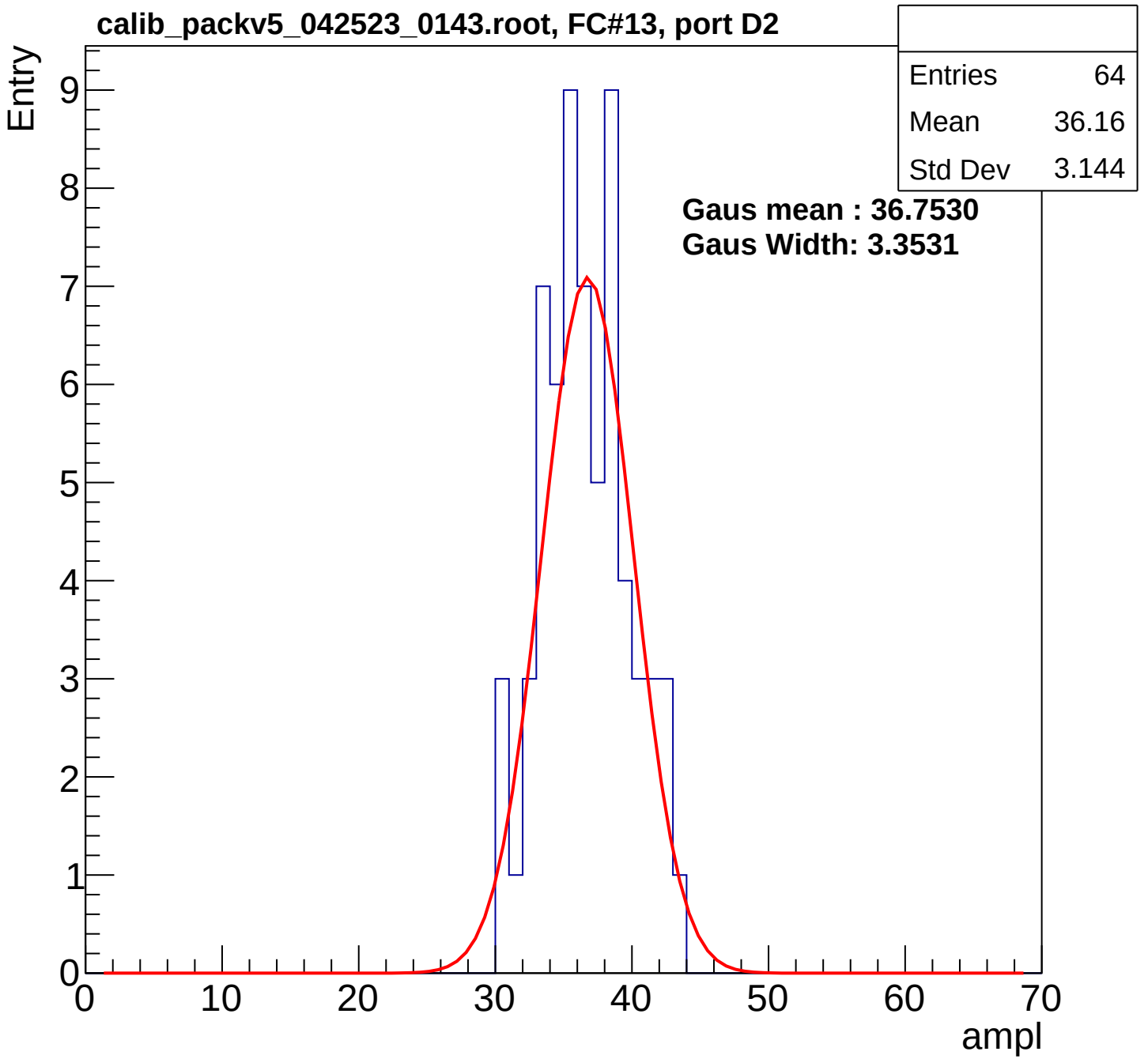

B1L003S, U24-ch43, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.65
Std Dev 2.902
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

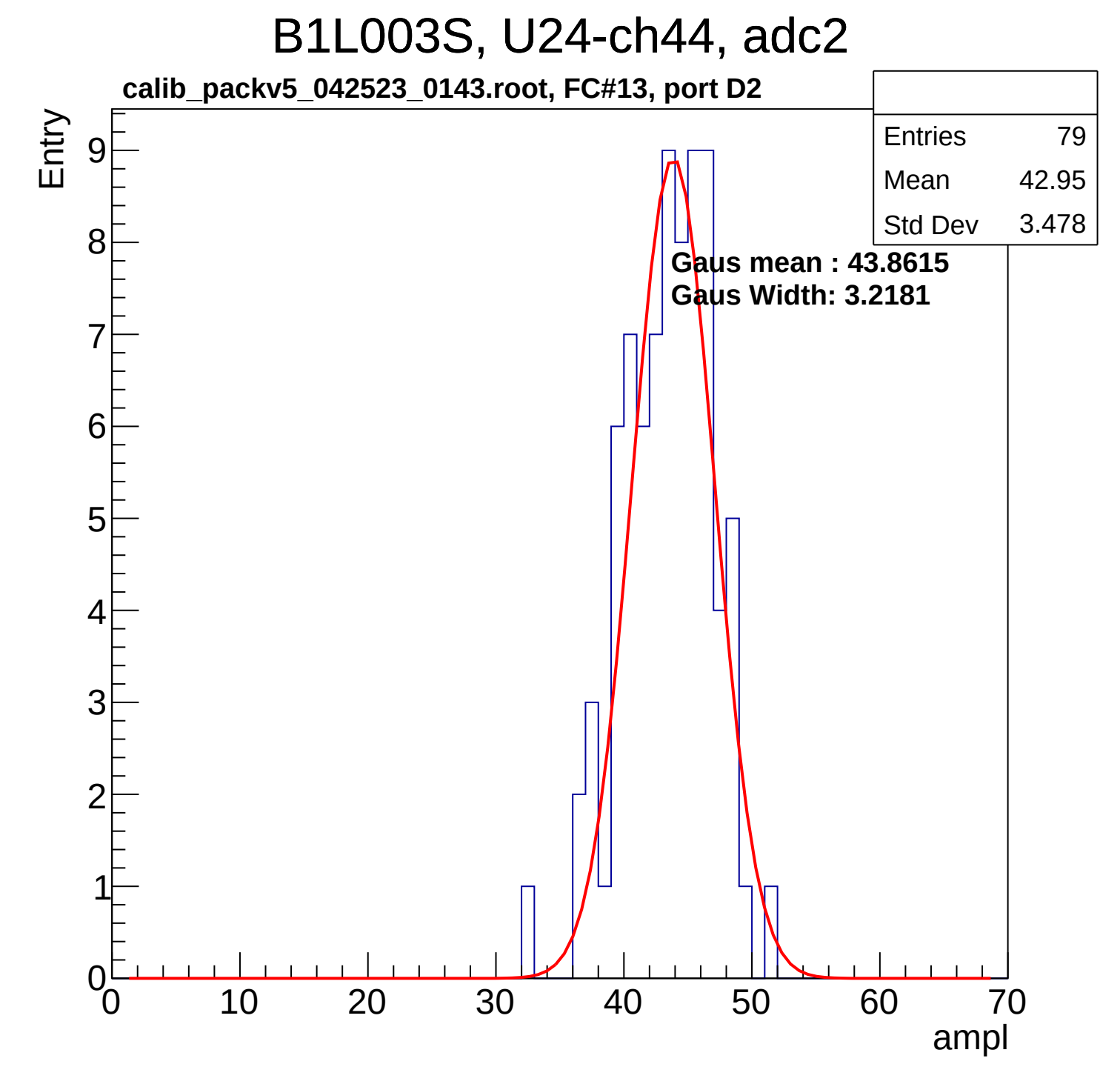

B1L003S, U24-ch43, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.79
Std Dev 8.851
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

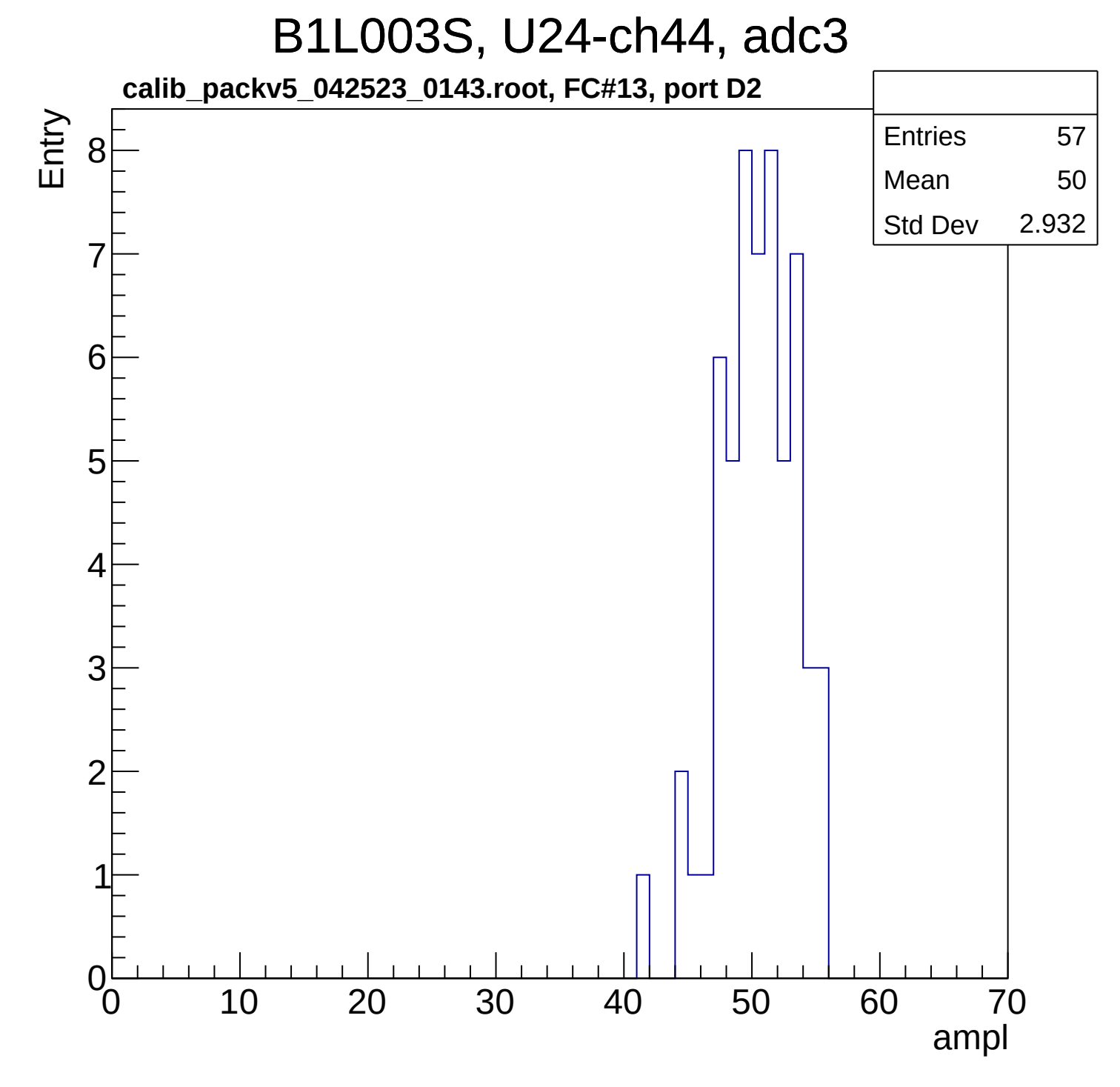

B1L003S, U24-ch43, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch43, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch44, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.6
Std Dev 6.1
Gaus mean : 29.9484
Gaus Width: 3.1668
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch44, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.16
Std Dev 3.144
Gaus mean : 36.7530
Gaus Width: 3.3531
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch44, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 42.95
Std Dev 3.478
Gaus mean : 43.8615
Gaus Width: 3.2181
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch44, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 50
Std Dev 2.932
Entry
ampl
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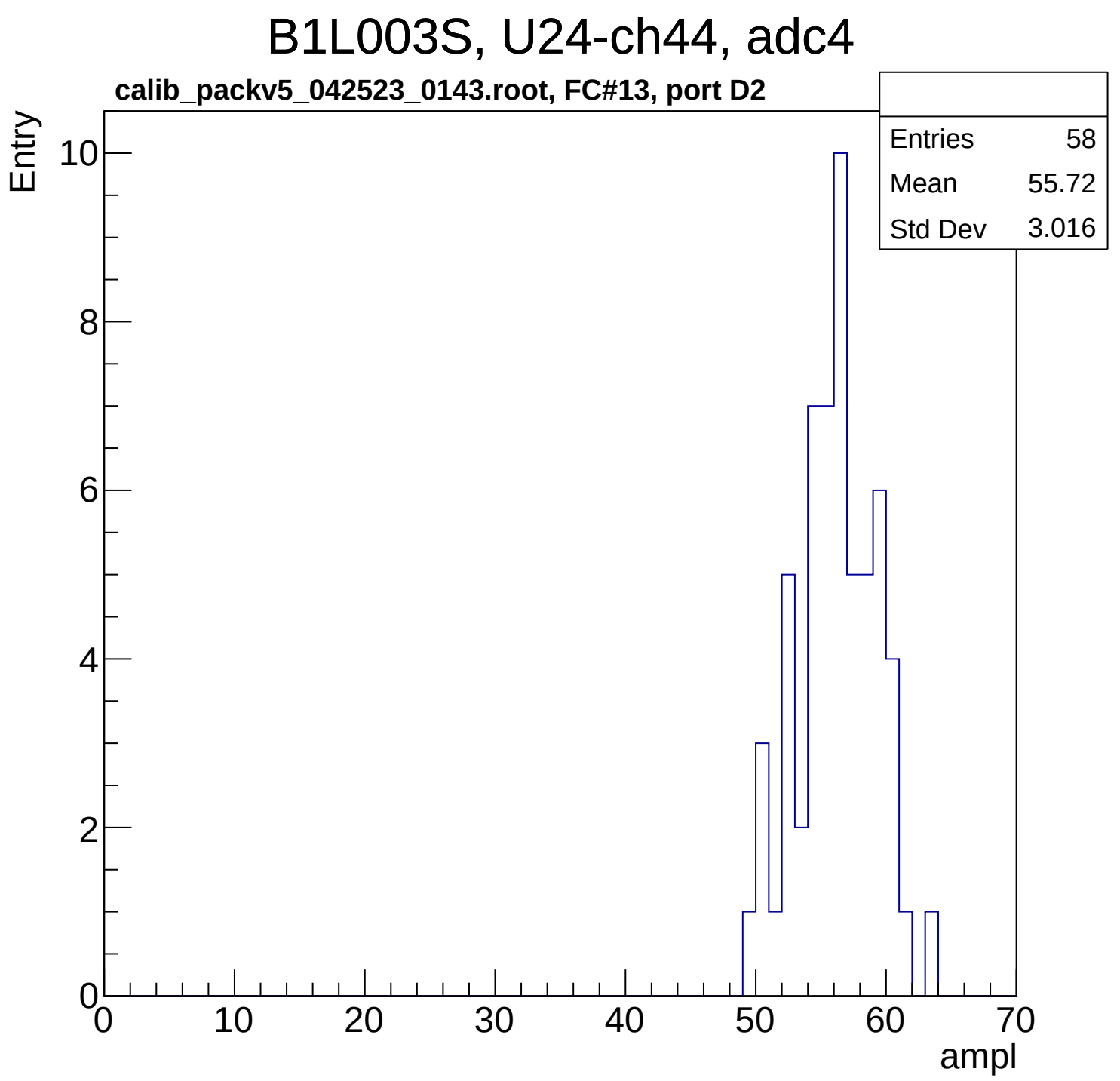
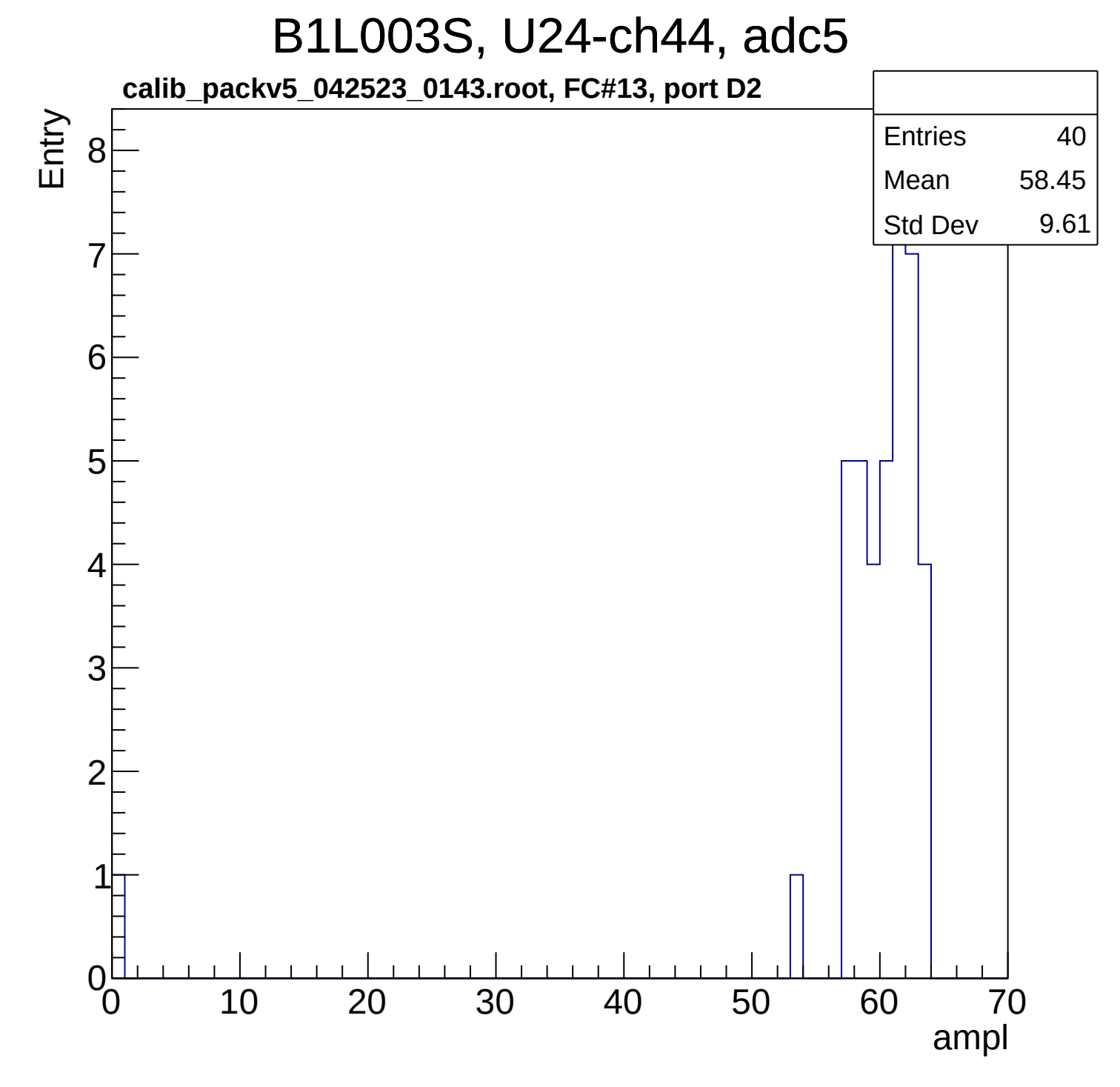
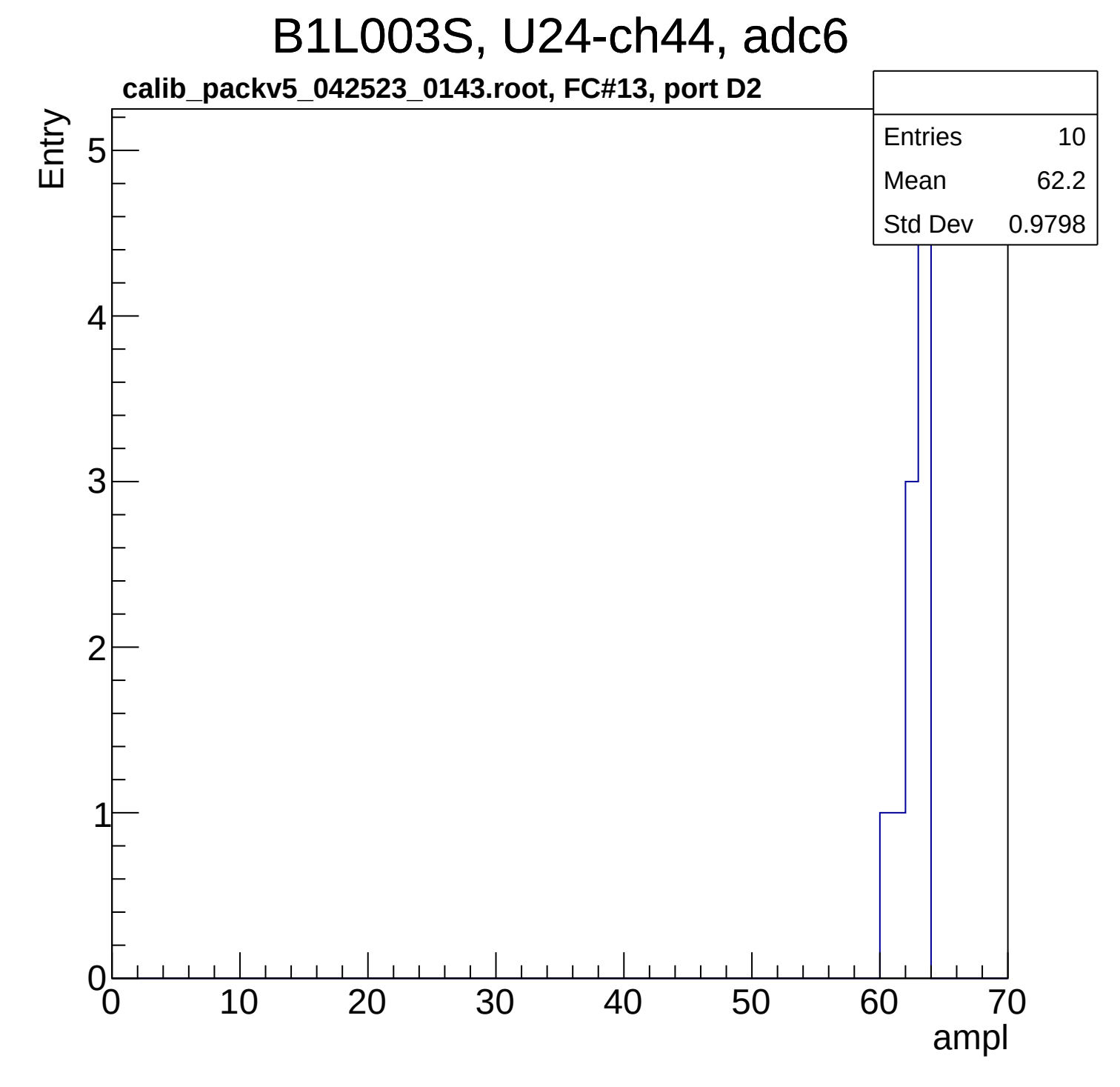
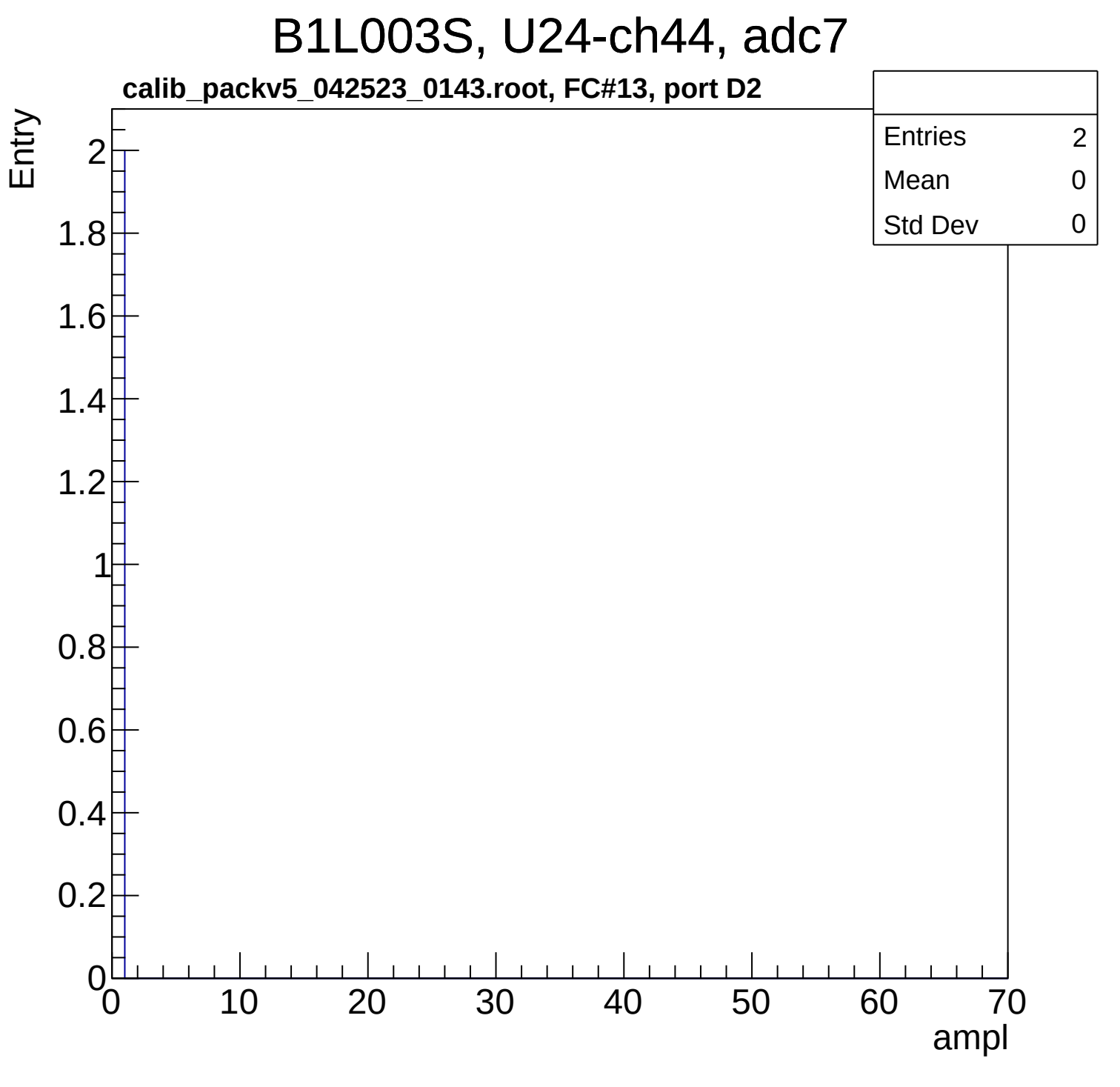
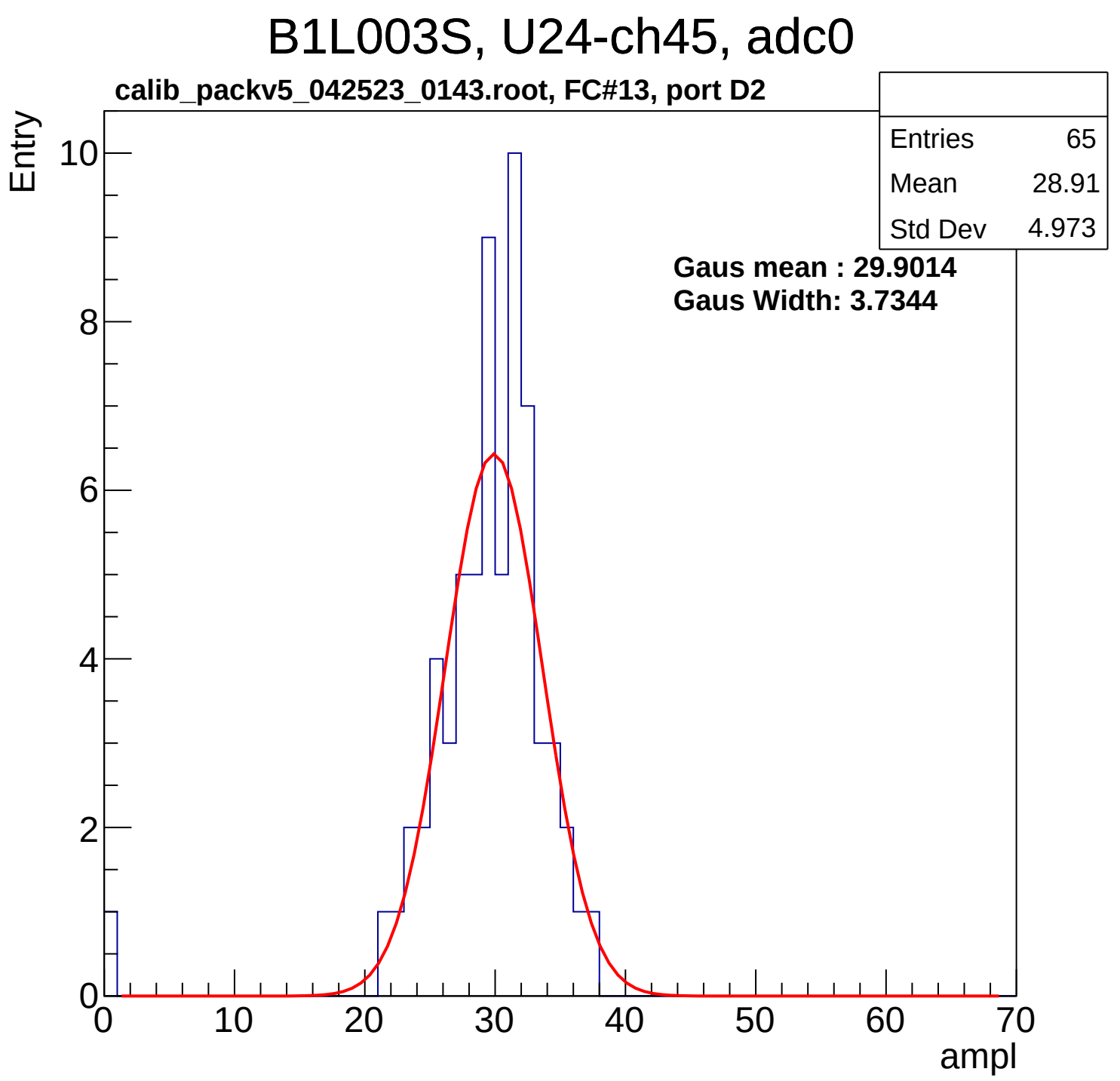
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

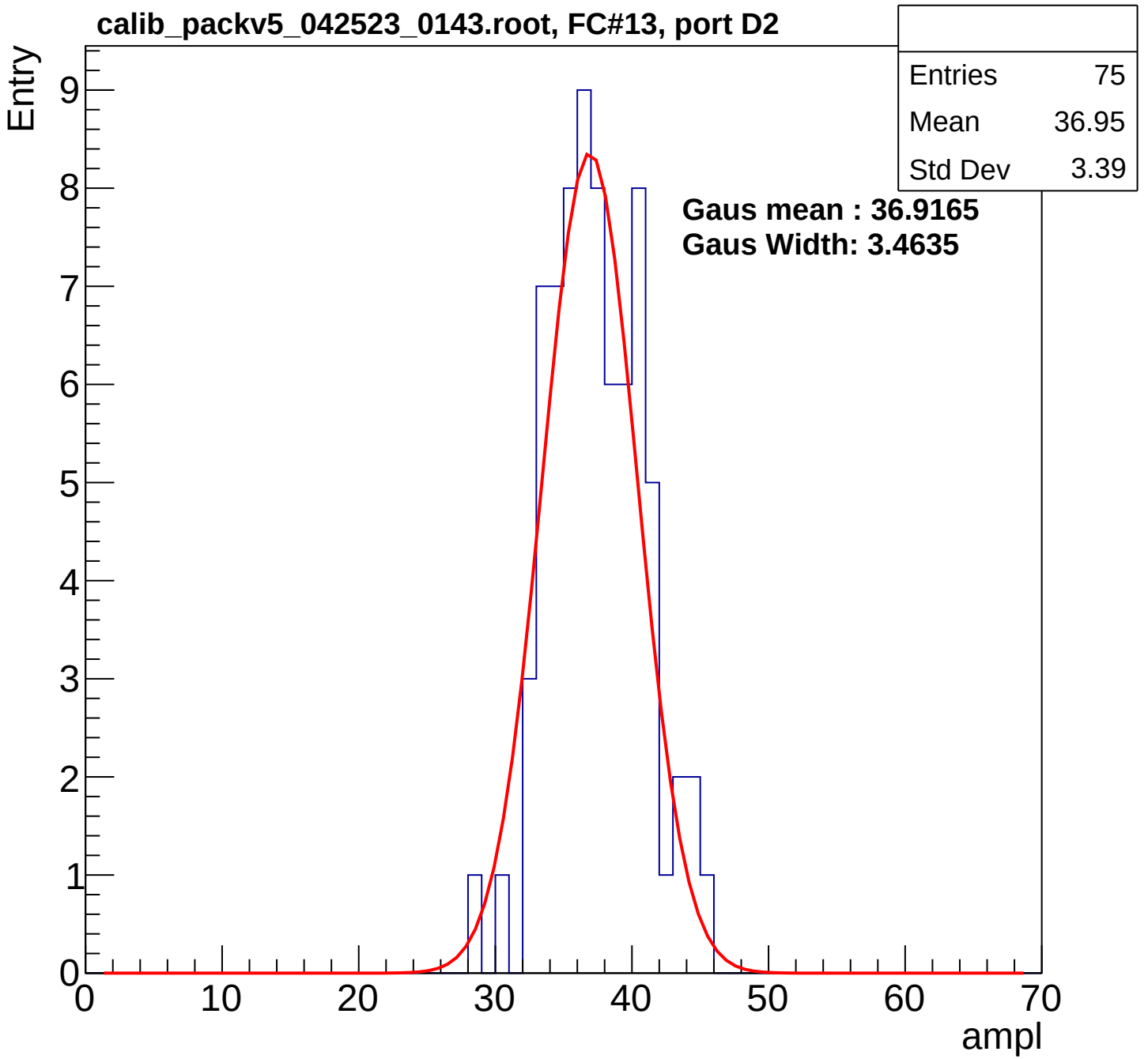

B1L003S, U24-ch44, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.72
Std Dev 3.016
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

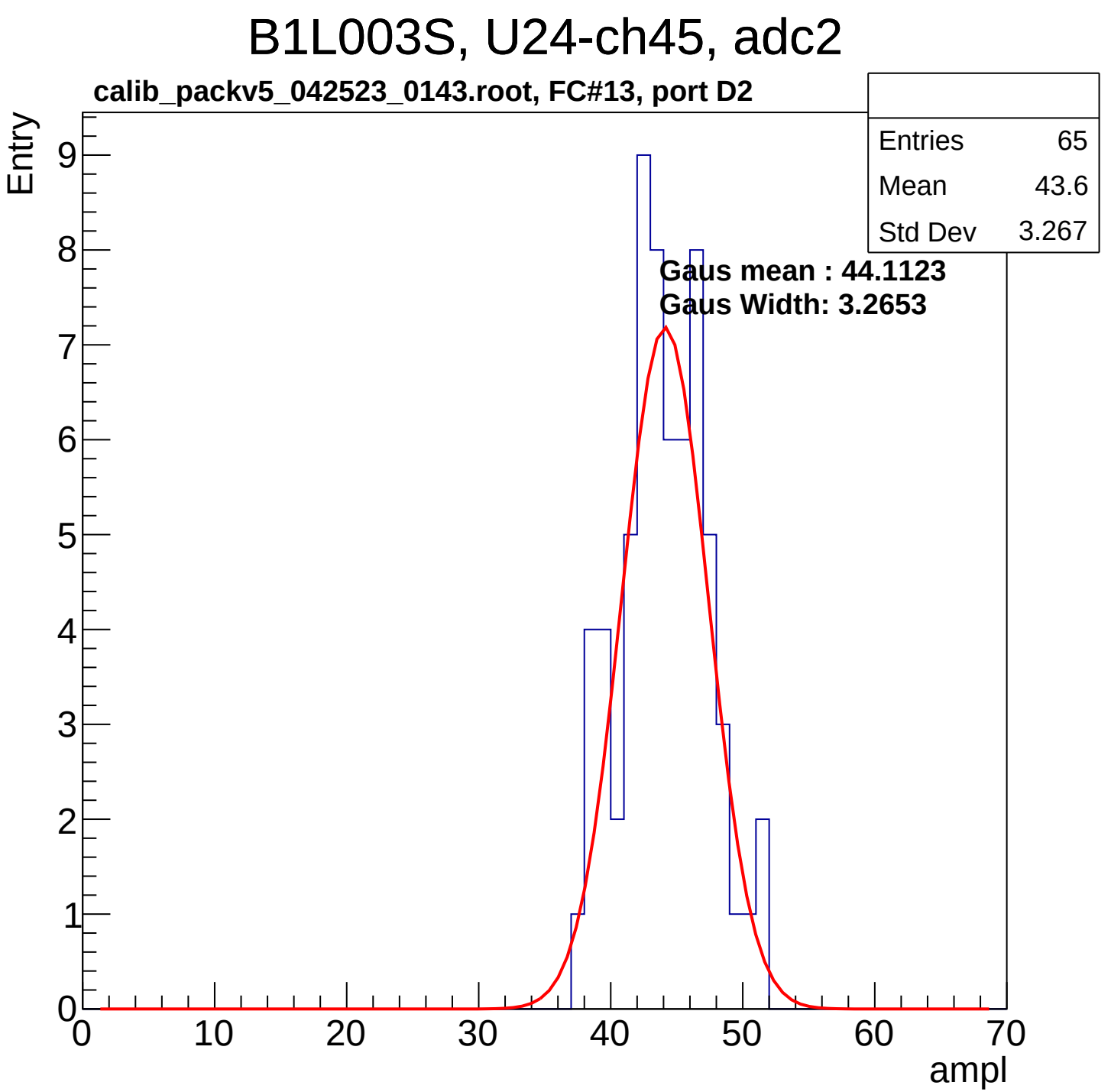

B1L003S, U24-ch44, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.45
Std Dev 9.61
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

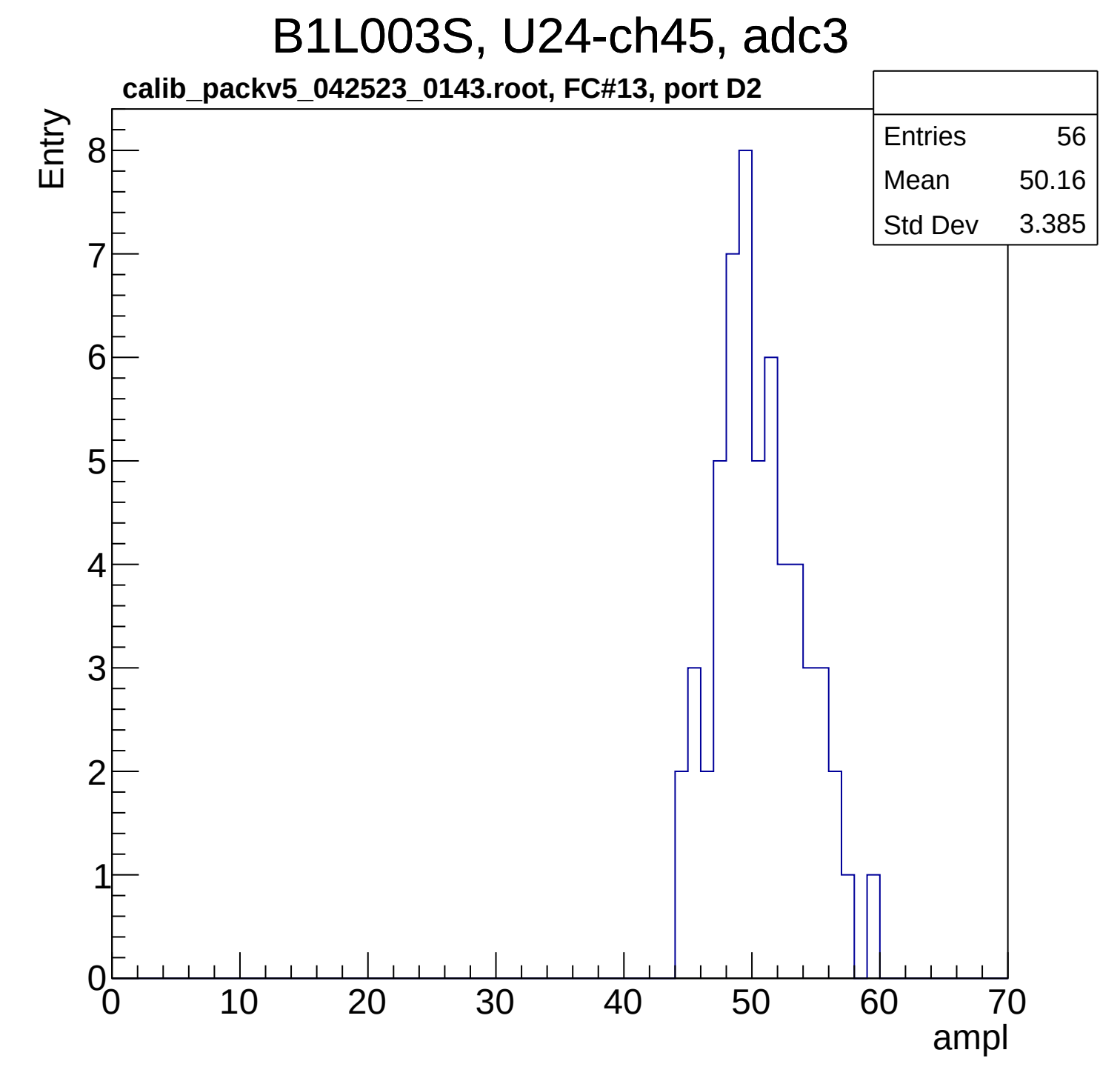

B1L003S, U24-ch44, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 62.2
Std Dev 0.9798
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch44, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch45, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.91
Std Dev 4.973
Gaus mean : 29.9014
Gaus Width: 3.7344
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch45, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 36.95
Std Dev 3.39
Gaus mean : 36.9165
Gaus Width: 3.4635
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch45, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.6
Std Dev 3.267
Gaus mean : 44.1123
Gaus Width: 3.2653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch45, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 50.16
Std Dev 3.385
Entry
ampl
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4
5
6
7
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0
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60
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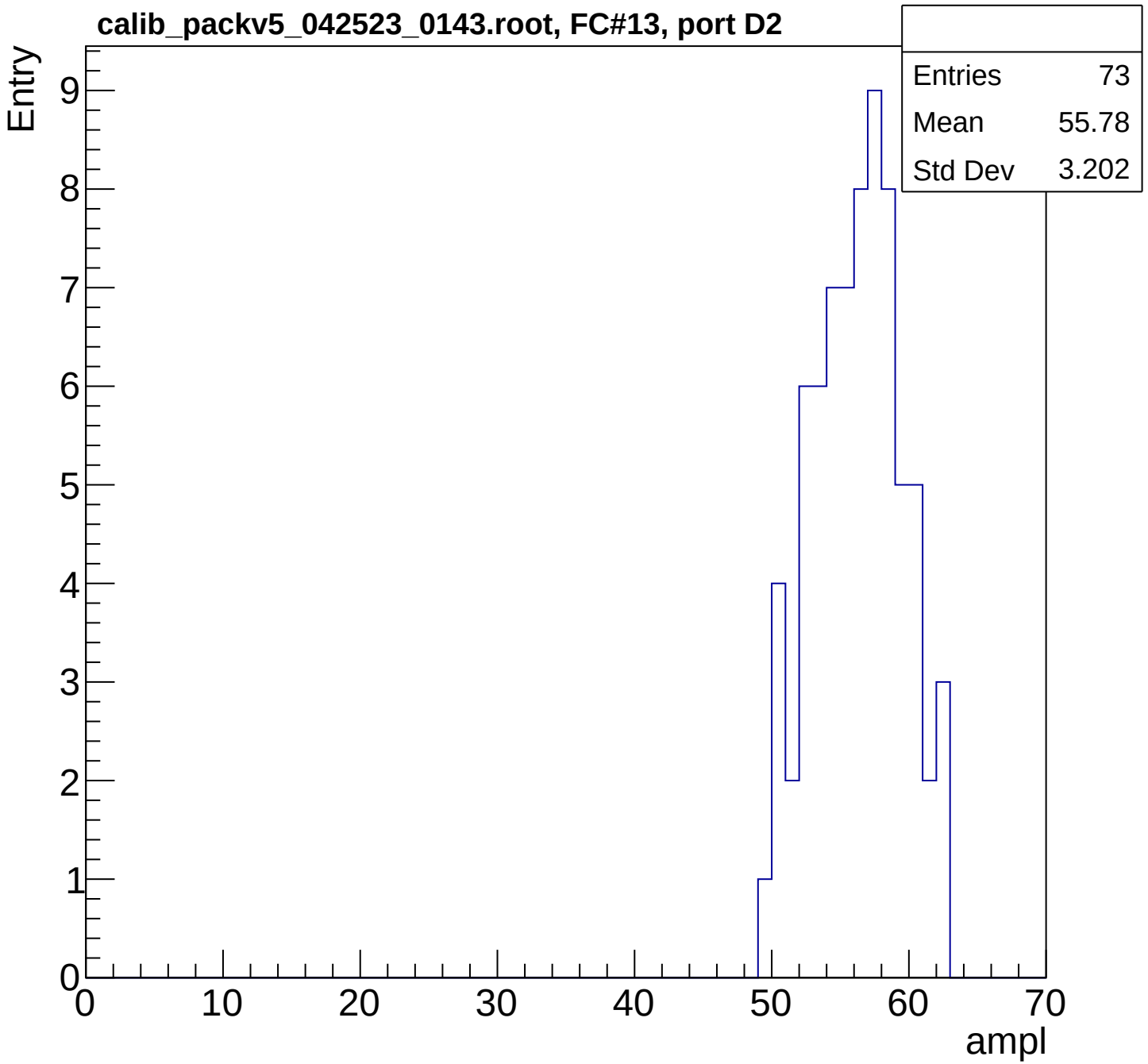
B1L003S, U24-ch45, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 55.78
Std Dev 3.202
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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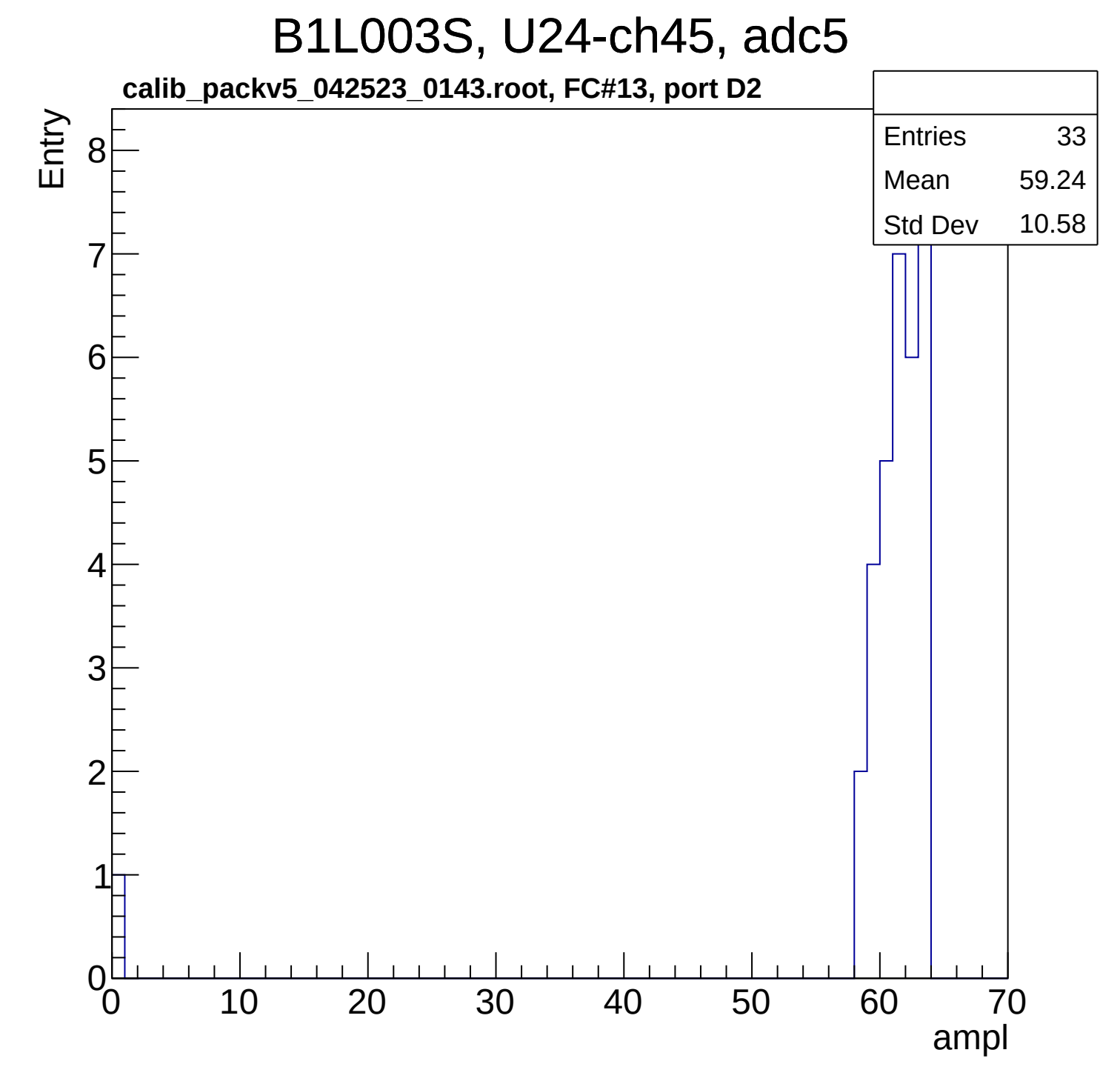
B1L003S, U24-ch45, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 59.24
Std Dev 10.58
Entry
ampl
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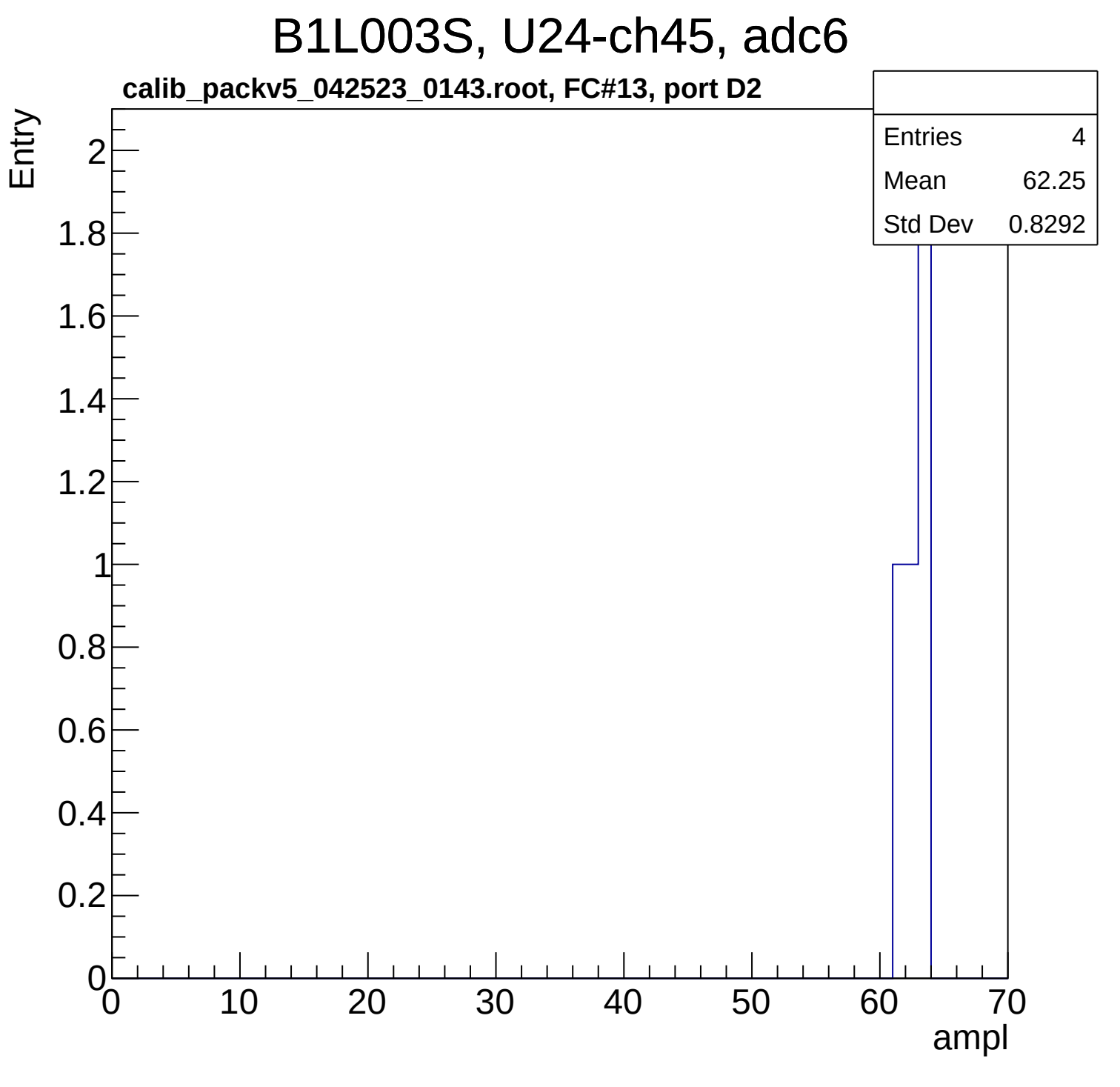
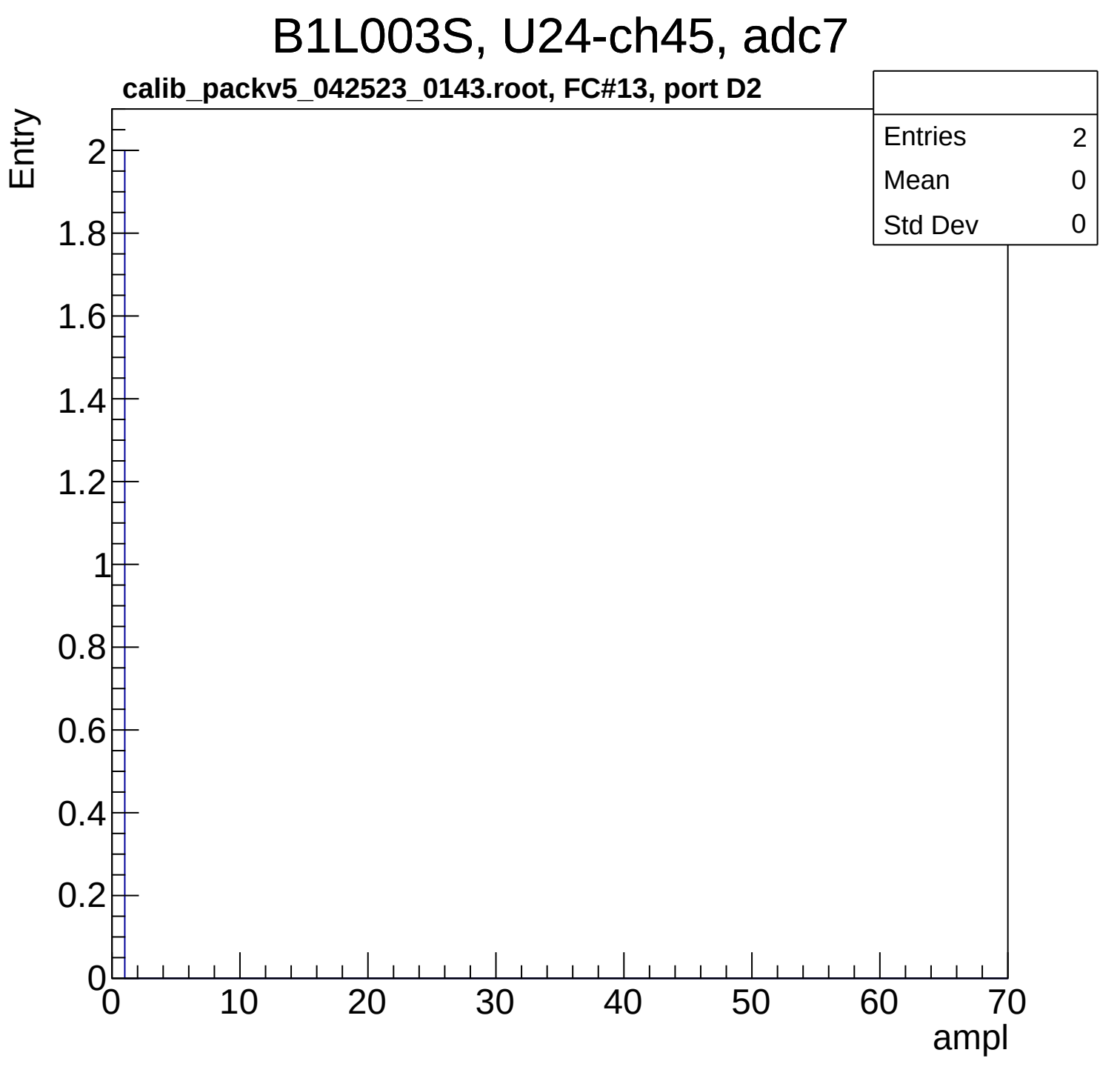
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch45, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch45, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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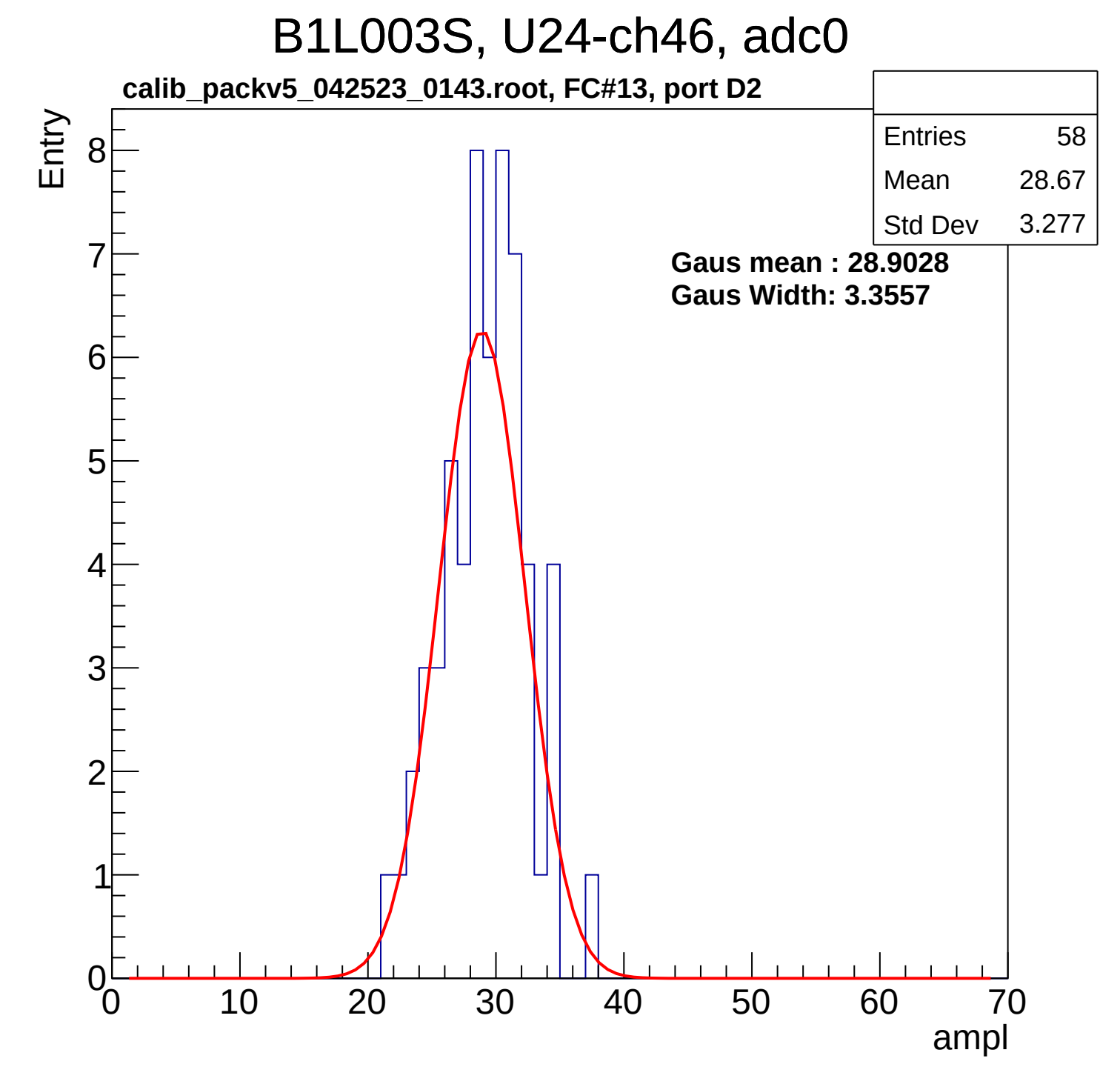
B1L003S, U24-ch46, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.67
Std Dev 3.277
Gaus mean : 28.9028
Gaus Width: 3.3557
Entry
ampl
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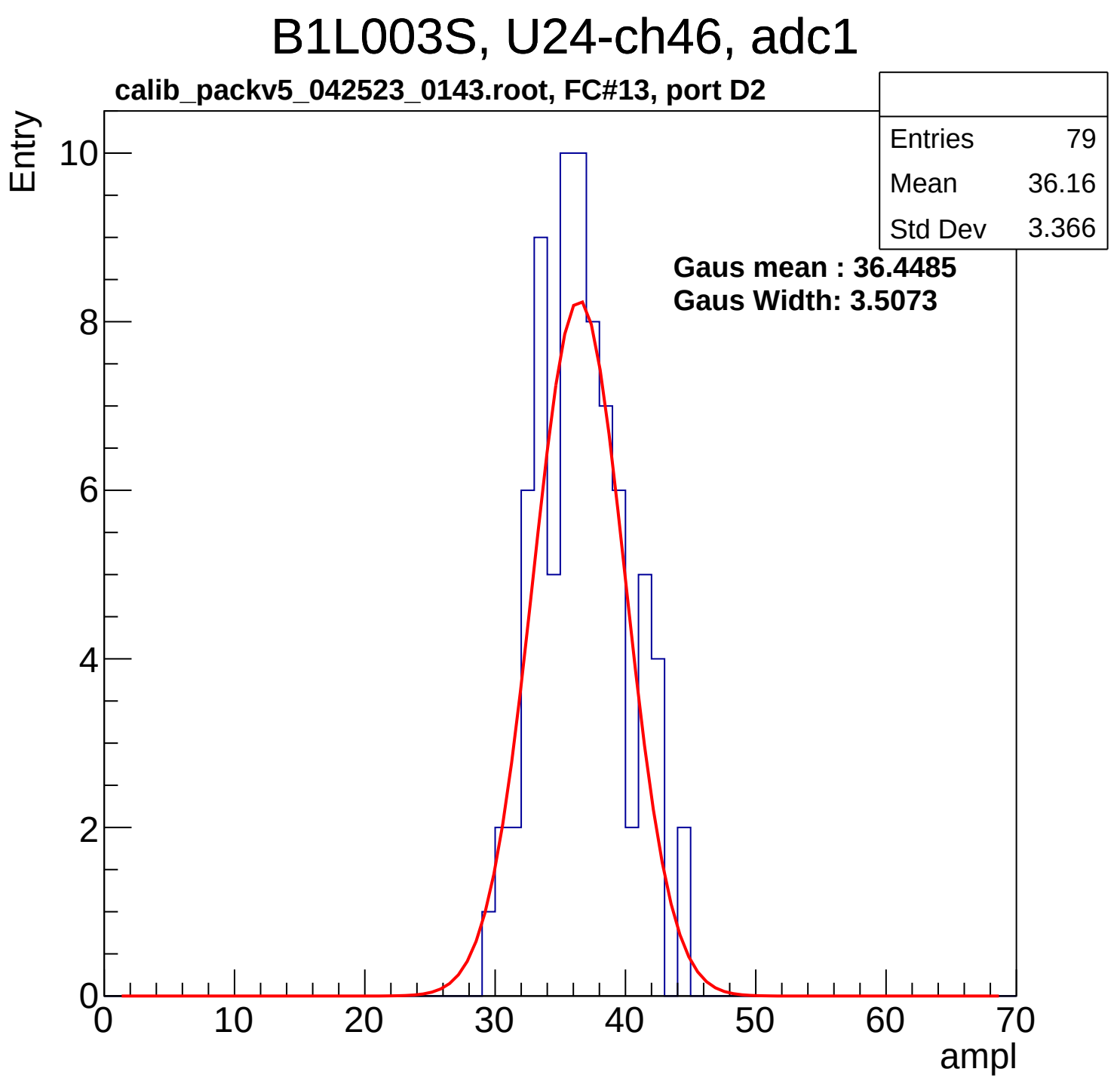
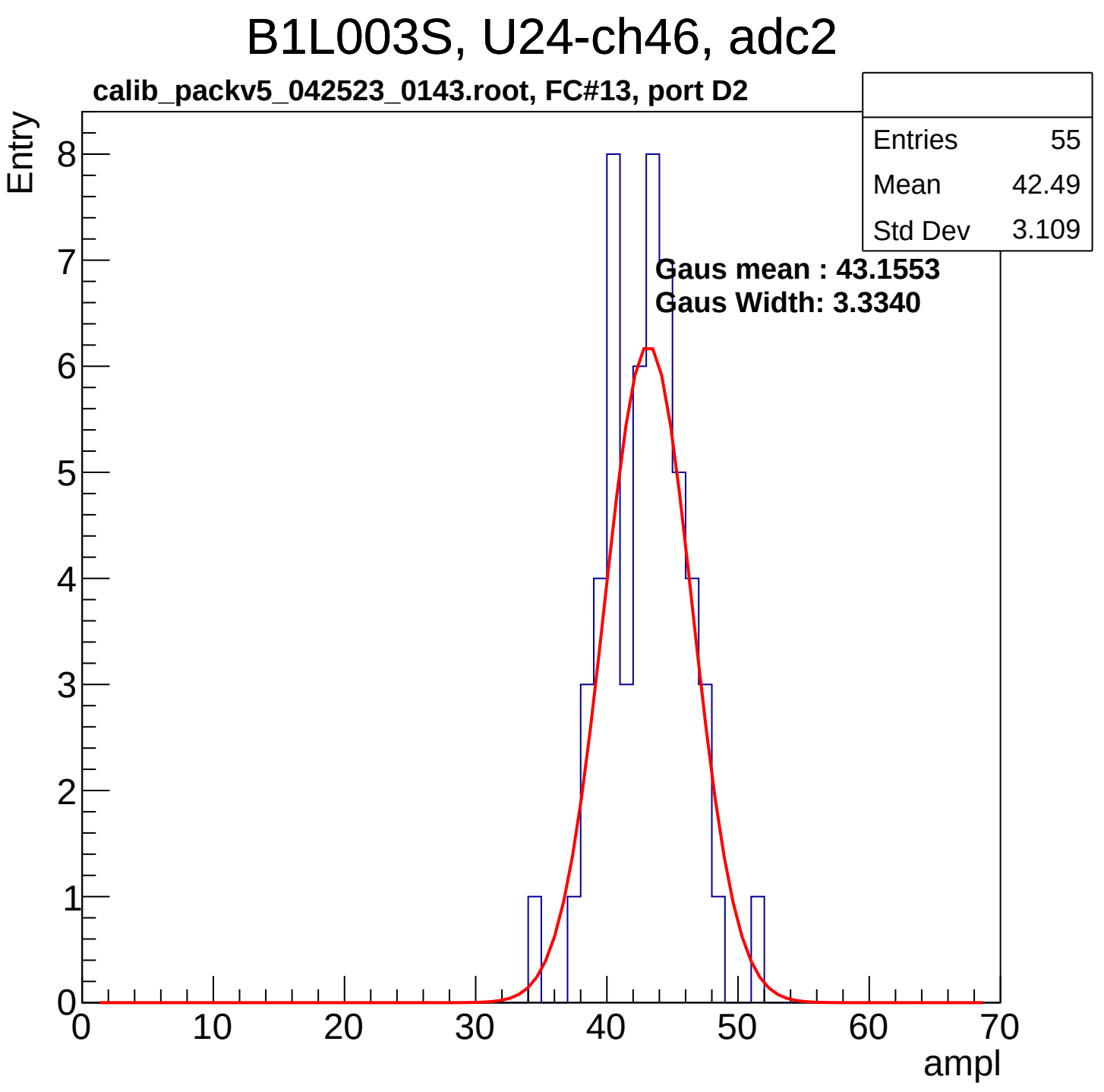
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch46, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 36.16
Std Dev 3.366
Gaus mean : 36.4485
Gaus Width: 3.5073
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch46, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 42.49
Std Dev 3.109
Gaus mean : 43.1553
Gaus Width: 3.3340
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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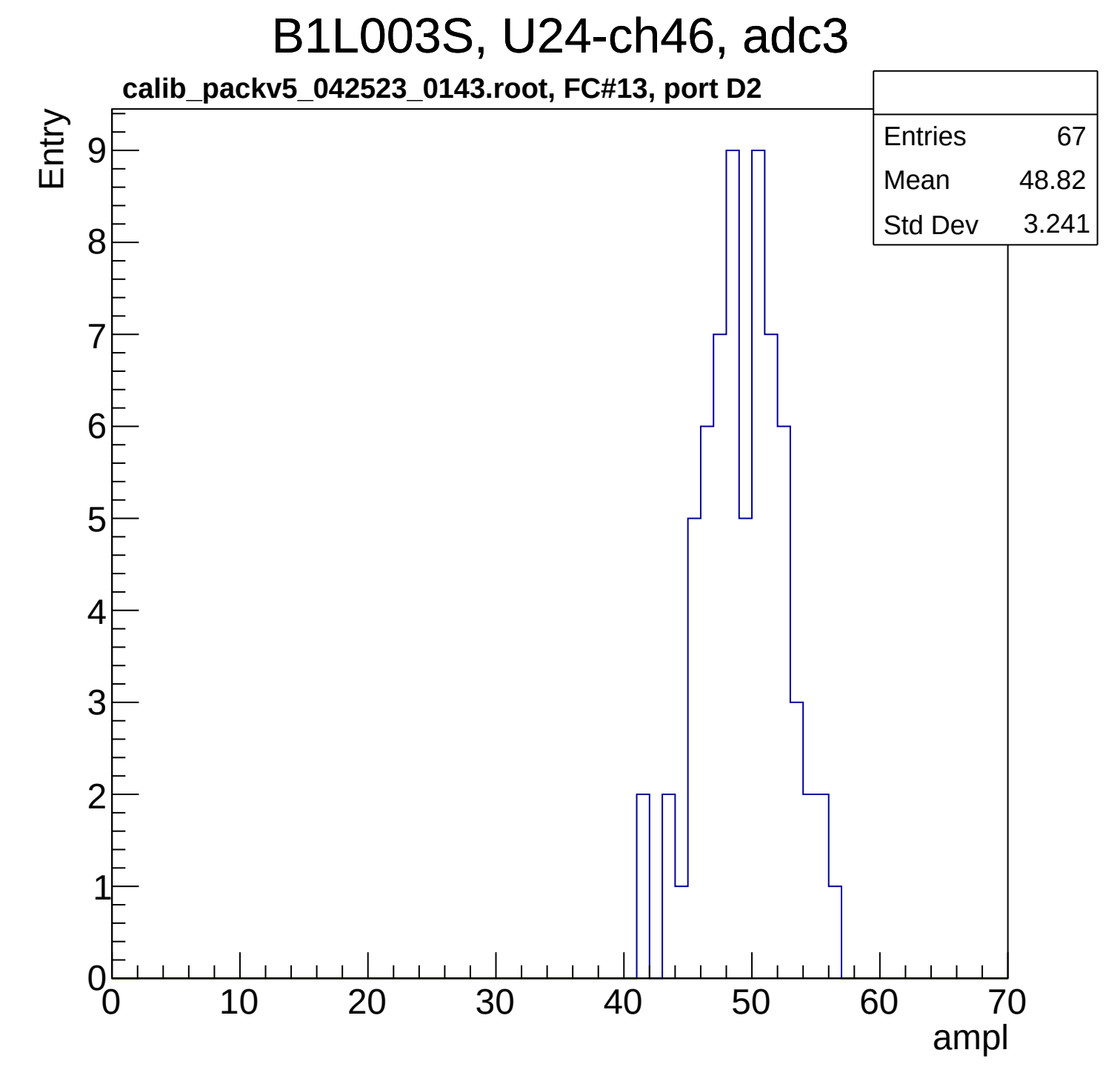
40
50
60
70


B1L003S, U24-ch46, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.82
Std Dev 3.241
Entry
ampl
0
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2
3
4
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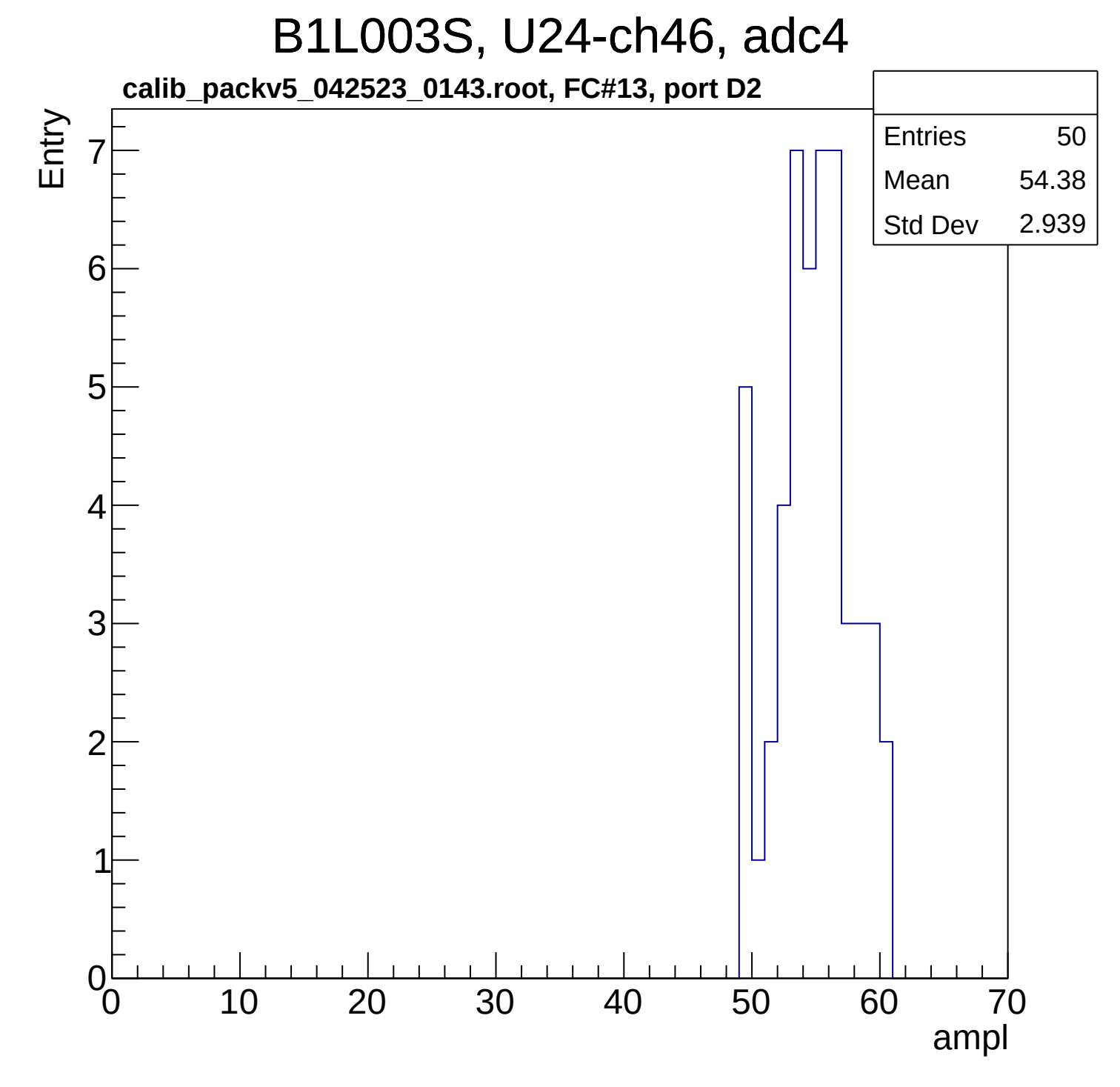
B1L003S, U24-ch46, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.38
Std Dev 2.939
Entry
ampl
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2
3
4
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6
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0
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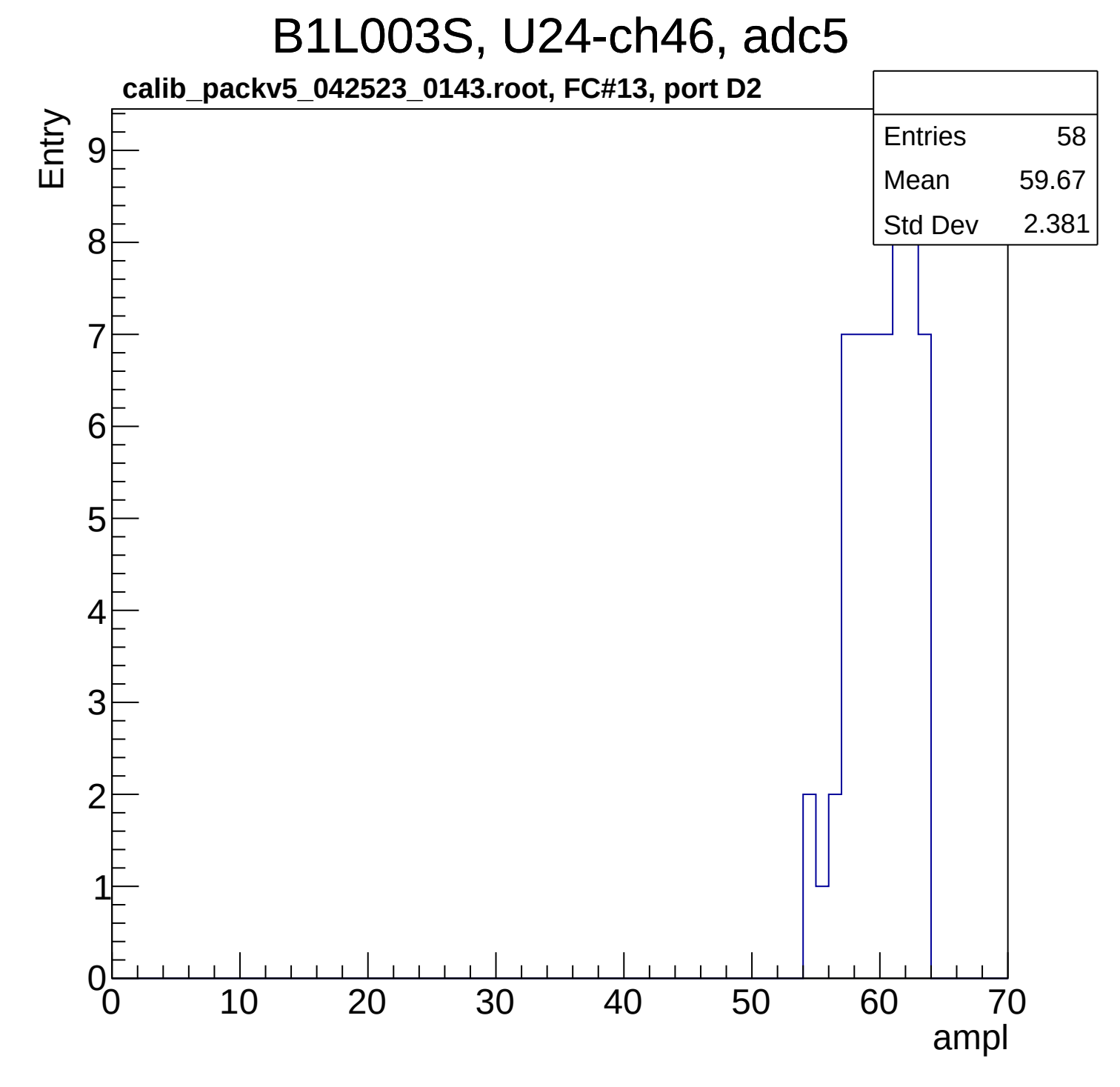
B1L003S, U24-ch46, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 59.67
Std Dev 2.381
Entry
ampl
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2
3
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6
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0
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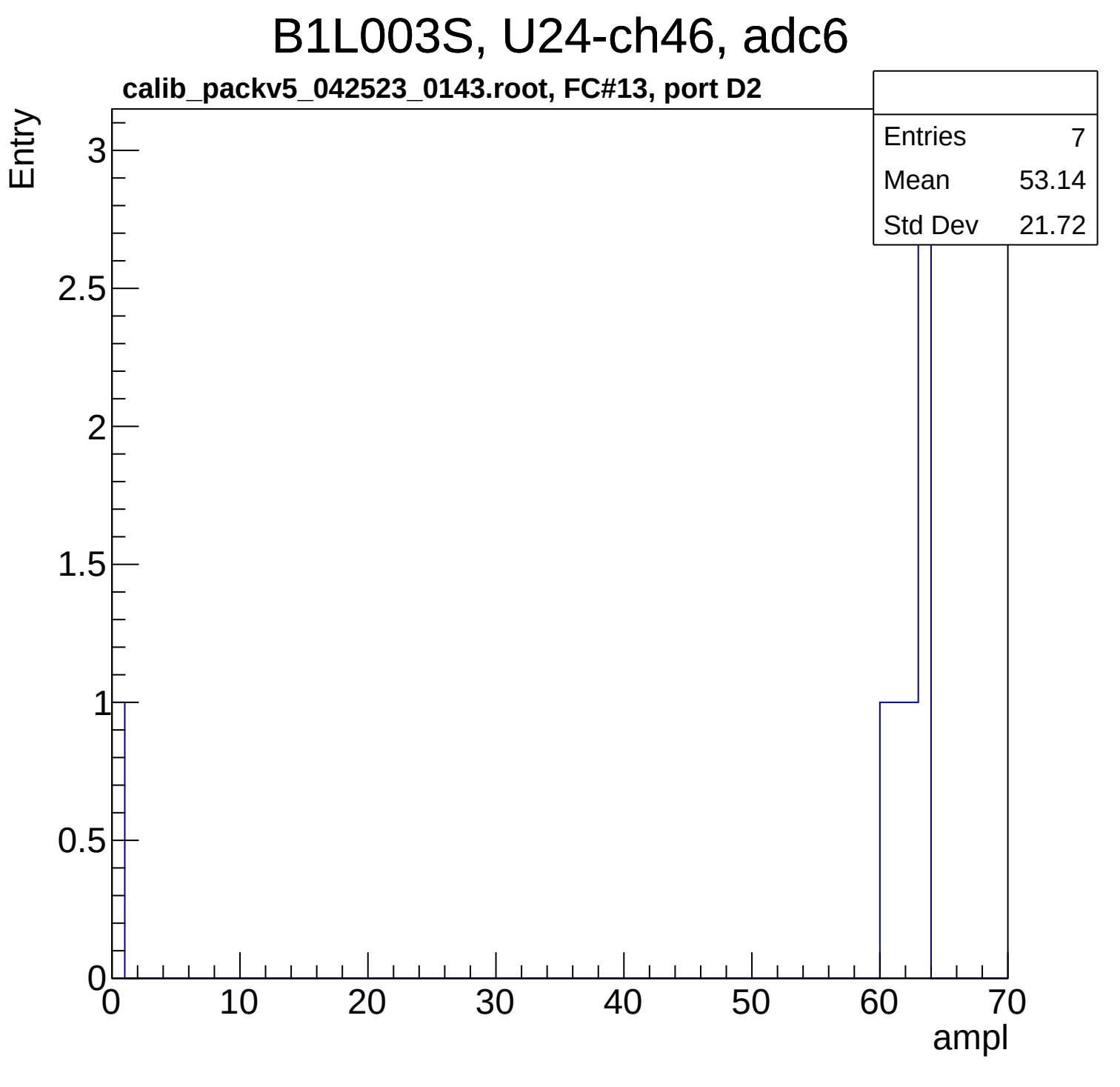
B1L003S, U24-ch46, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.14
Std Dev 21.72
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
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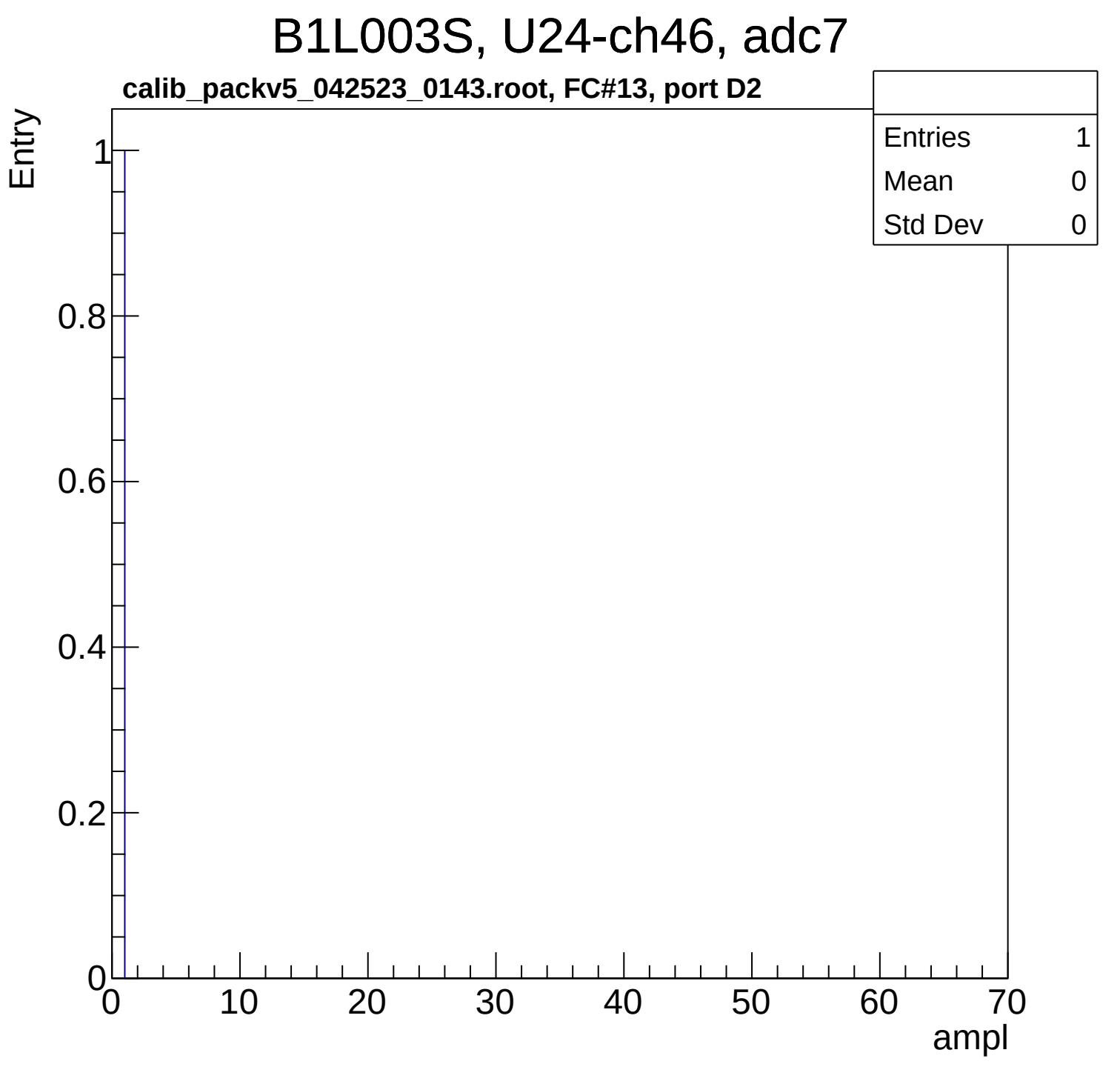
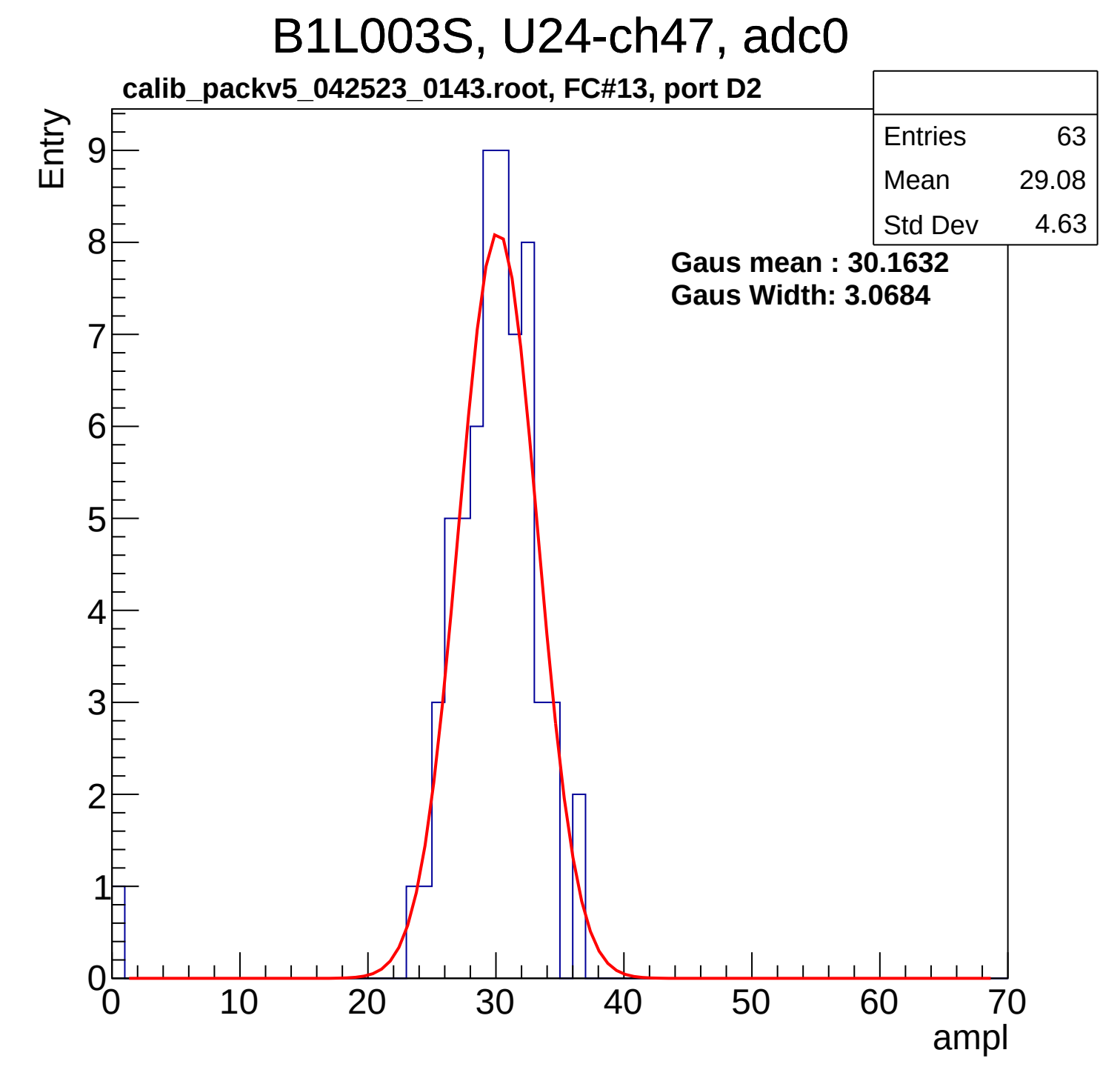
40
50
60
70
ampl


B1L003S, U24-ch46, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch47, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29.08
Std Dev 4.63
Gaus mean : 30.1632
Gaus Width: 3.0684
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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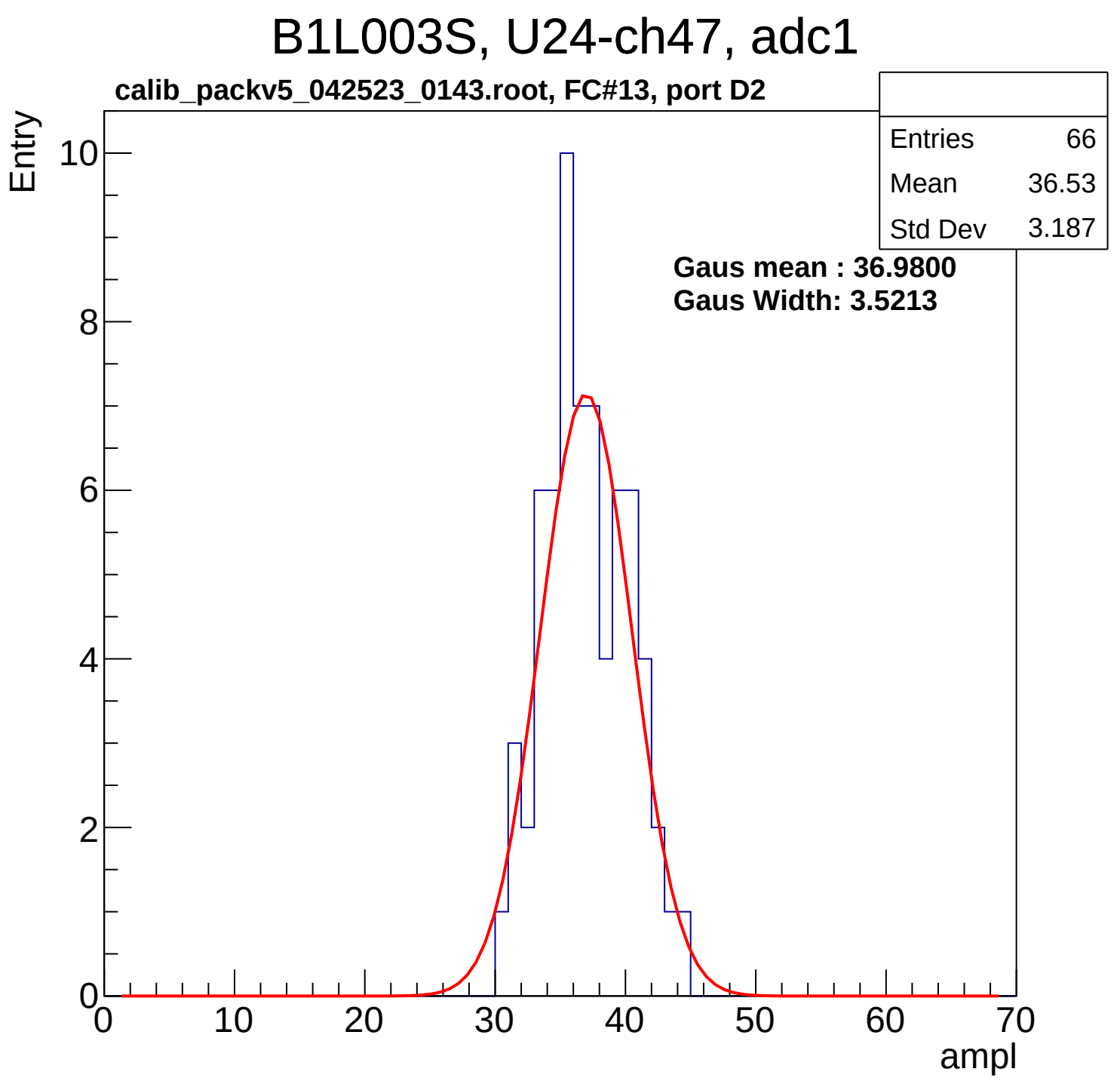
B1L003S, U24-ch47, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 36.53
Std Dev 3.187
Gaus mean : 36.9800
Gaus Width: 3.5213
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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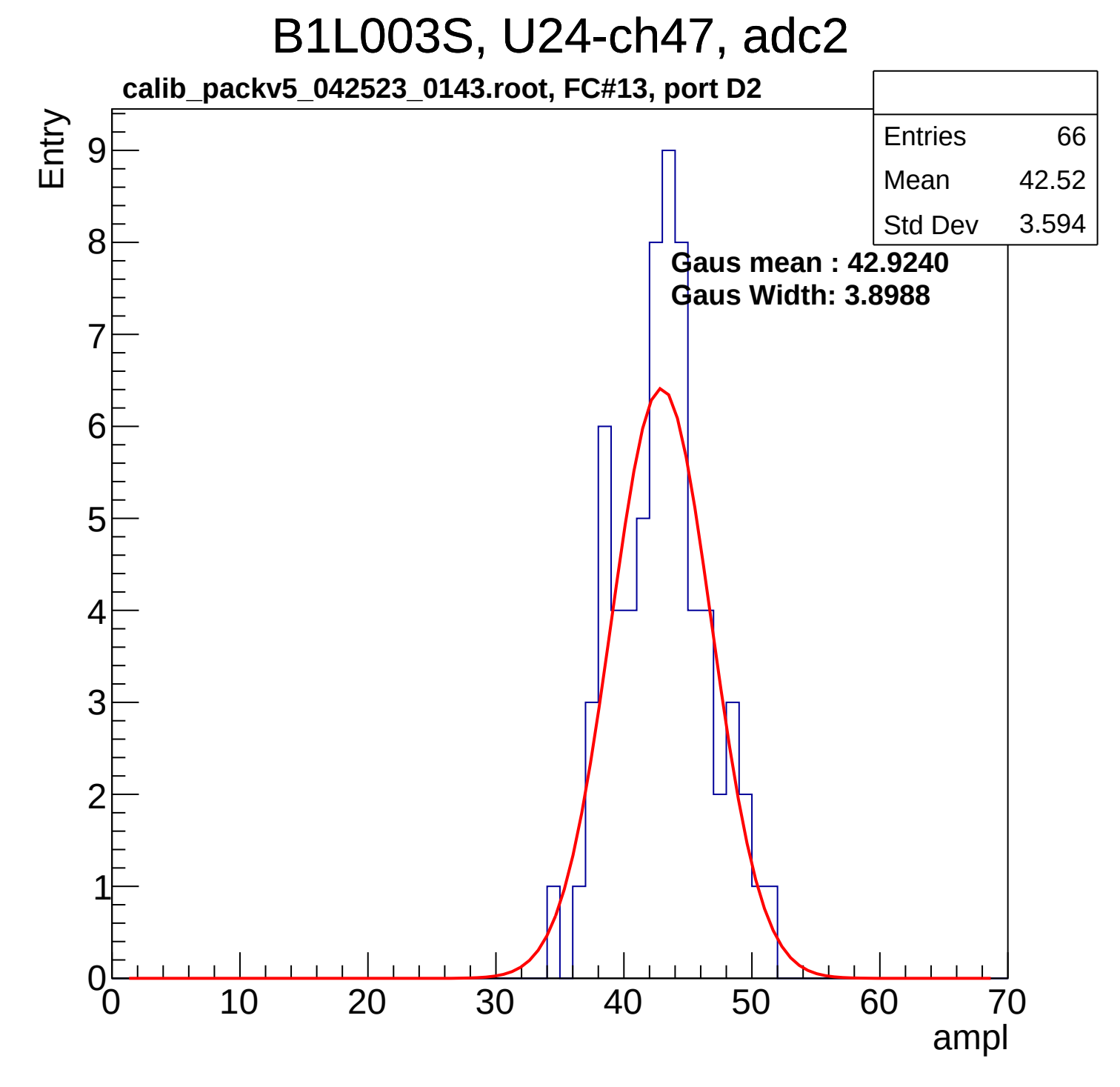
B1L003S, U24-ch47, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.52
Std Dev 3.594
Gaus mean : 42.9240
Gaus Width: 3.8988
Entry
ampl
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2
3
4
5
6
7
8
9
0
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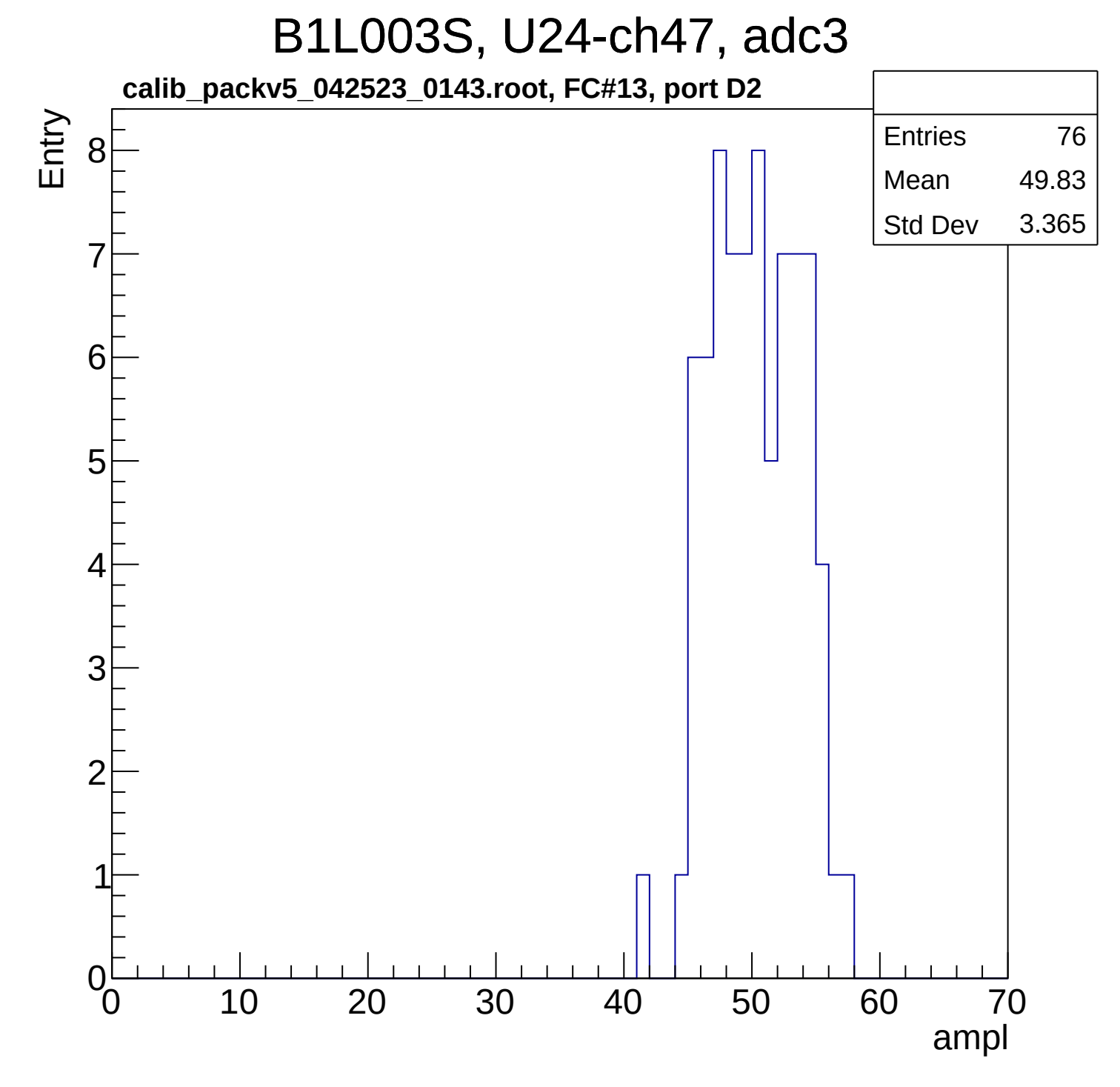
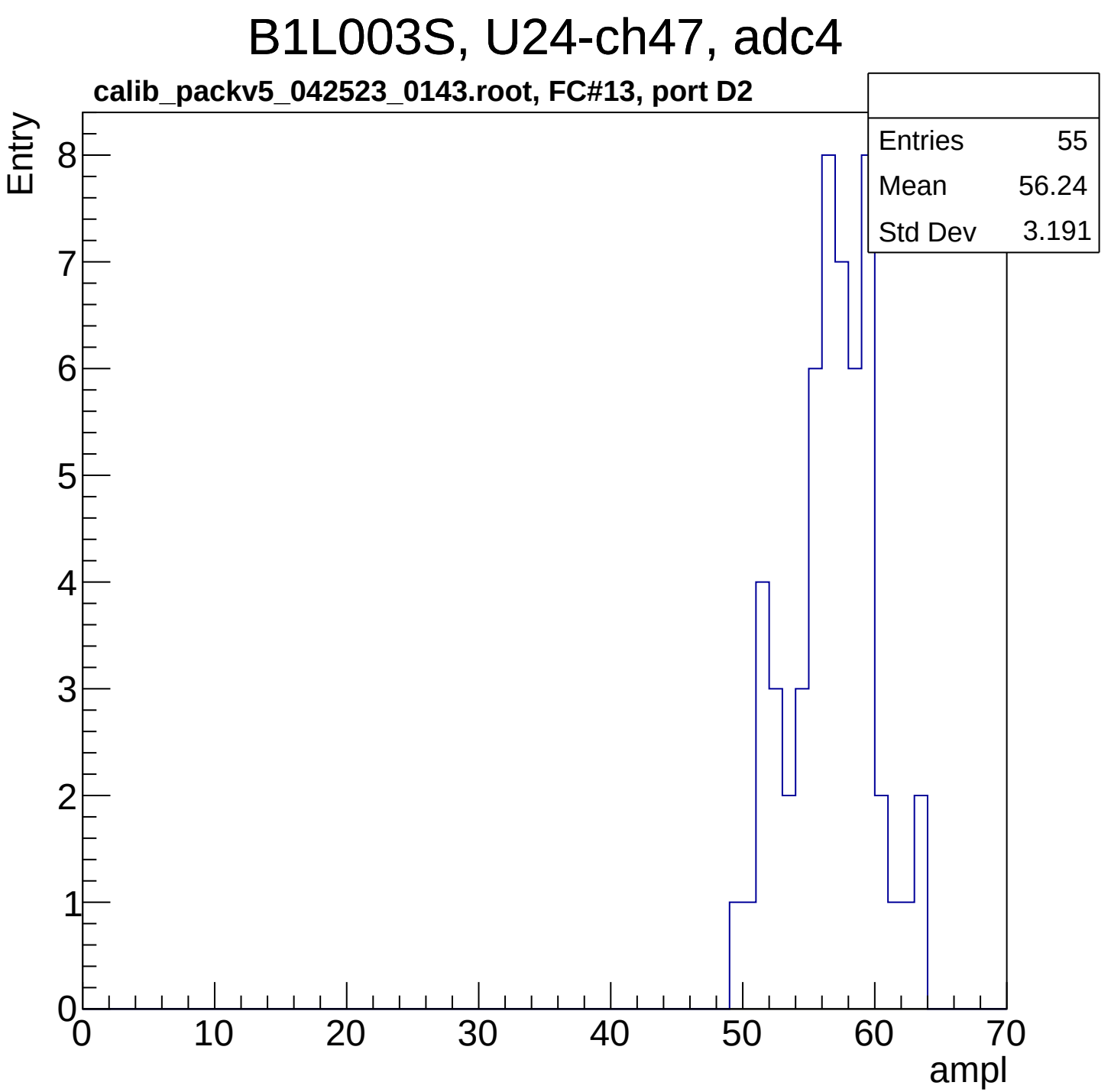

B1L003S, U24-ch47, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 49.83
Std Dev 3.365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch47, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 56.24
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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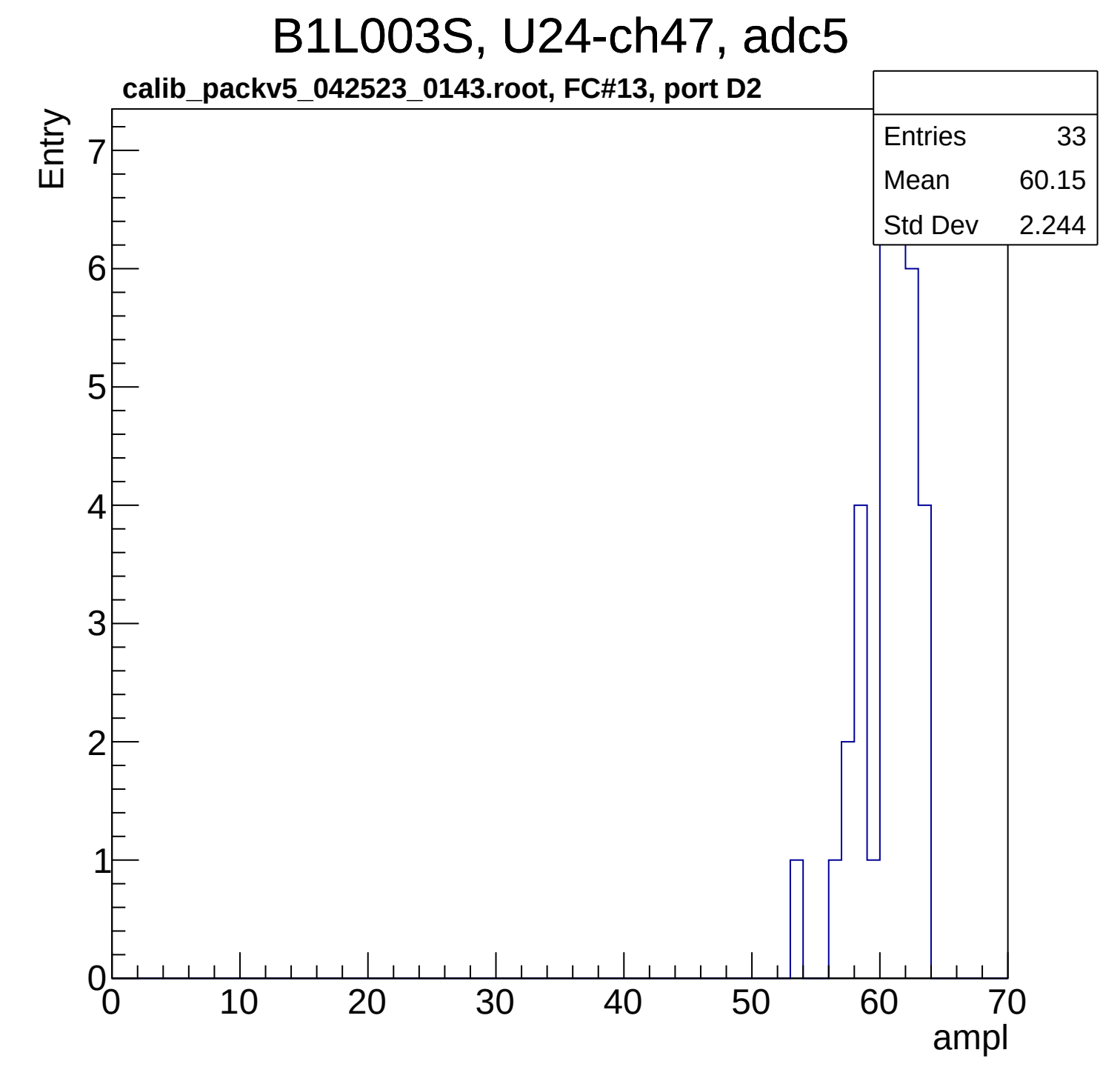
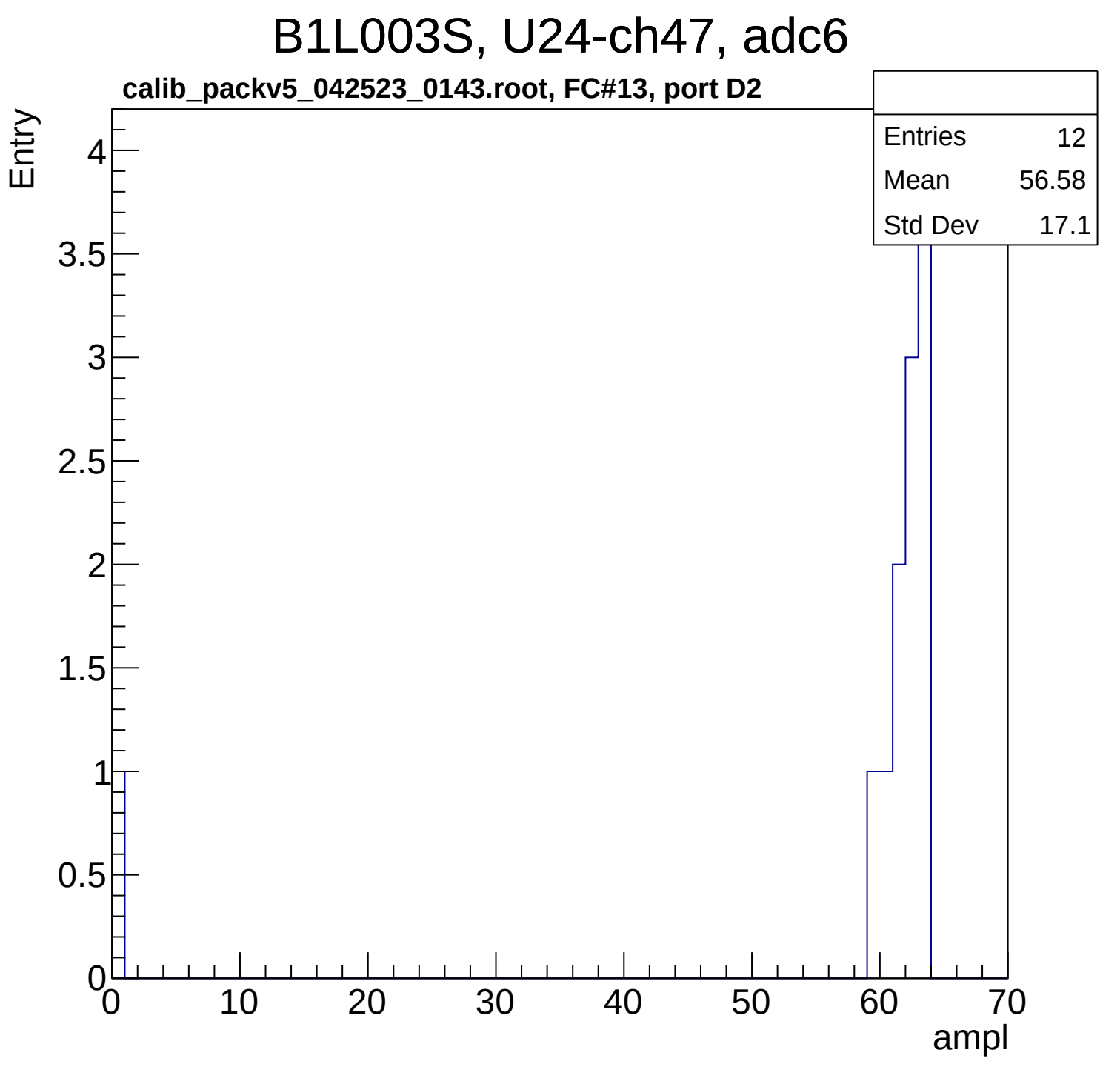

B1L003S, U24-ch47, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 60.15
Std Dev 2.244
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch47, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.58
Std Dev 17.1
Entry
ampl
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0.5
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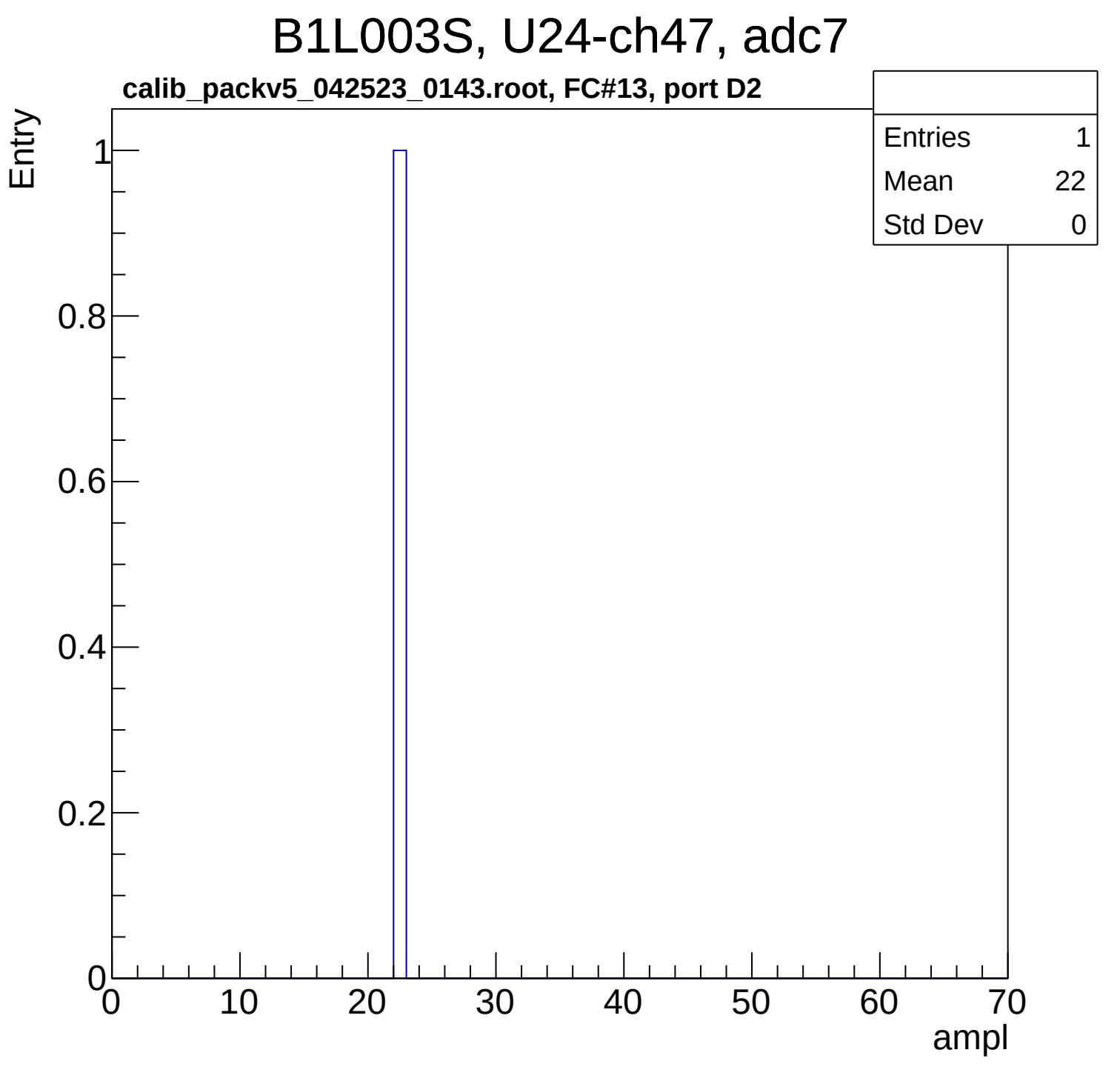
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U24-ch47, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
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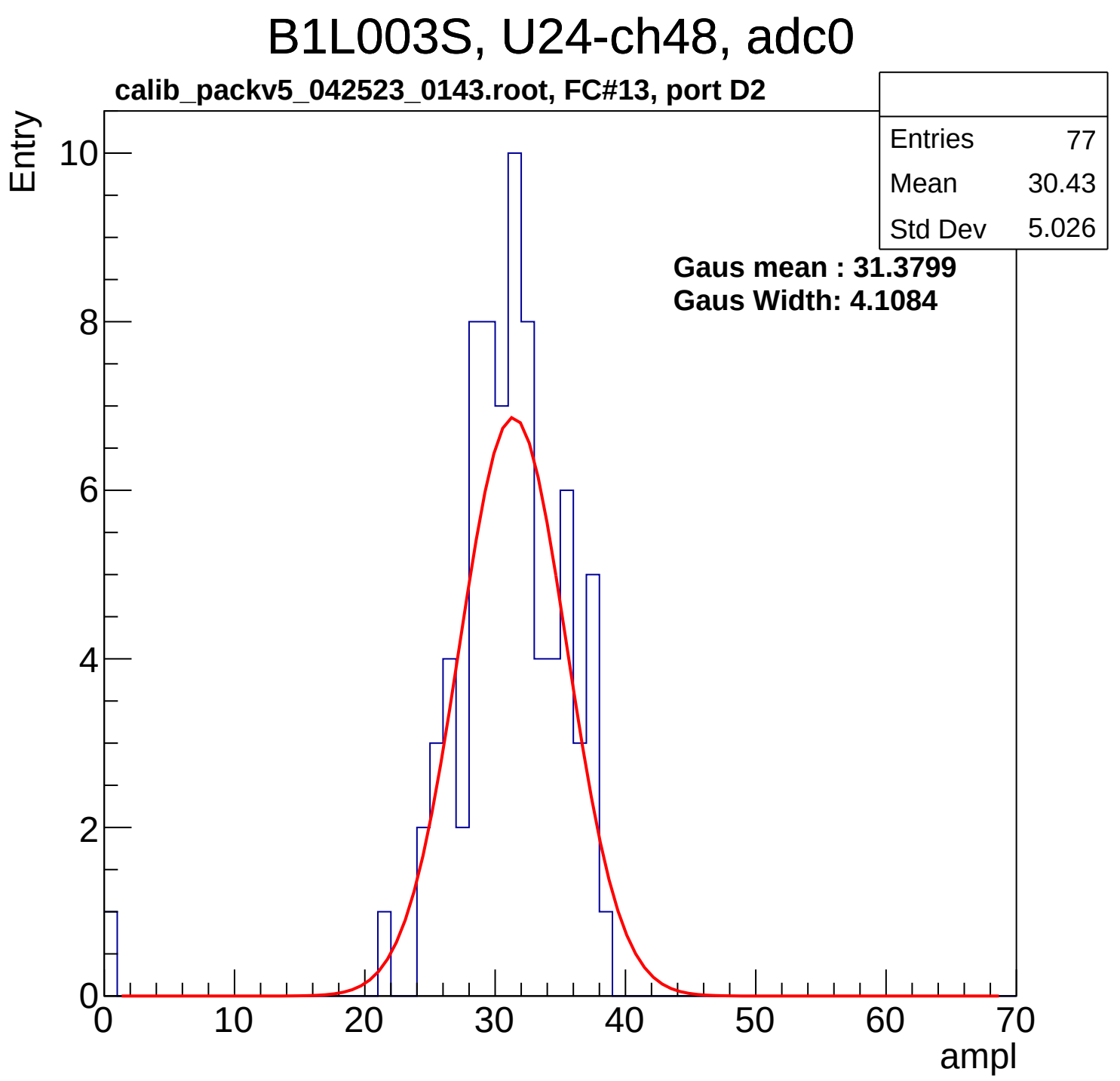
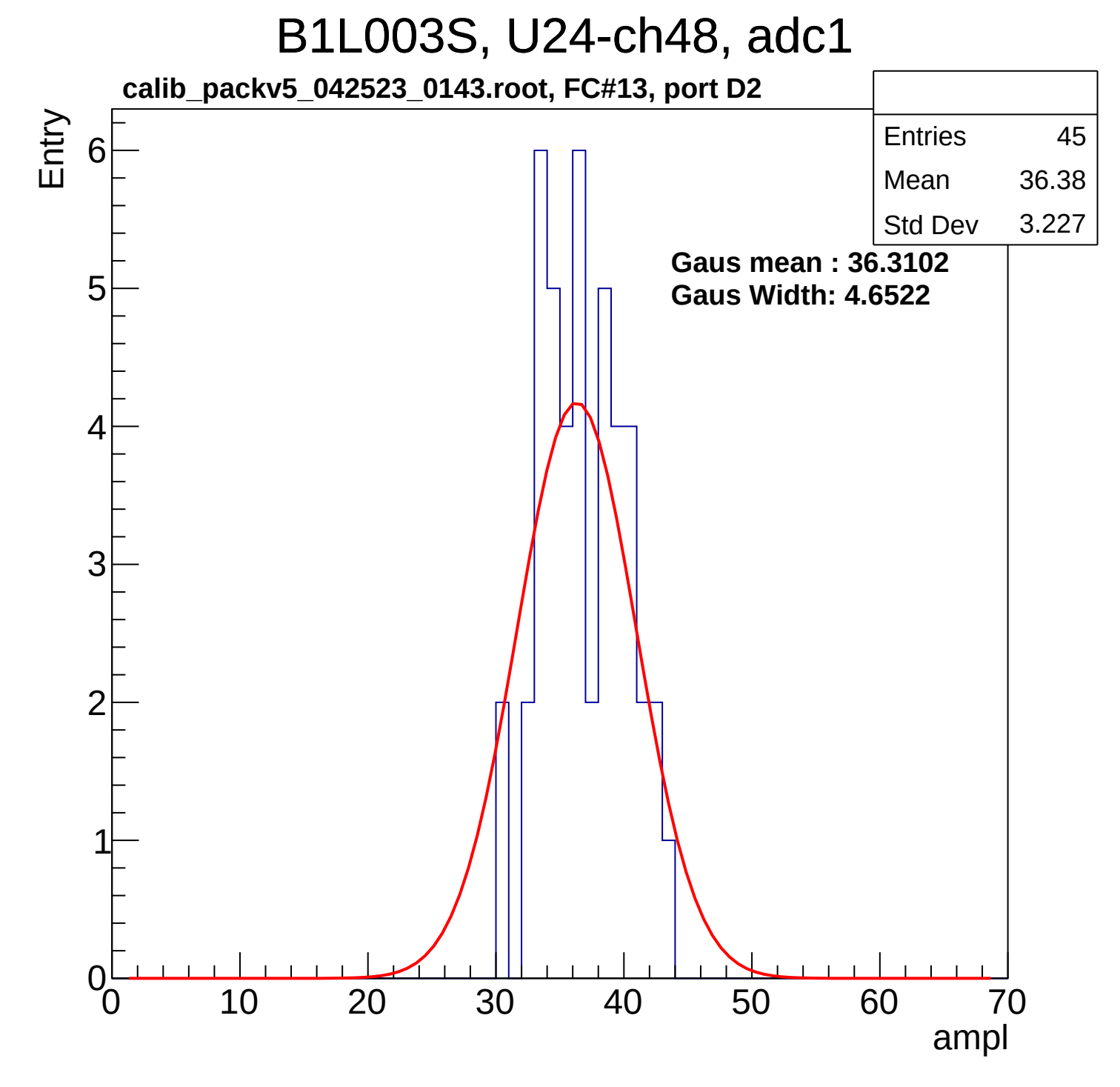
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch48, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 30.43
Std Dev 5.026
Gaus mean : 31.3799
Gaus Width: 4.1084
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch48, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 36.38
Std Dev 3.227
Gaus mean : 36.3102
Gaus Width: 4.6522
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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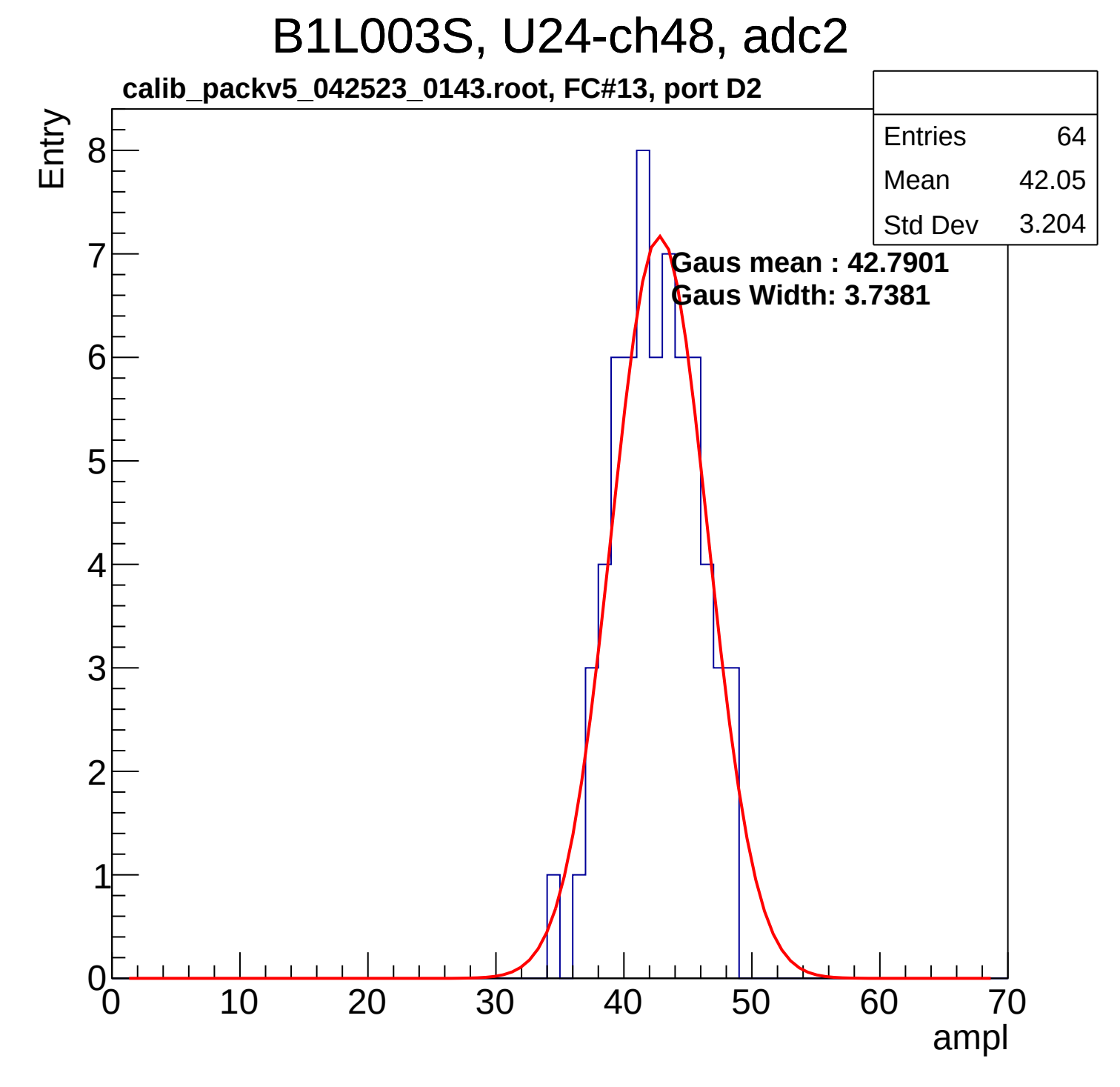
B1L003S, U24-ch48, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 42.05
Std Dev 3.204
Gaus mean : 42.7901
Gaus Width: 3.7381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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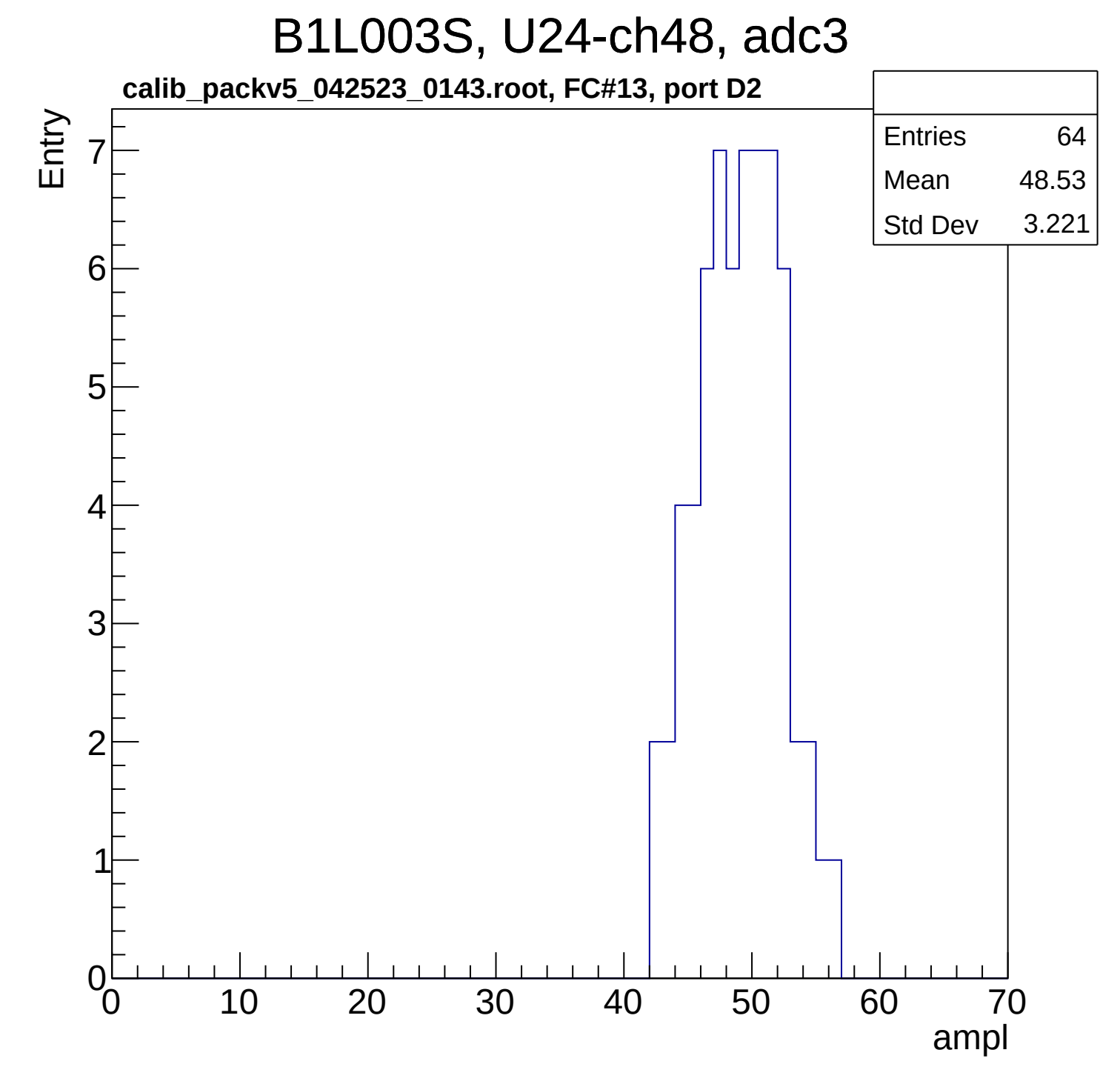
B1L003S, U24-ch48, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 48.53
Std Dev 3.221
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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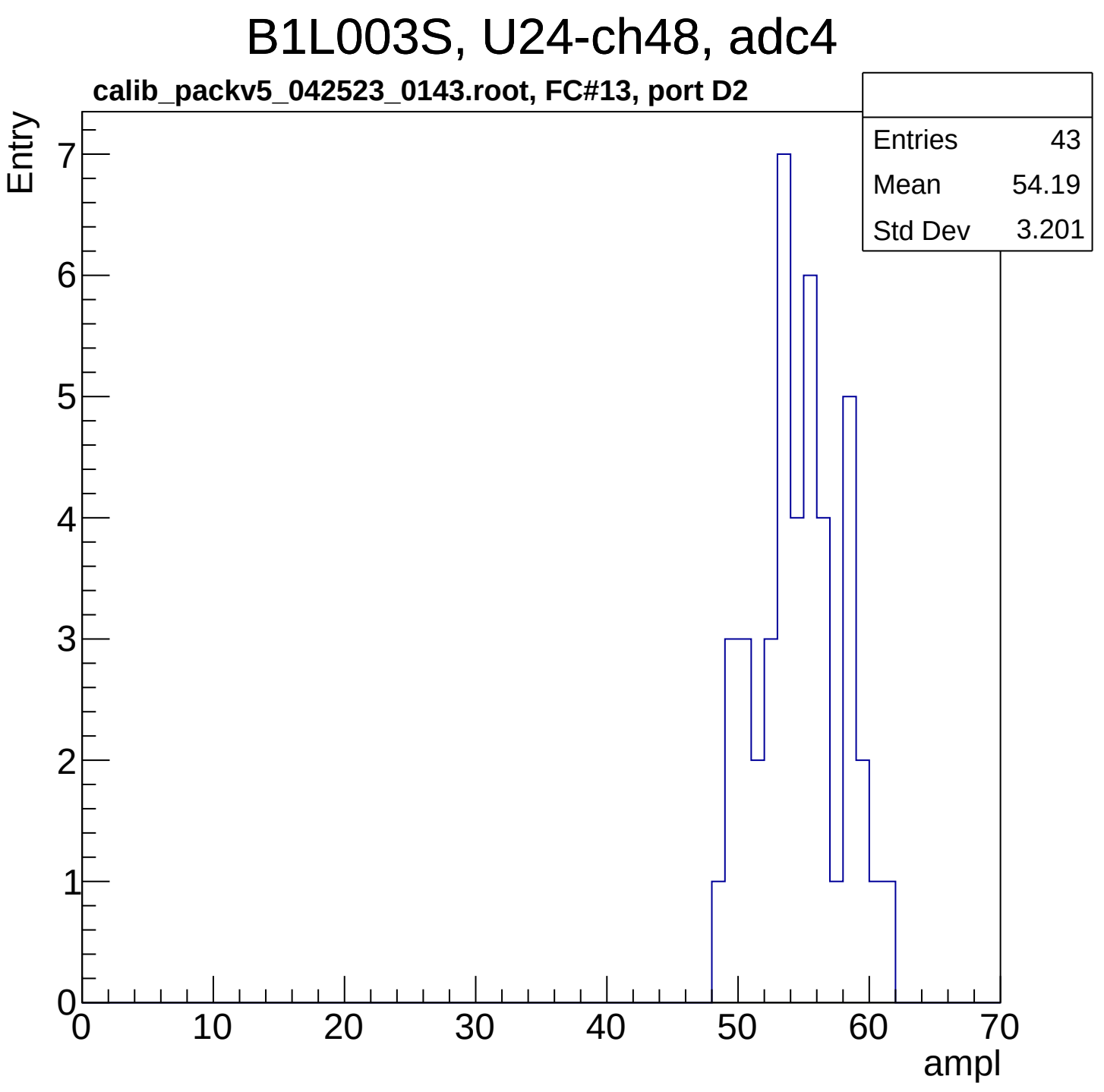
B1L003S, U24-ch48, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 54.19
Std Dev 3.201
Entry
ampl
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1
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3
4
5
6
7
0
10
20
30
40
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60
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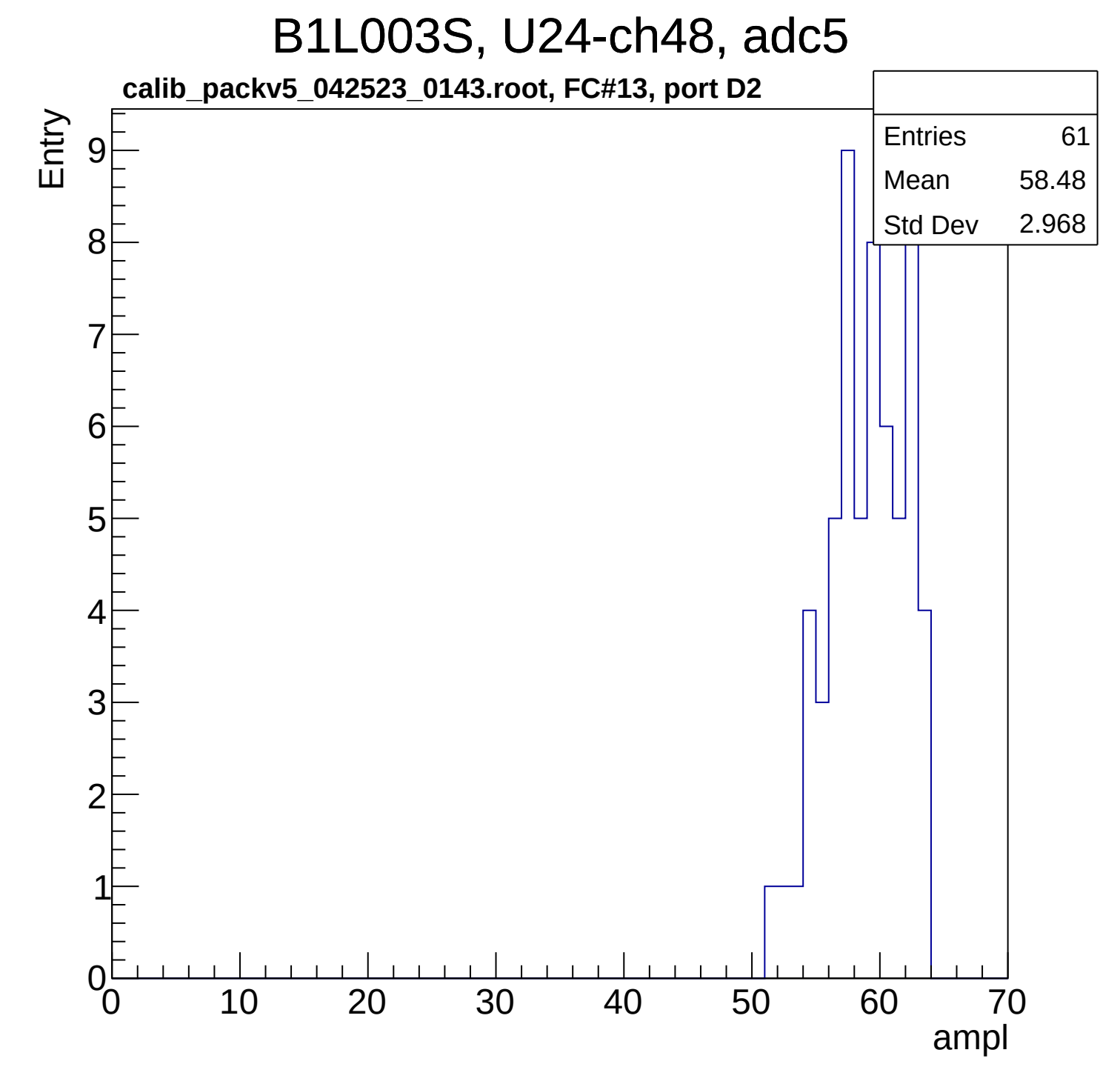
B1L003S, U24-ch48, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58.48
Std Dev 2.968
Entry
ampl
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3
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5
6
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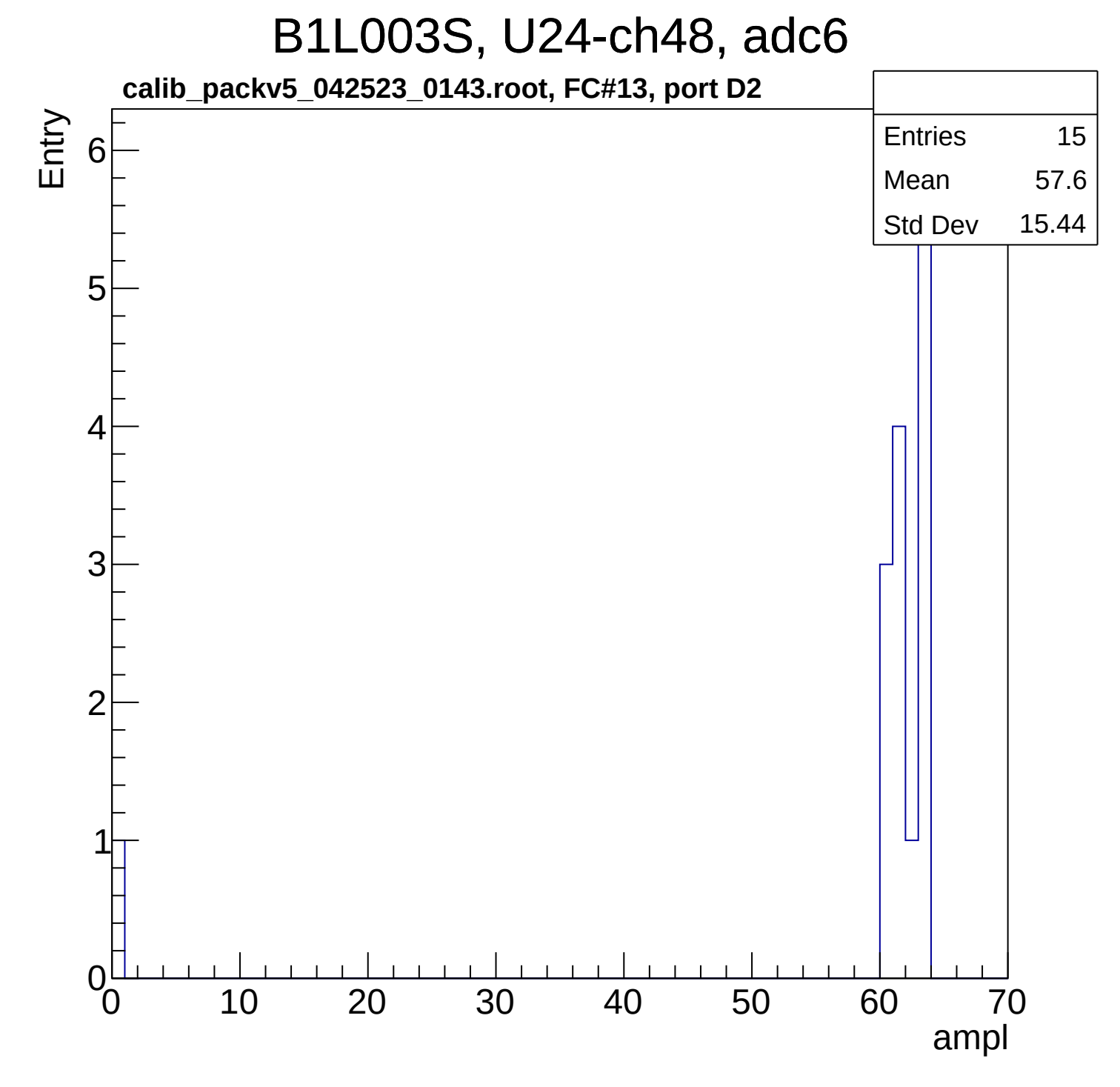
B1L003S, U24-ch48, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.6
Std Dev 15.44
Entry
0
1
2
3
4
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10
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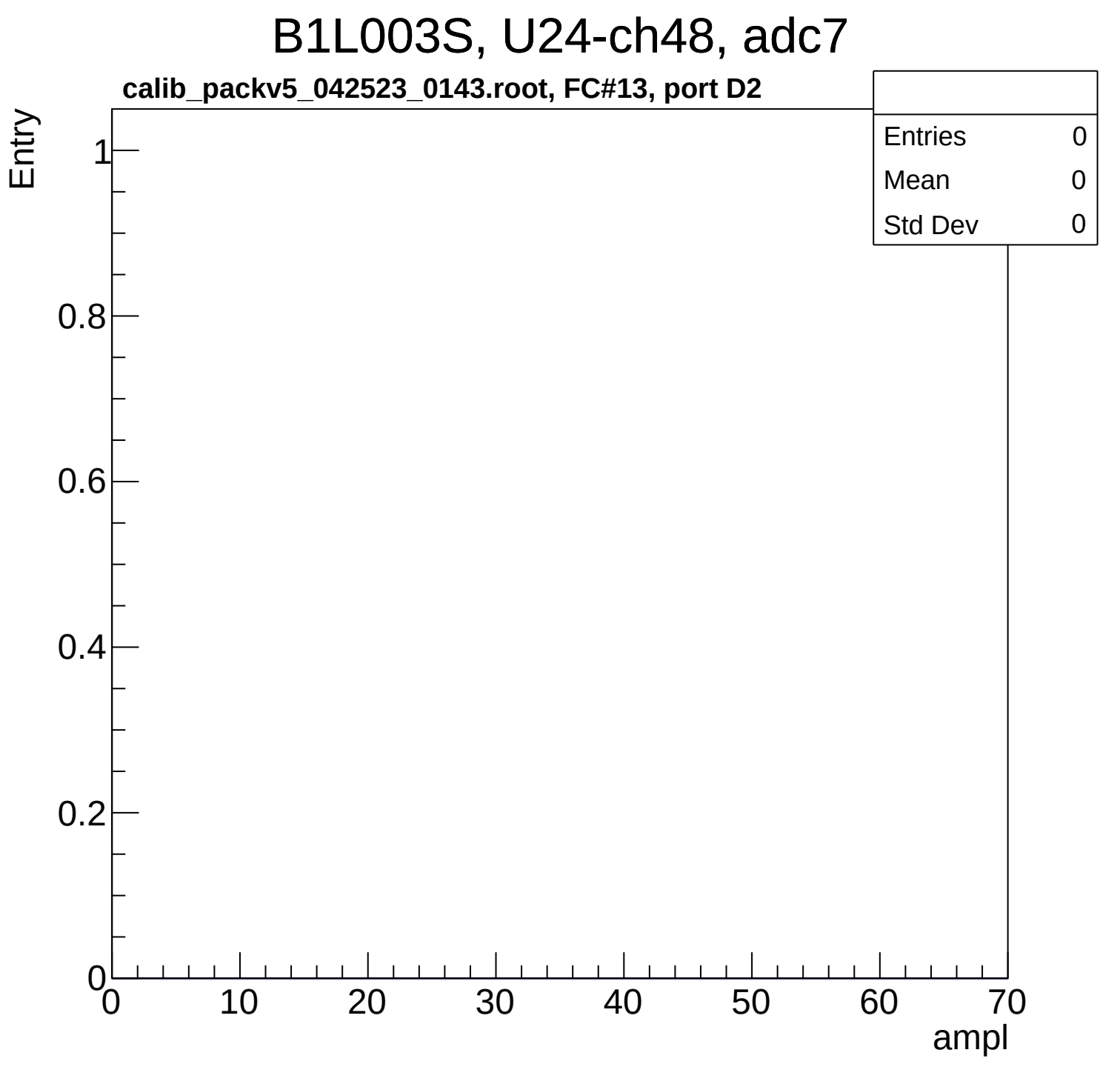
50
60
70
ampl


B1L003S, U24-ch48, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
30
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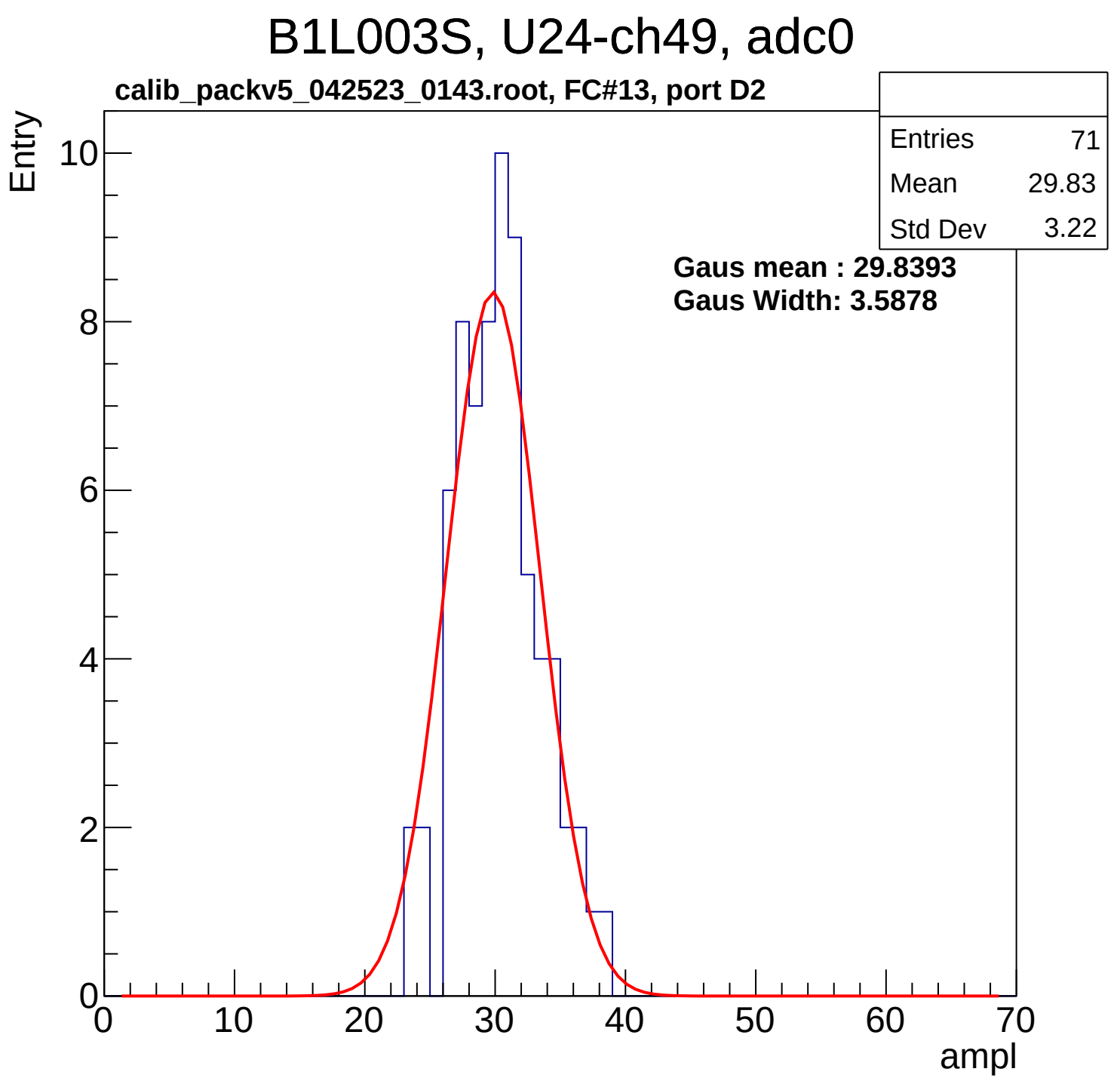
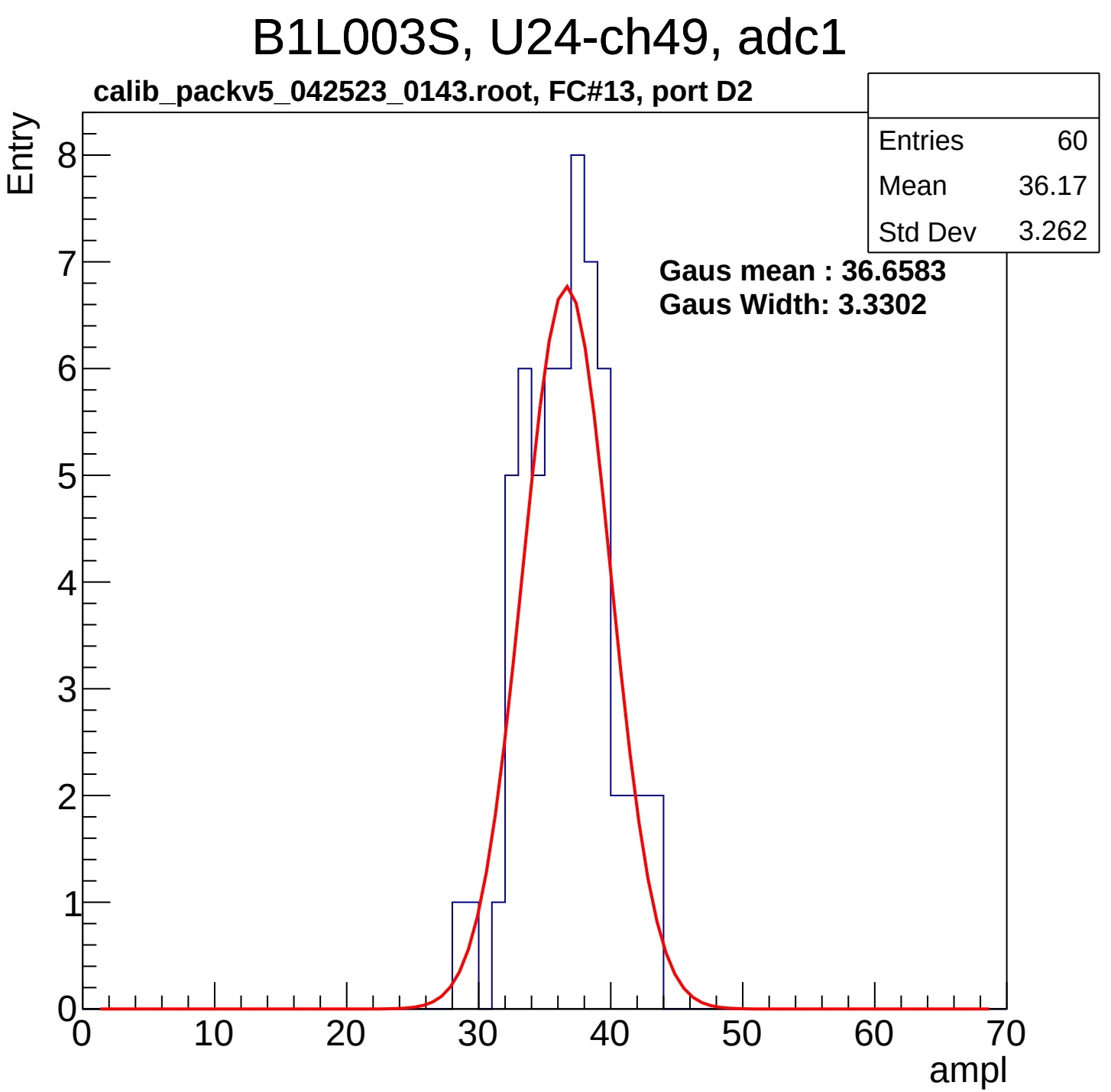
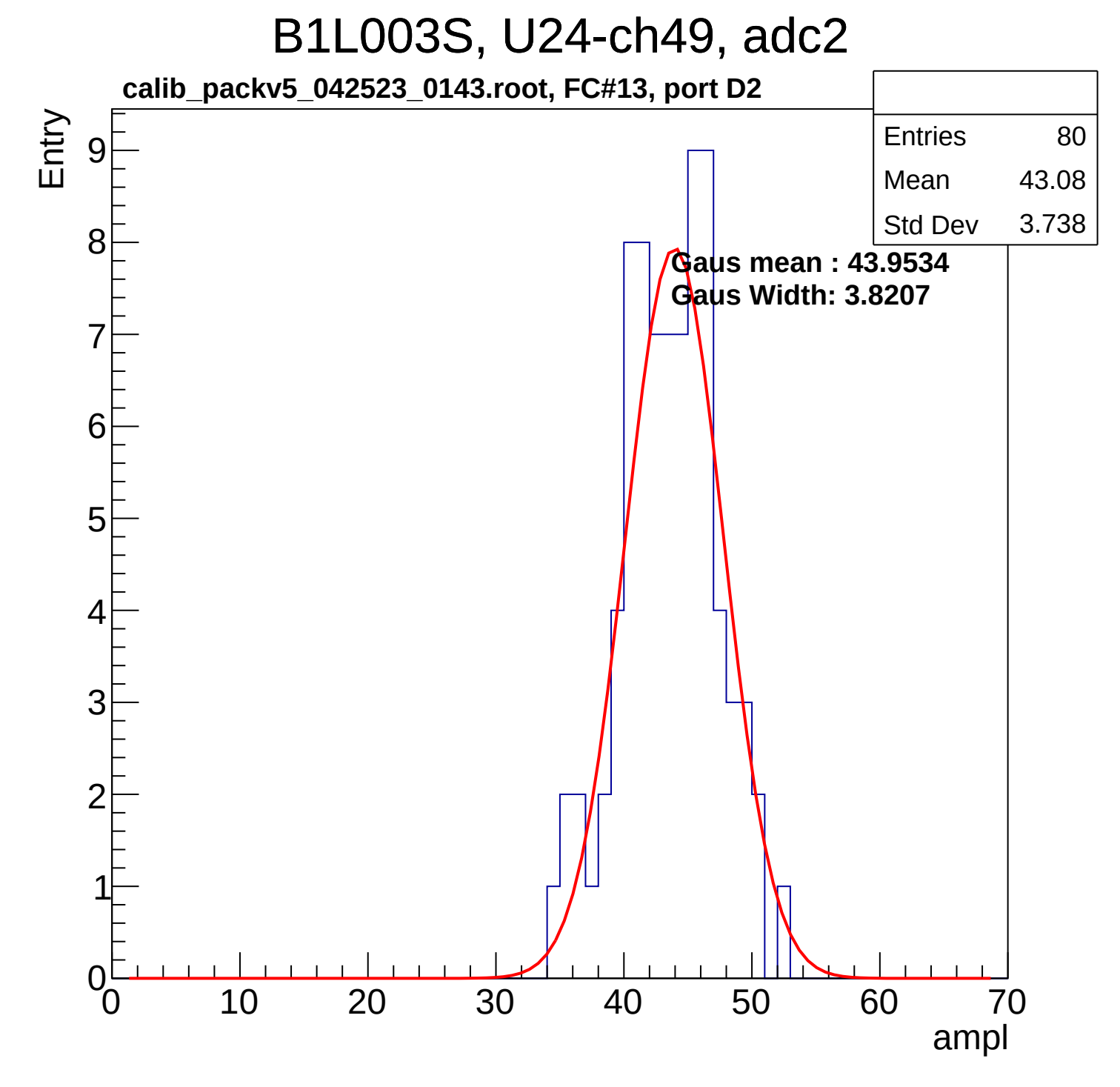
50
60
70
ampl


B1L003S, U24-ch49, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.83
Std Dev 3.22
Gaus mean : 29.8393
Gaus Width: 3.5878
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch49, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.17
Std Dev 3.262
Gaus mean : 36.6583
Gaus Width: 3.3302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch49, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 43.08
Std Dev 3.738
Gaus mean : 43.9534
Gaus Width: 3.8207
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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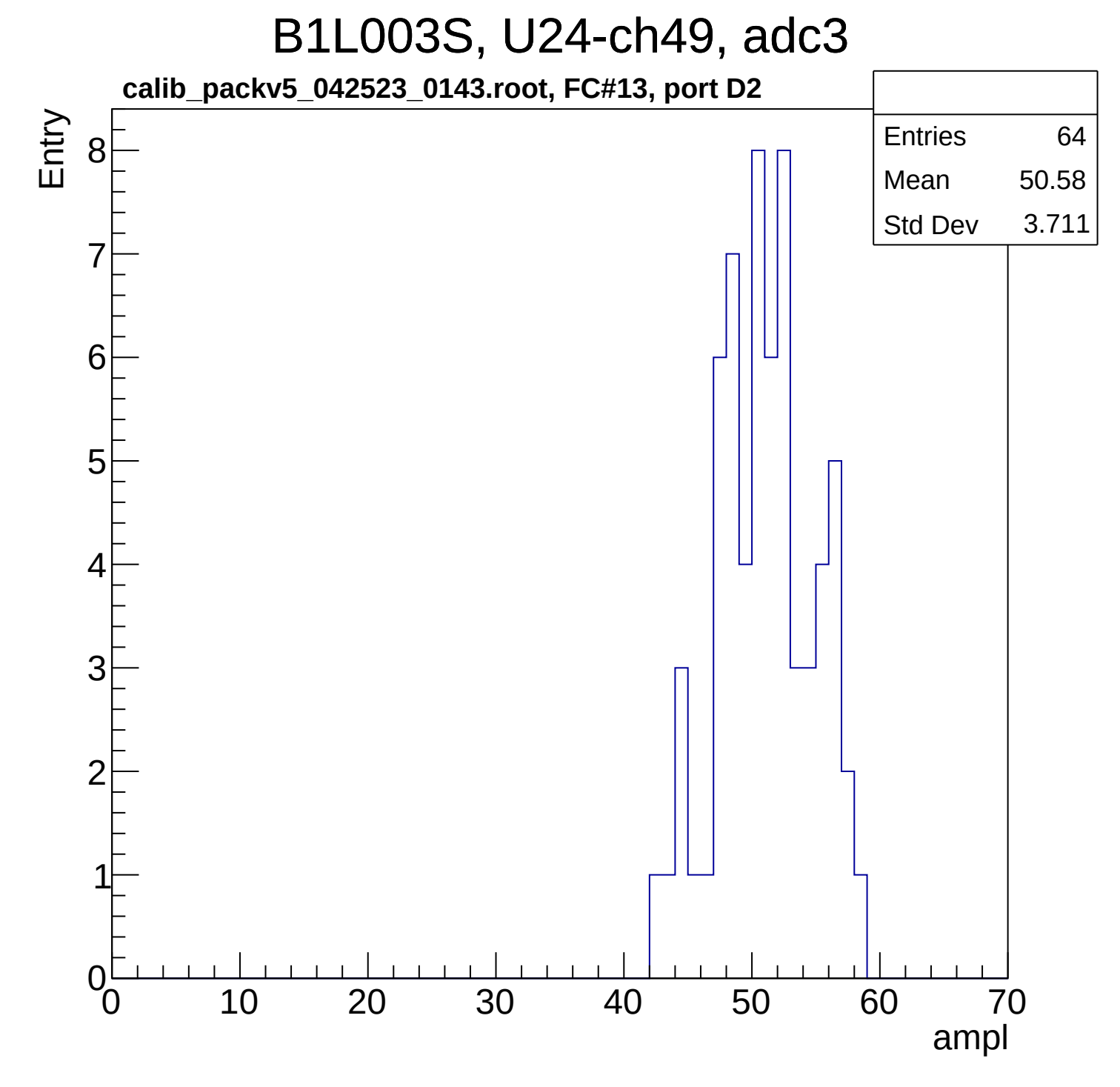
B1L003S, U24-ch49, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 50.58
Std Dev 3.711
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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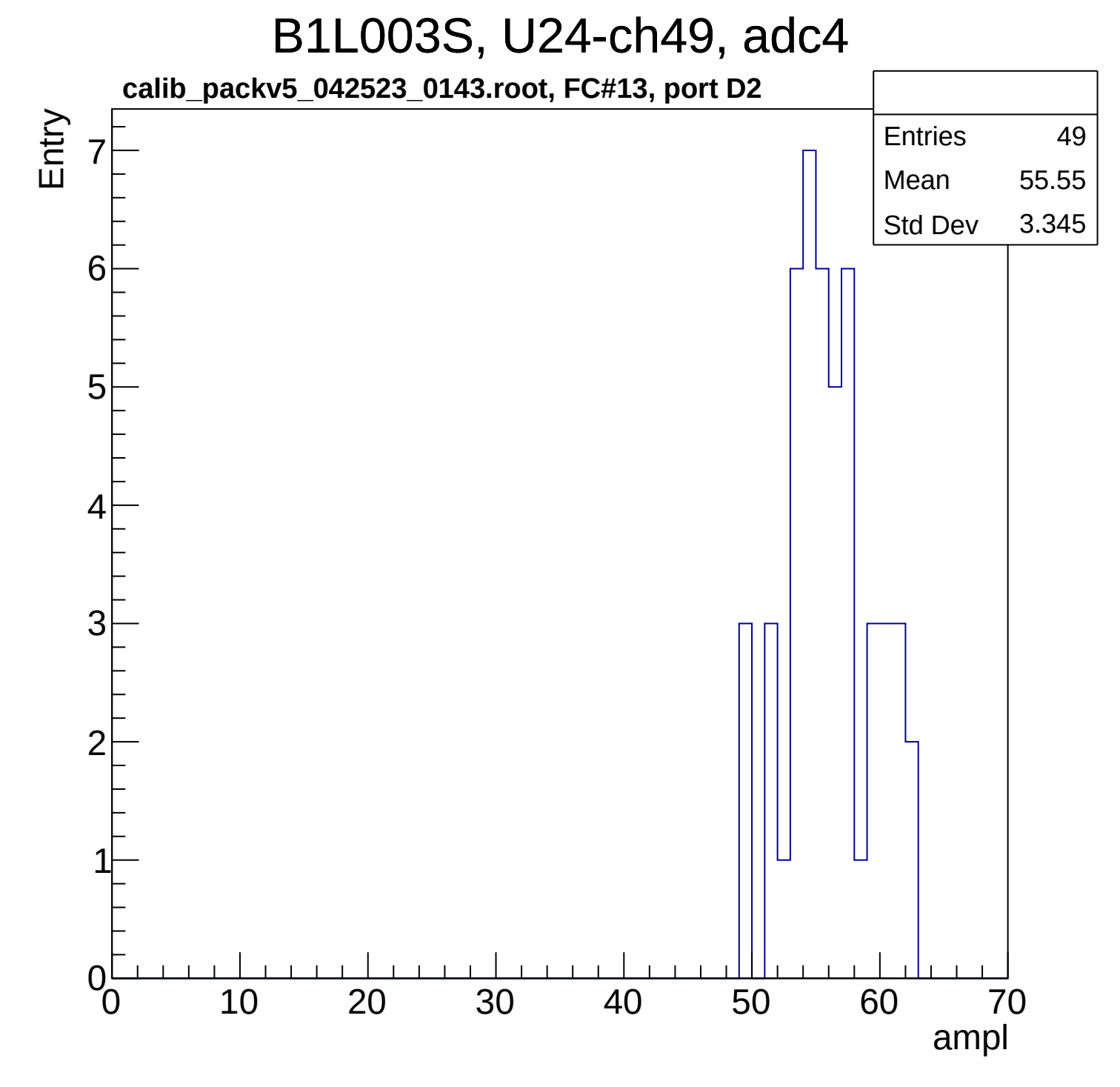
B1L003S, U24-ch49, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 55.55
Std Dev 3.345
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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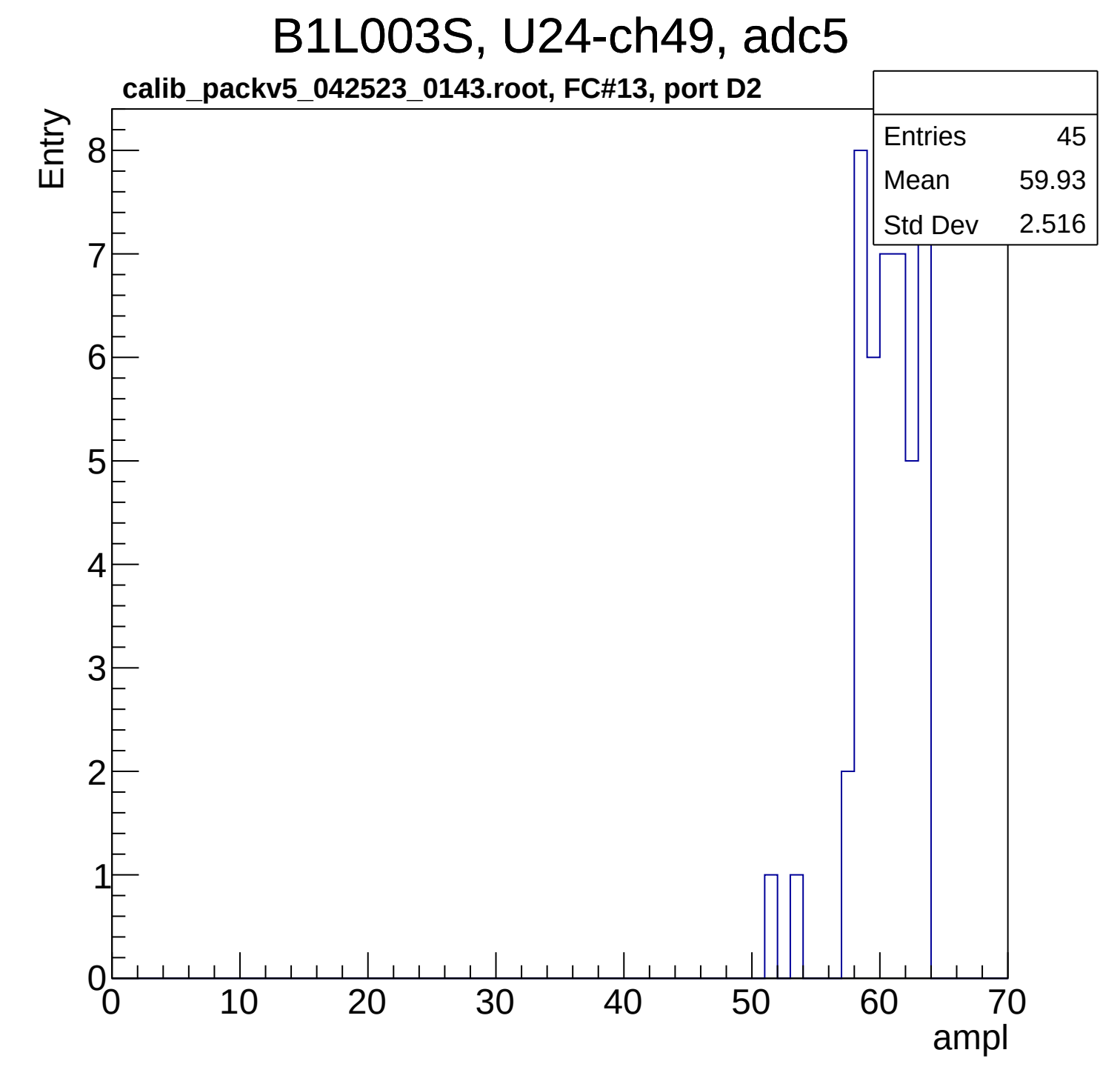
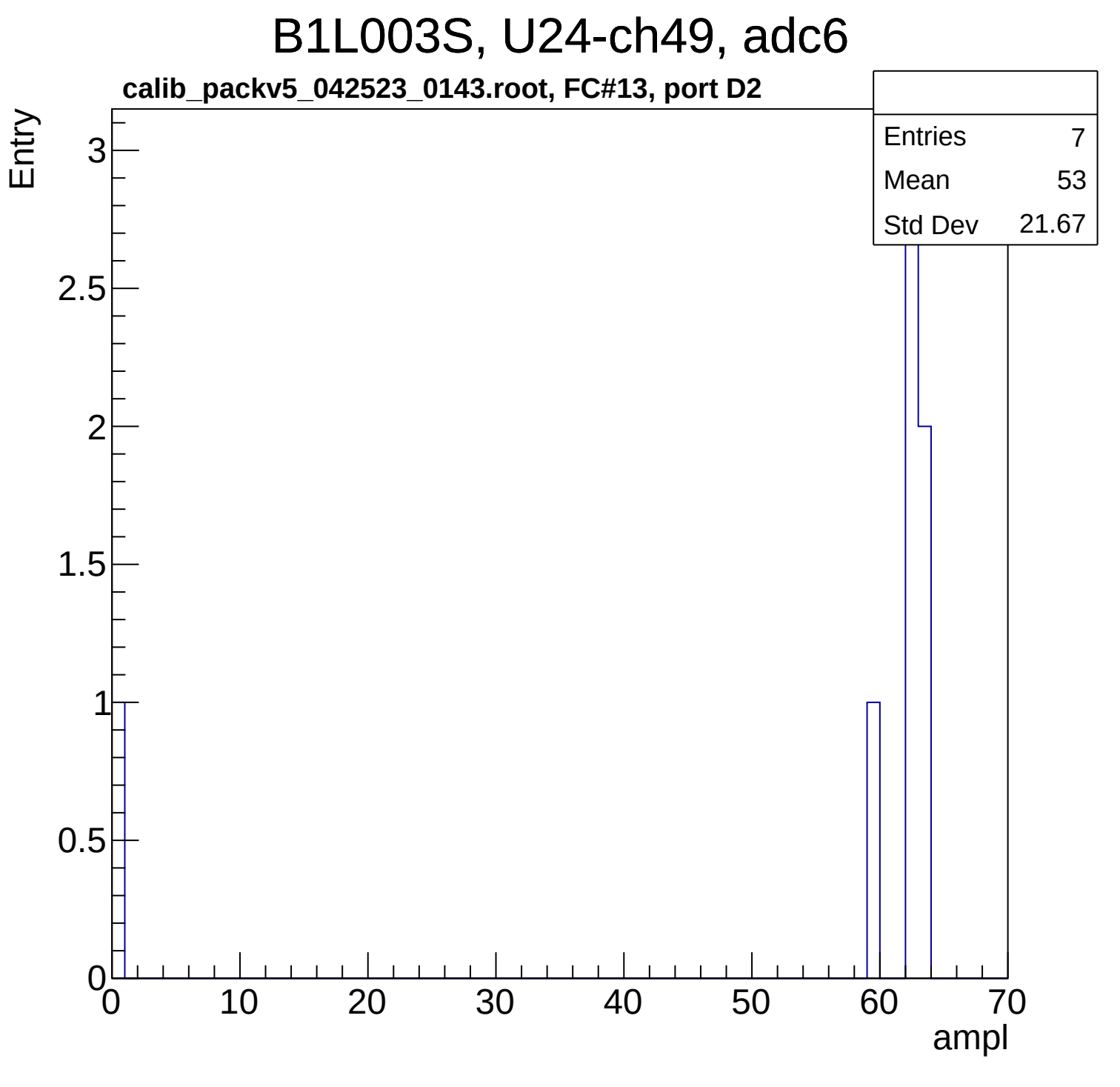
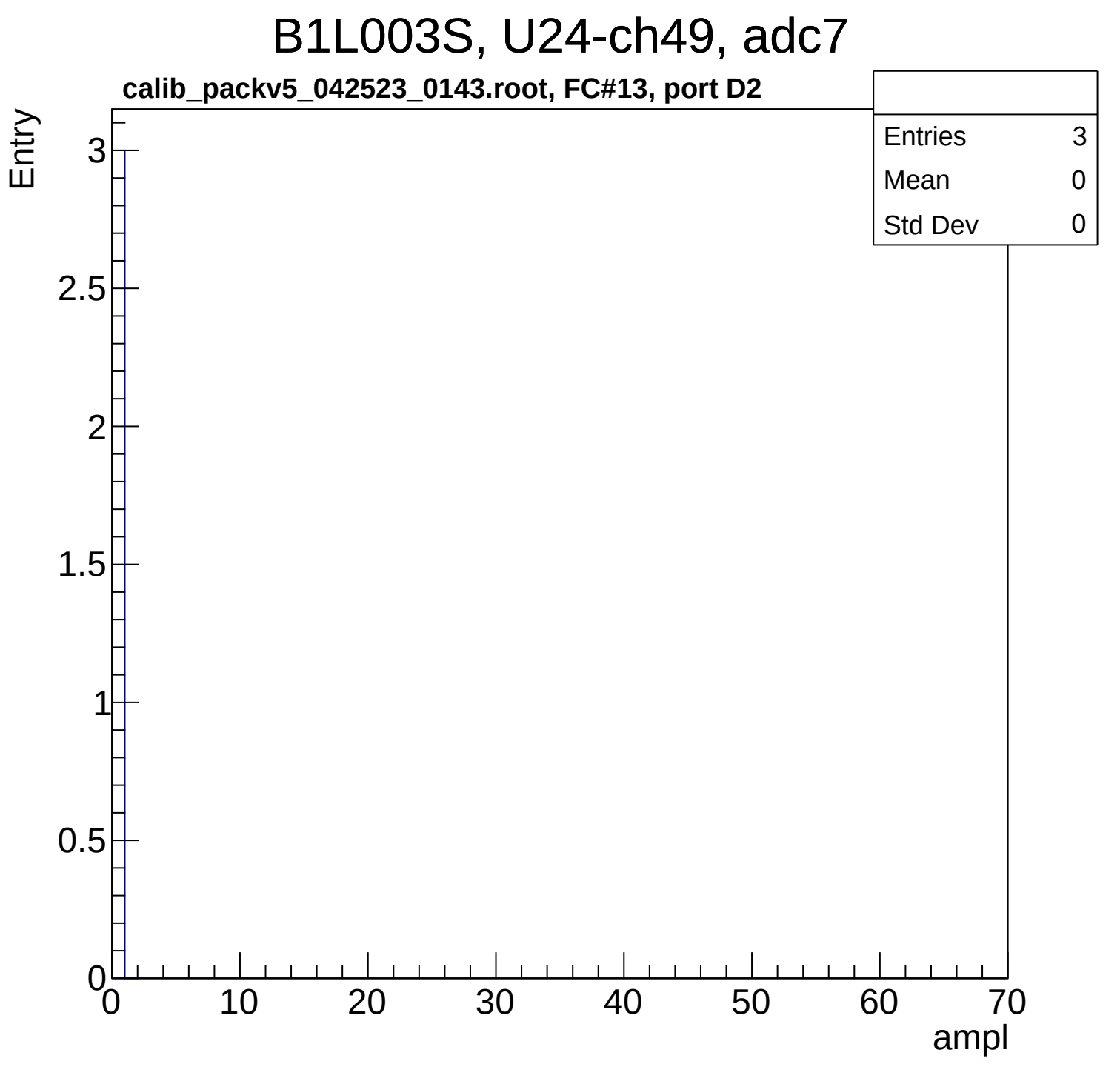

B1L003S, U24-ch49, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 59.93
Std Dev 2.516
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch49, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53
Std Dev 21.67
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch49, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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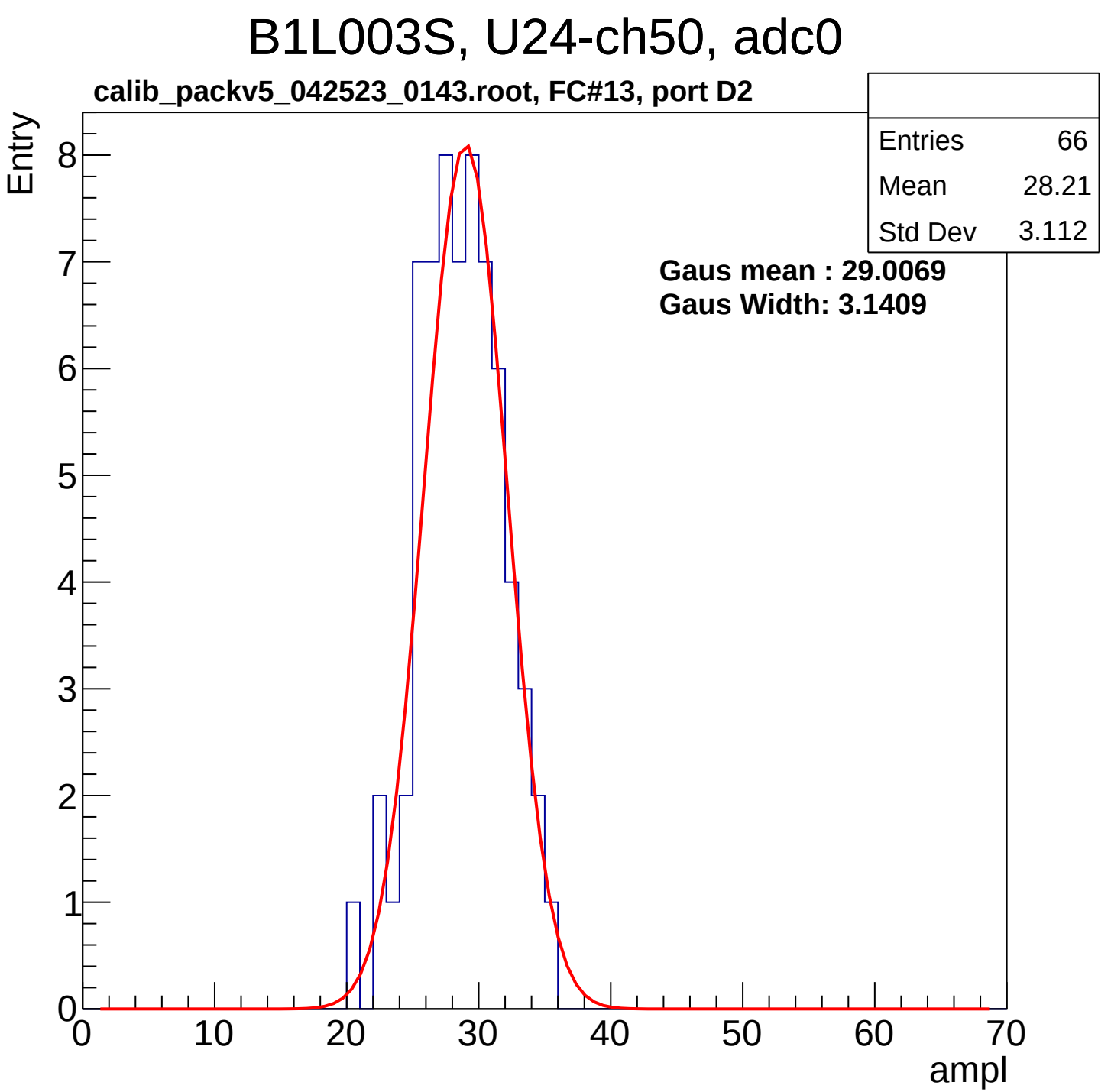
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch50, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.21
Std Dev 3.112
Gaus mean : 29.0069
Gaus Width: 3.1409
Entry
ampl
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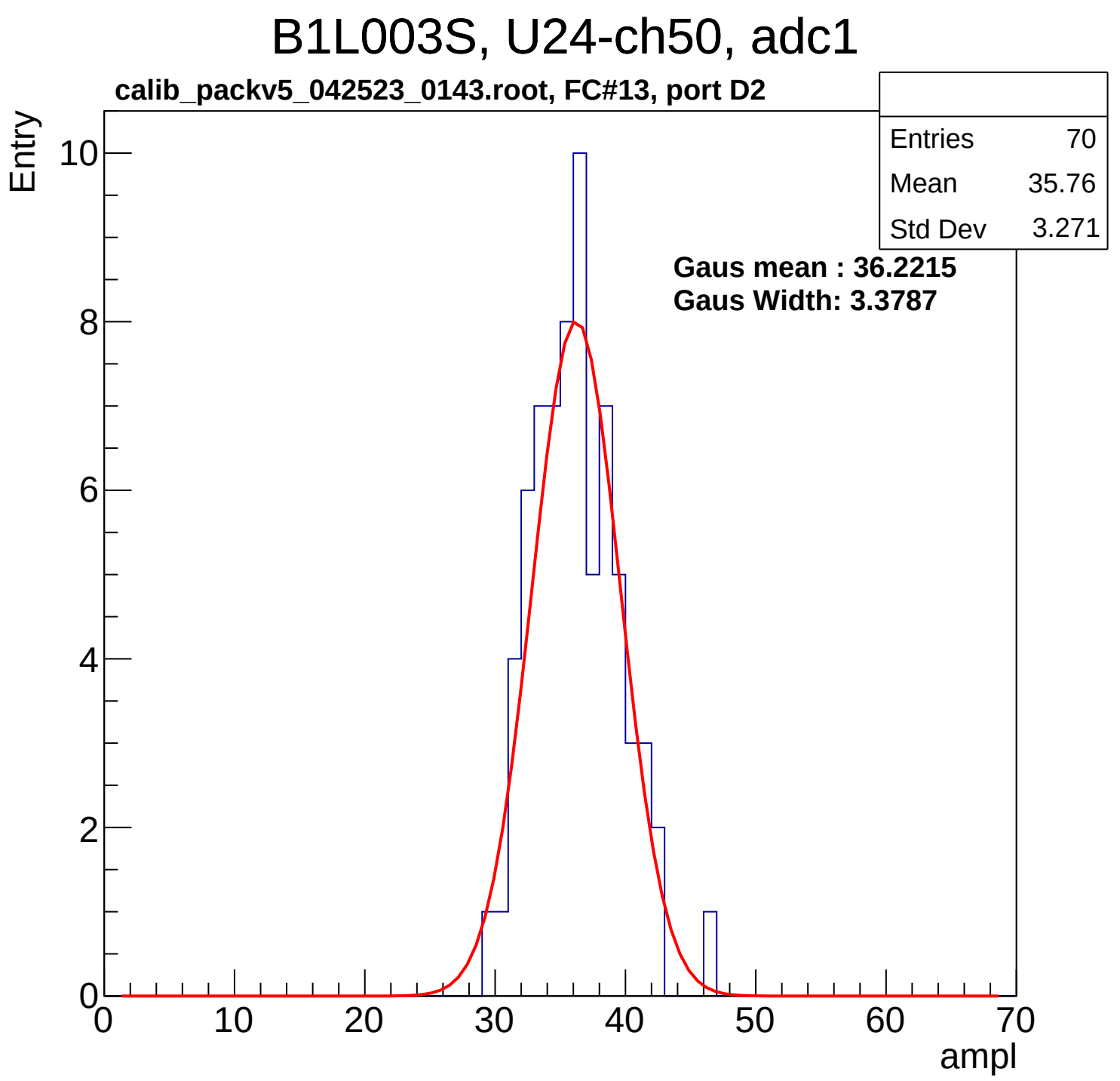
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch50, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 35.76
Std Dev 3.271
Gaus mean : 36.2215
Gaus Width: 3.3787
Entry
ampl
0
2
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60
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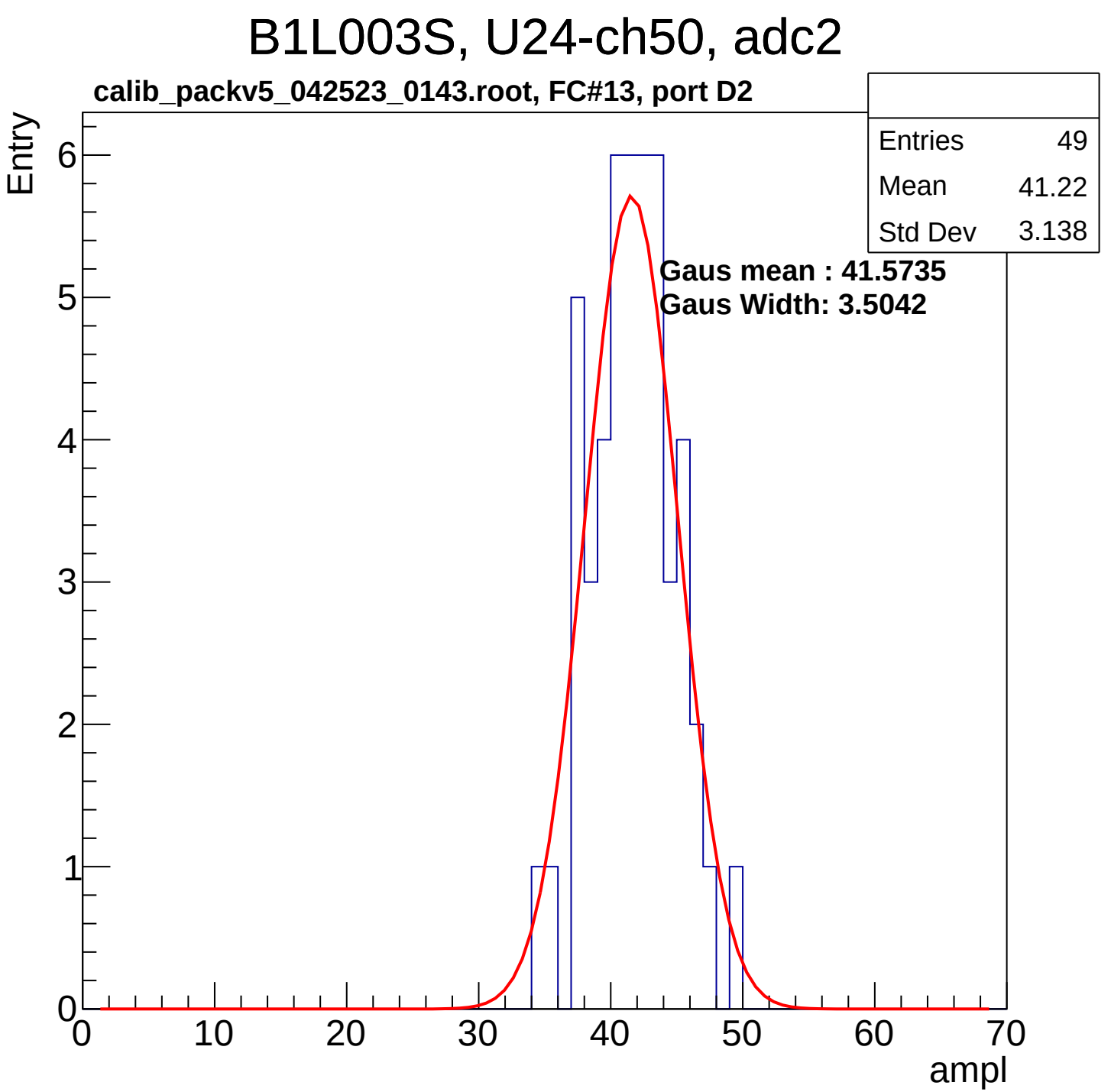
B1L003S, U24-ch50, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 41.22
Std Dev 3.138
Gaus mean : 41.5735
Gaus Width: 3.5042
Entry
ampl
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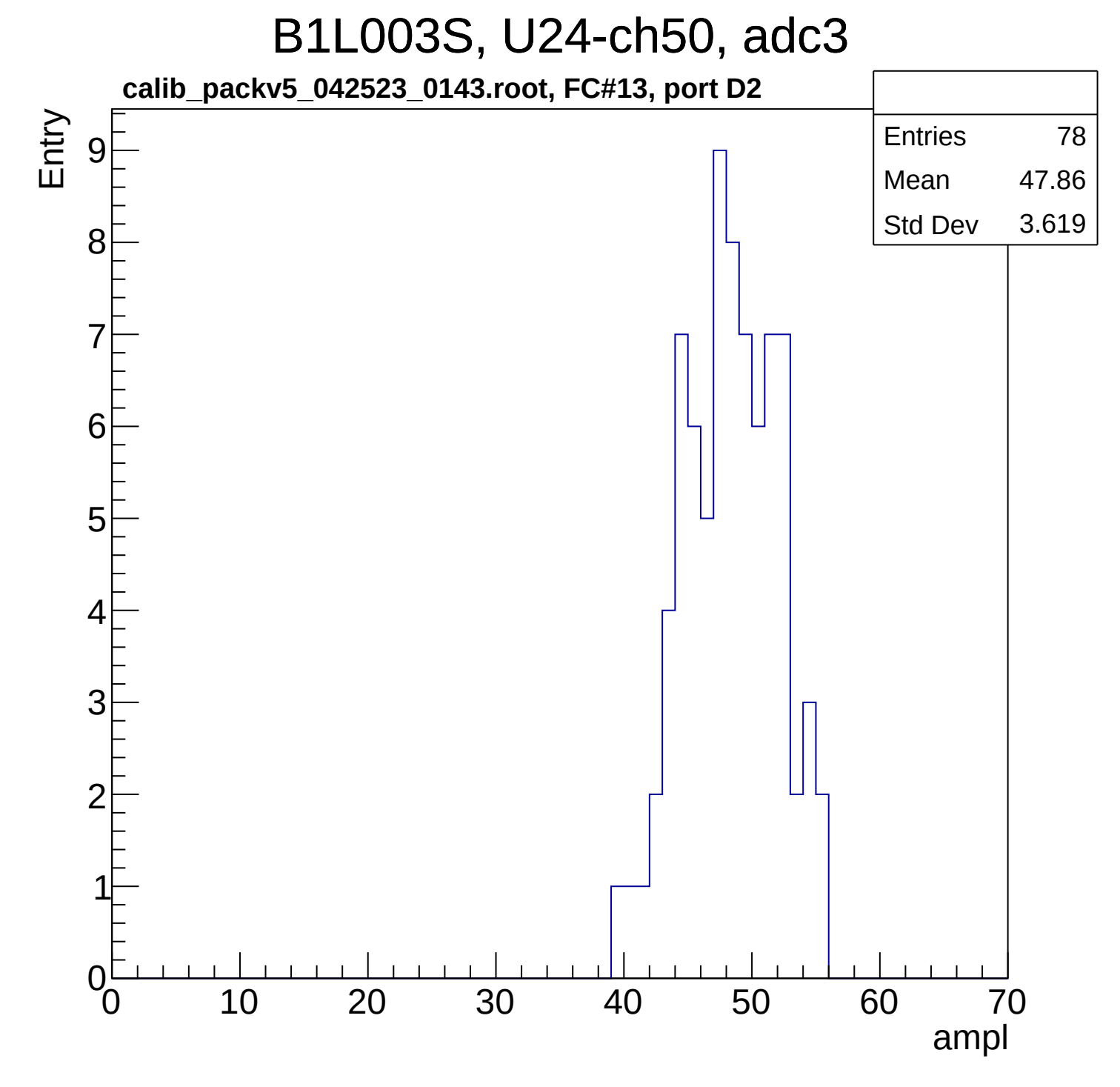
B1L003S, U24-ch50, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 47.86
Std Dev 3.619
Entry
ampl
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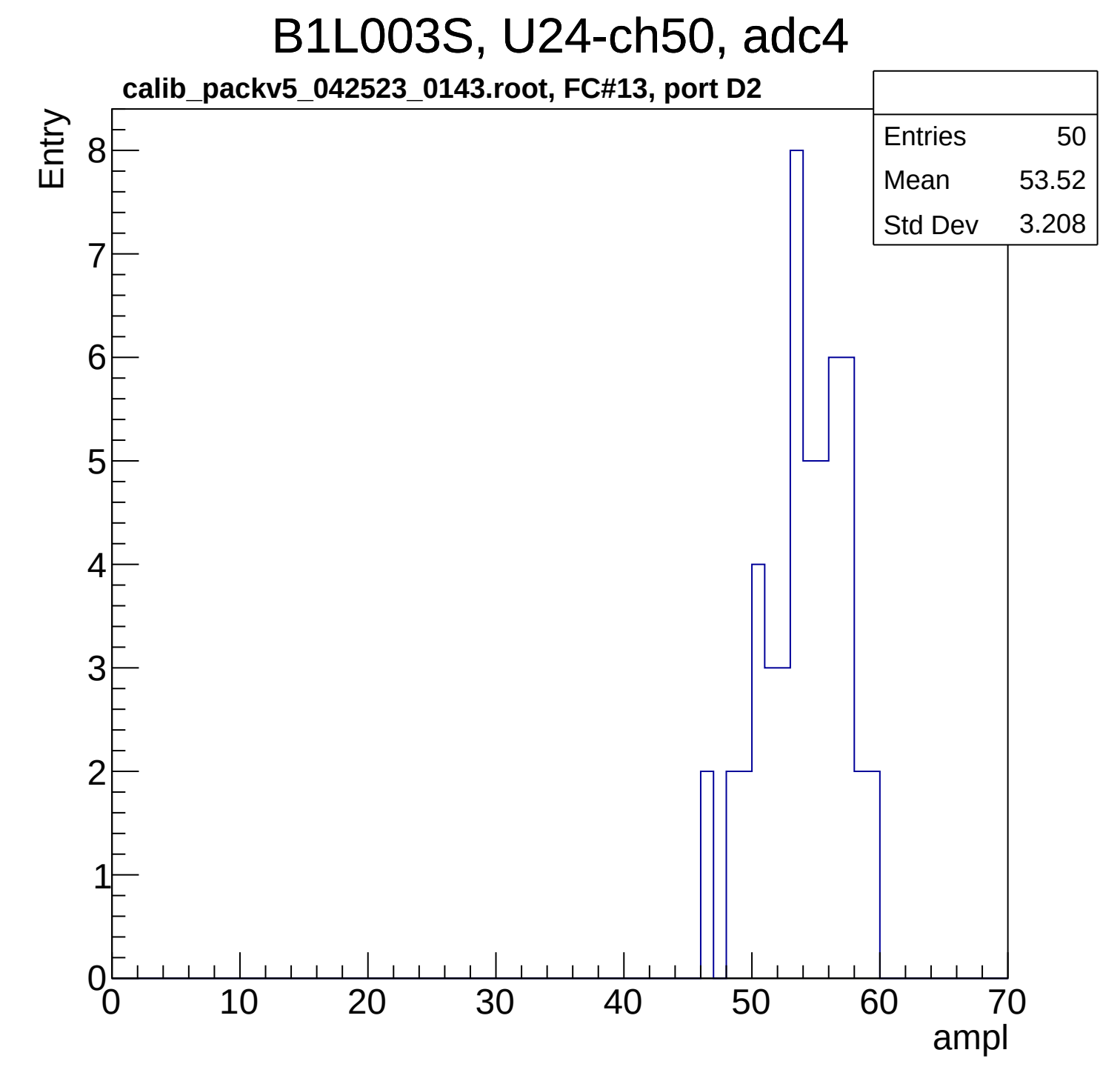
B1L003S, U24-ch50, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 53.52
Std Dev 3.208
Entry
ampl
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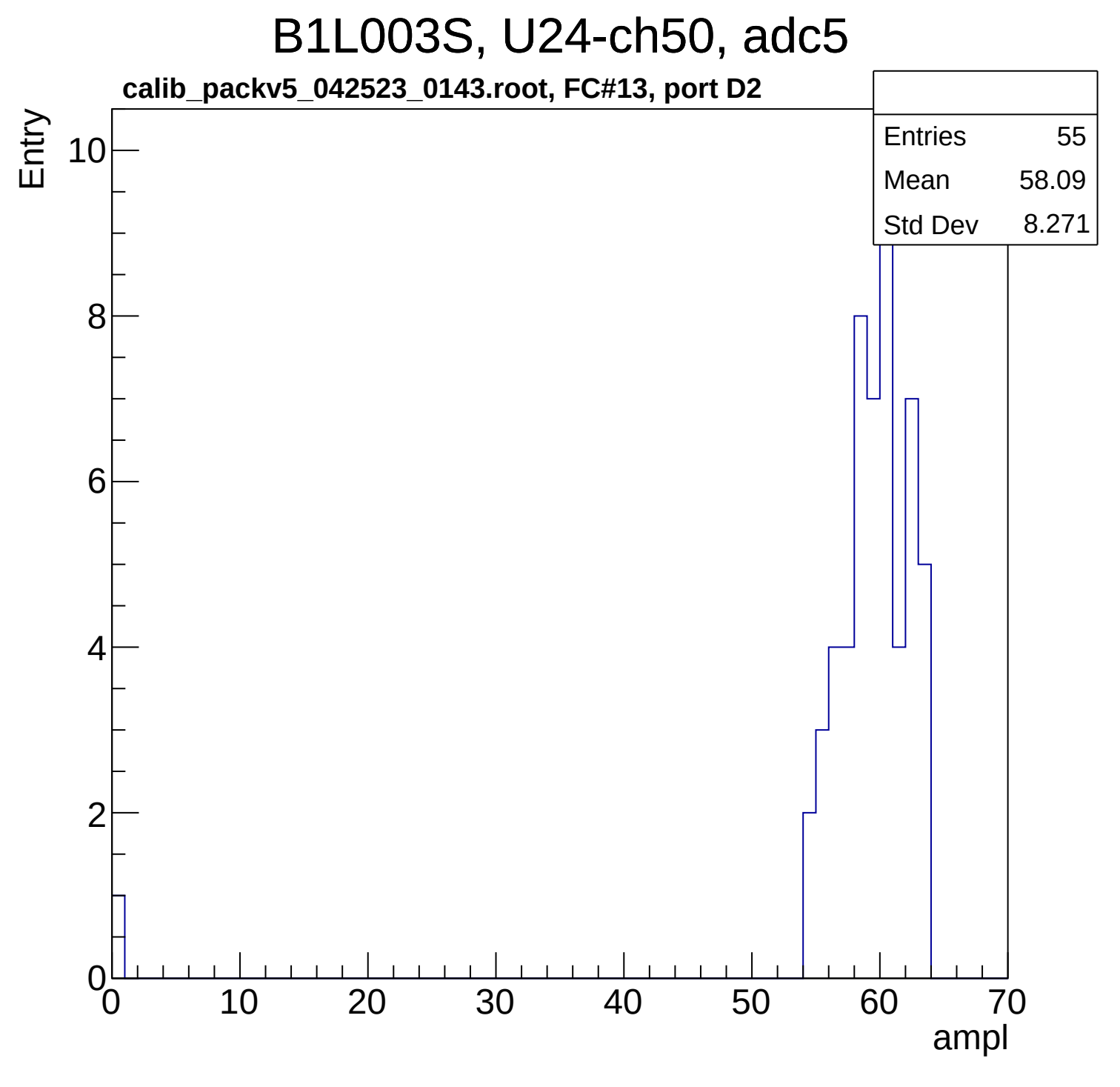
B1L003S, U24-ch50, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.09
Std Dev 8.271
Entry
ampl
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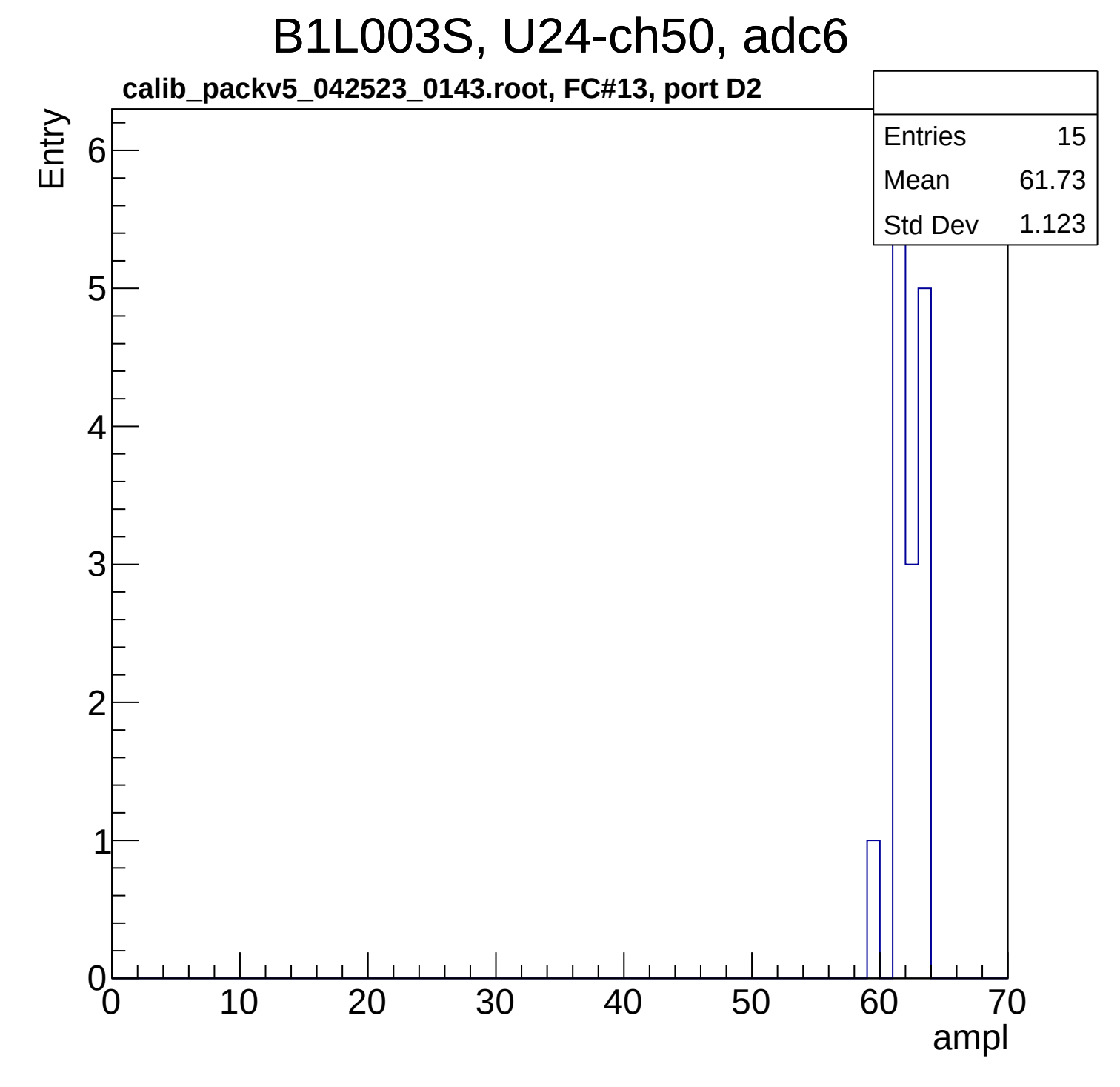
B1L003S, U24-ch50, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.73
Std Dev 1.123
Entry
ampl
0
1
2
3
4
5
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0
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60
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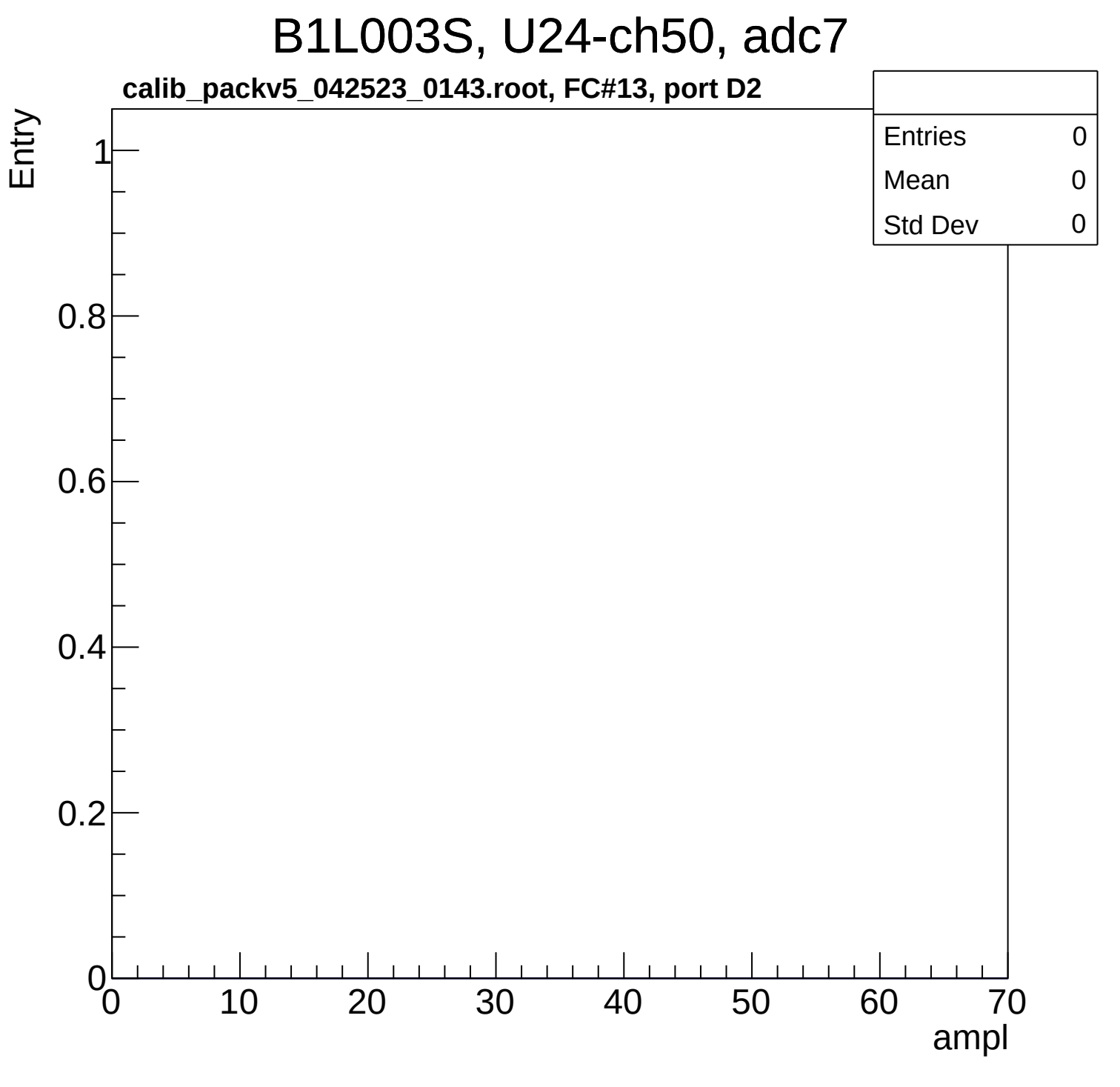
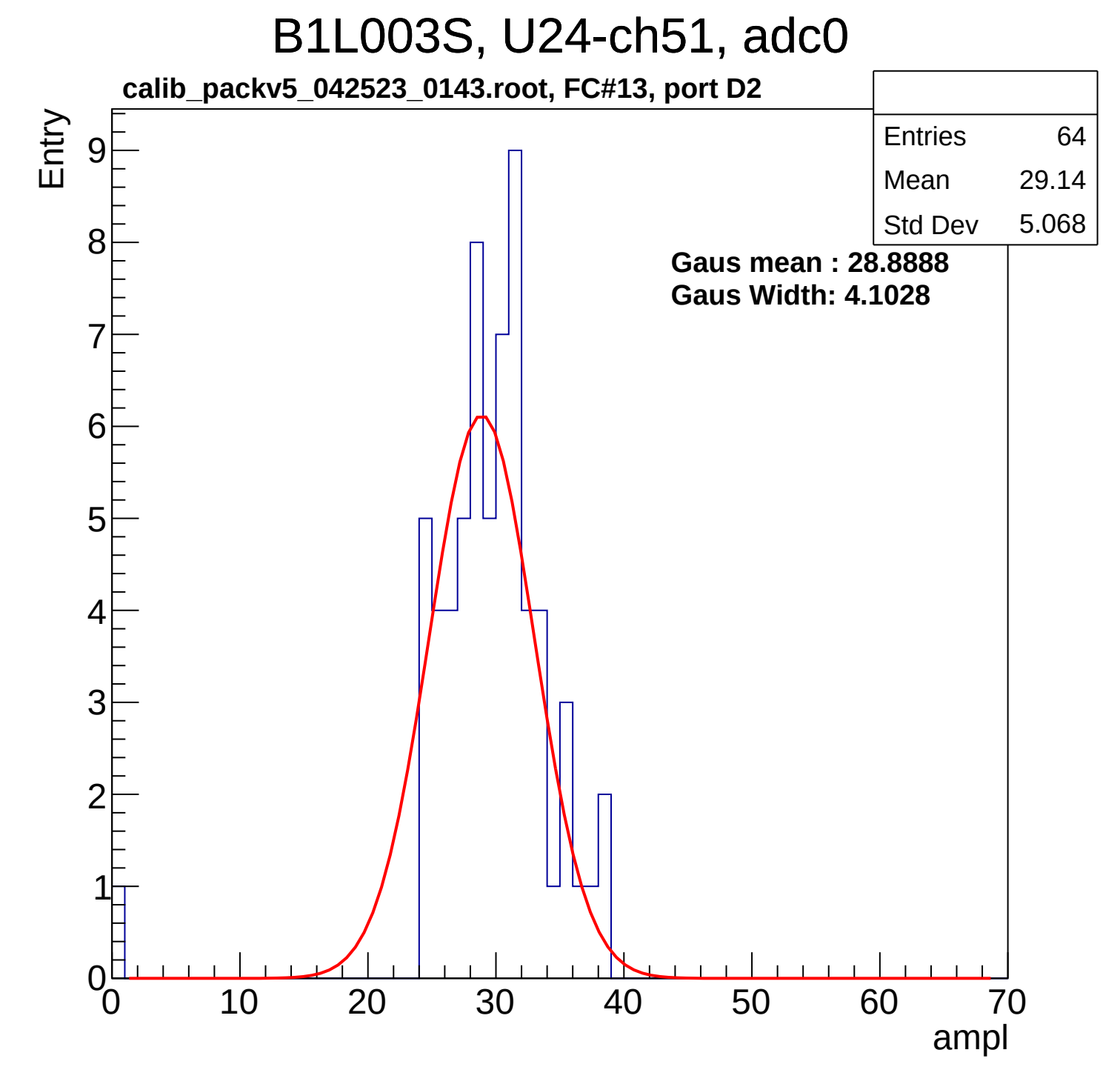

B1L003S, U24-ch50, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch51, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.14
Std Dev 5.068
Gaus mean : 28.8888
Gaus Width: 4.1028
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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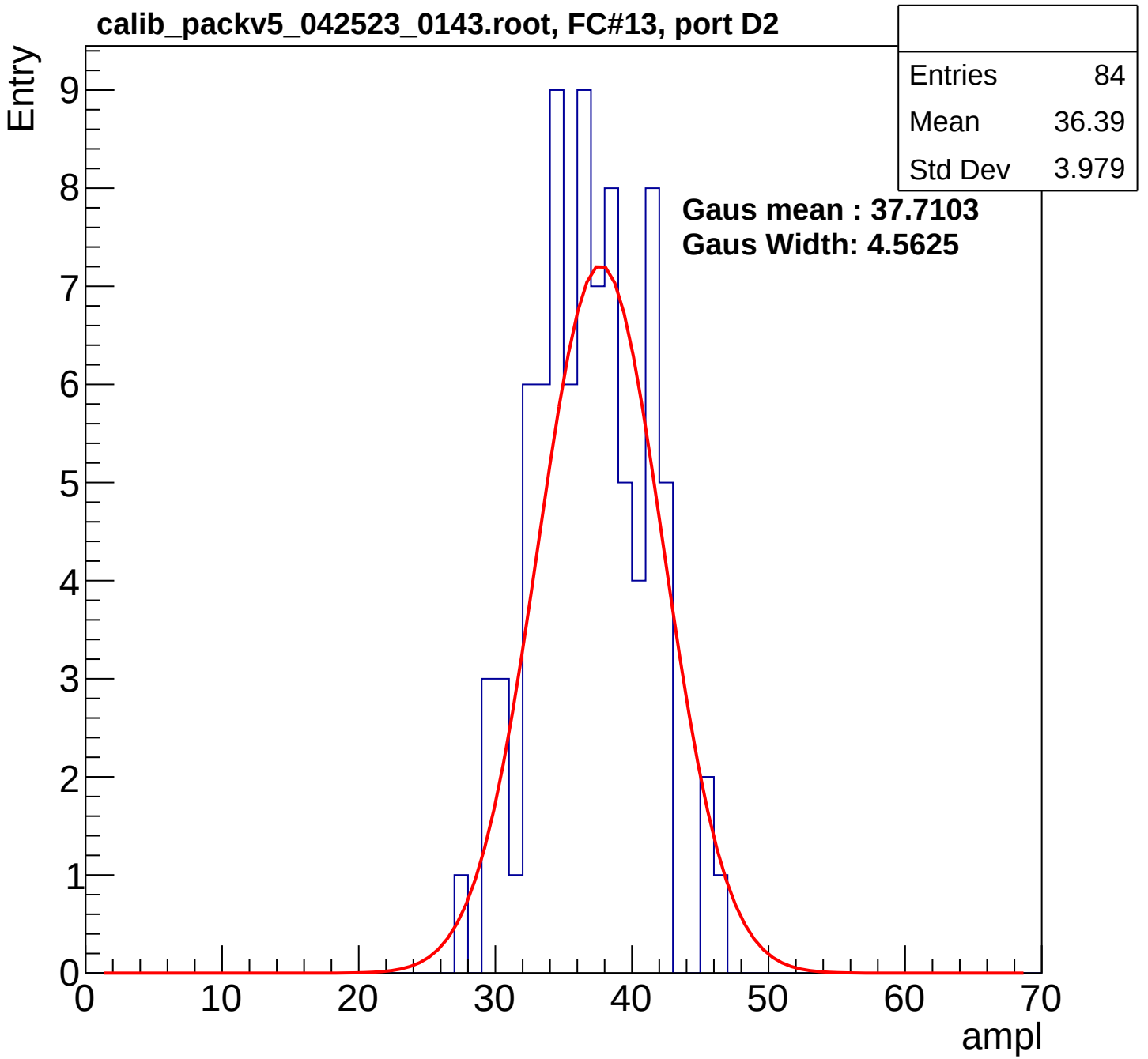
B1L003S, U24-ch51, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 36.39
Std Dev 3.979
Gaus mean : 37.7103
Gaus Width: 4.5625
Entry
ampl
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3
4
5
6
7
8
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0
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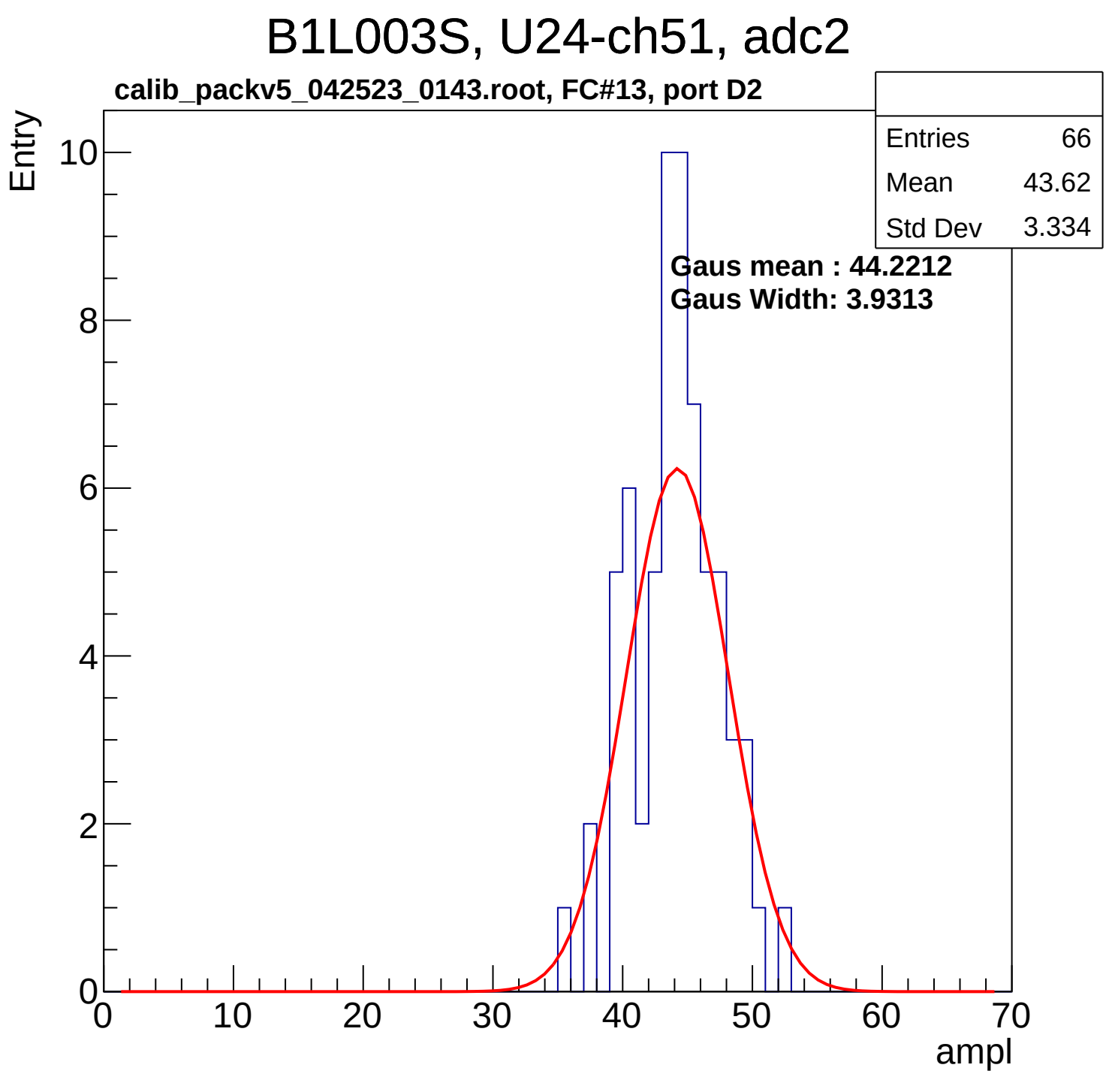
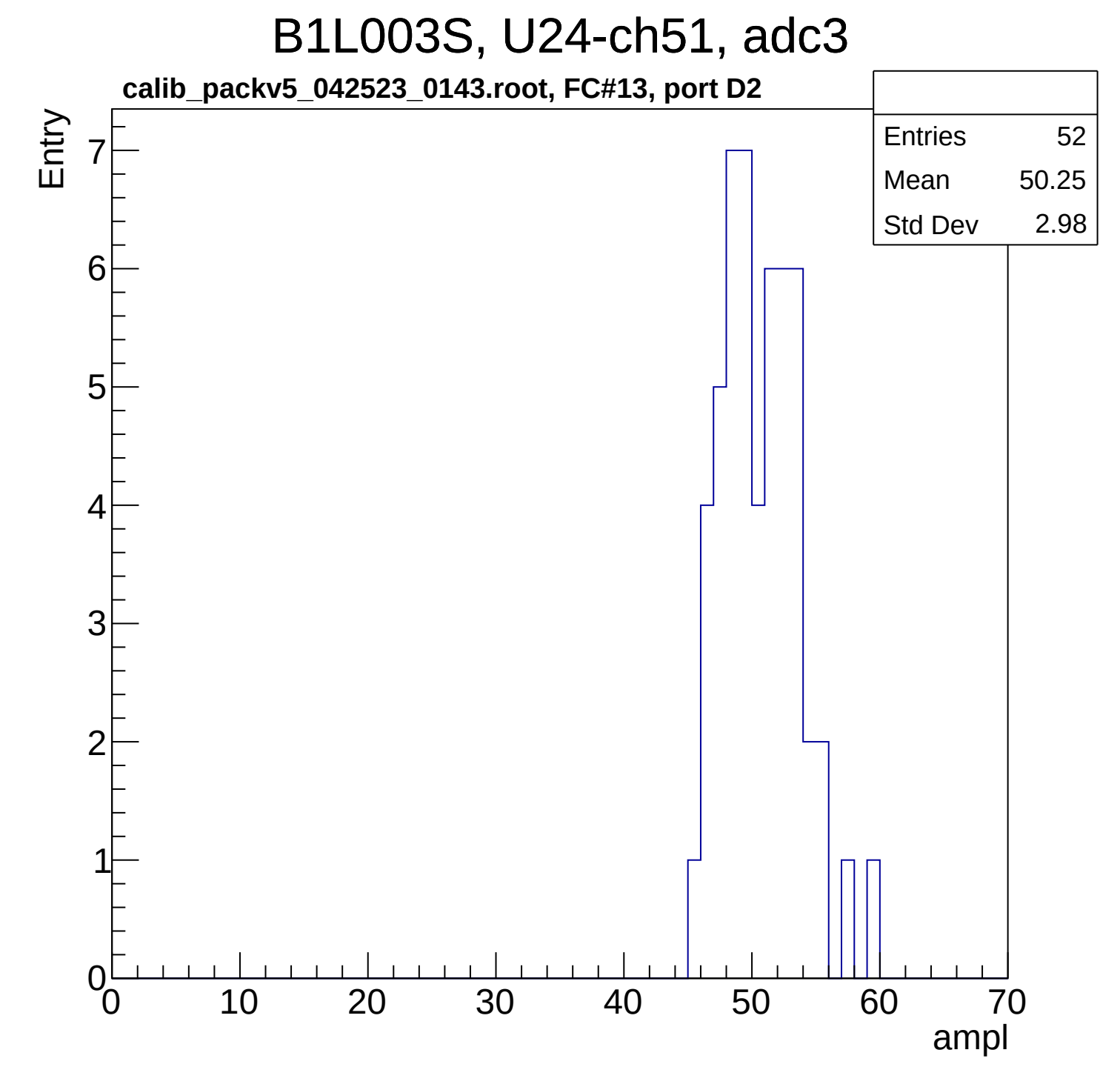
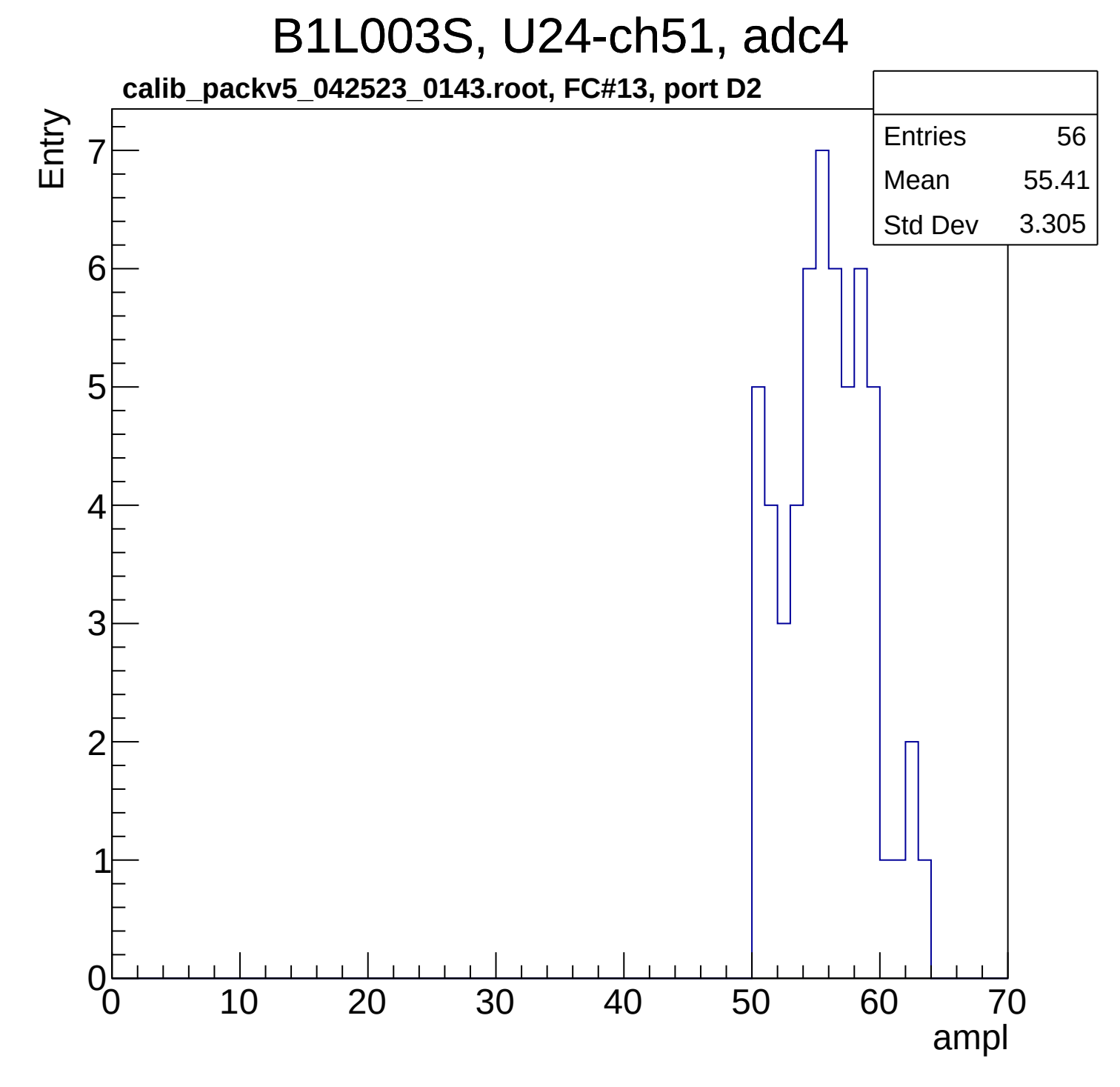

B1L003S, U24-ch51, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.62
Std Dev 3.334
Gaus mean : 44.2212
Gaus Width: 3.9313
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch51, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 50.25
Std Dev 2.98
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch51, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 55.41
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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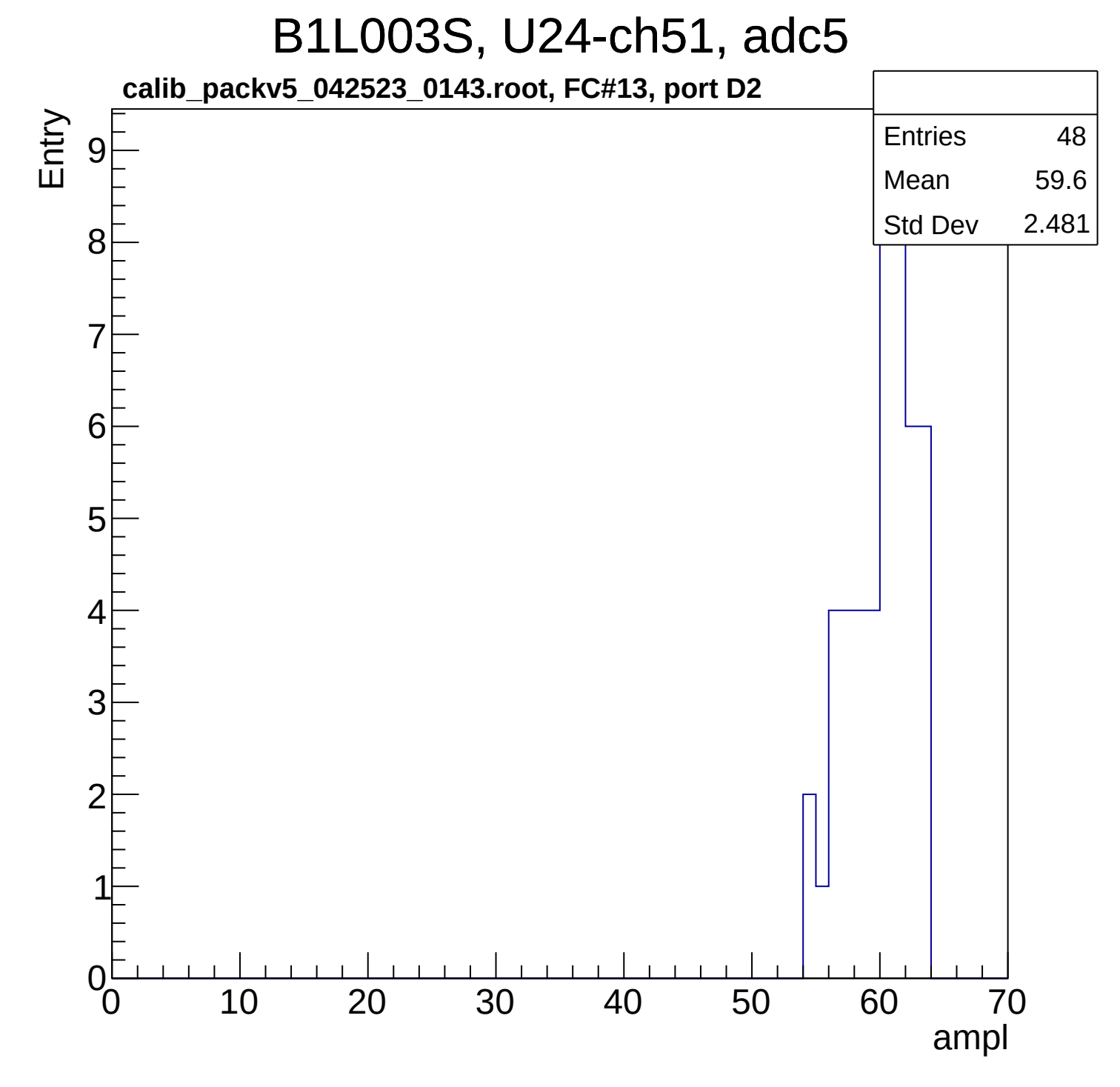
B1L003S, U24-ch51, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.6
Std Dev 2.481
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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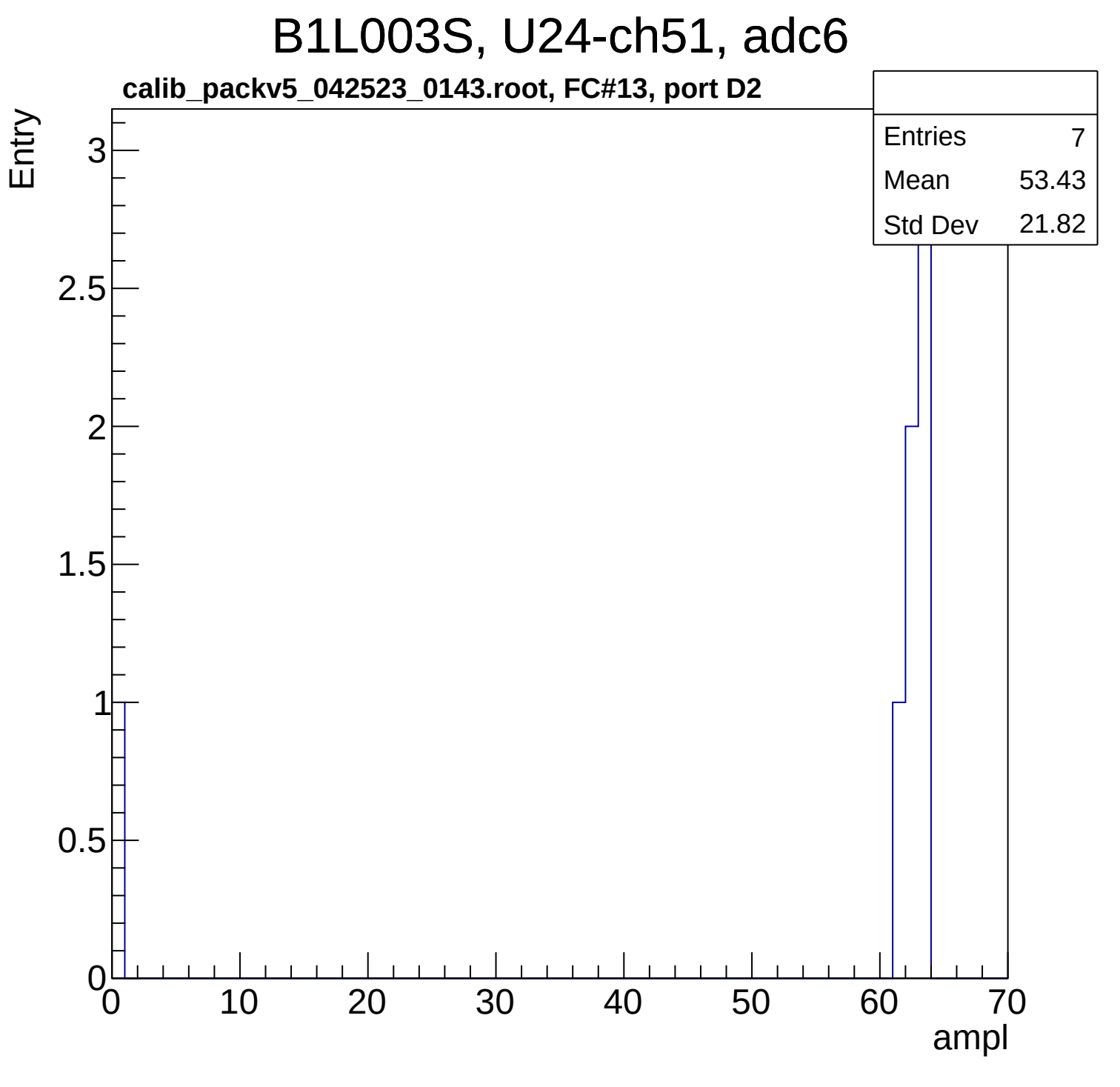
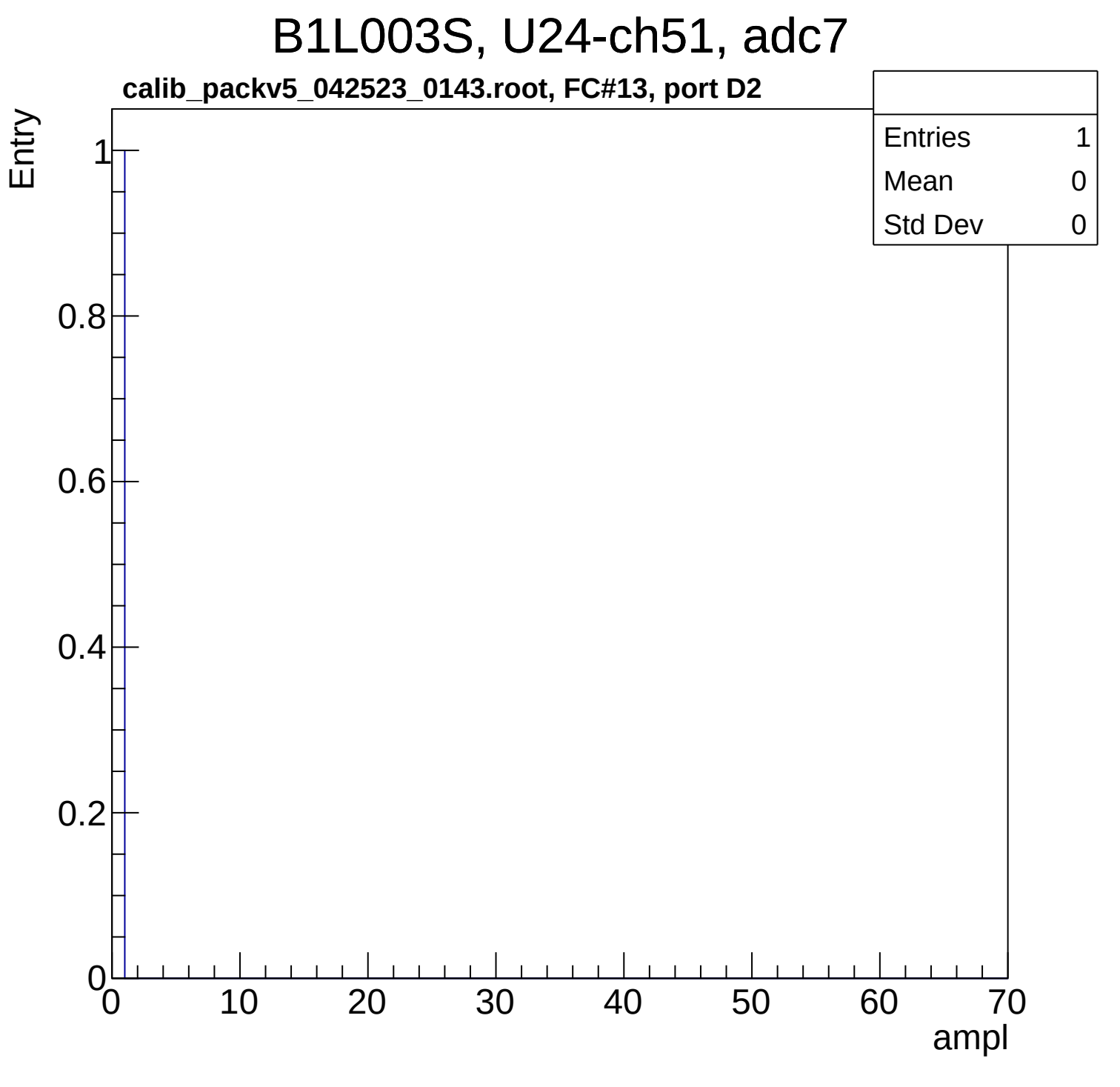
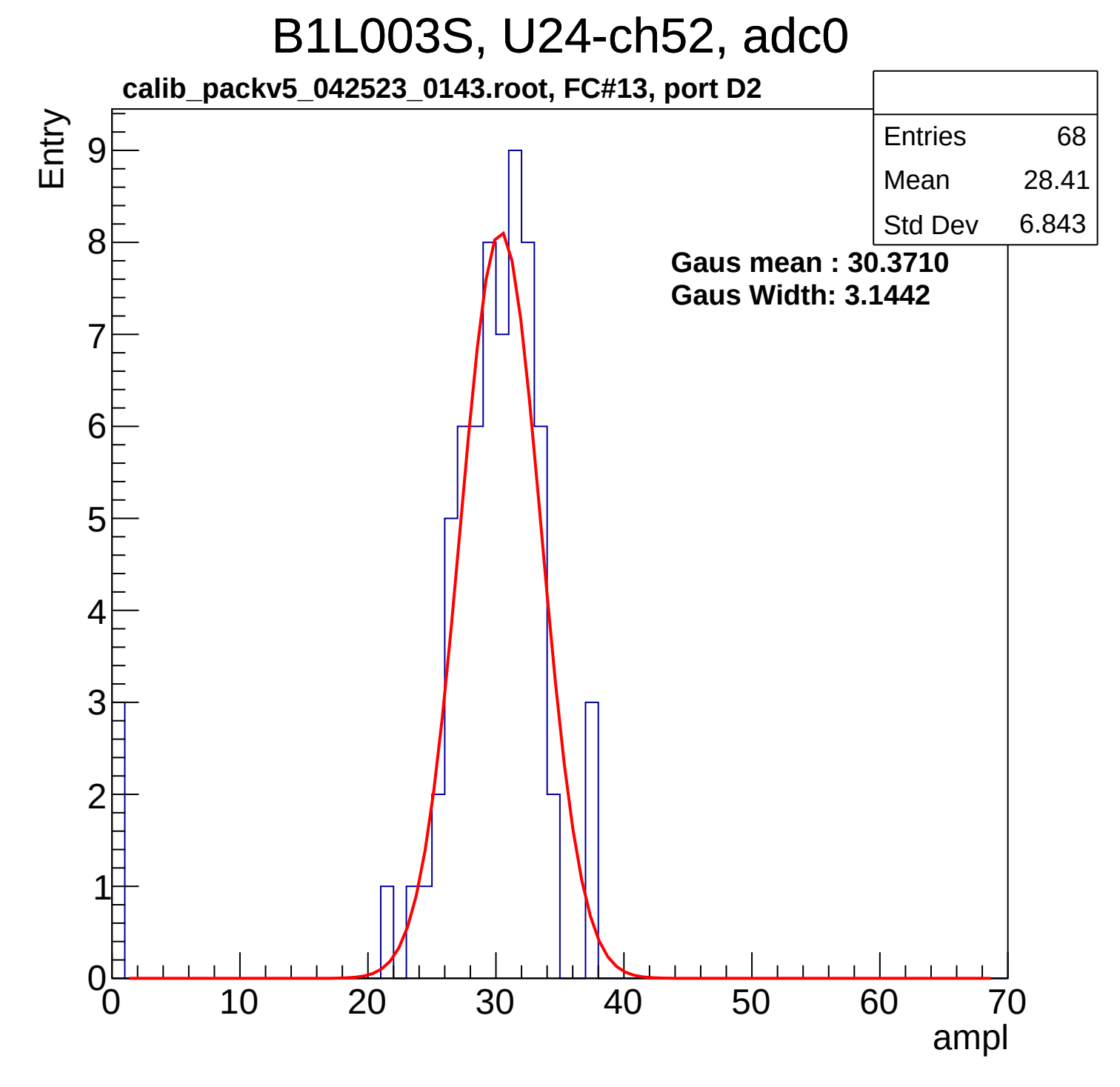

B1L003S, U24-ch51, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.43
Std Dev 21.82
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch51, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch52, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.41
Std Dev 6.843
Gaus mean : 30.3710
Gaus Width: 3.1442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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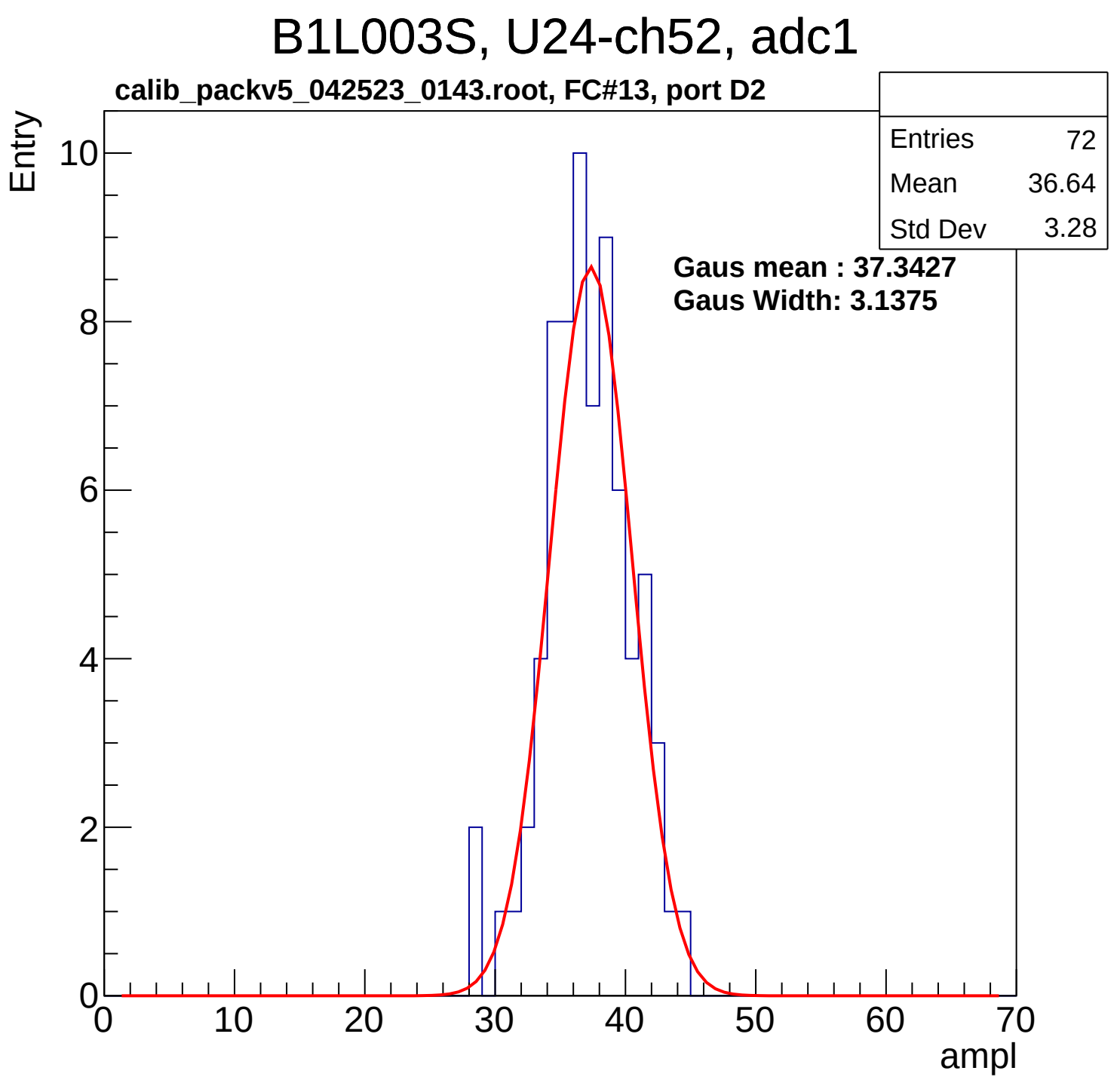
B1L003S, U24-ch52, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.64
Std Dev 3.28
Gaus mean : 37.3427
Gaus Width: 3.1375
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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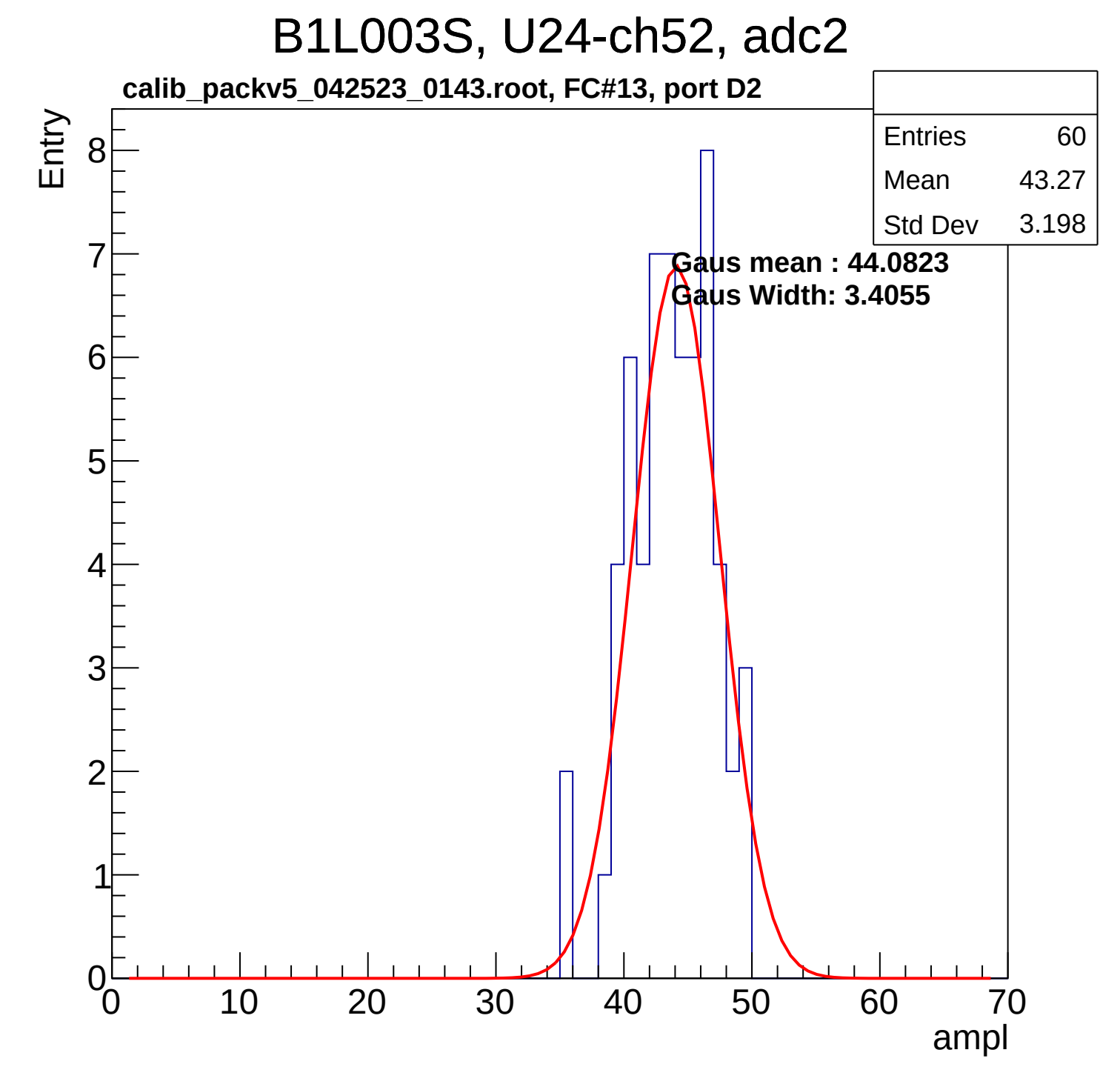
B1L003S, U24-ch52, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.27
Std Dev 3.198
Gaus mean : 44.0823
Gaus Width: 3.4055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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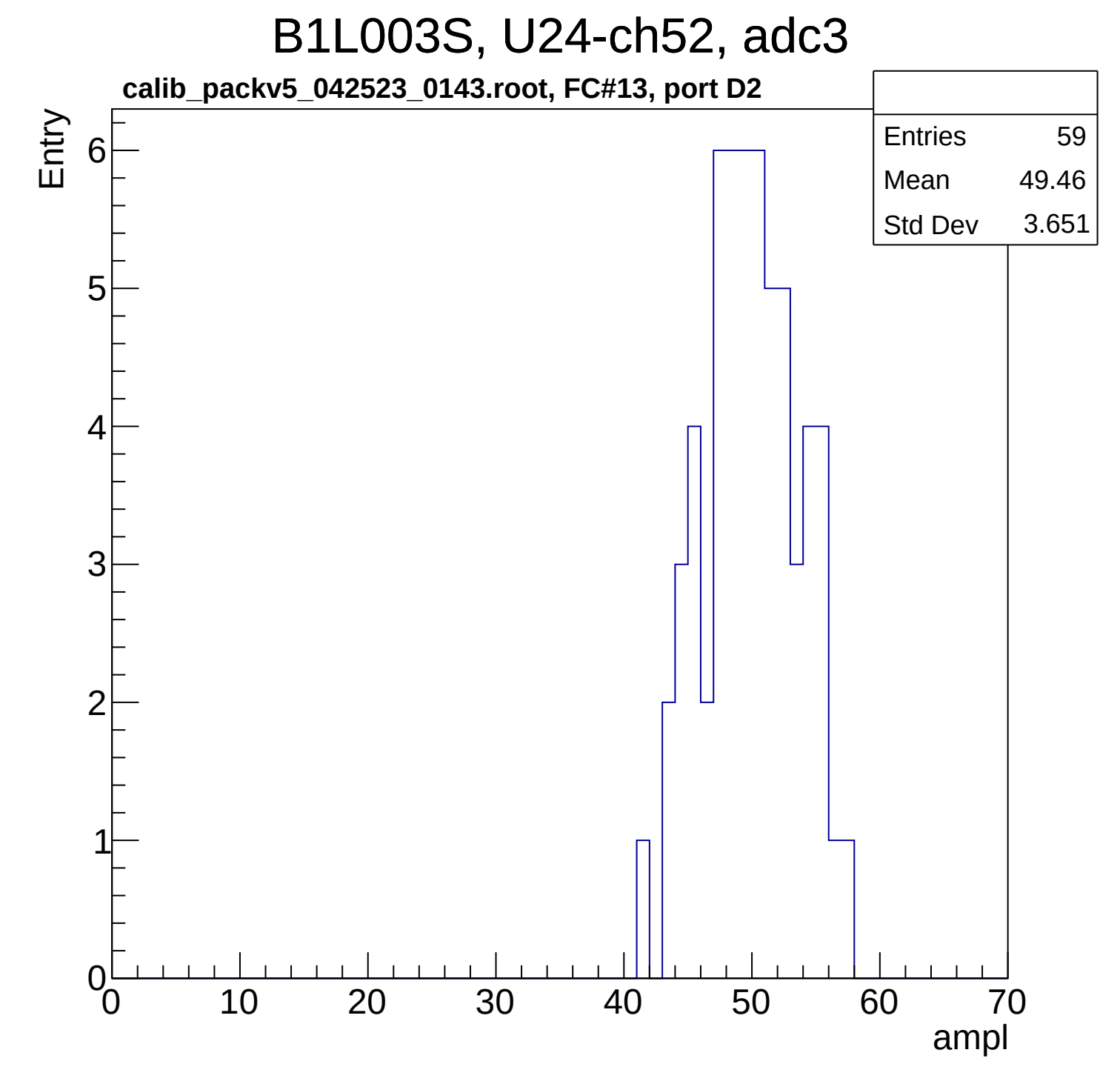
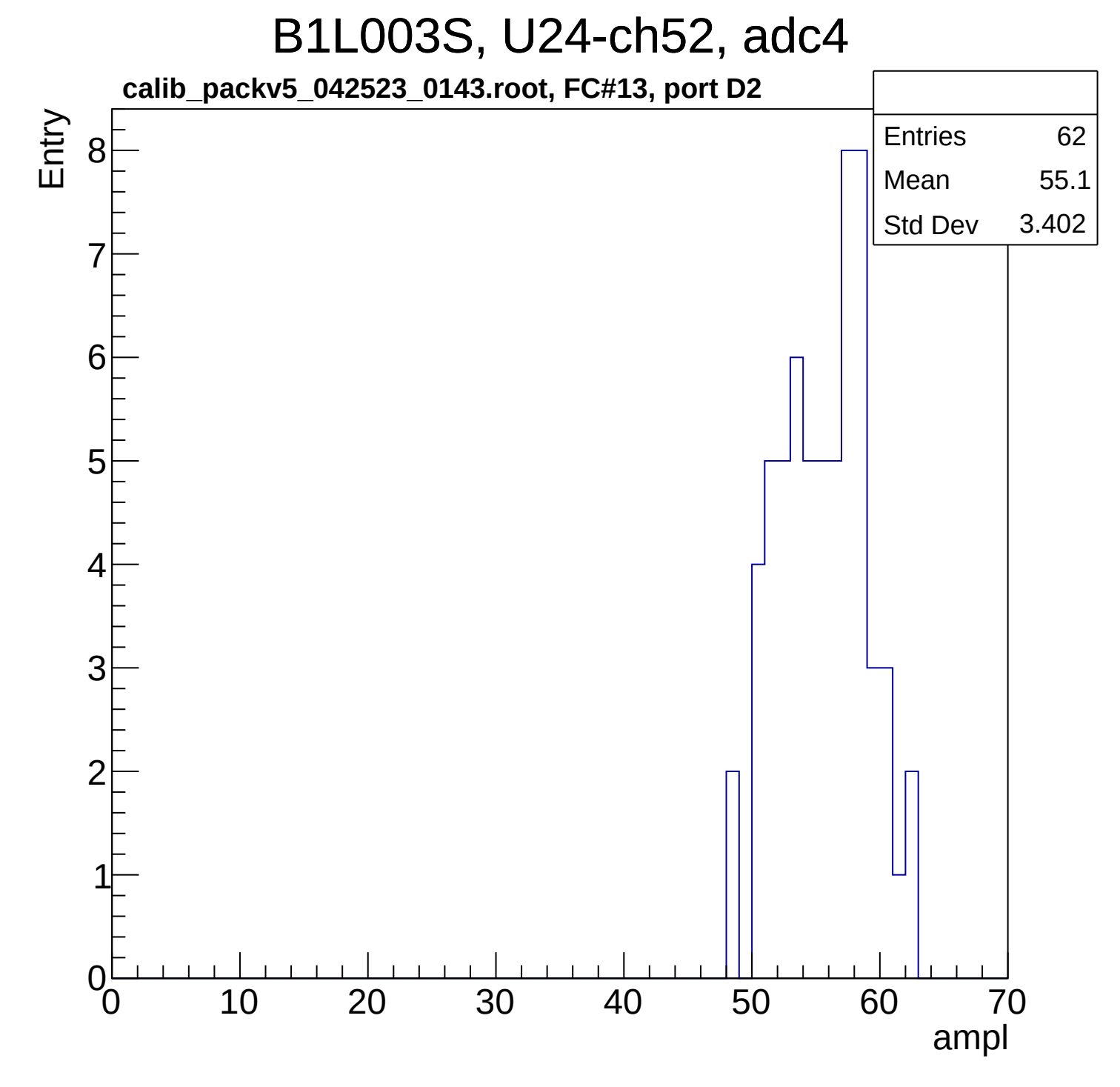
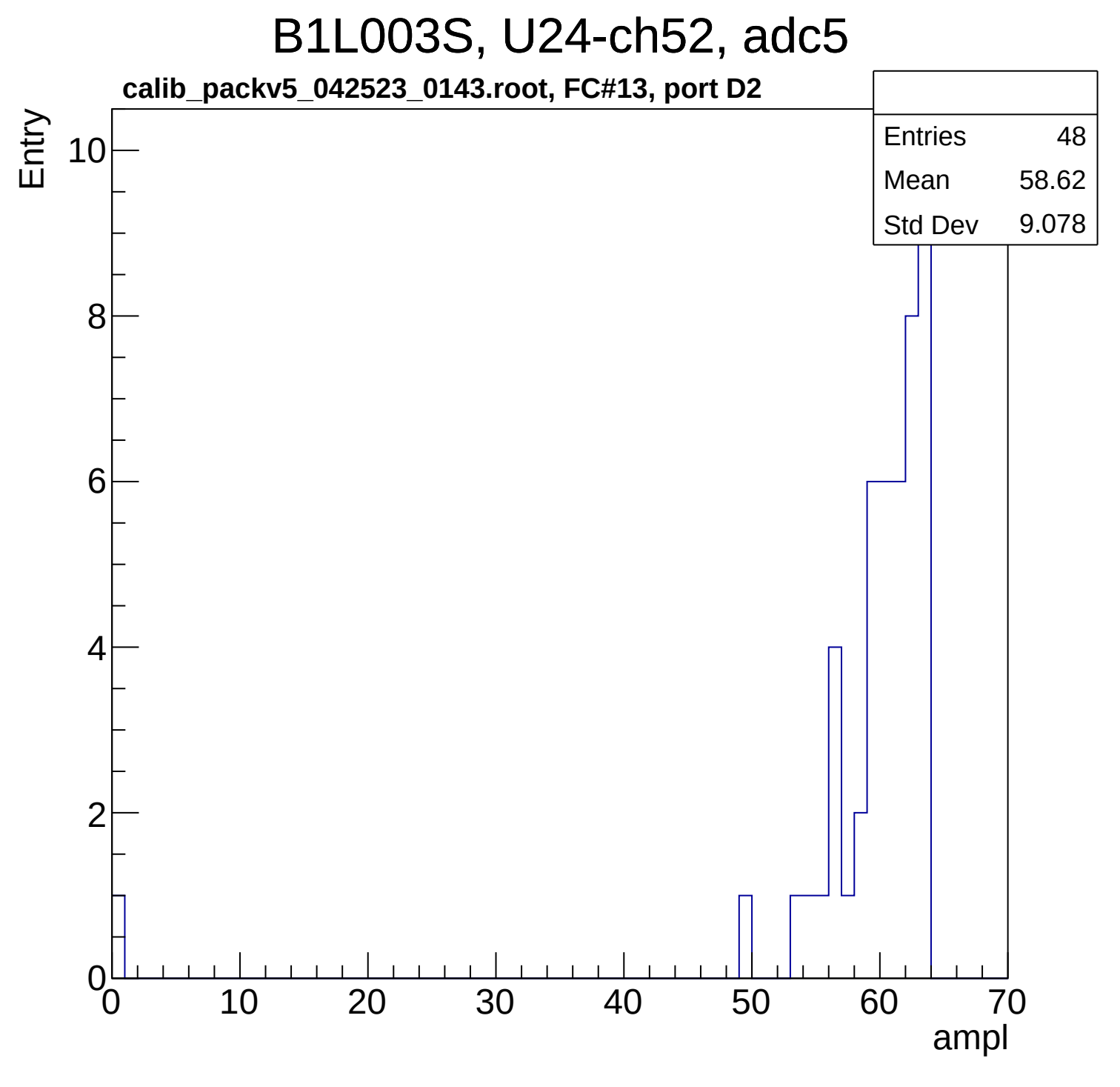

B1L003S, U24-ch52, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.46
Std Dev 3.651
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch52, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 55.1
Std Dev 3.402
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch52, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.62
Std Dev 9.078
Entry
0
2
4
6
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10
0
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20
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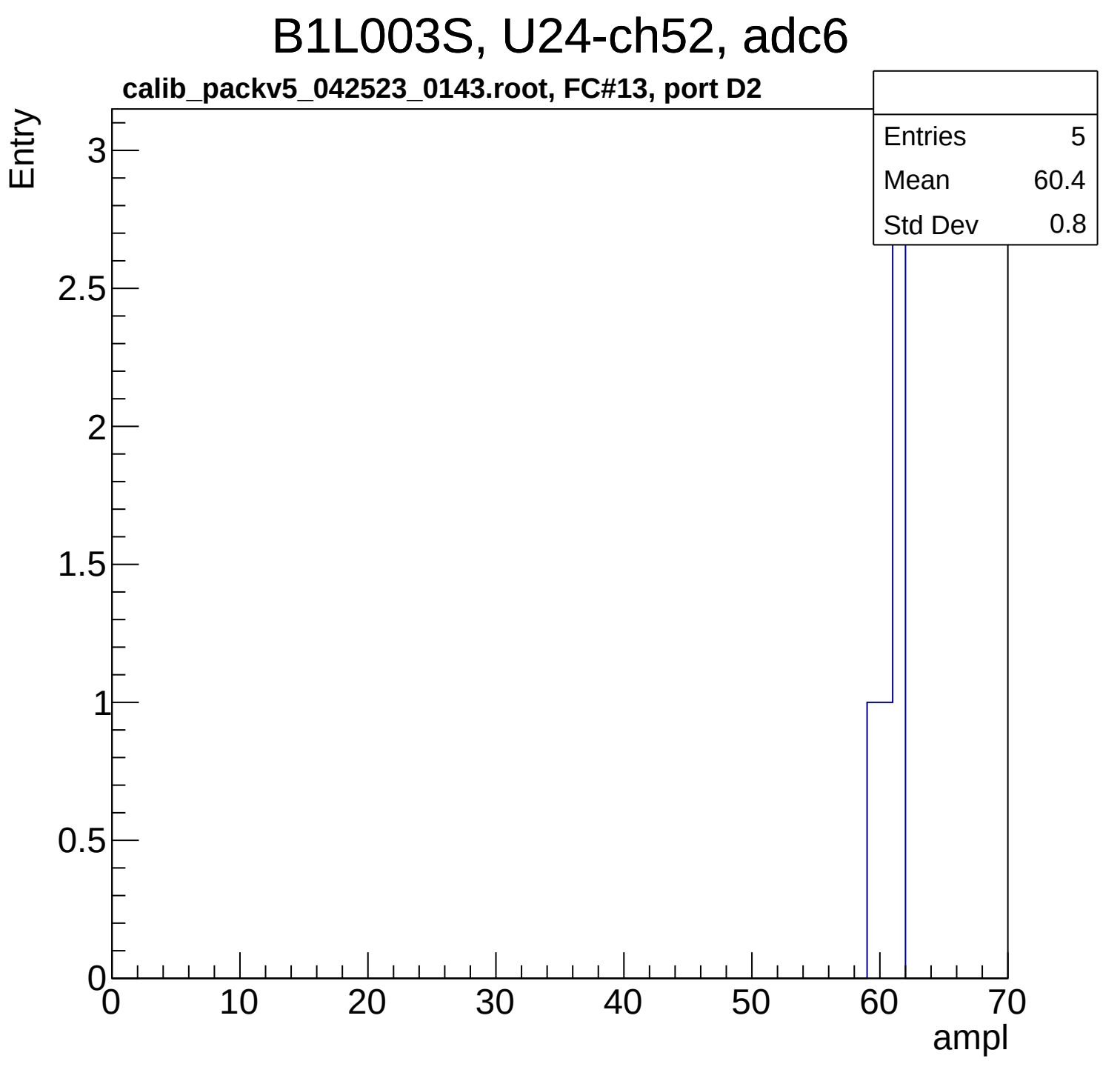
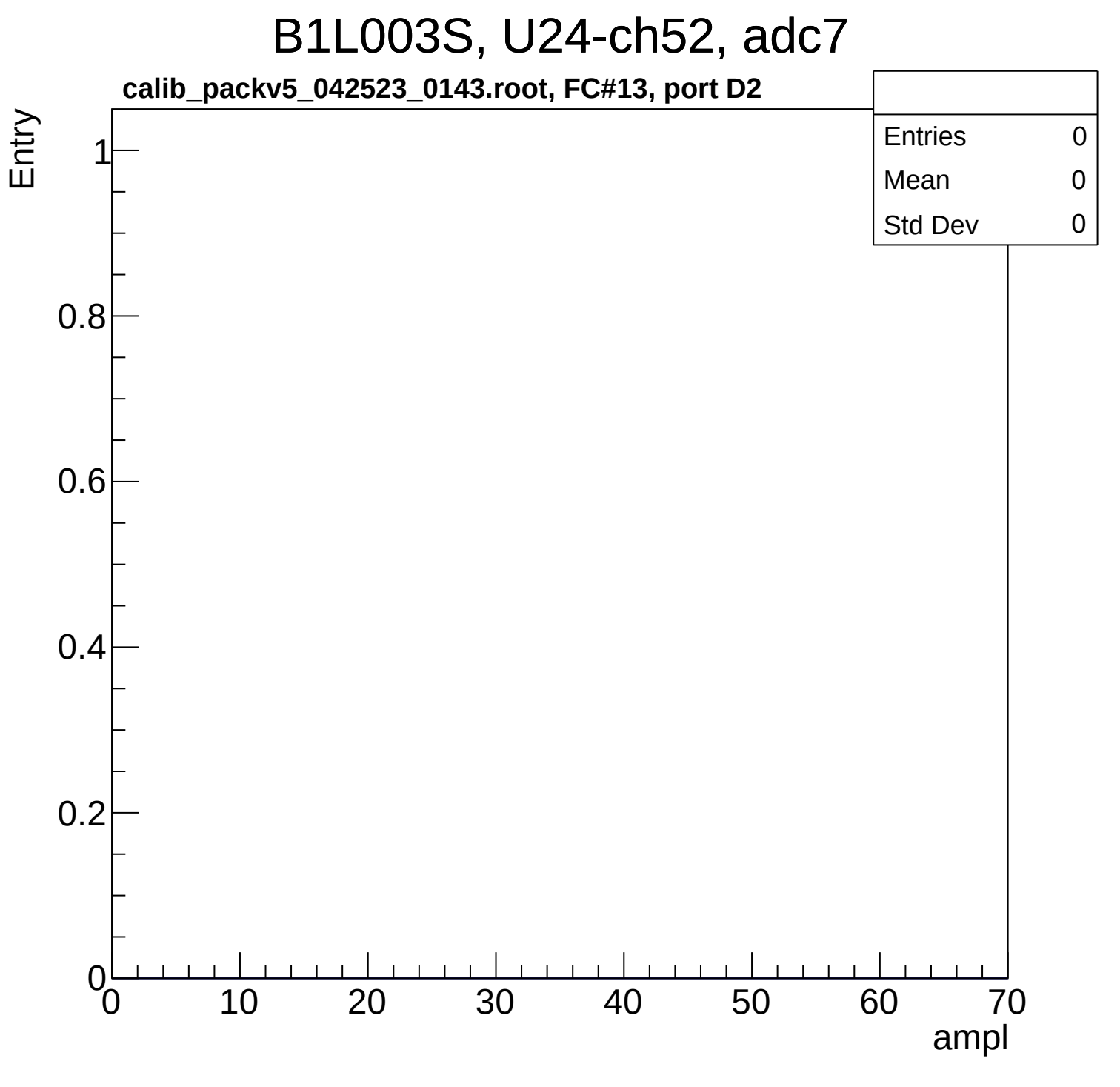
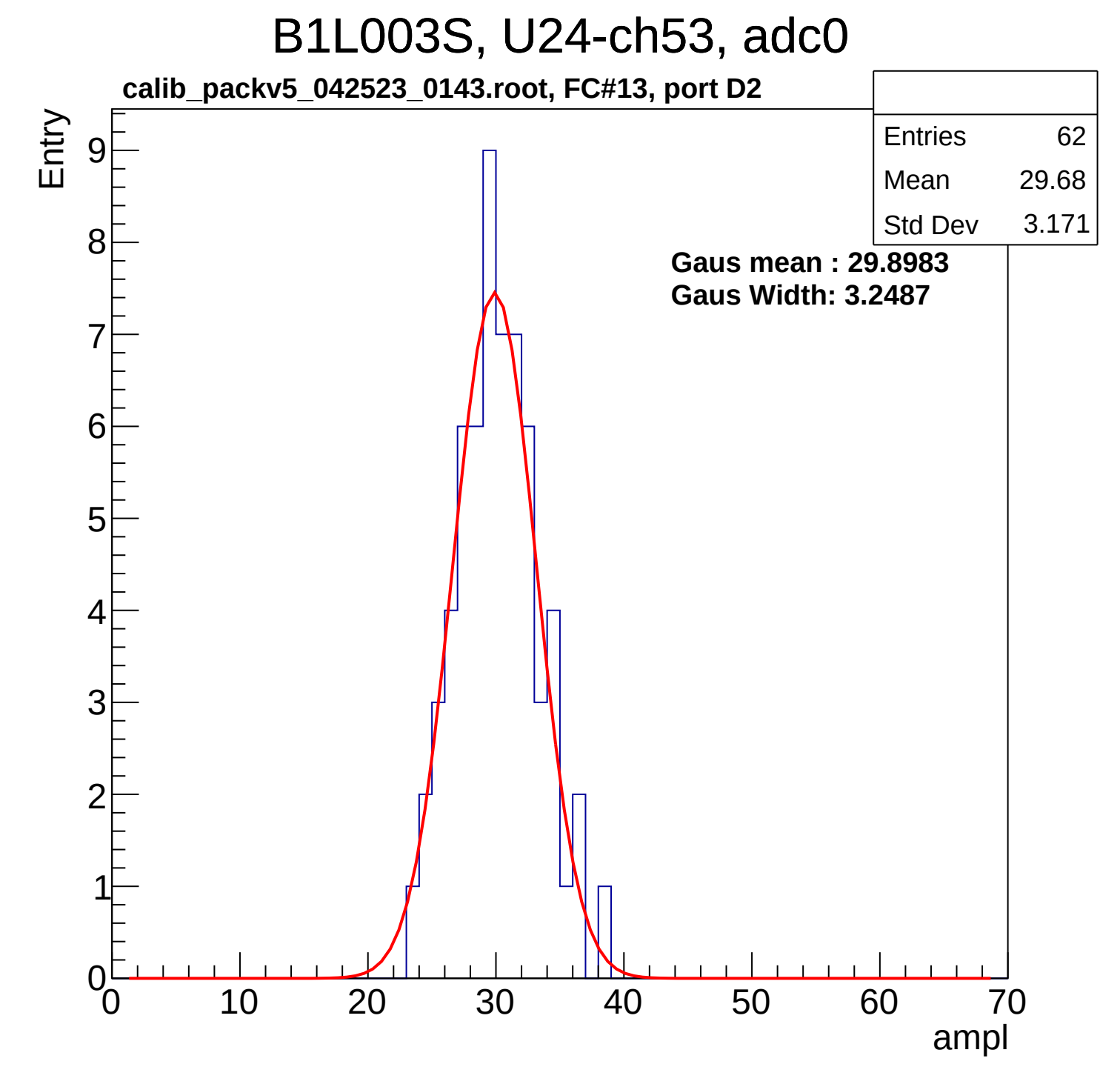
40
50
60
70
ampl


B1L003S, U24-ch52, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 60.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch52, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch53, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 29.68
Std Dev 3.171
Gaus mean : 29.8983
Gaus Width: 3.2487
Entry
ampl
0
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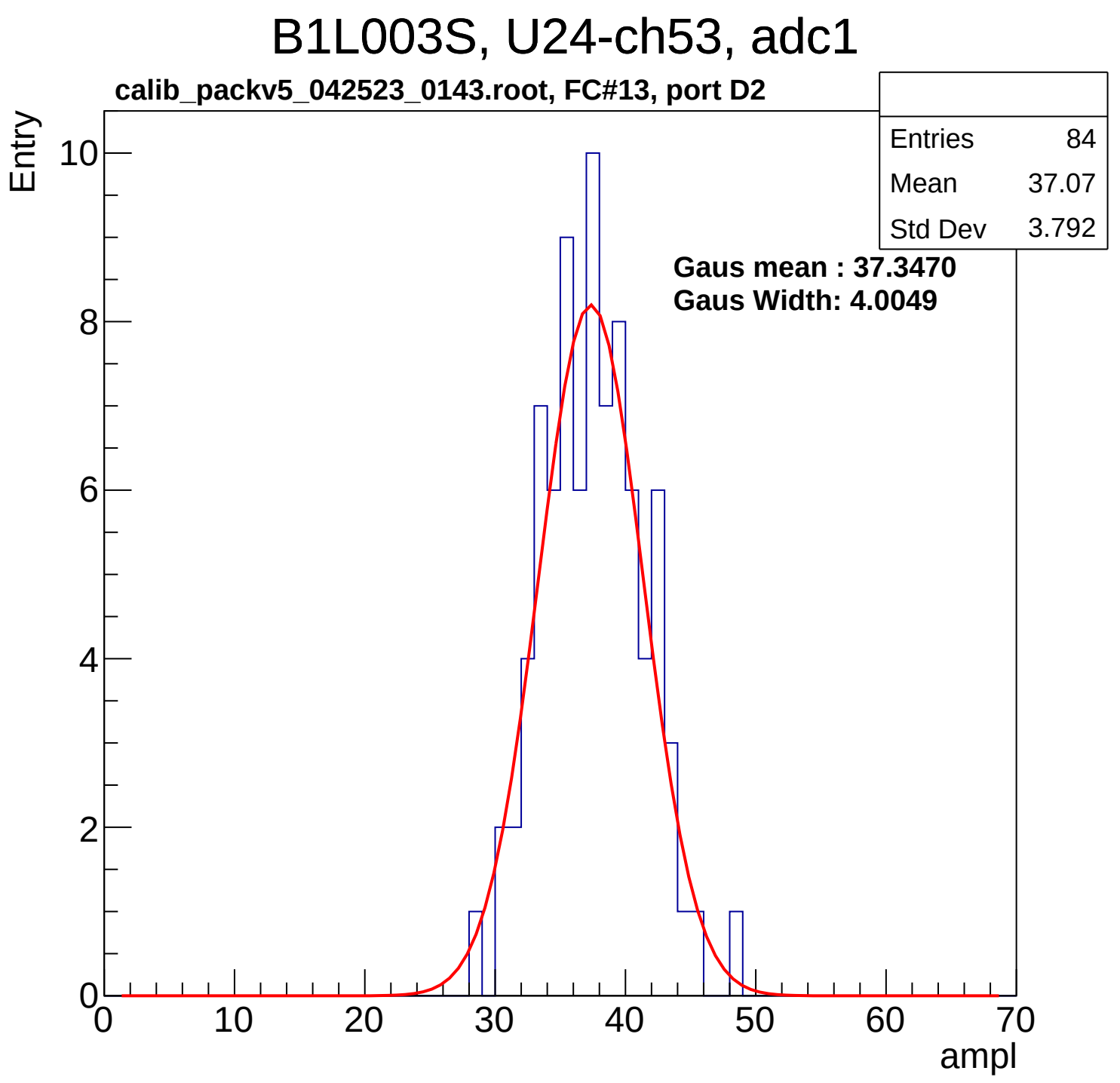
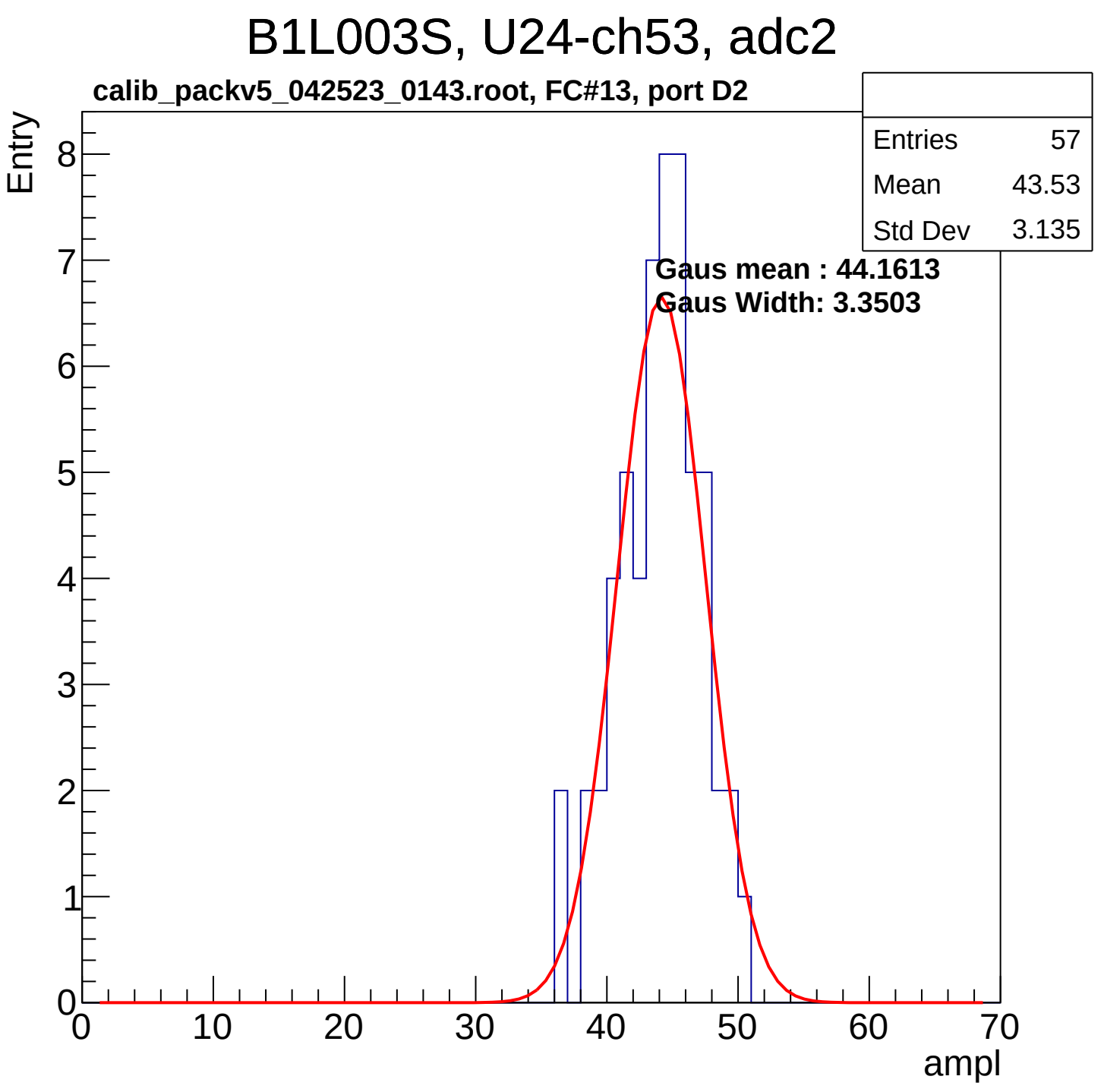
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch53, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 37.07
Std Dev 3.792
Gaus mean : 37.3470
Gaus Width: 4.0049
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch53, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 43.53
Std Dev 3.135
Gaus mean : 44.1613
Gaus Width: 3.3503
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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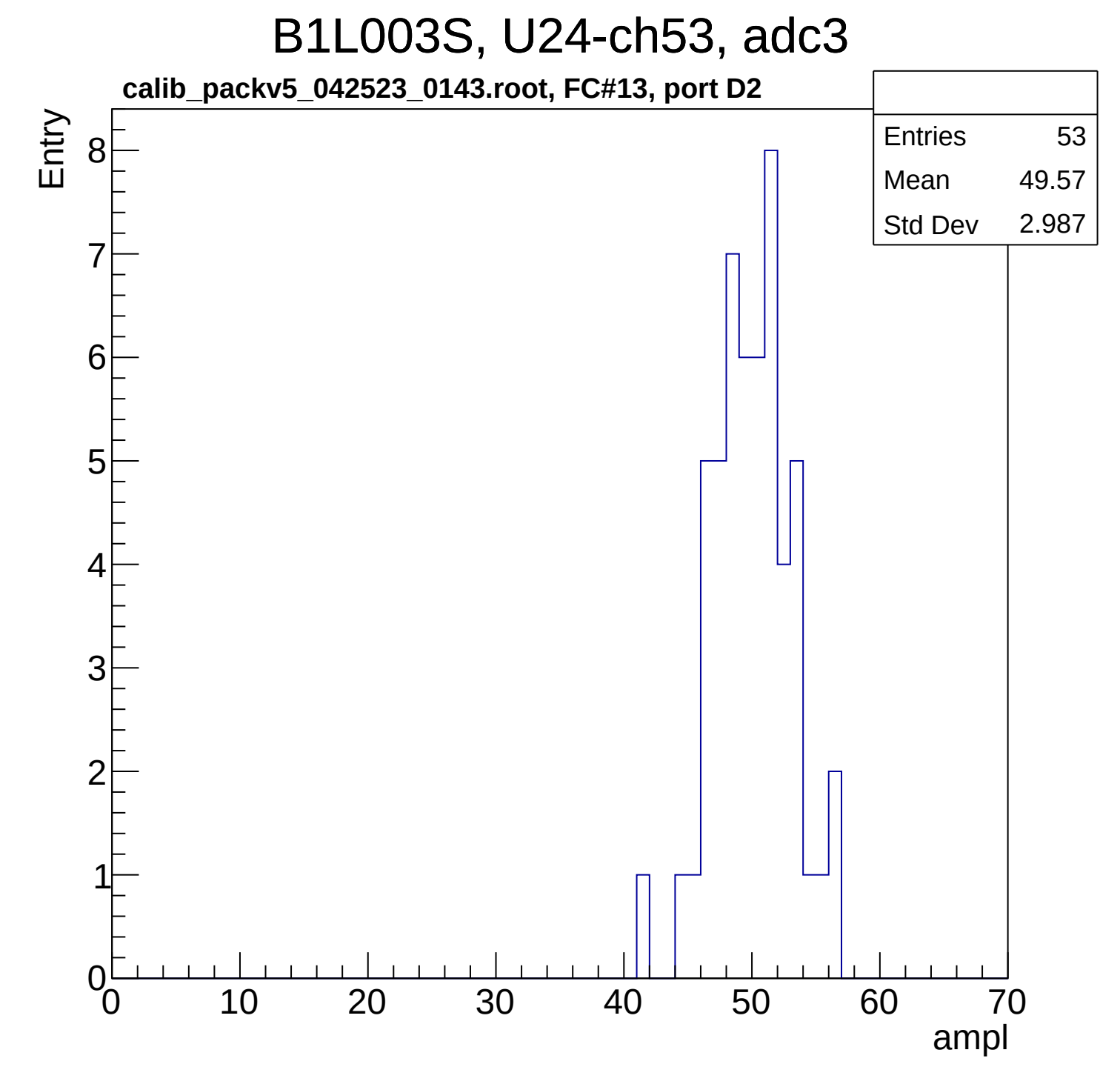
B1L003S, U24-ch53, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 49.57
Std Dev 2.987
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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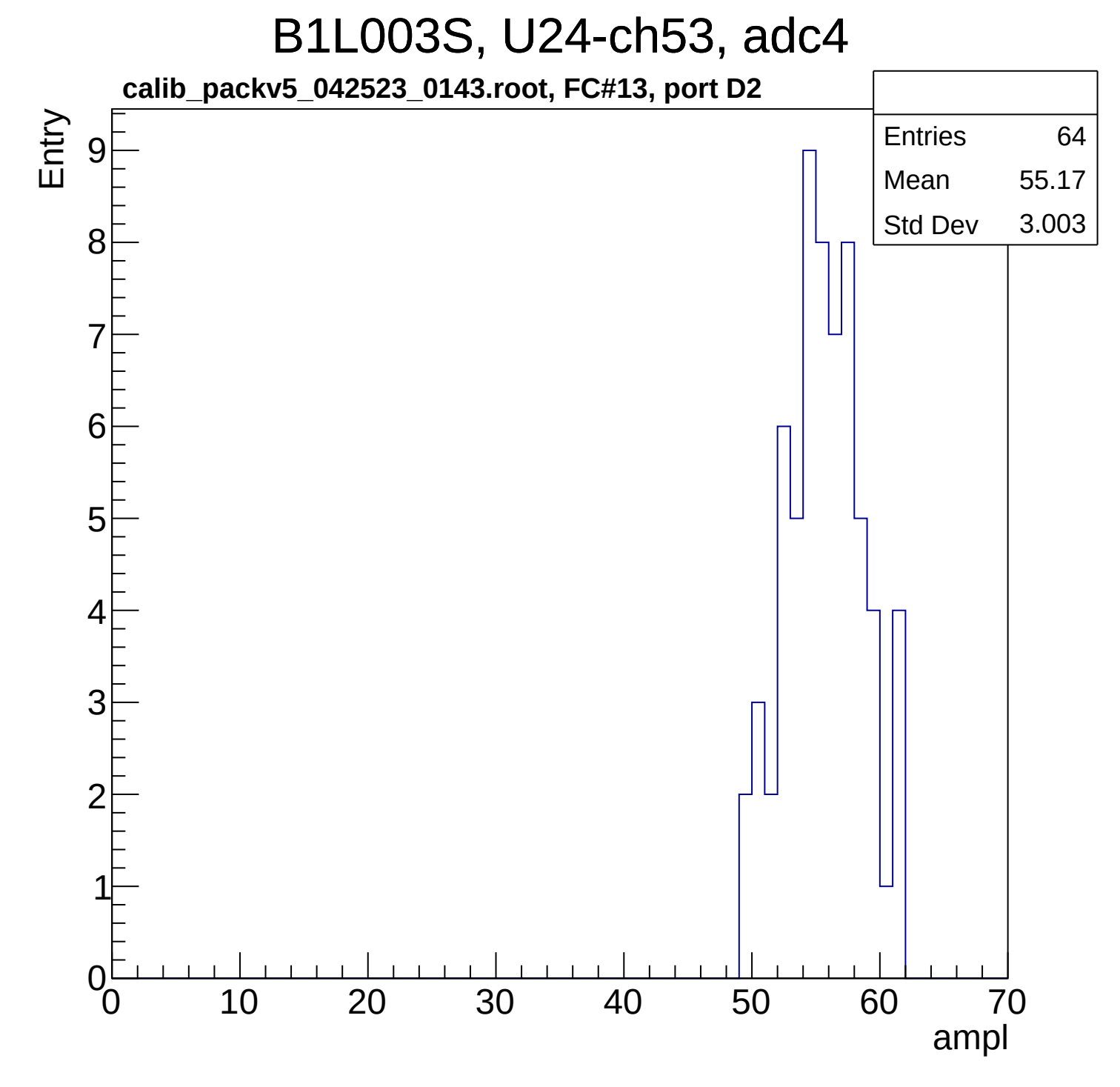
B1L003S, U24-ch53, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 55.17
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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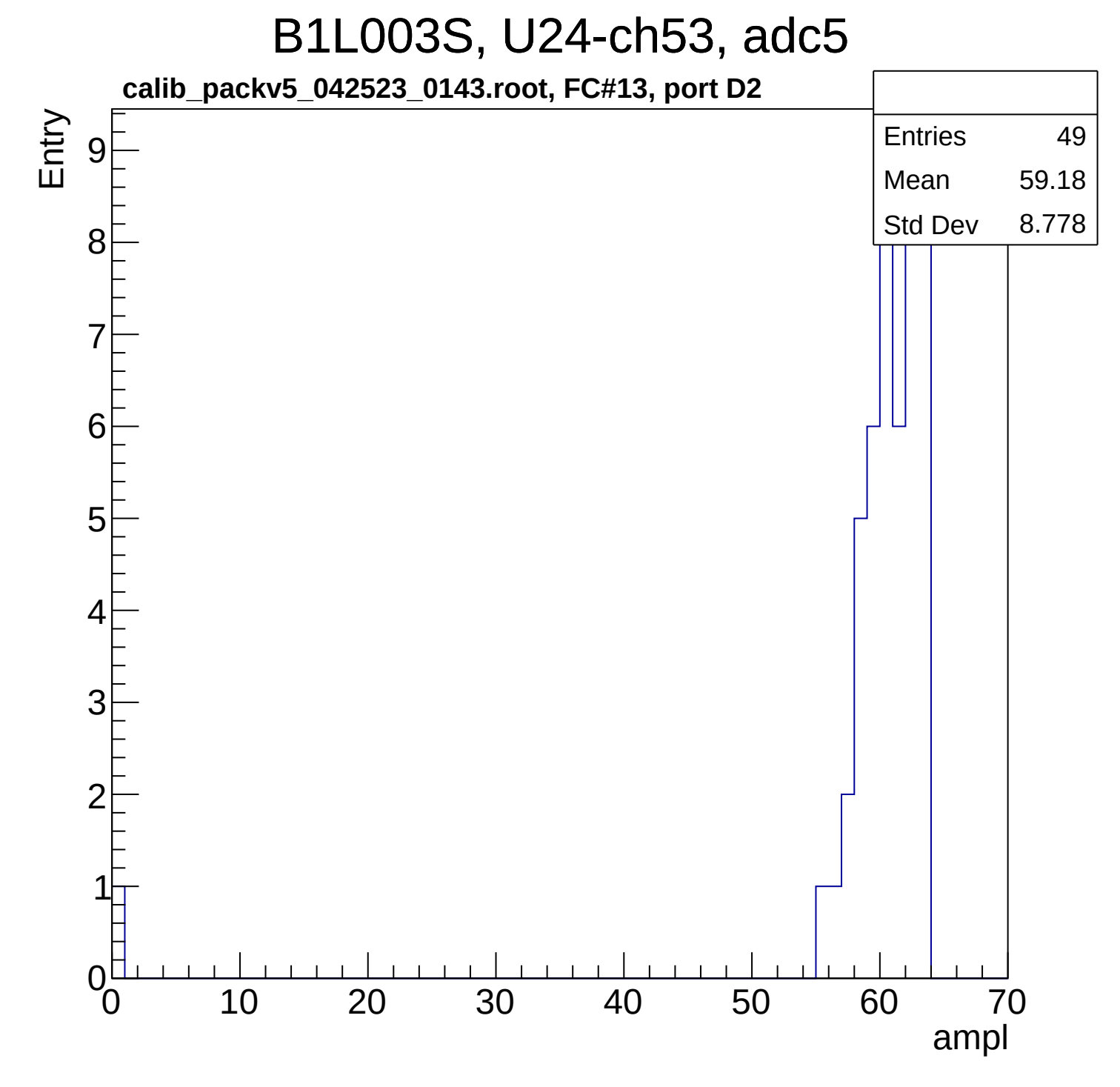
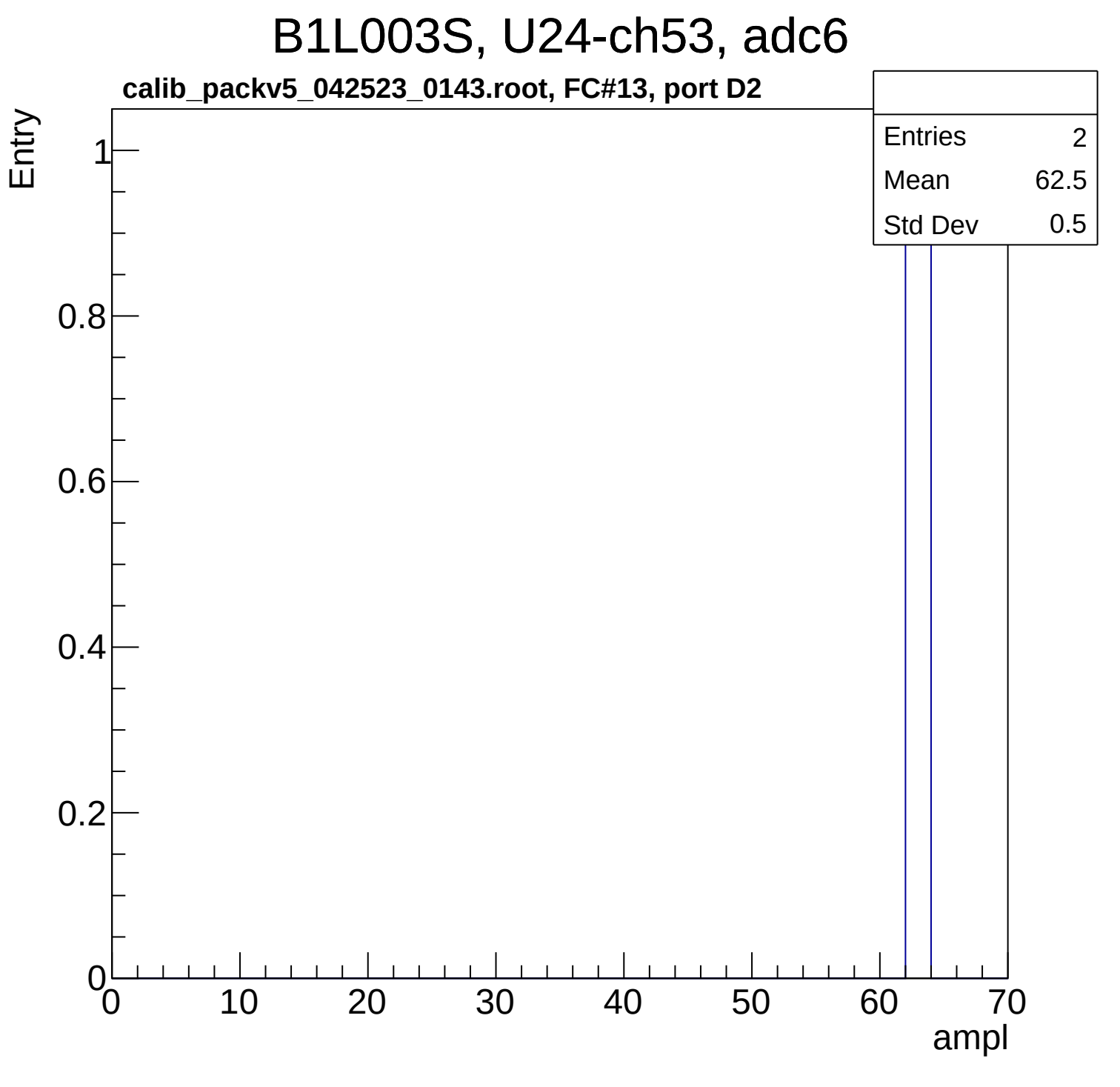

B1L003S, U24-ch53, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 59.18
Std Dev 8.778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch53, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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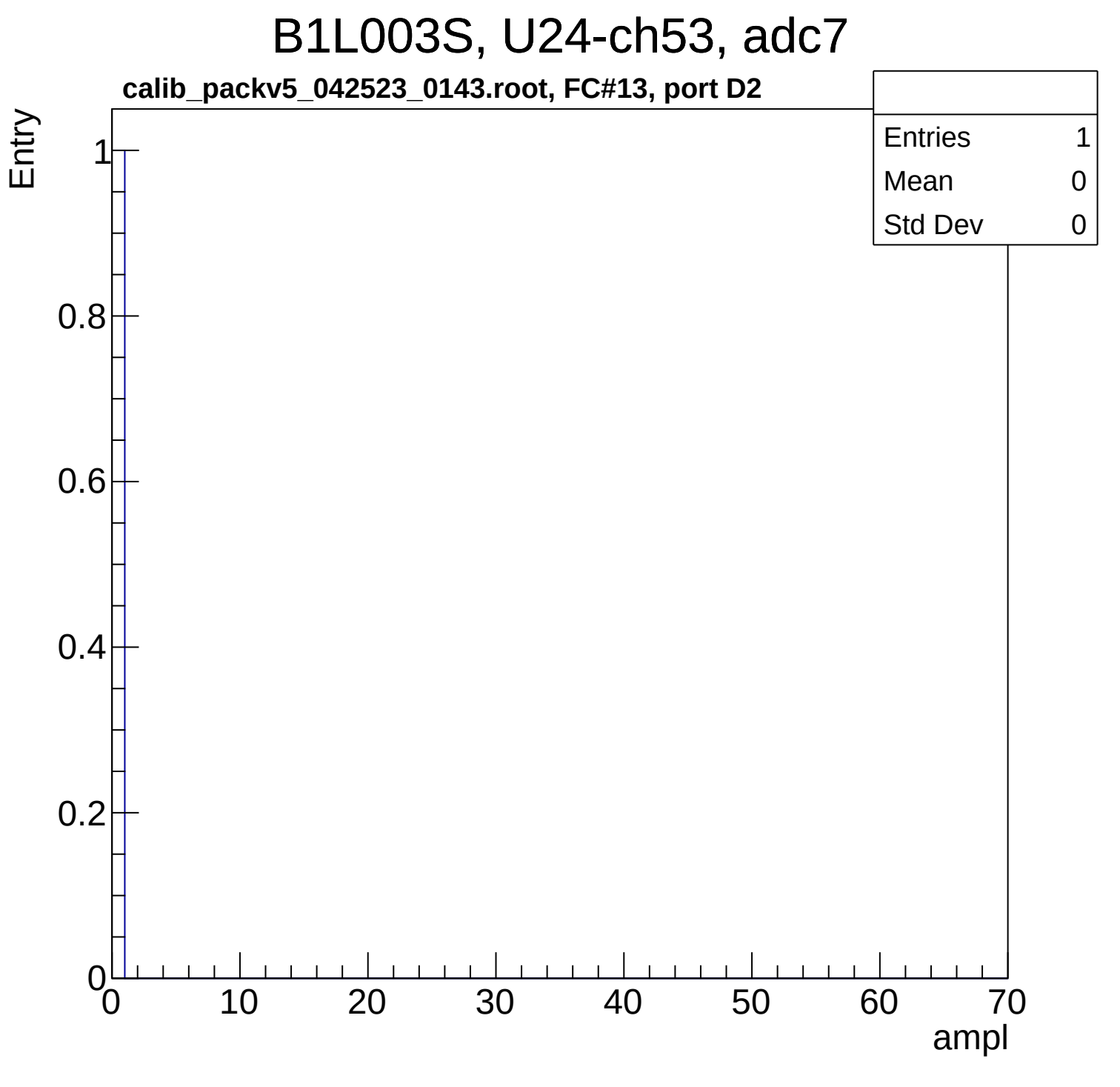
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch53, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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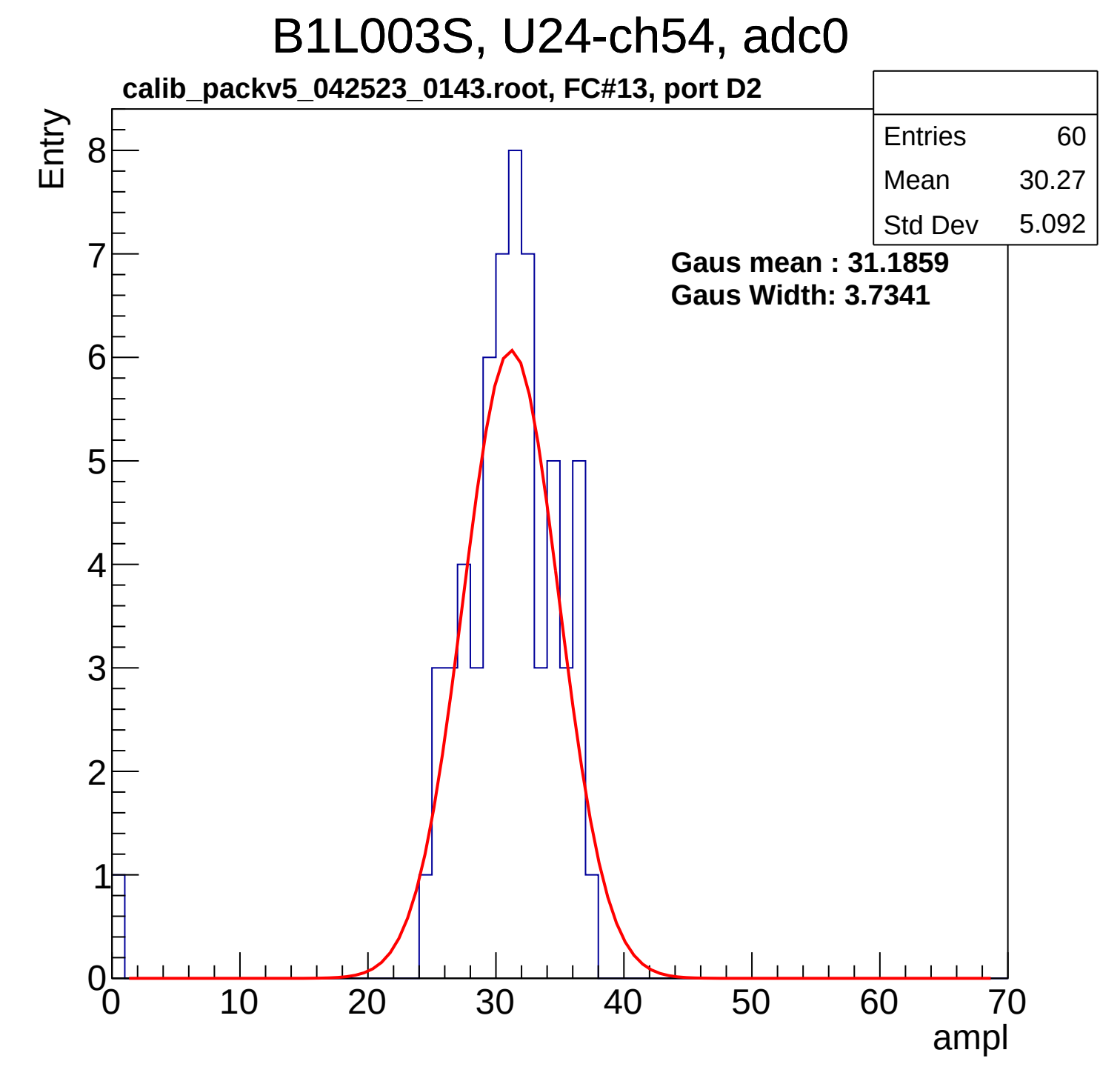
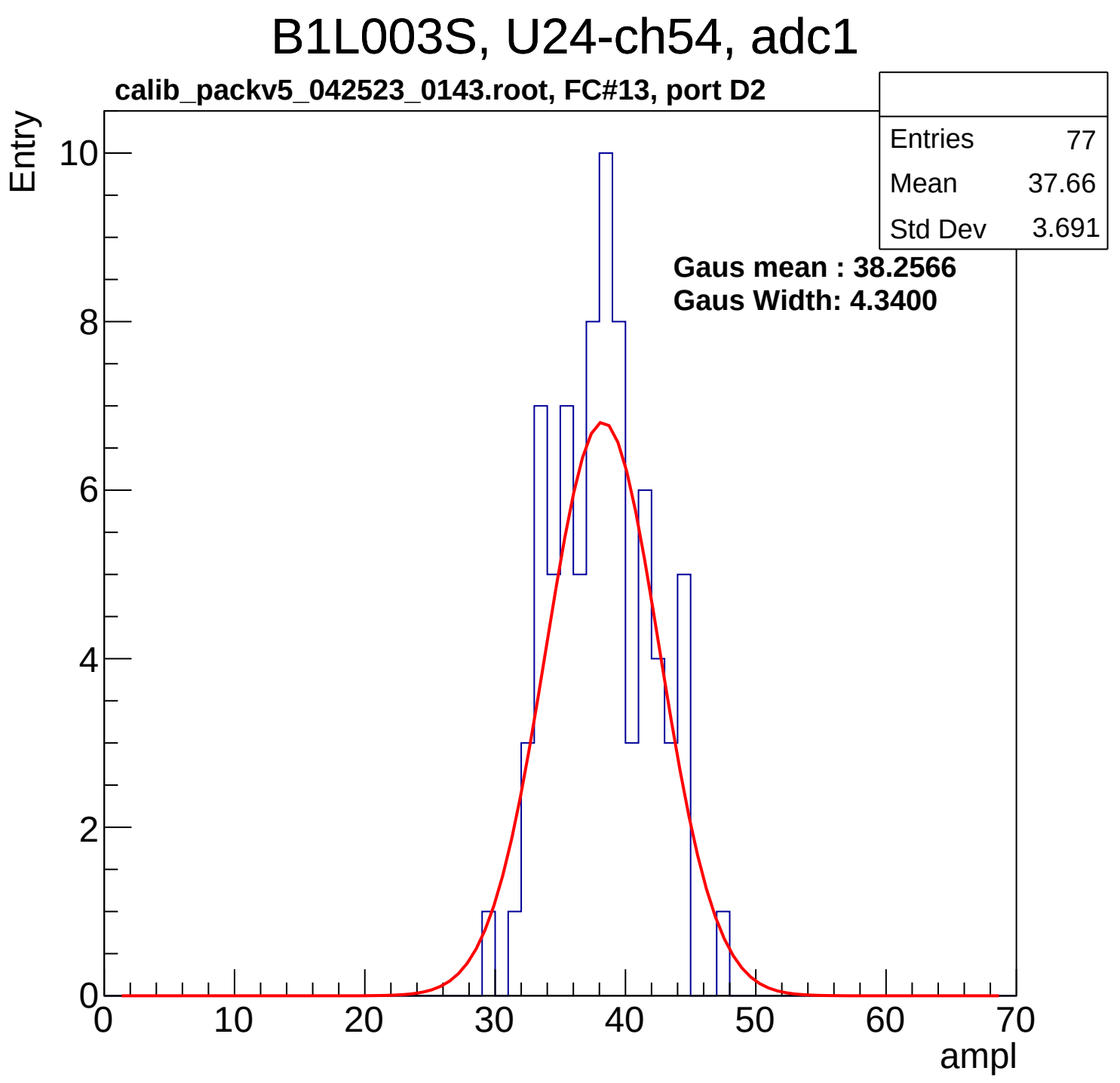
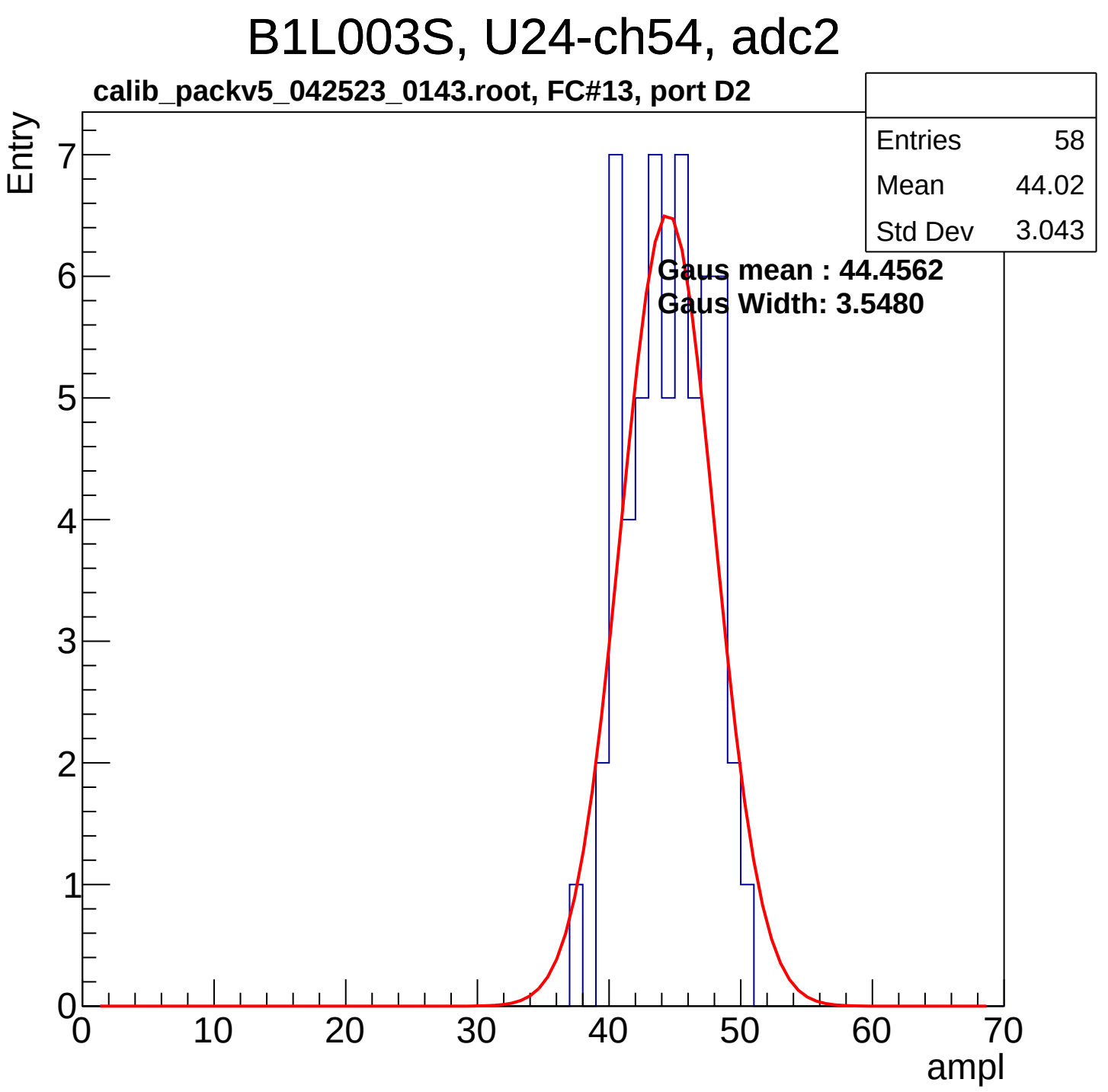
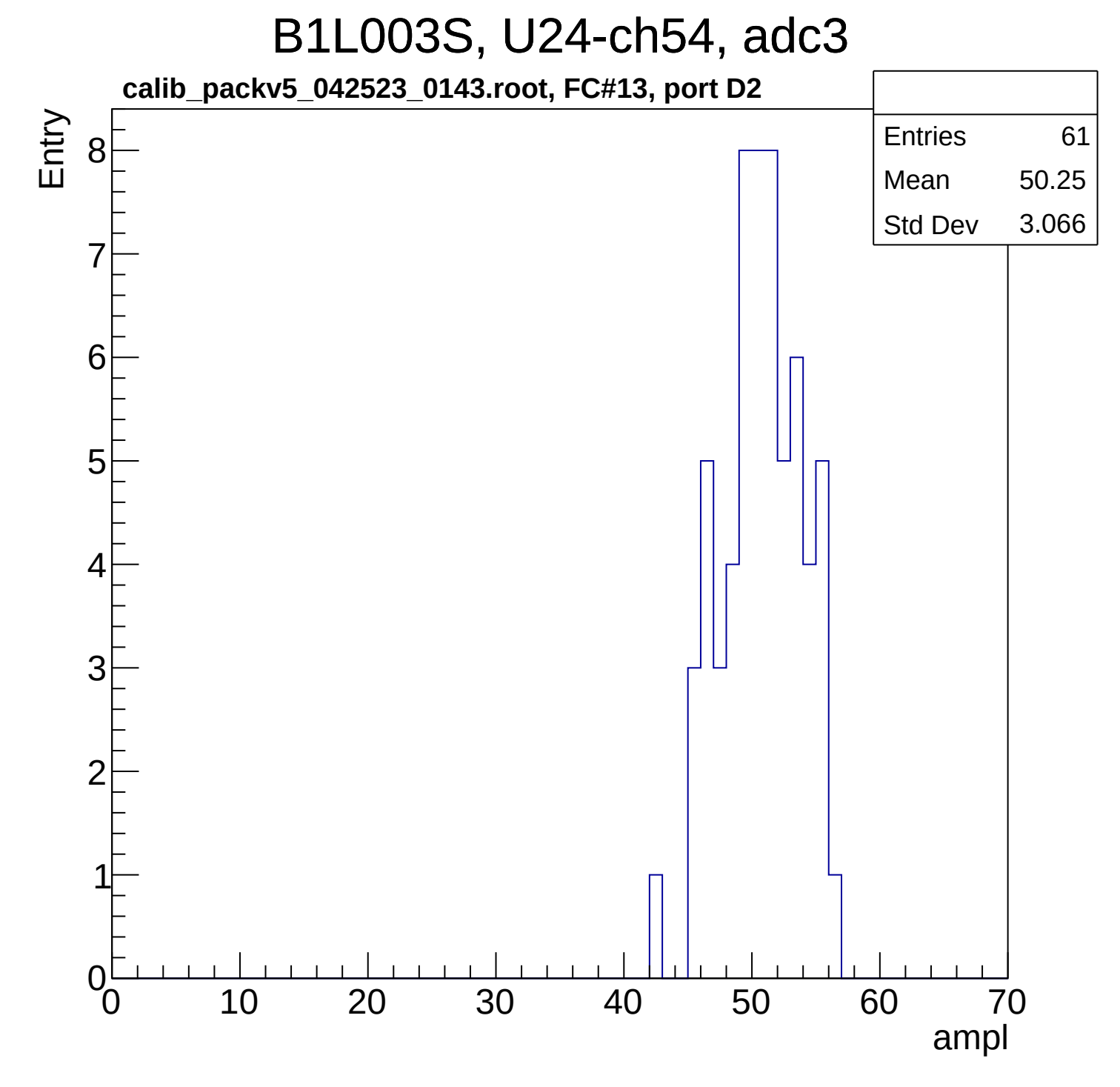
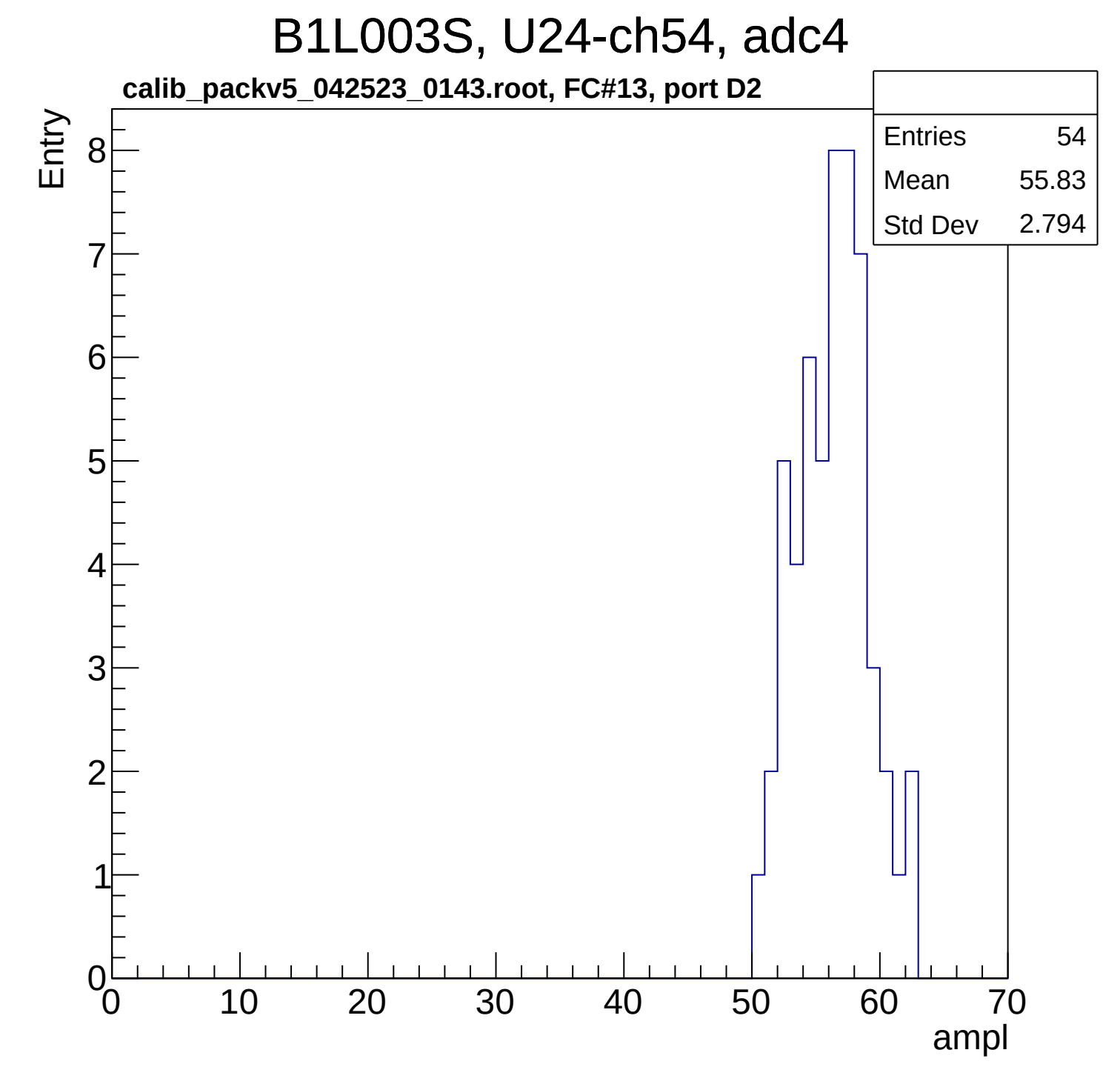
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

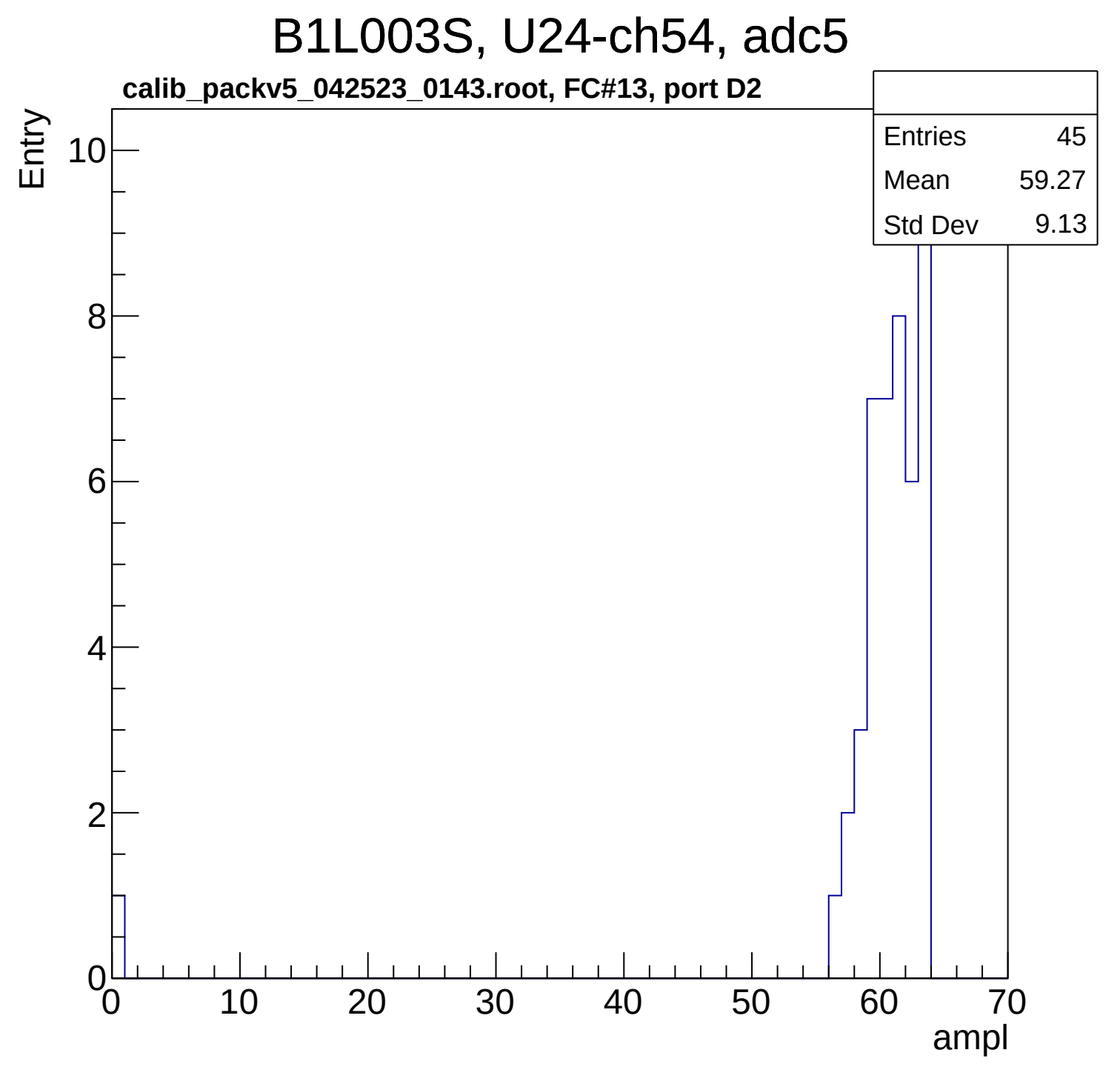

B1L003S, U24-ch54, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 30.27
Std Dev 5.092
Gaus mean : 31.1859
Gaus Width: 3.7341
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

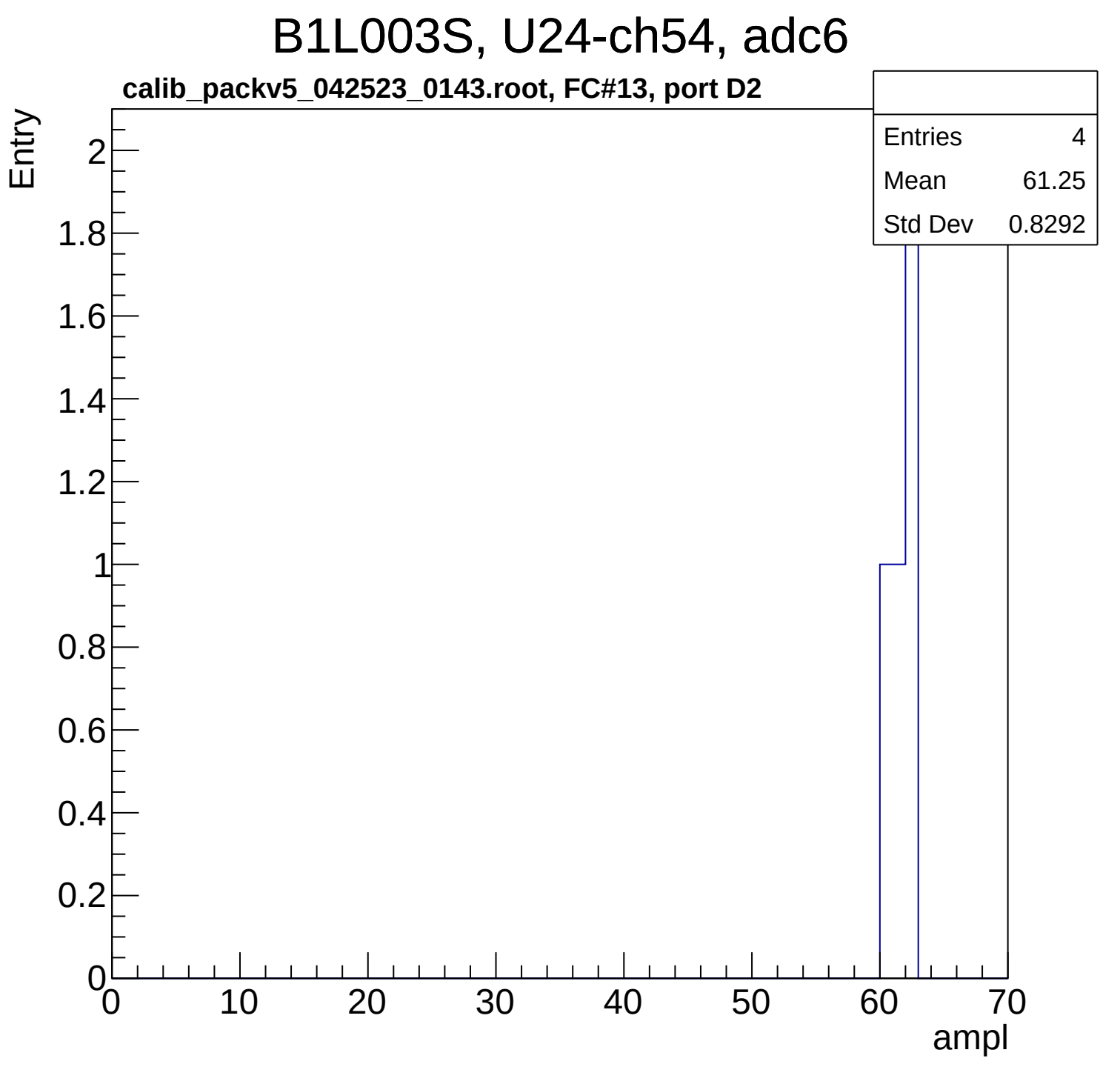

B1L003S, U24-ch54, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 37.66
Std Dev 3.691
Gaus mean : 38.2566
Gaus Width: 4.3400
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

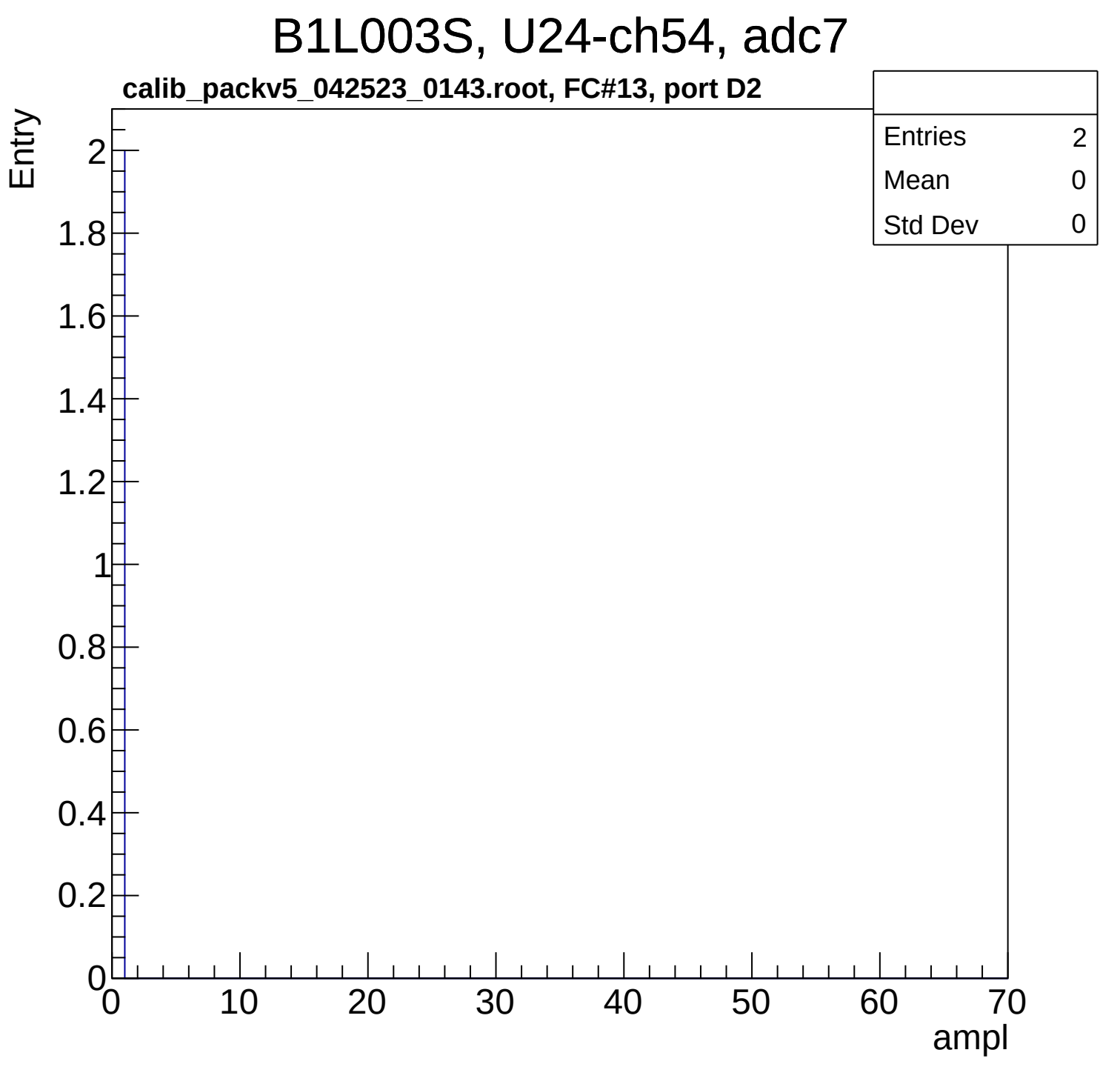

B1L003S, U24-ch54, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 44.02
Std Dev 3.043
Gaus mean : 44.4562
Gaus Width: 3.5480
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch54, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.25
Std Dev 3.066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch54, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.83
Std Dev 2.794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch54, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 59.27
Std Dev 9.13
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch54, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch54, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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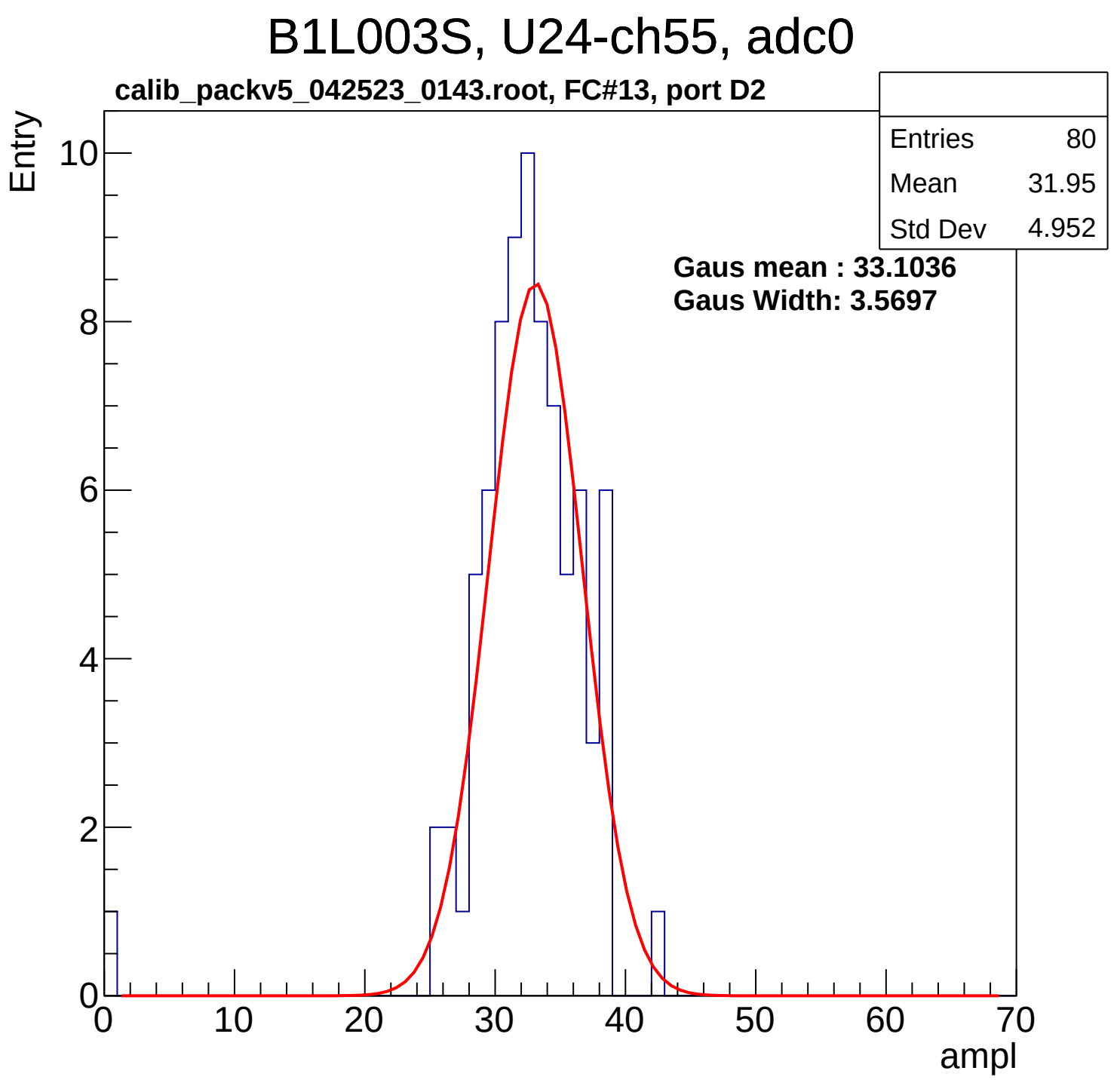
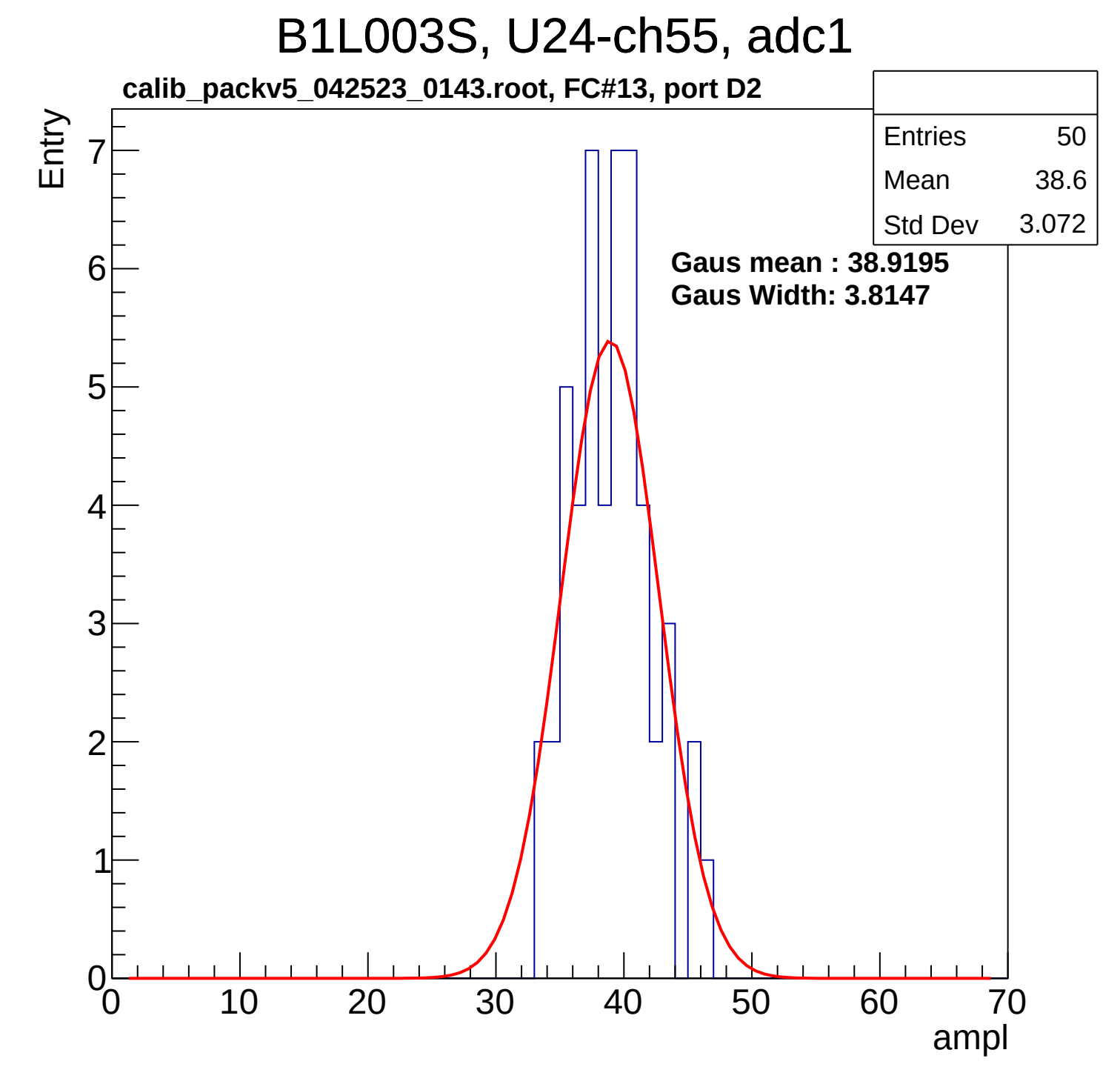
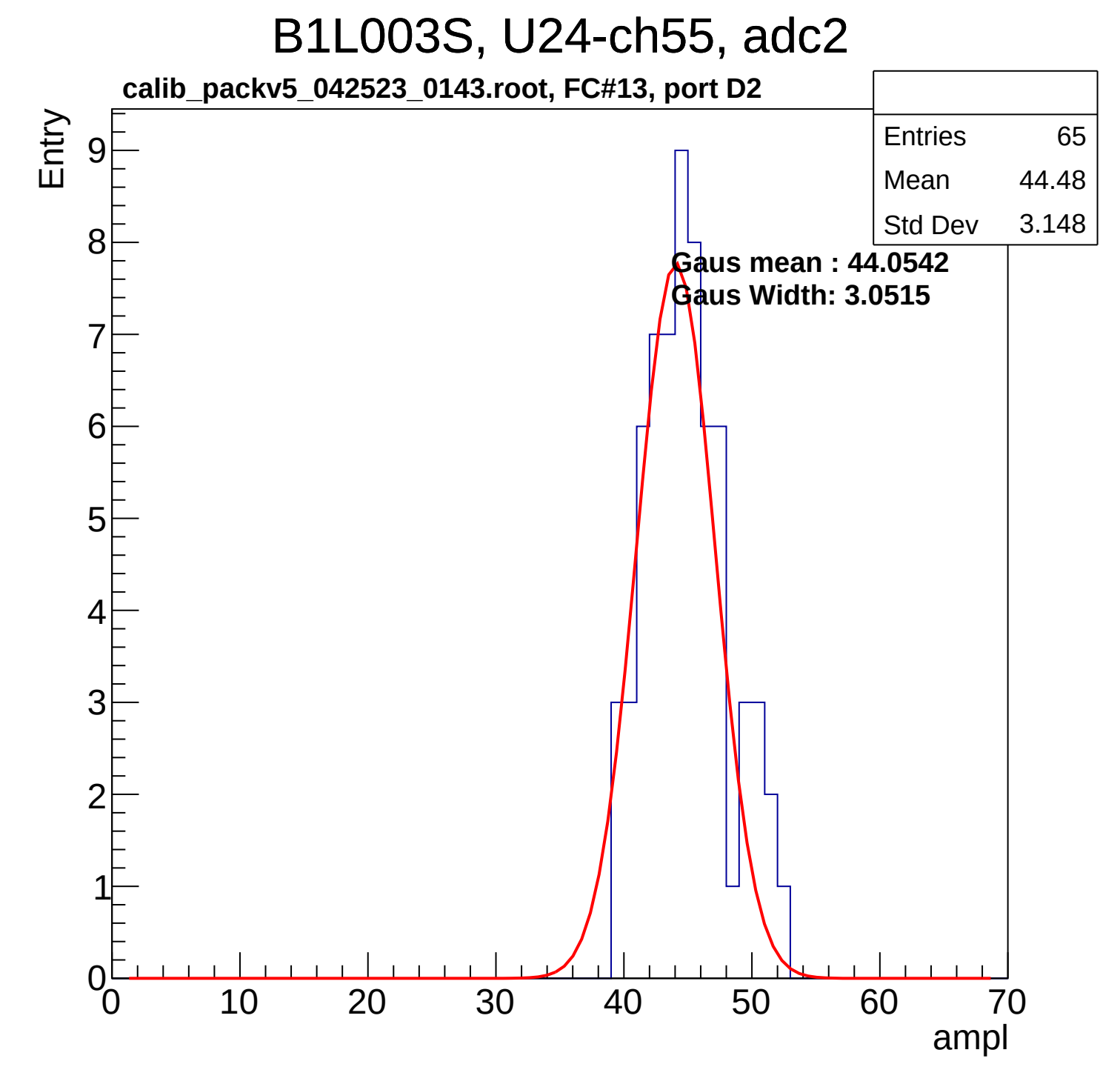
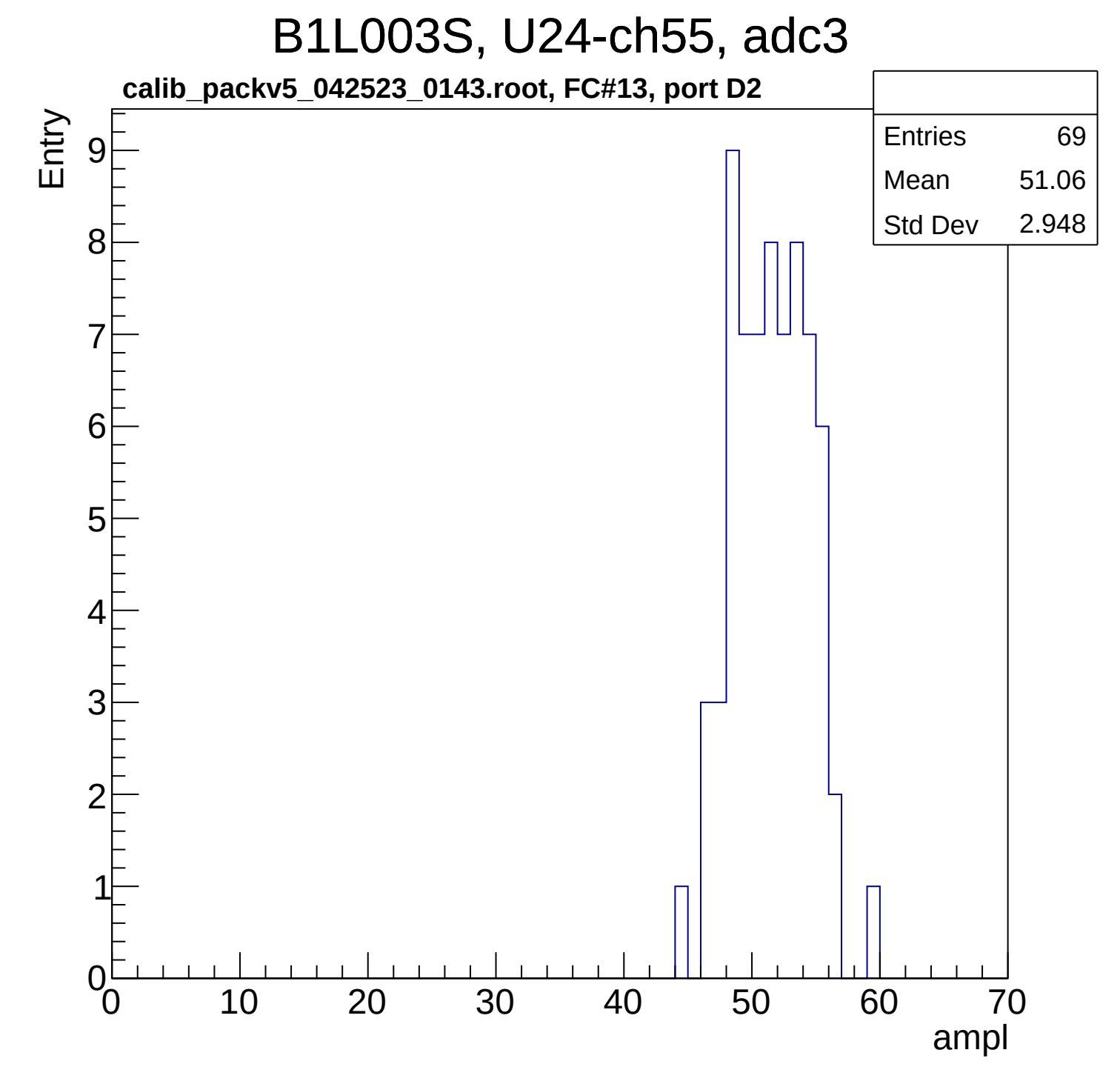
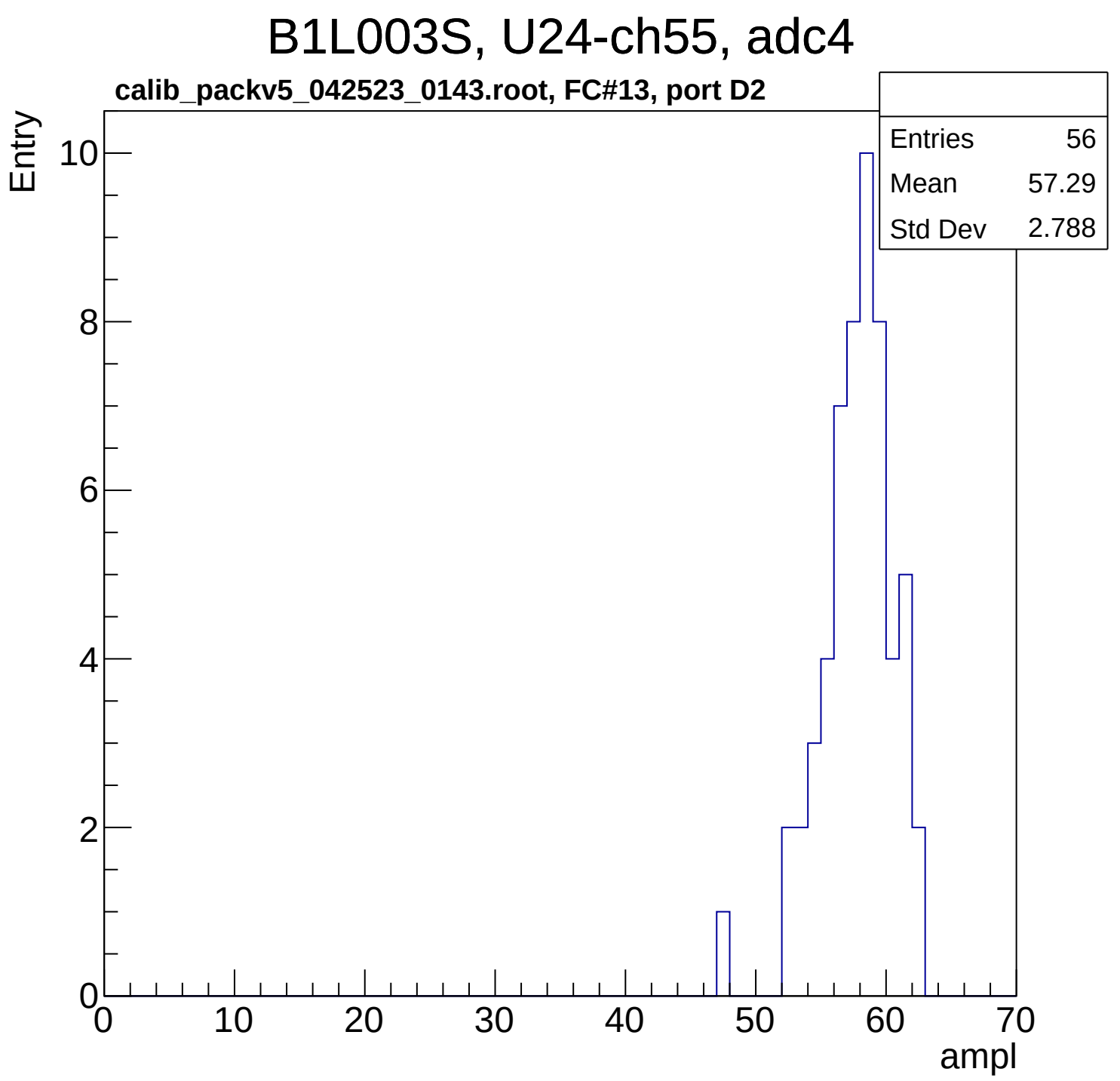
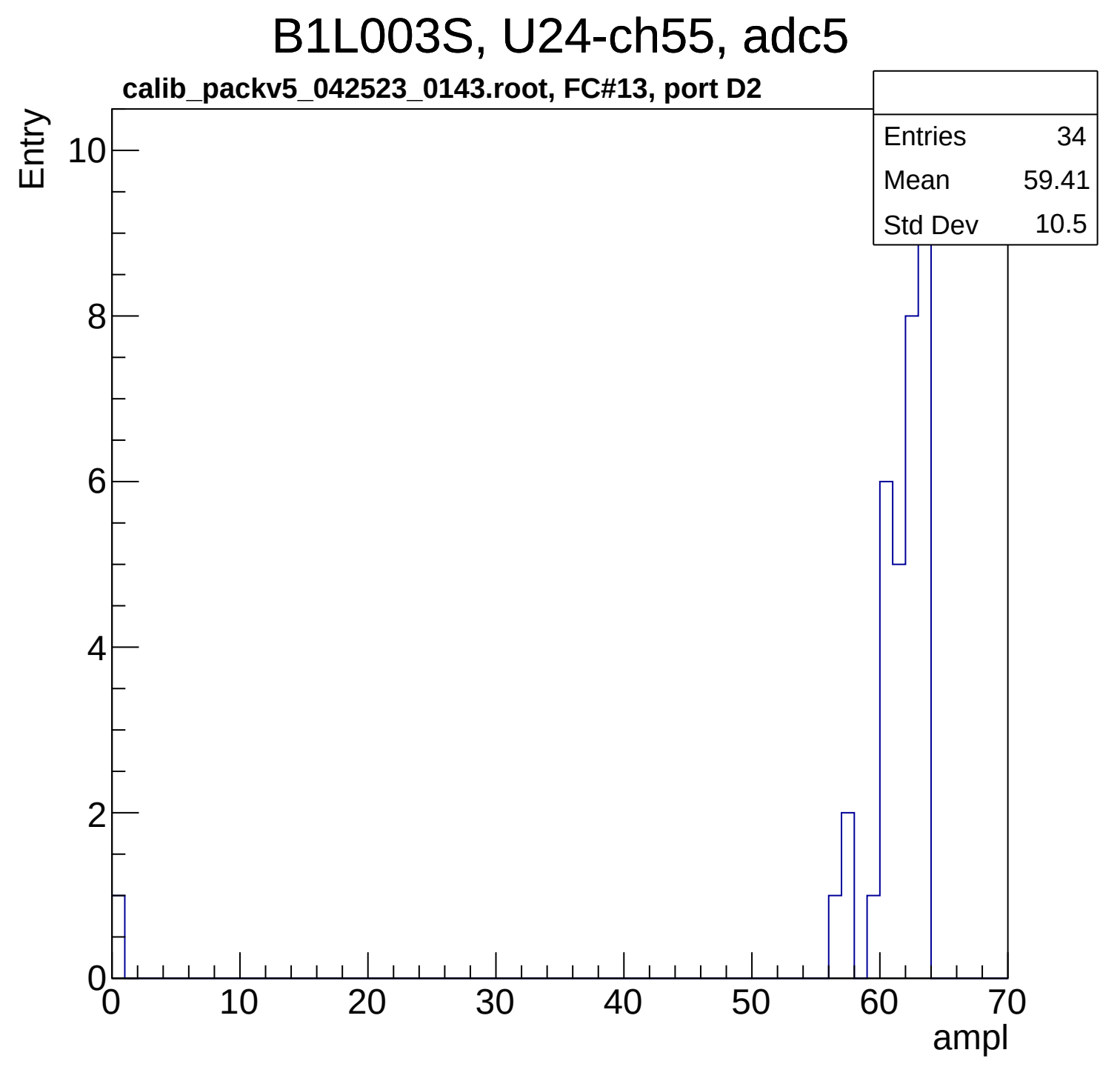

B1L003S, U24-ch55, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 31.95
Std Dev 4.952
Gaus mean : 33.1036
Gaus Width: 3.5697
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

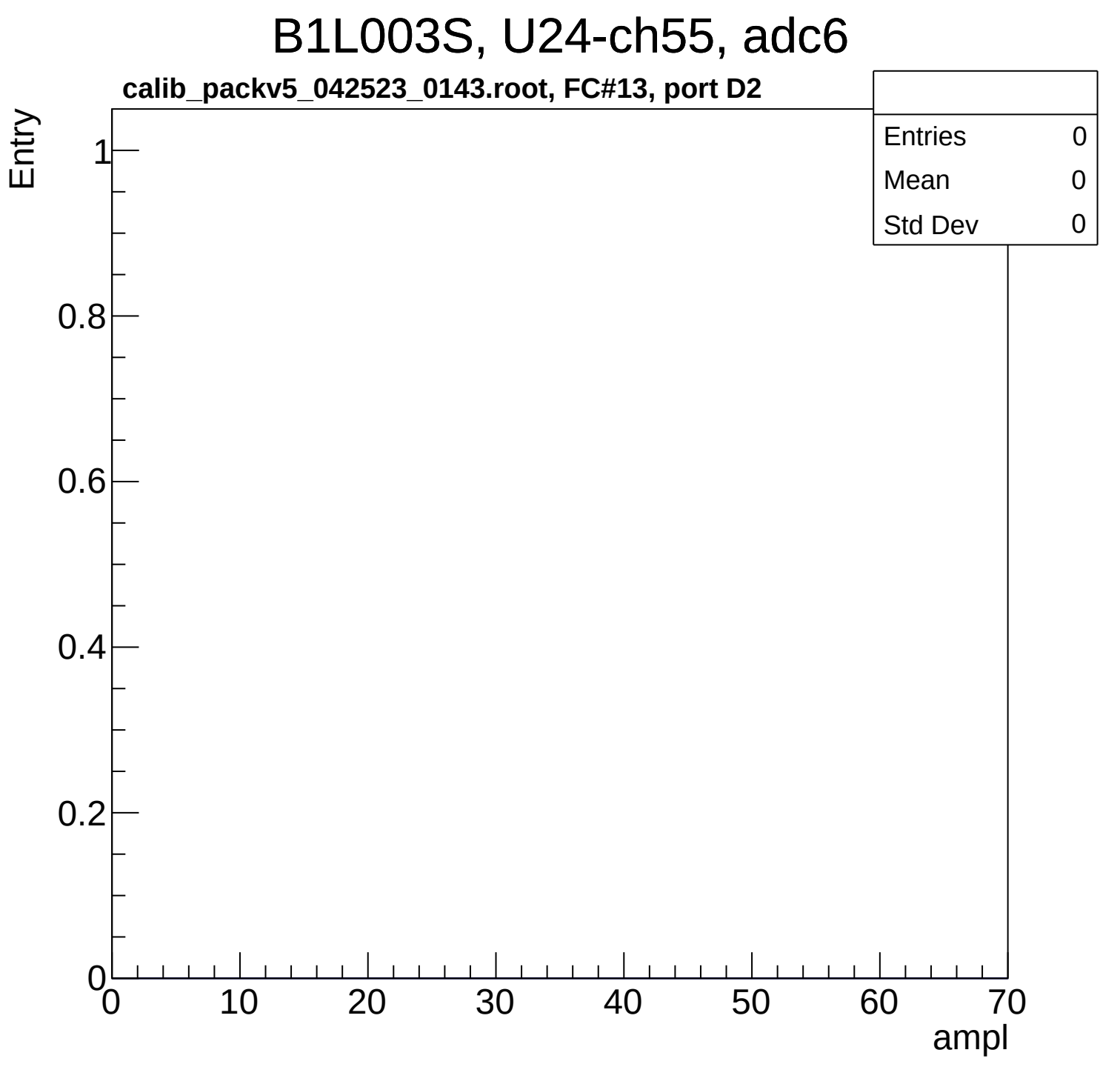

B1L003S, U24-ch55, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 38.6
Std Dev 3.072
Gaus mean : 38.9195
Gaus Width: 3.8147
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

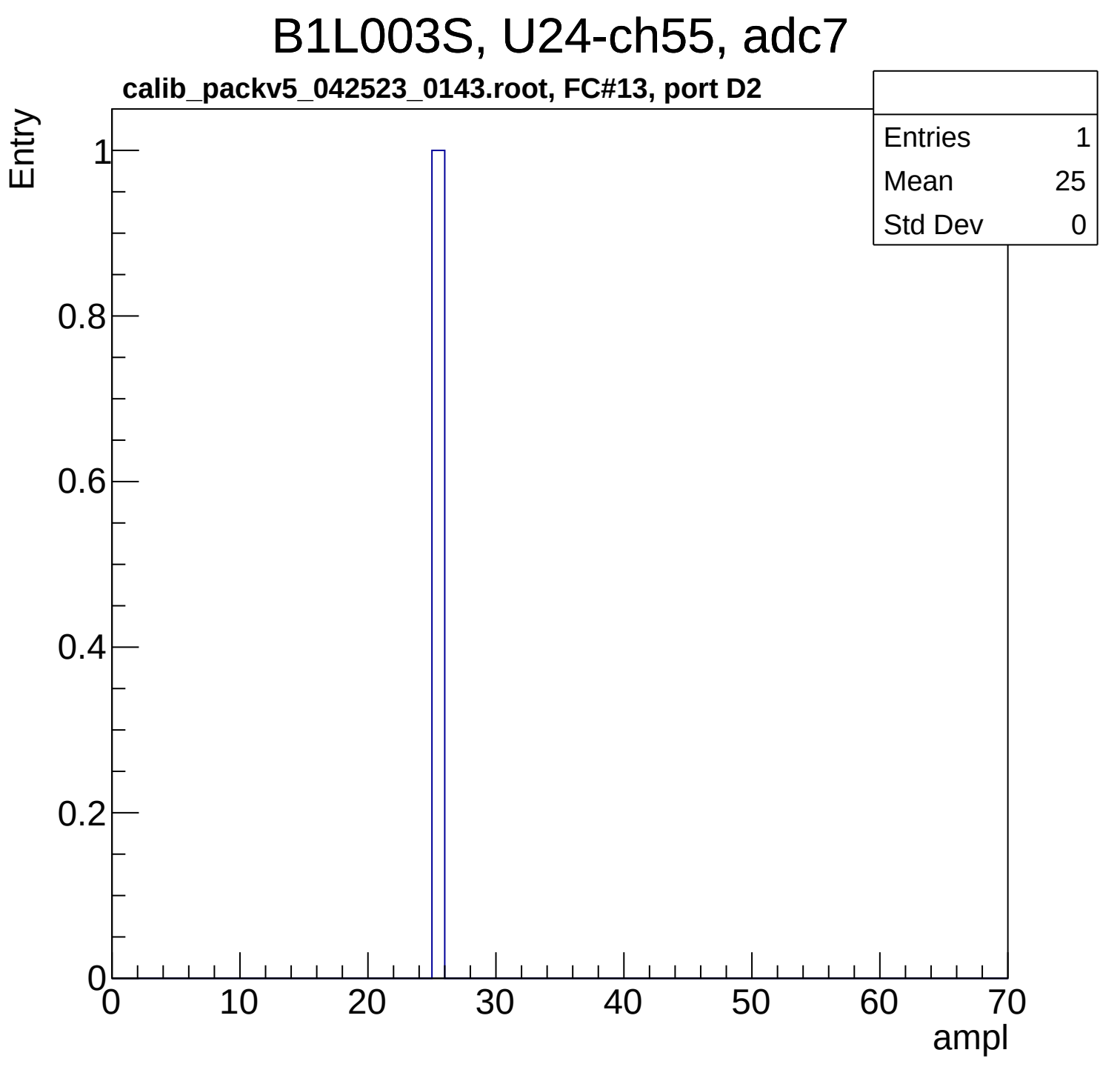

B1L003S, U24-ch55, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 44.48
Std Dev 3.148
Gaus mean : 44.0542
Gaus Width: 3.0515
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

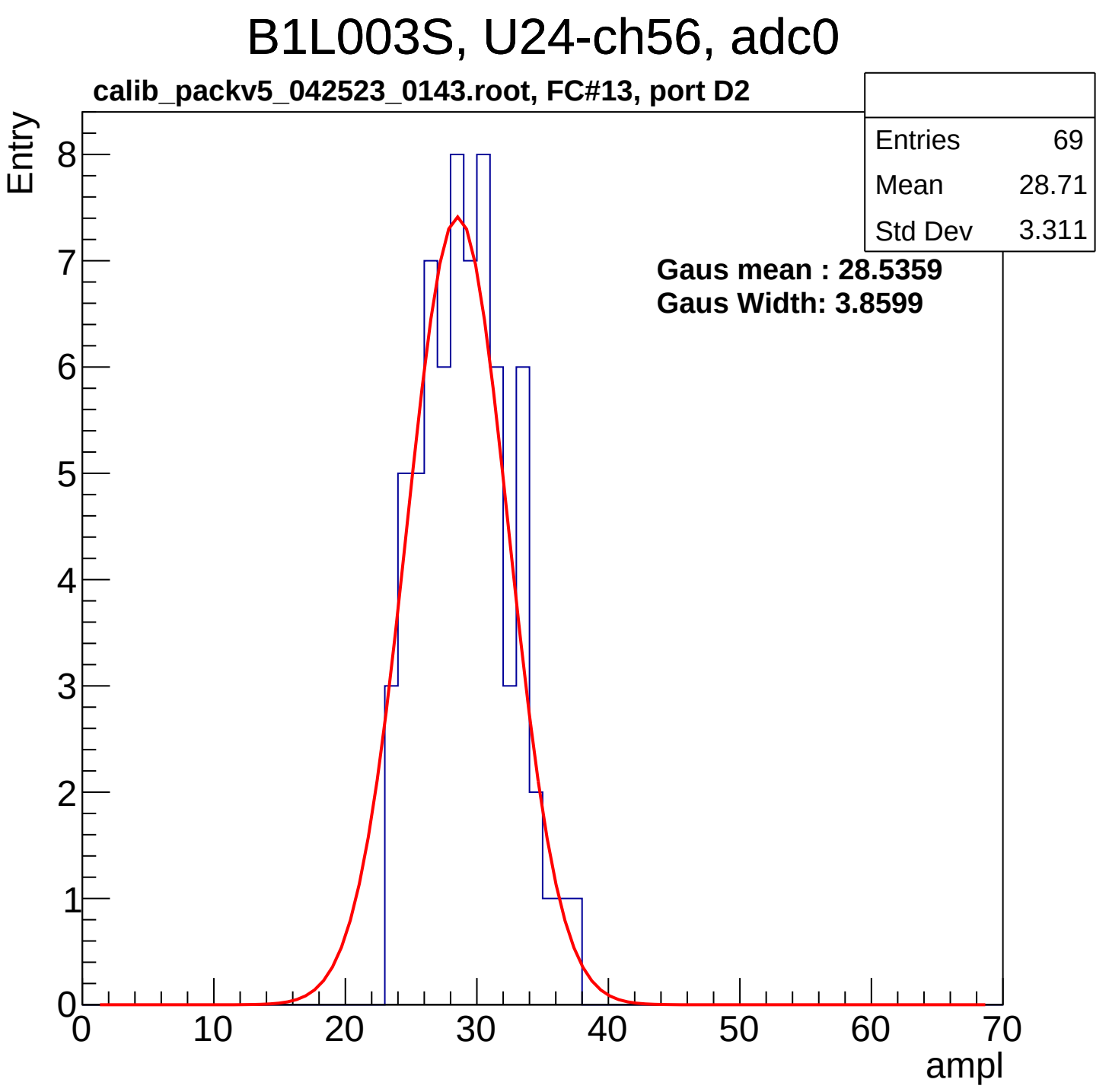

B1L003S, U24-ch55, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 51.06
Std Dev 2.948
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

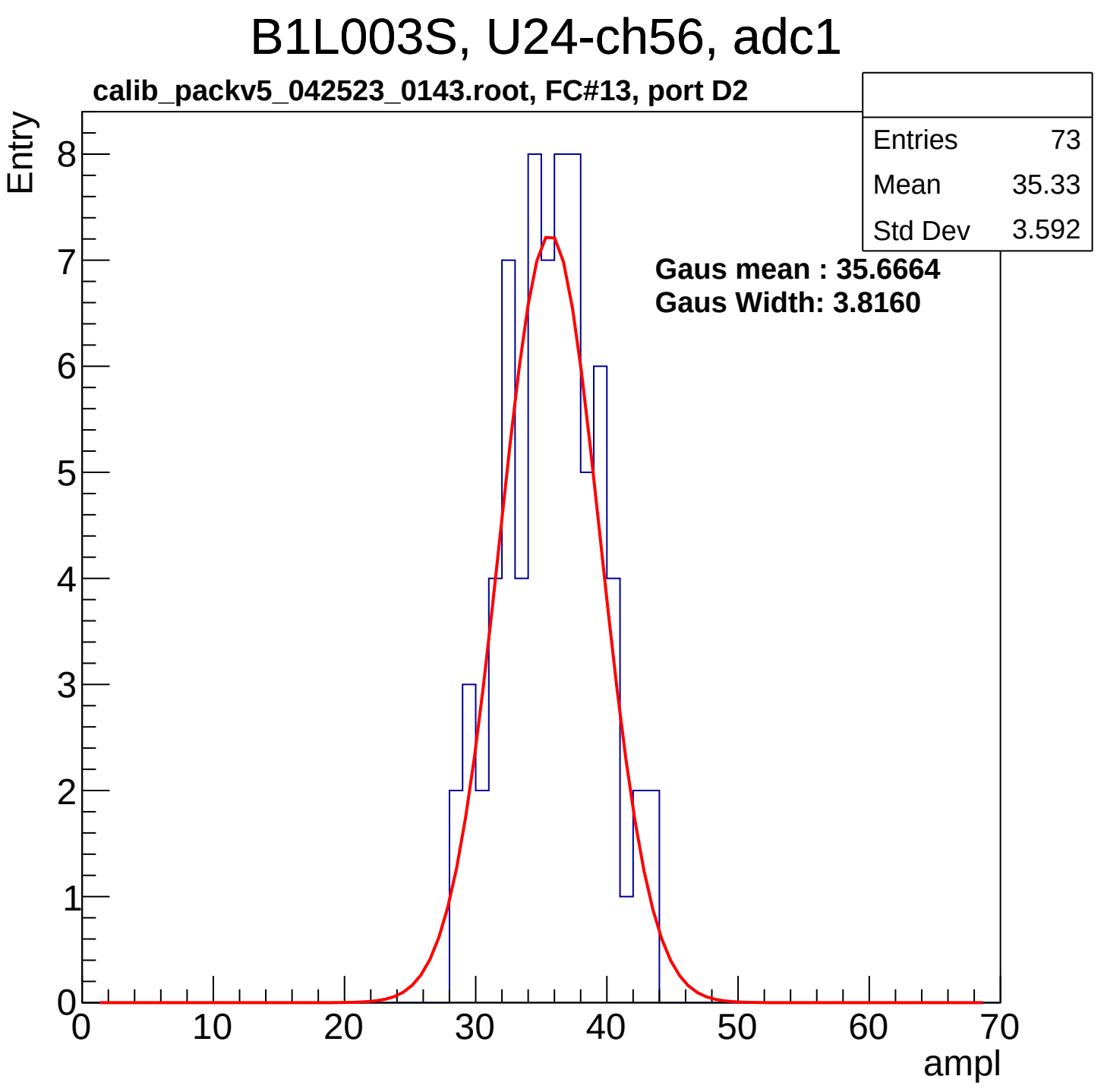

B1L003S, U24-ch55, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 57.29
Std Dev 2.788
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

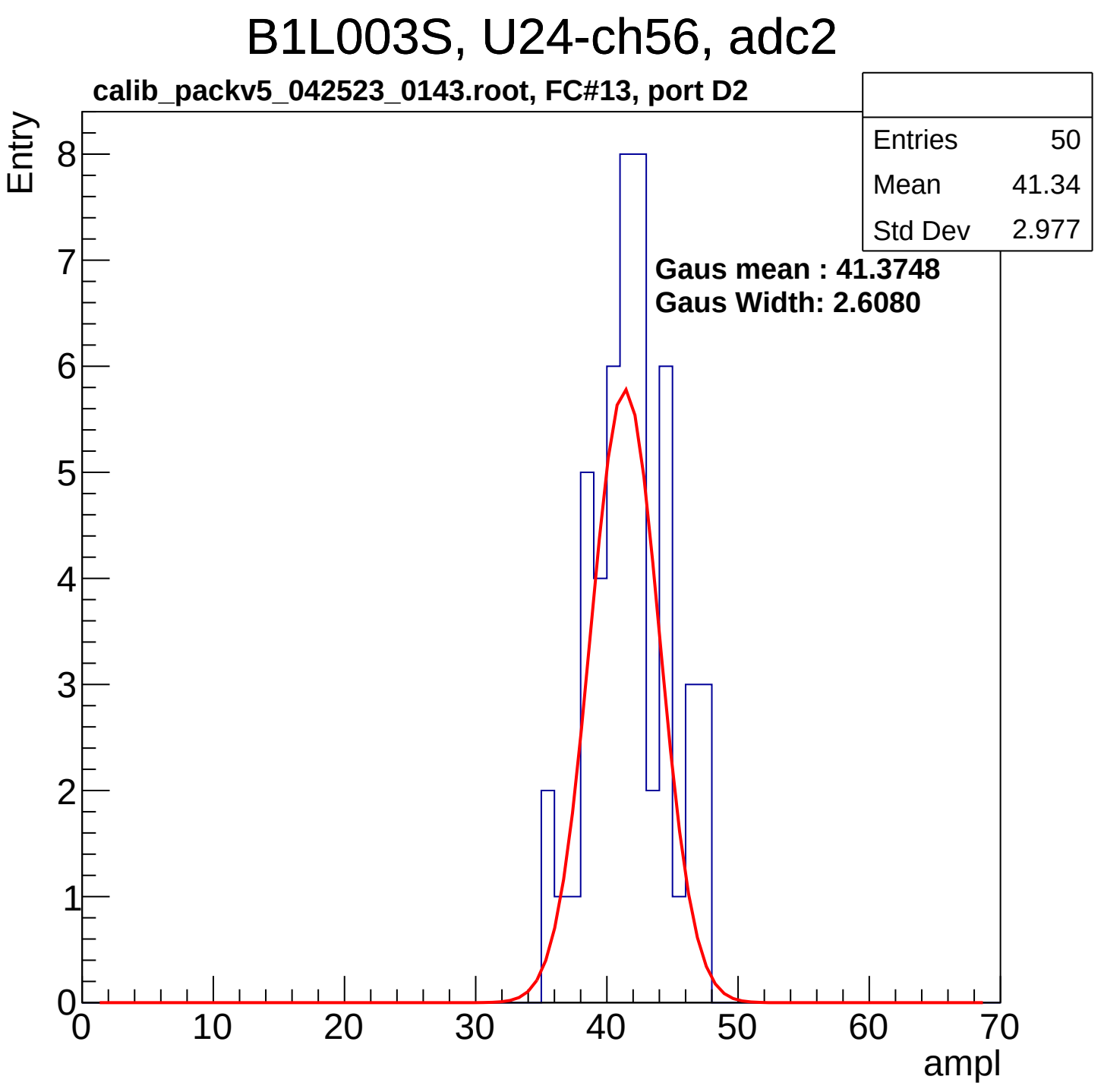

B1L003S, U24-ch55, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 59.41
Std Dev 10.5
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

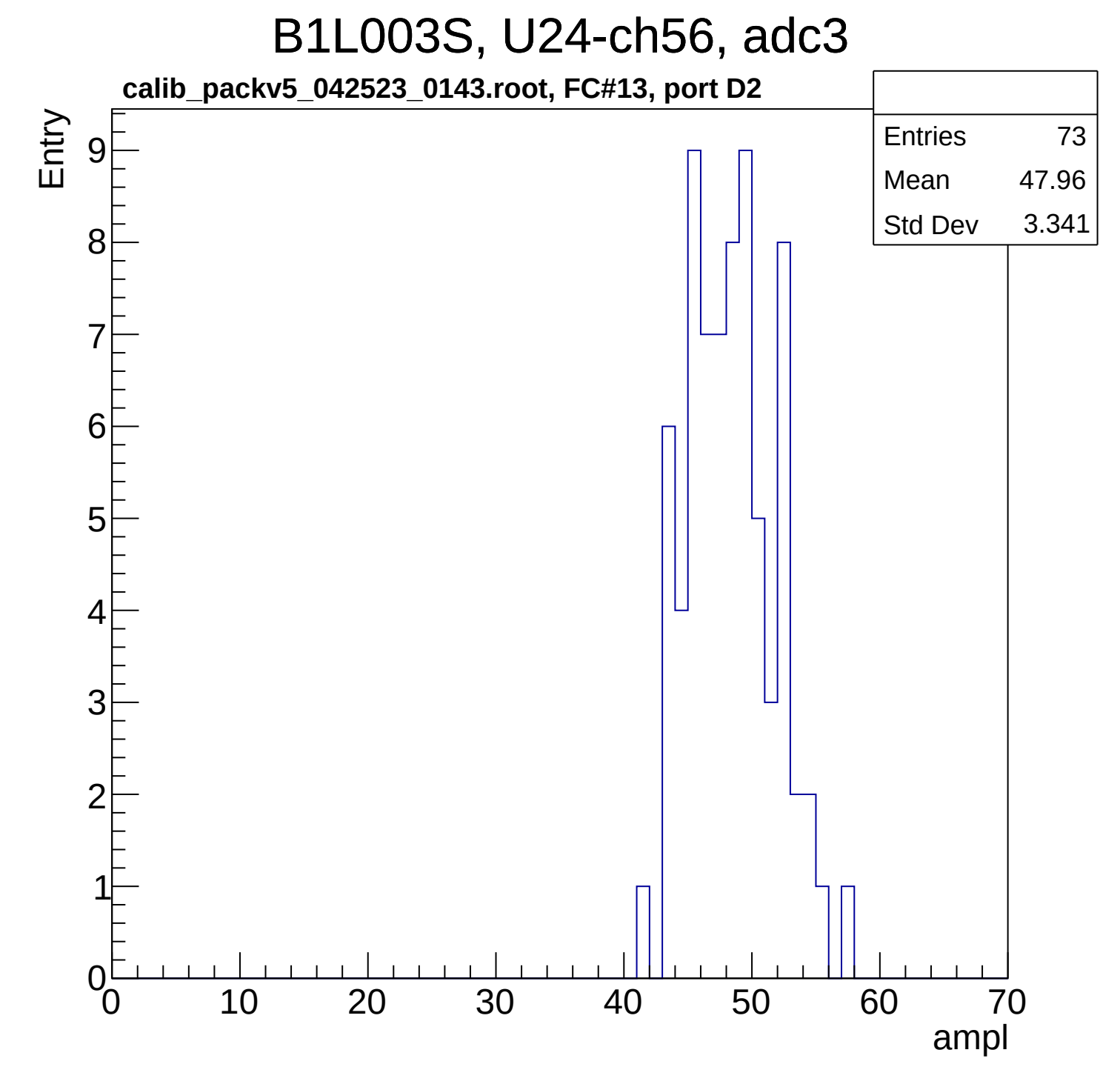

B1L003S, U24-ch55, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

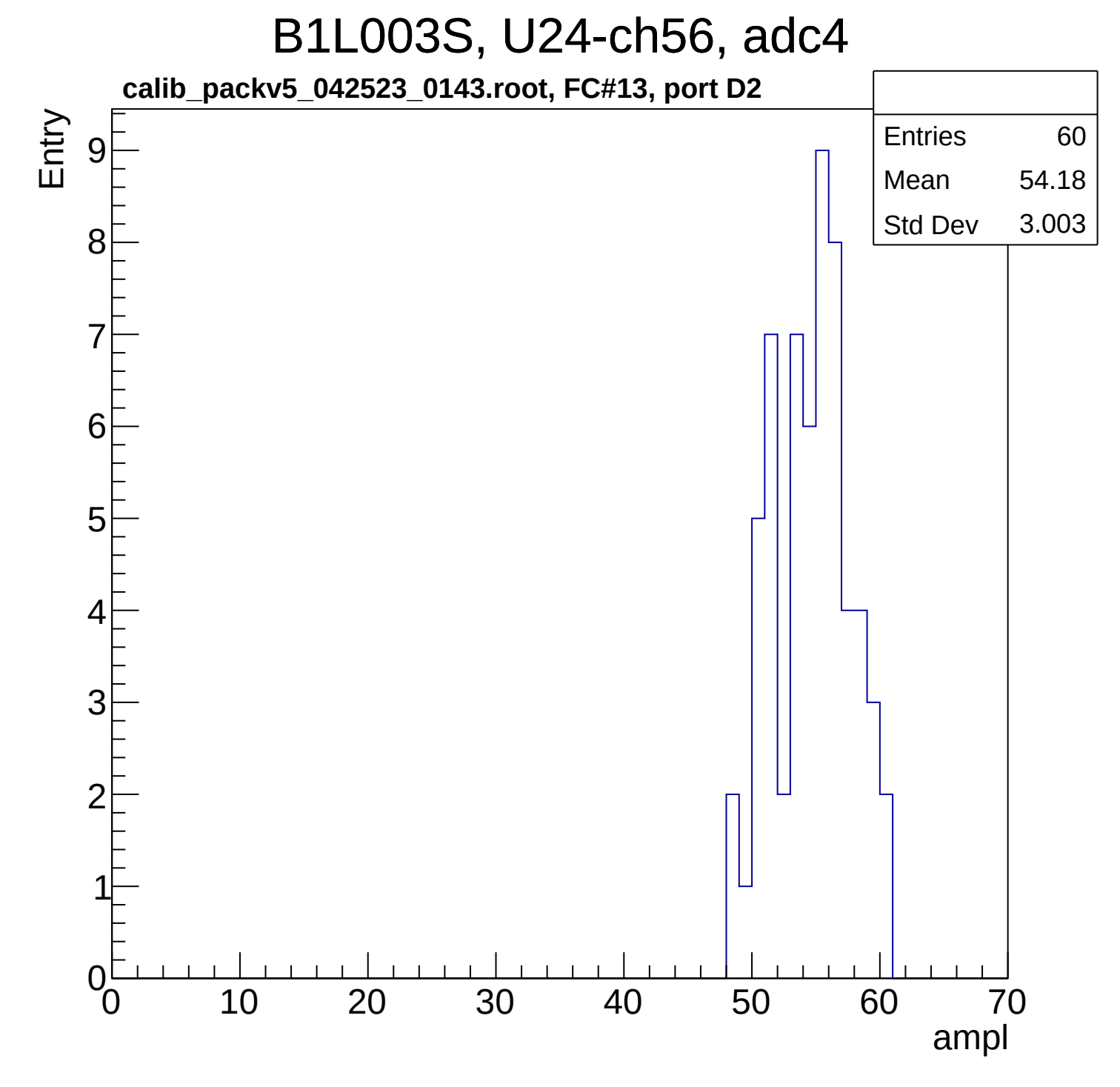

B1L003S, U24-ch55, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

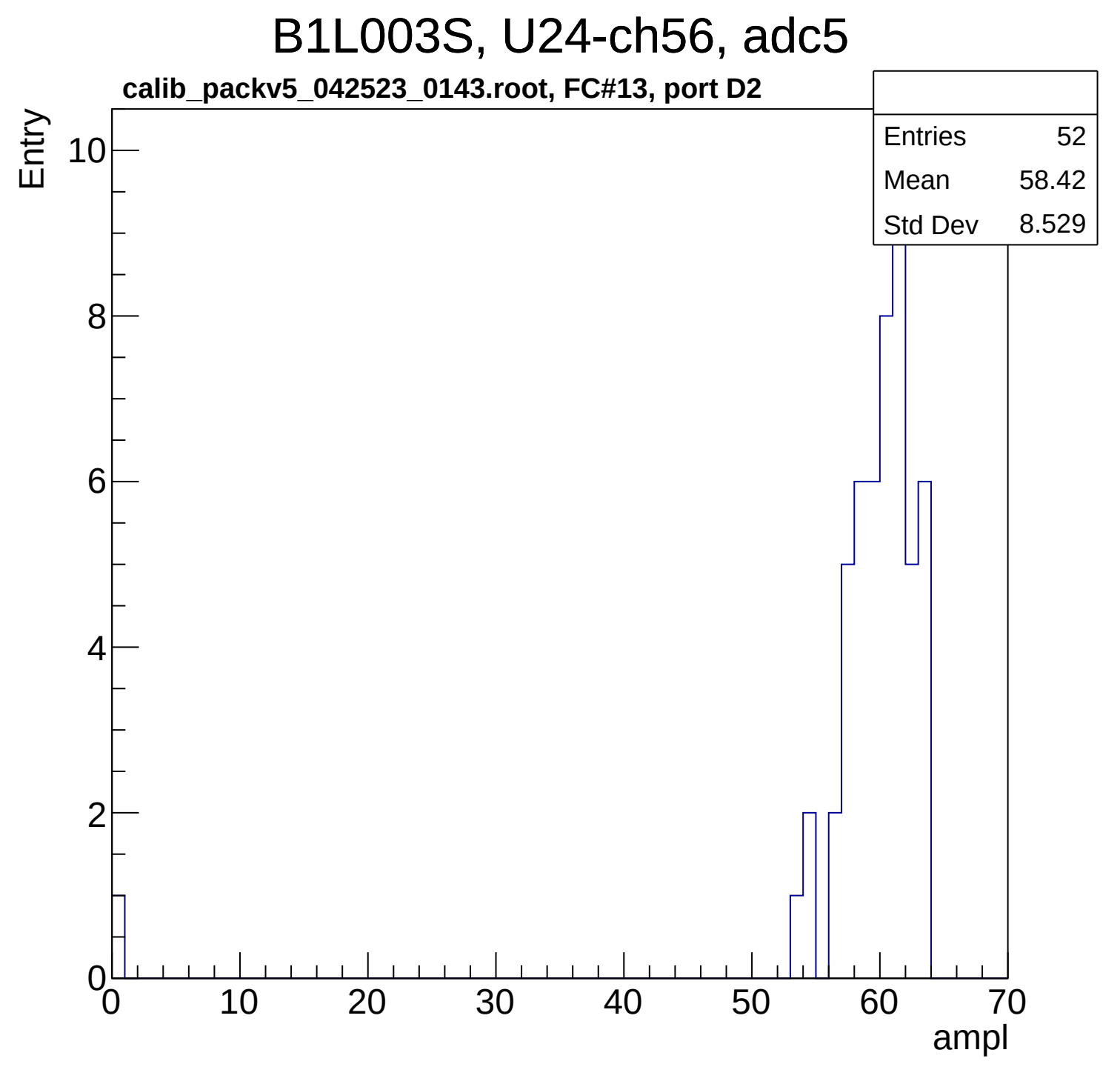

B1L003S, U24-ch56, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 28.71
Std Dev 3.311
Gaus mean : 28.5359
Gaus Width: 3.8599
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch56, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.33
Std Dev 3.592
Gaus mean : 35.6664
Gaus Width: 3.8160
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch56, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 41.34
Std Dev 2.977
Gaus mean : 41.3748
Gaus Width: 2.6080
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch56, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 47.96
Std Dev 3.341
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch56, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.18
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch56, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.42
Std Dev 8.529
Entry
ampl
0
2
4
6
8
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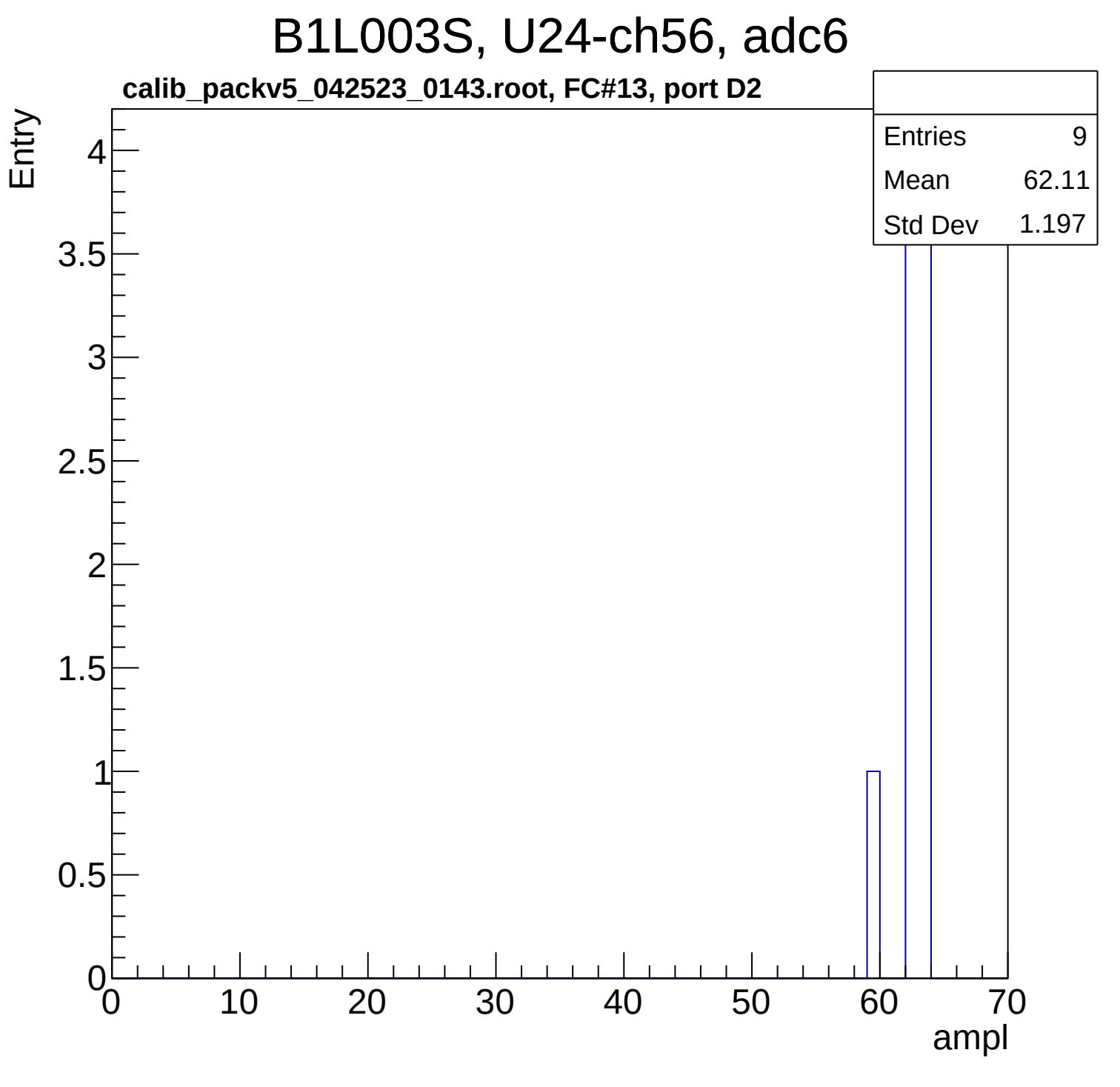
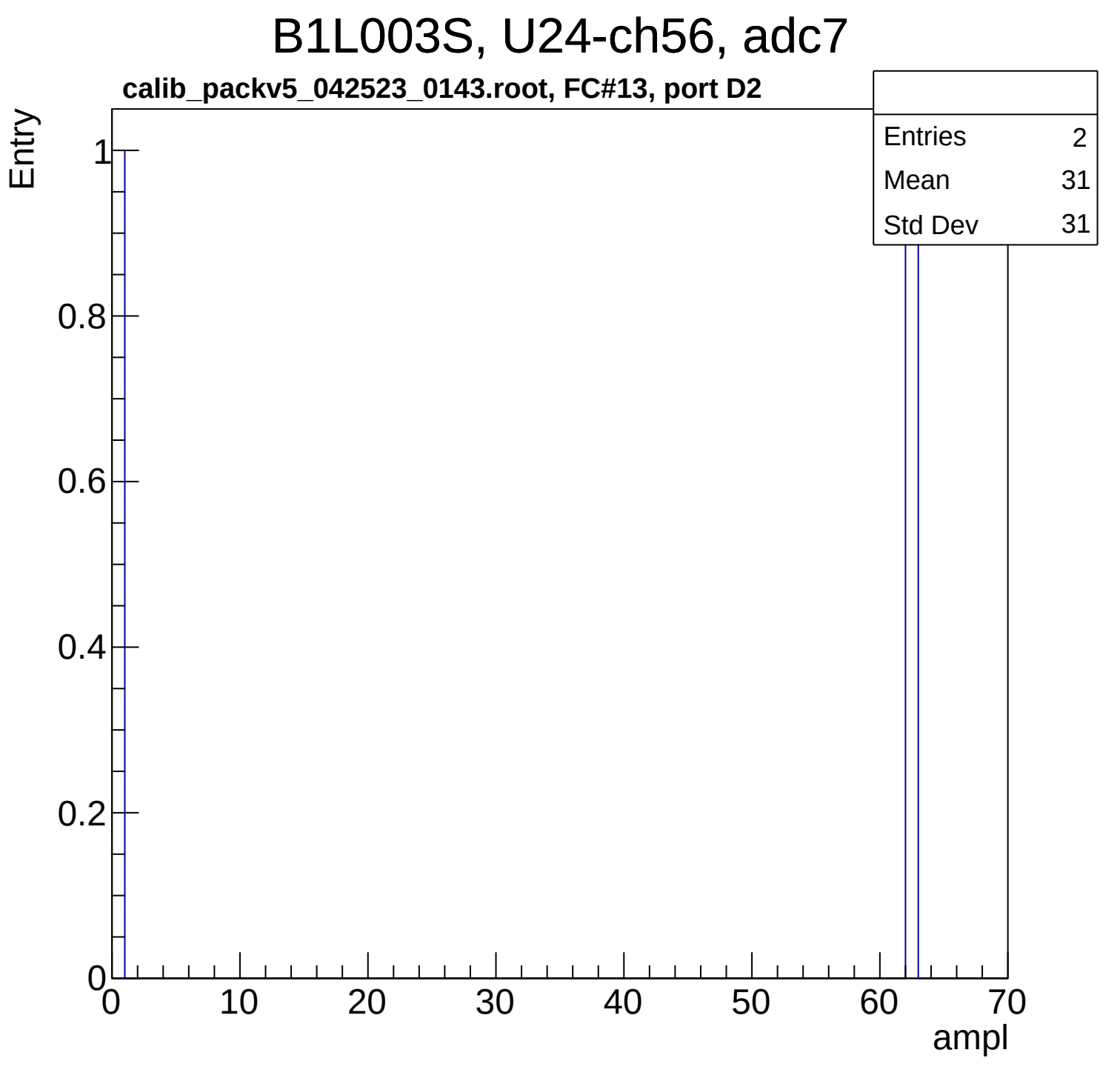
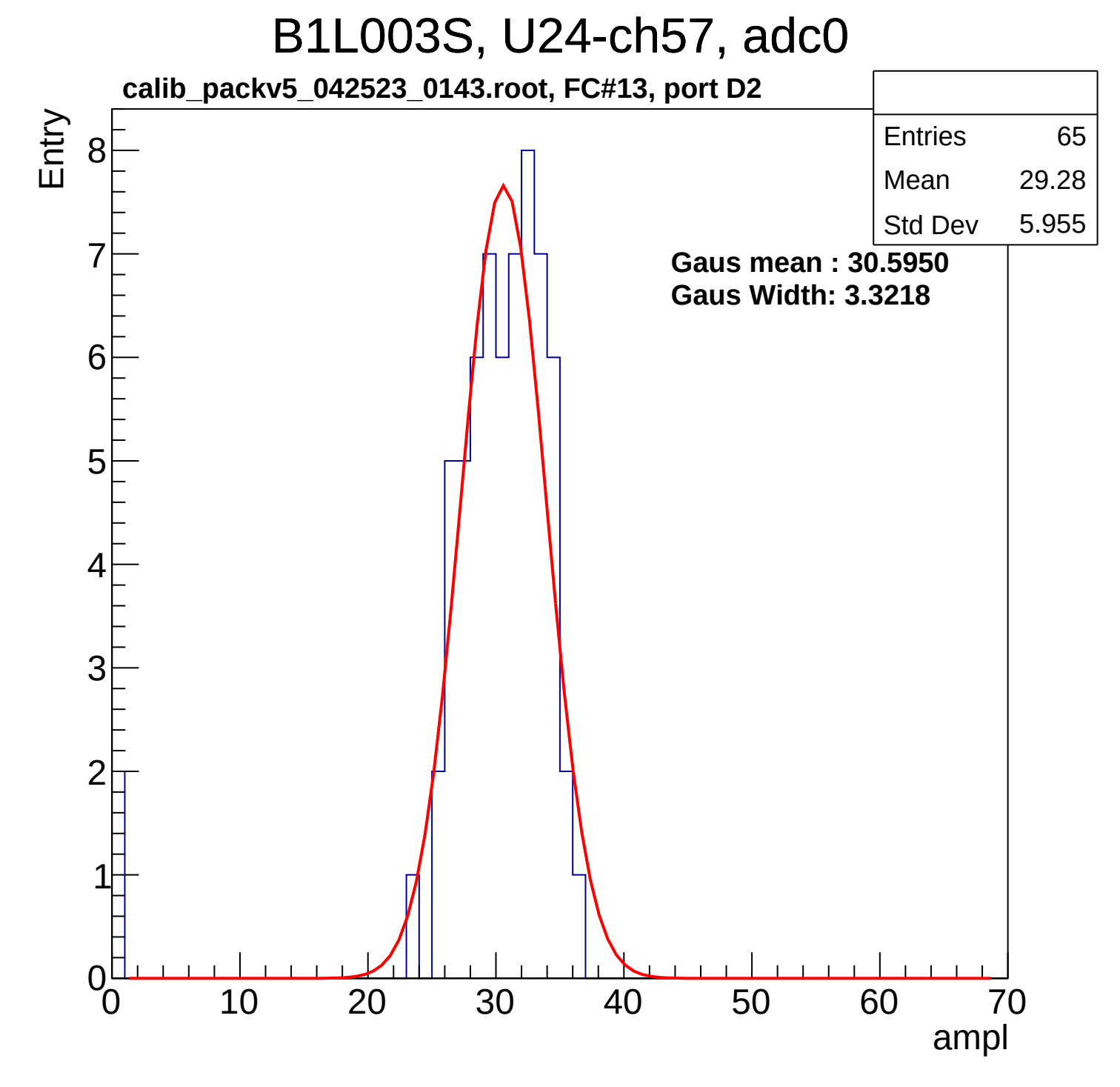
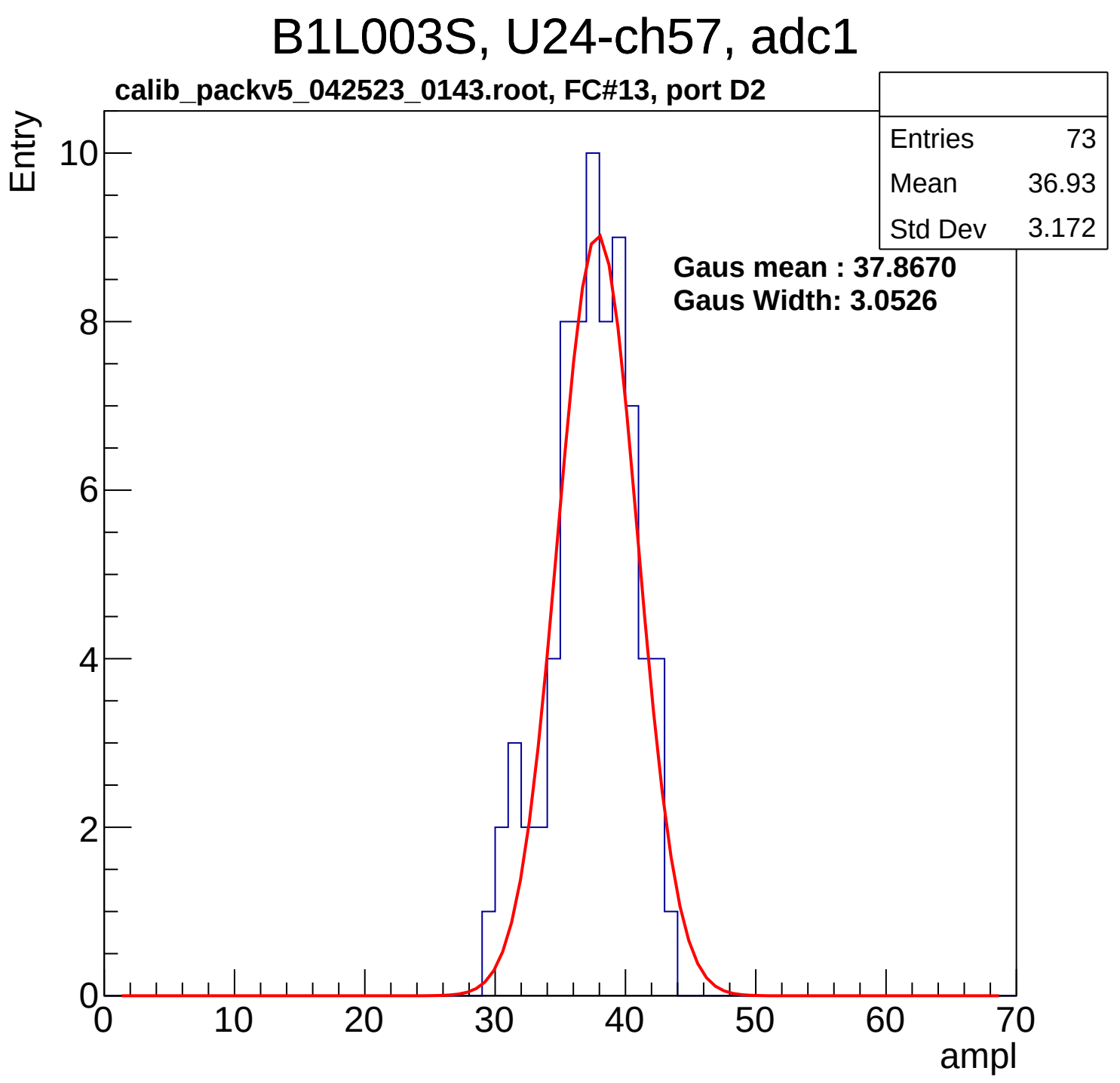
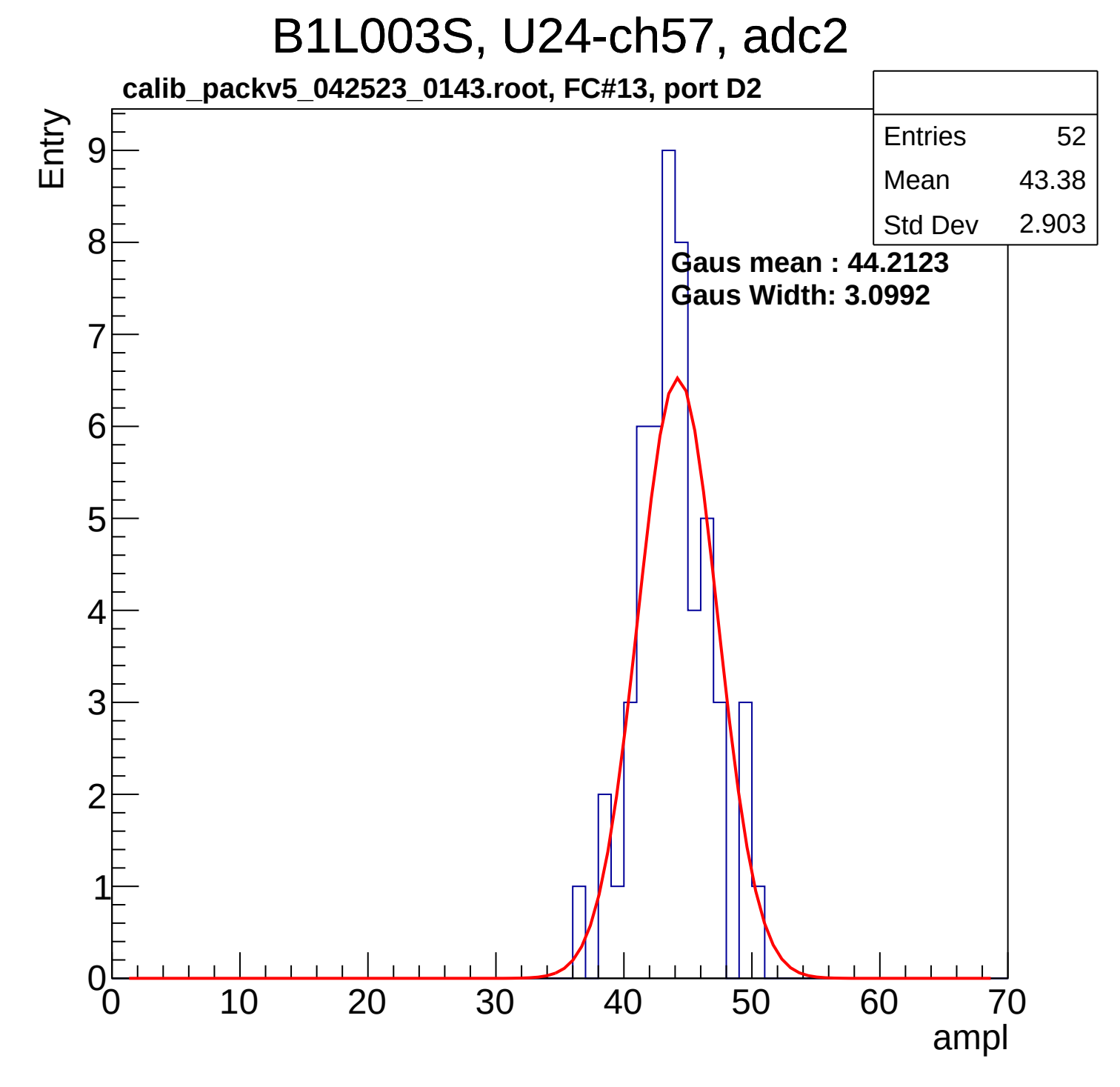
0
10
20
30
40
50
60
70

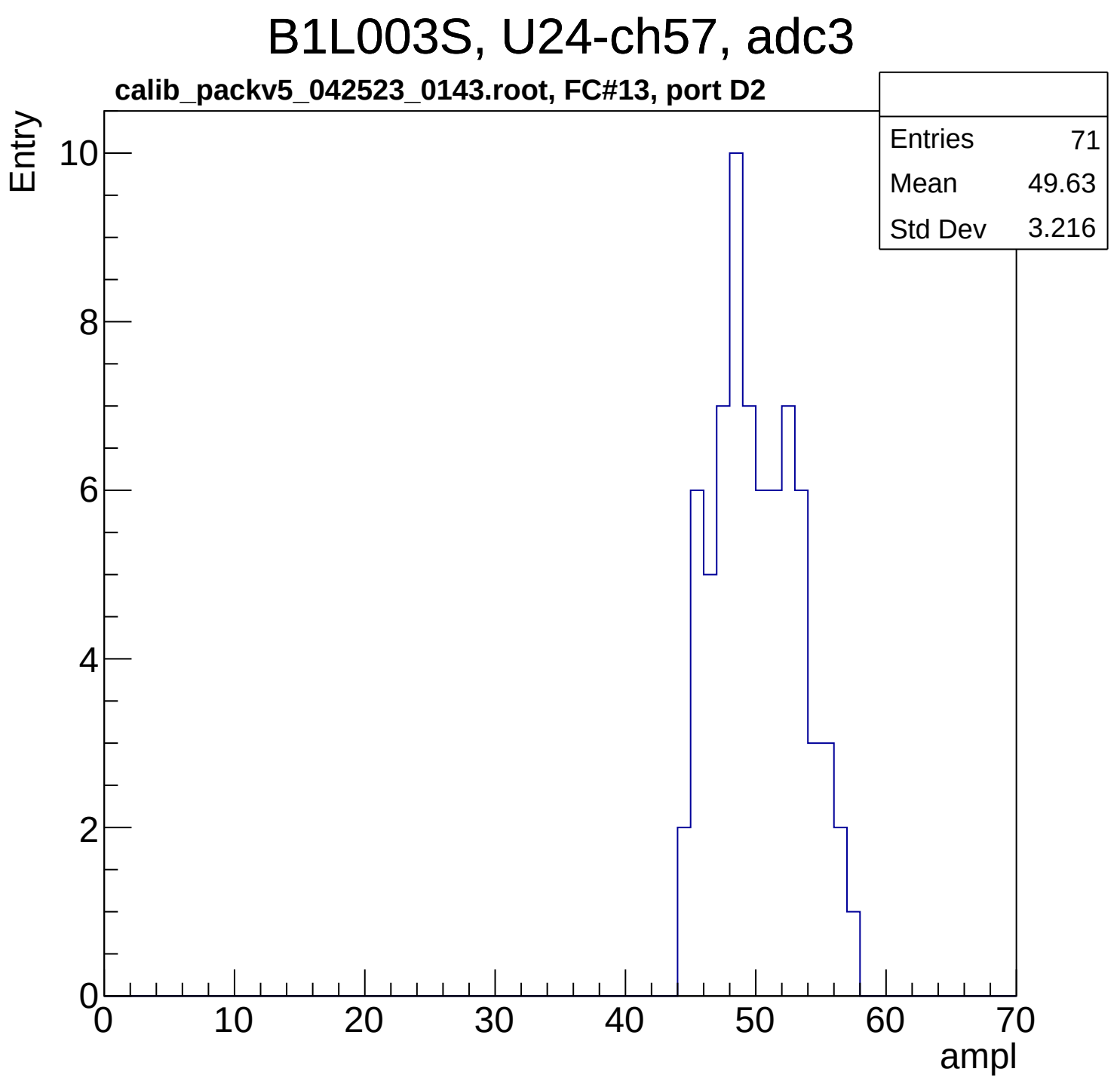

B1L003S, U24-ch56, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 1.197
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

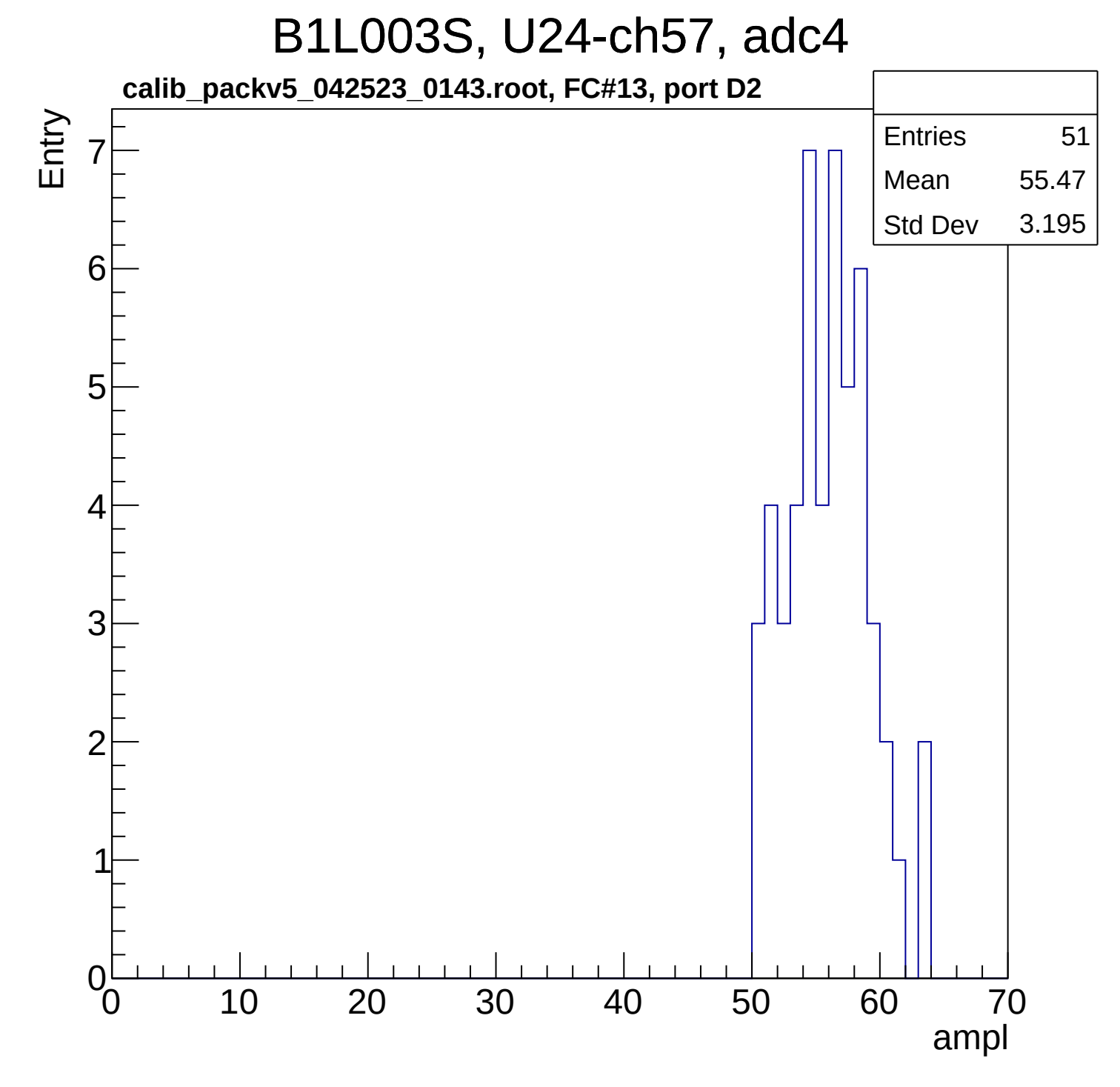

B1L003S, U24-ch56, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

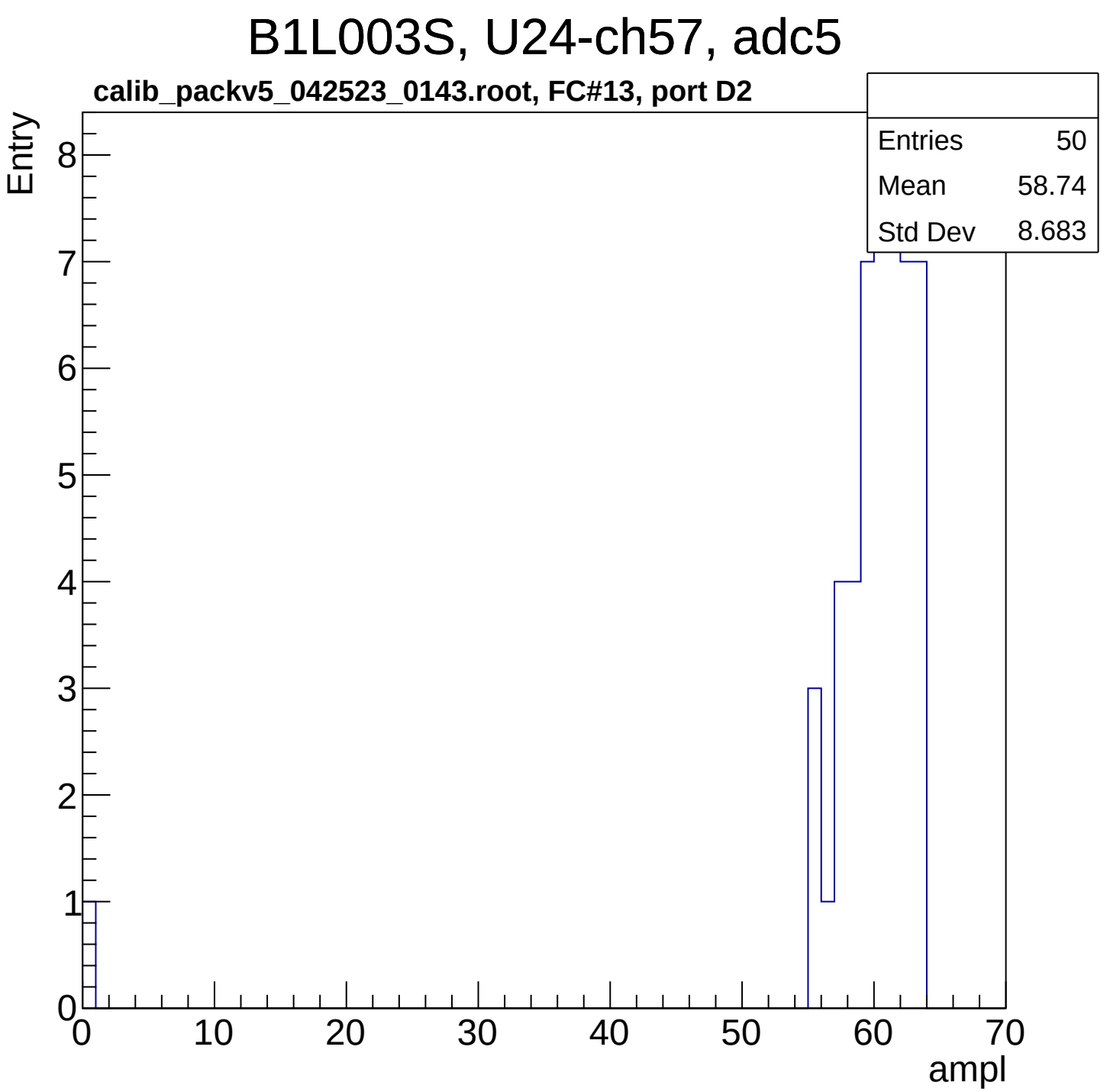

B1L003S, U24-ch57, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.28
Std Dev 5.955
Gaus mean : 30.5950
Gaus Width: 3.3218
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch57, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.93
Std Dev 3.172
Gaus mean : 37.8670
Gaus Width: 3.0526
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch57, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 43.38
Std Dev 2.903
Gaus mean : 44.2123
Gaus Width: 3.0992
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch57, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.63
Std Dev 3.216
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch57, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.47
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch57, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.74
Std Dev 8.683
Entry
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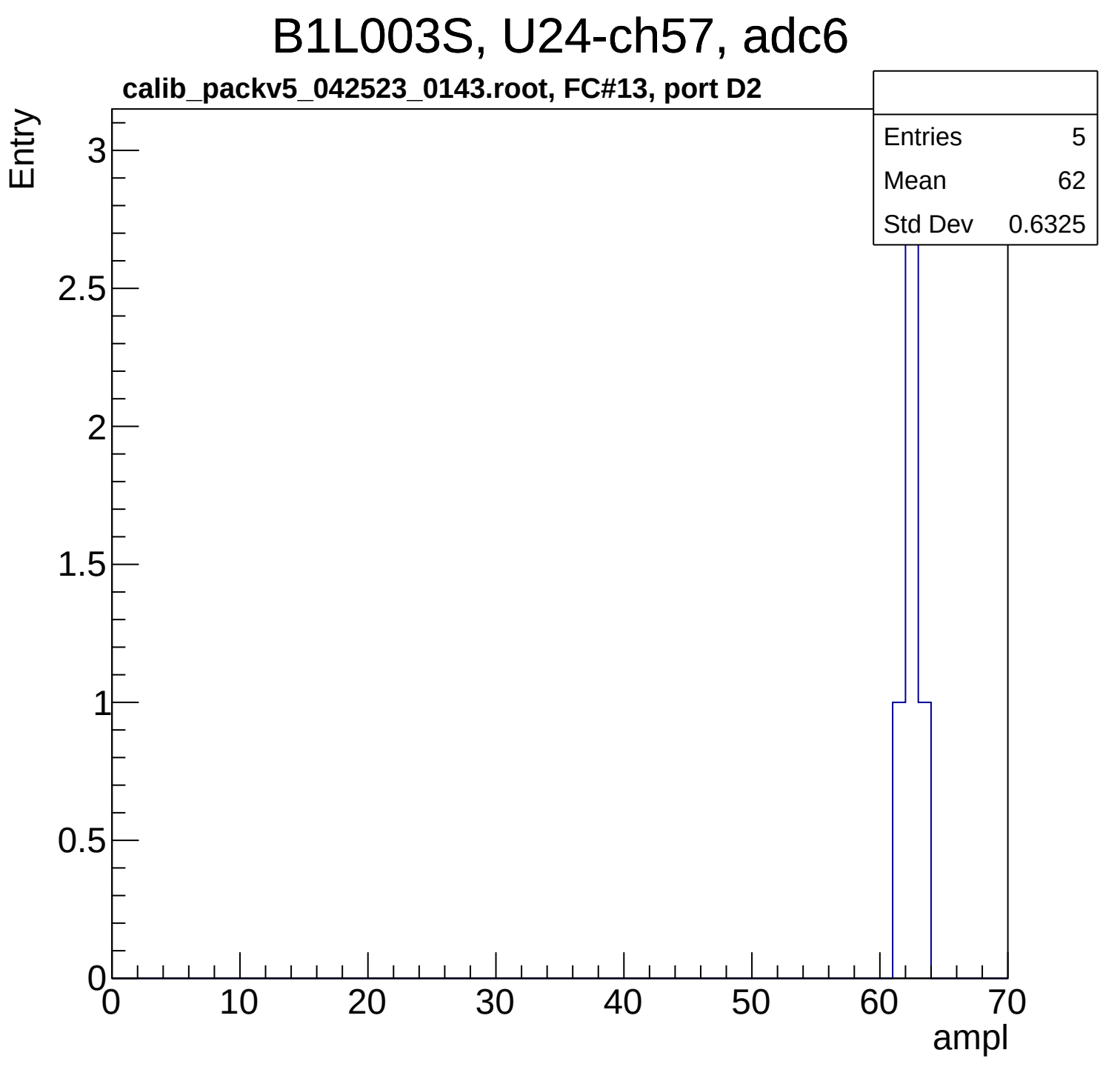
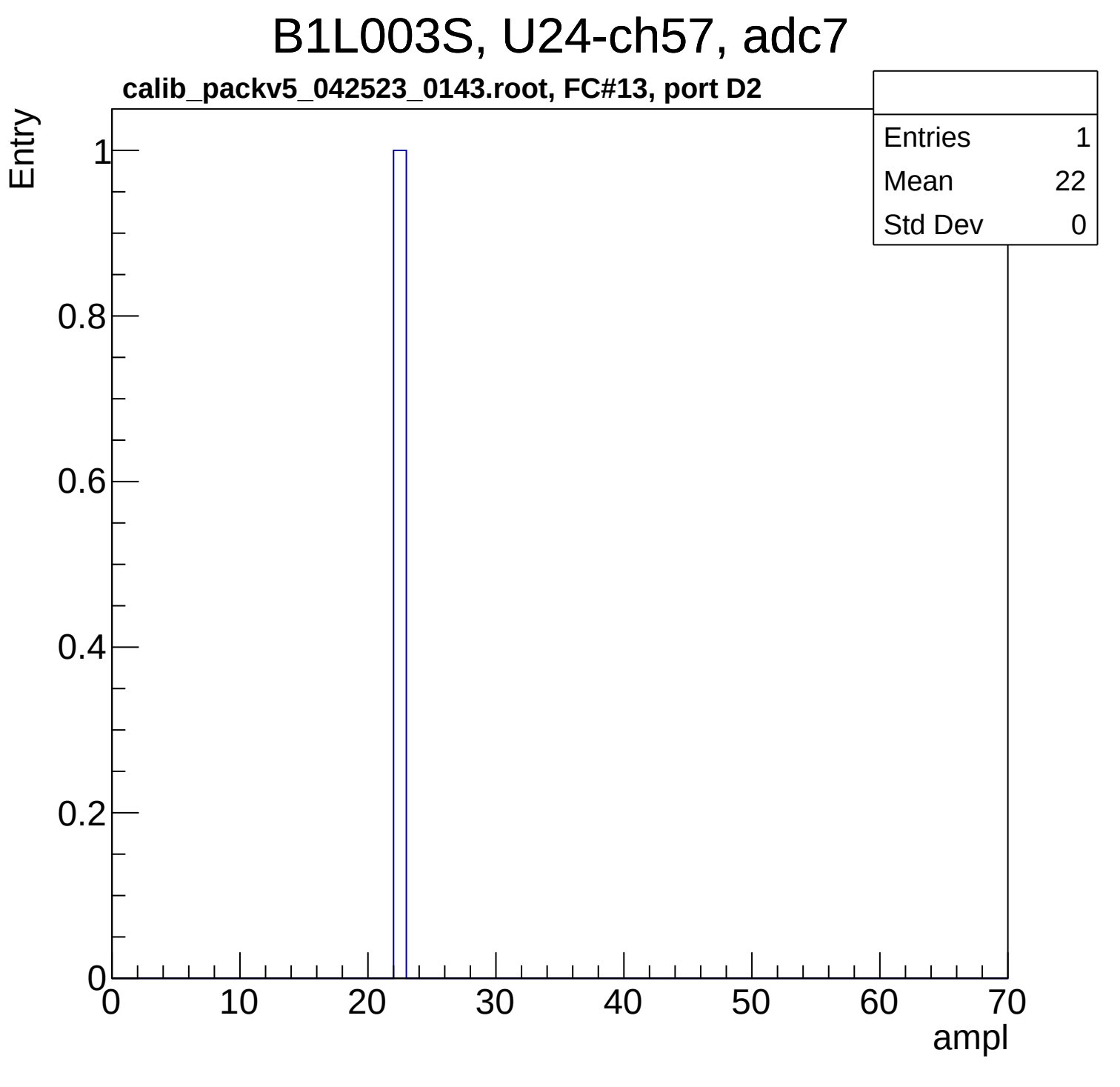
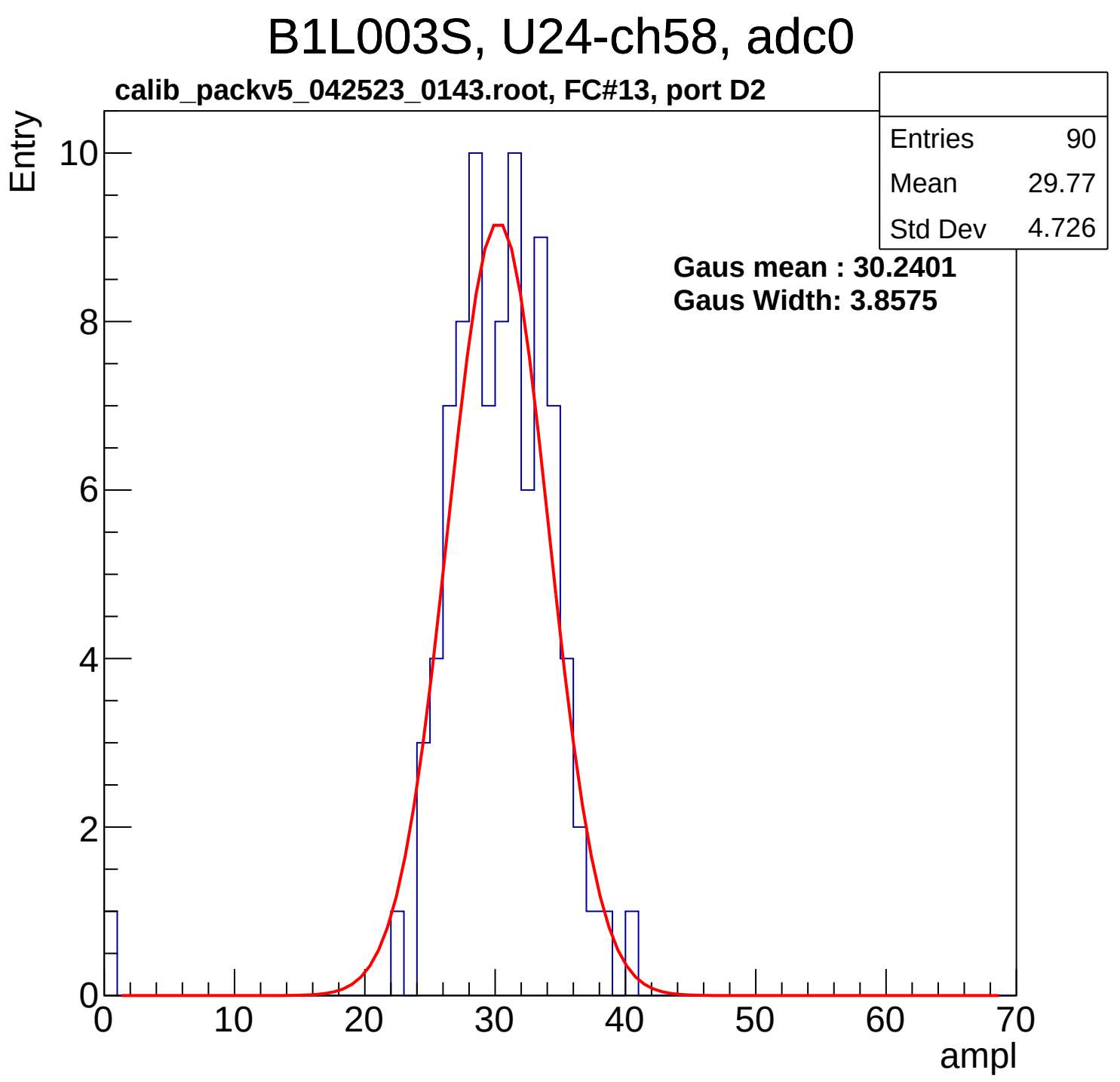
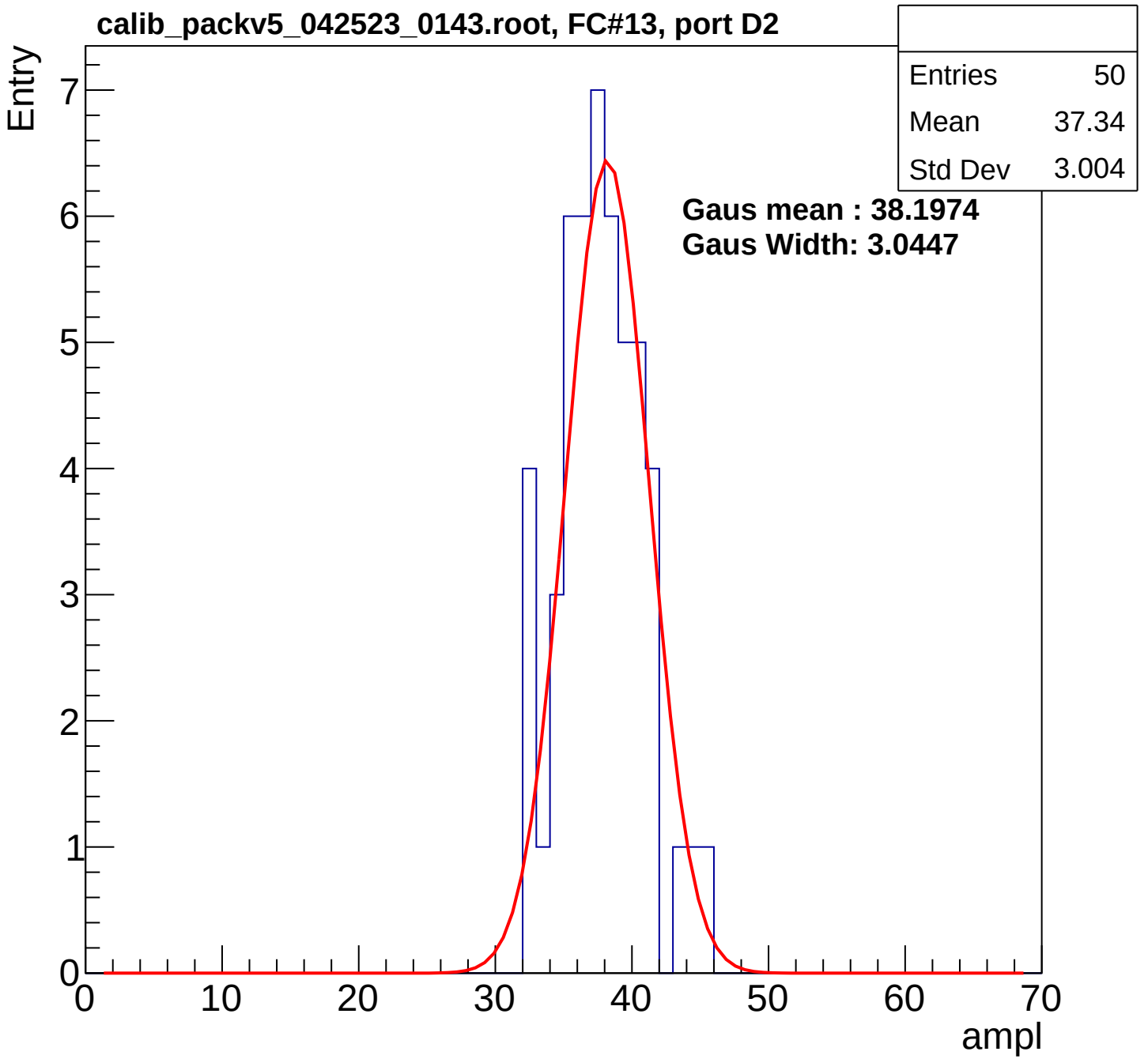
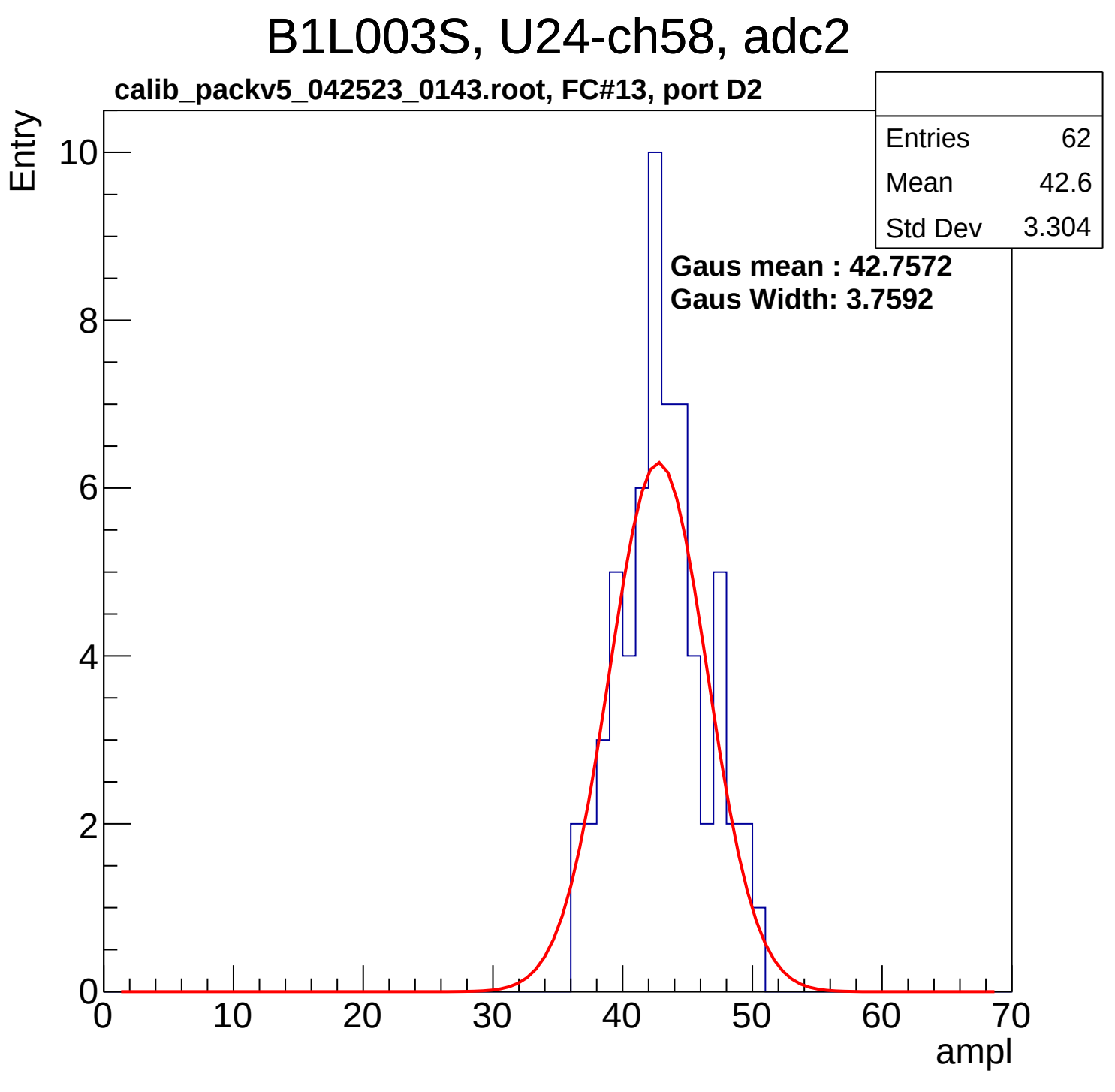
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

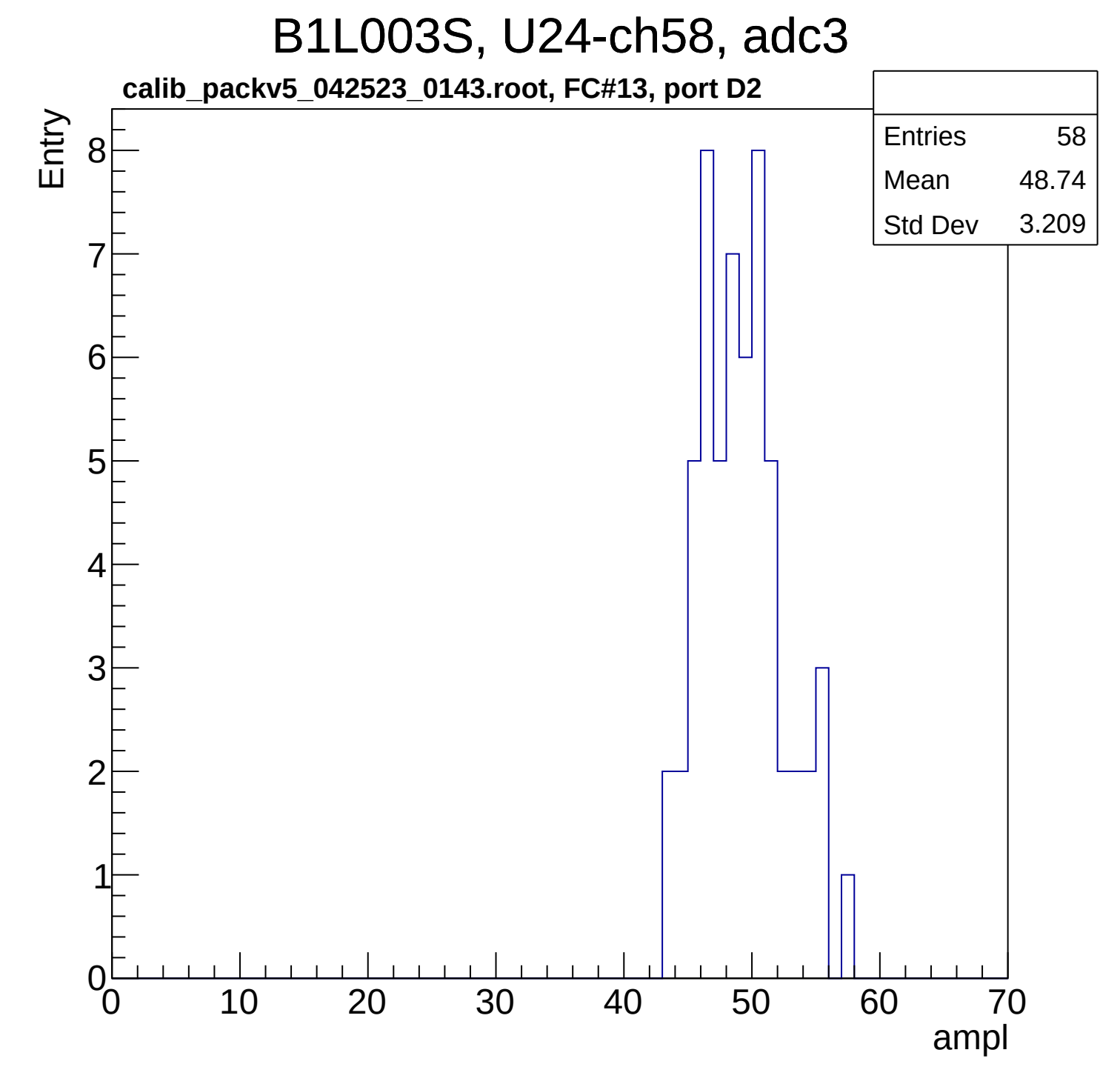

B1L003S, U24-ch57, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 0.6325
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch57, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch58, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 90
Mean 29.77
Std Dev 4.726
Gaus mean : 30.2401
Gaus Width: 3.8575
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch58, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 37.34
Std Dev 3.004
Gaus mean : 38.1974
Gaus Width: 3.0447
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch58, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.6
Std Dev 3.304
Gaus mean : 42.7572
Gaus Width: 3.7592
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch58, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.74
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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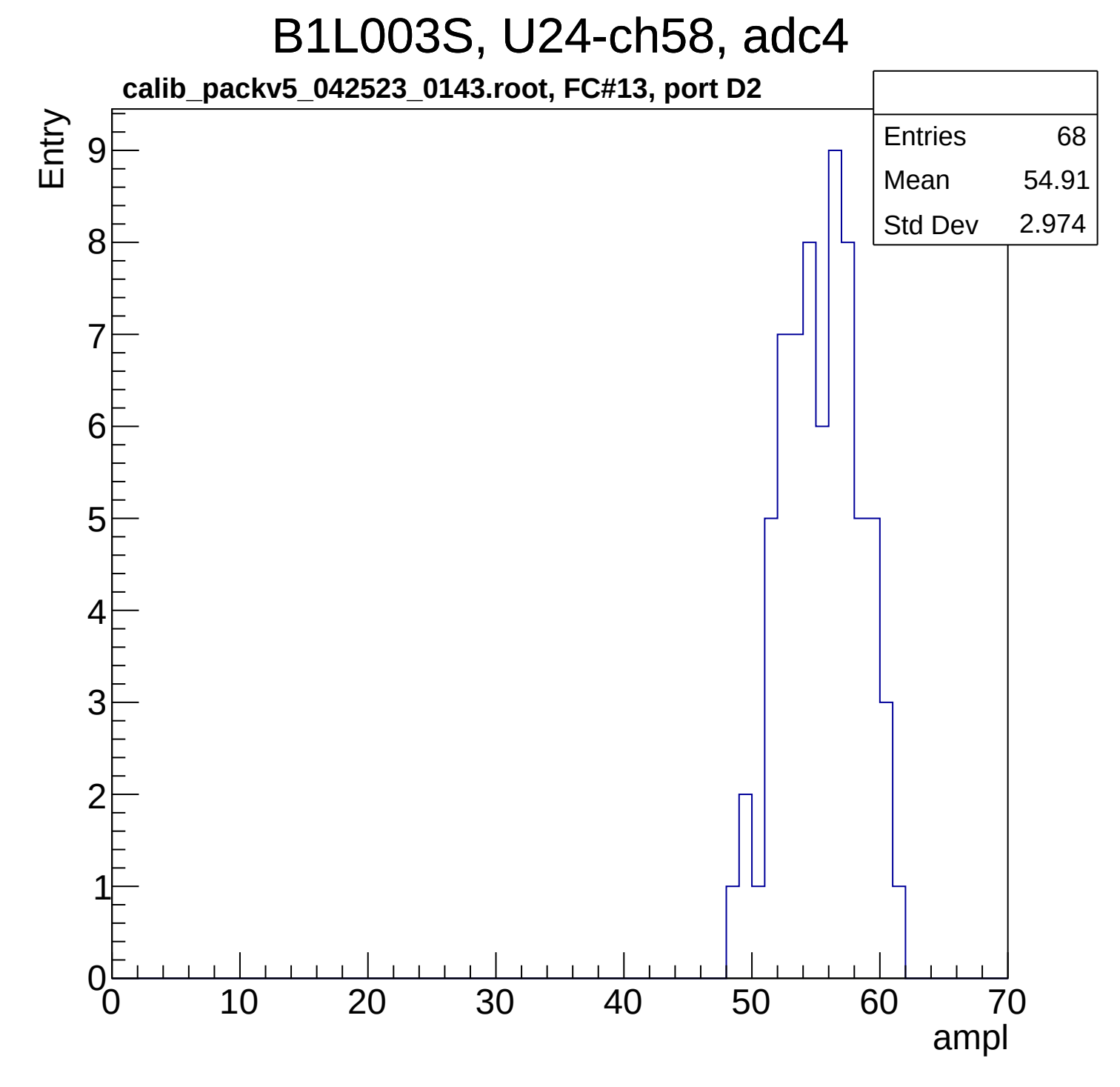
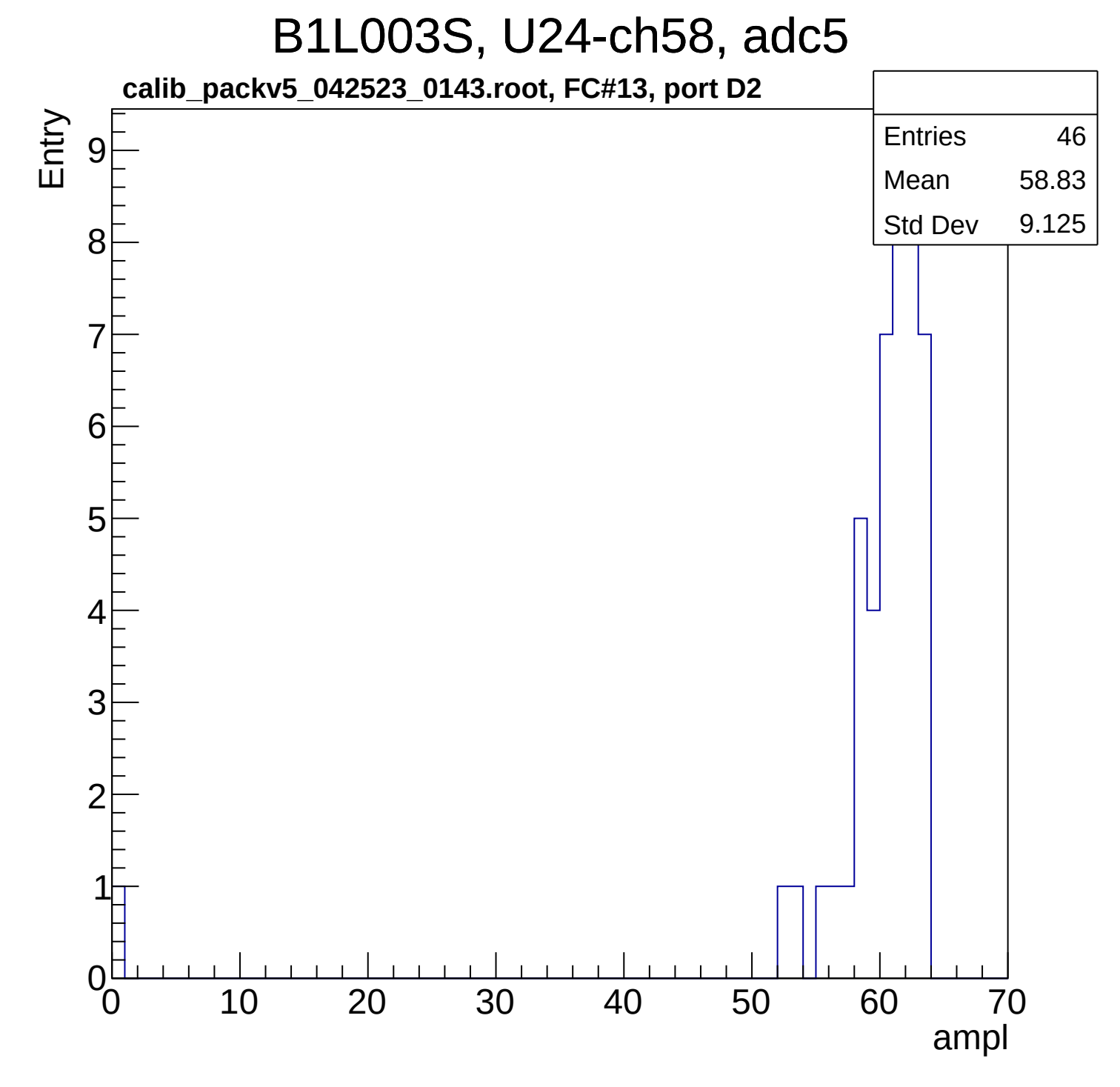
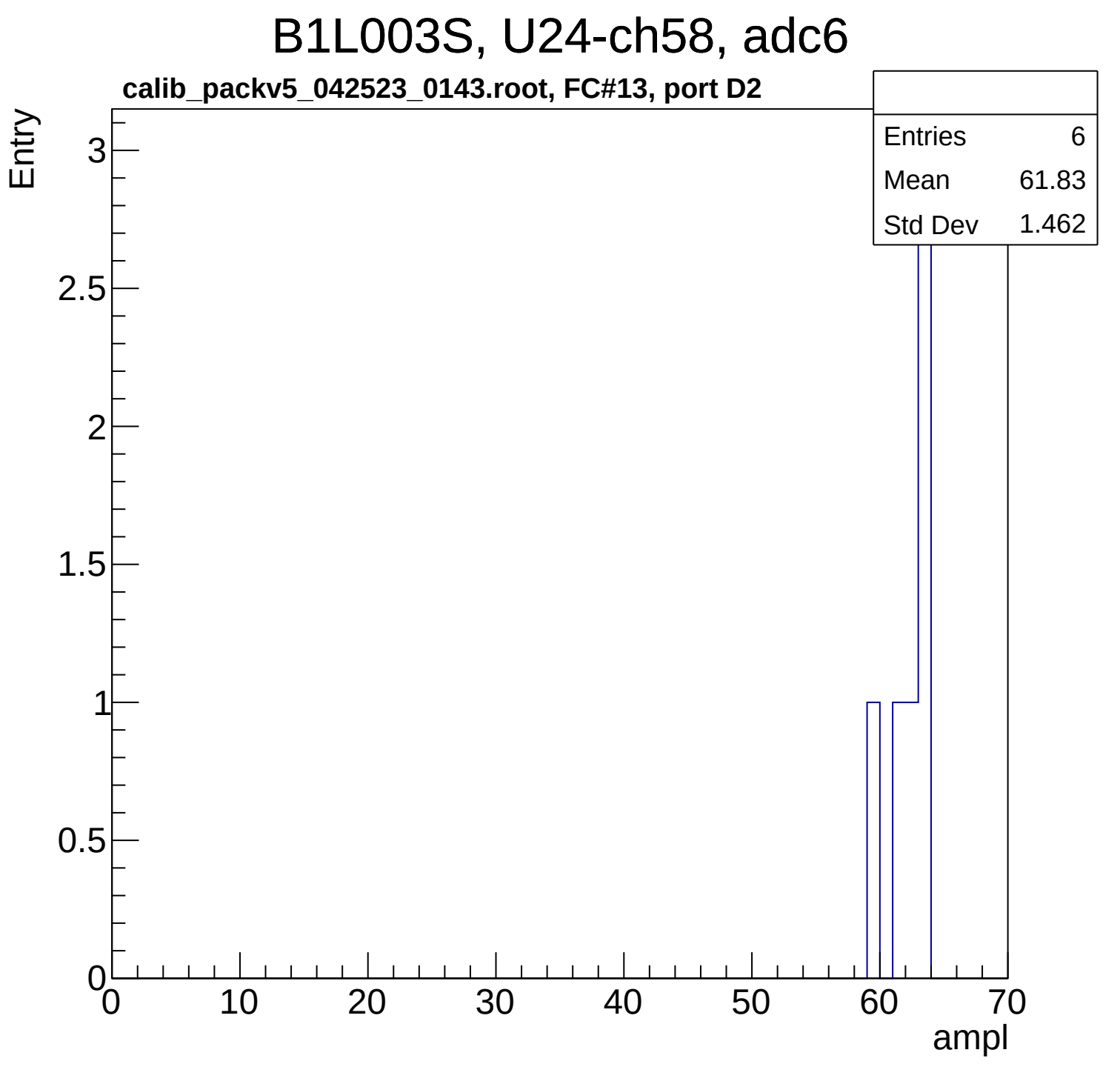
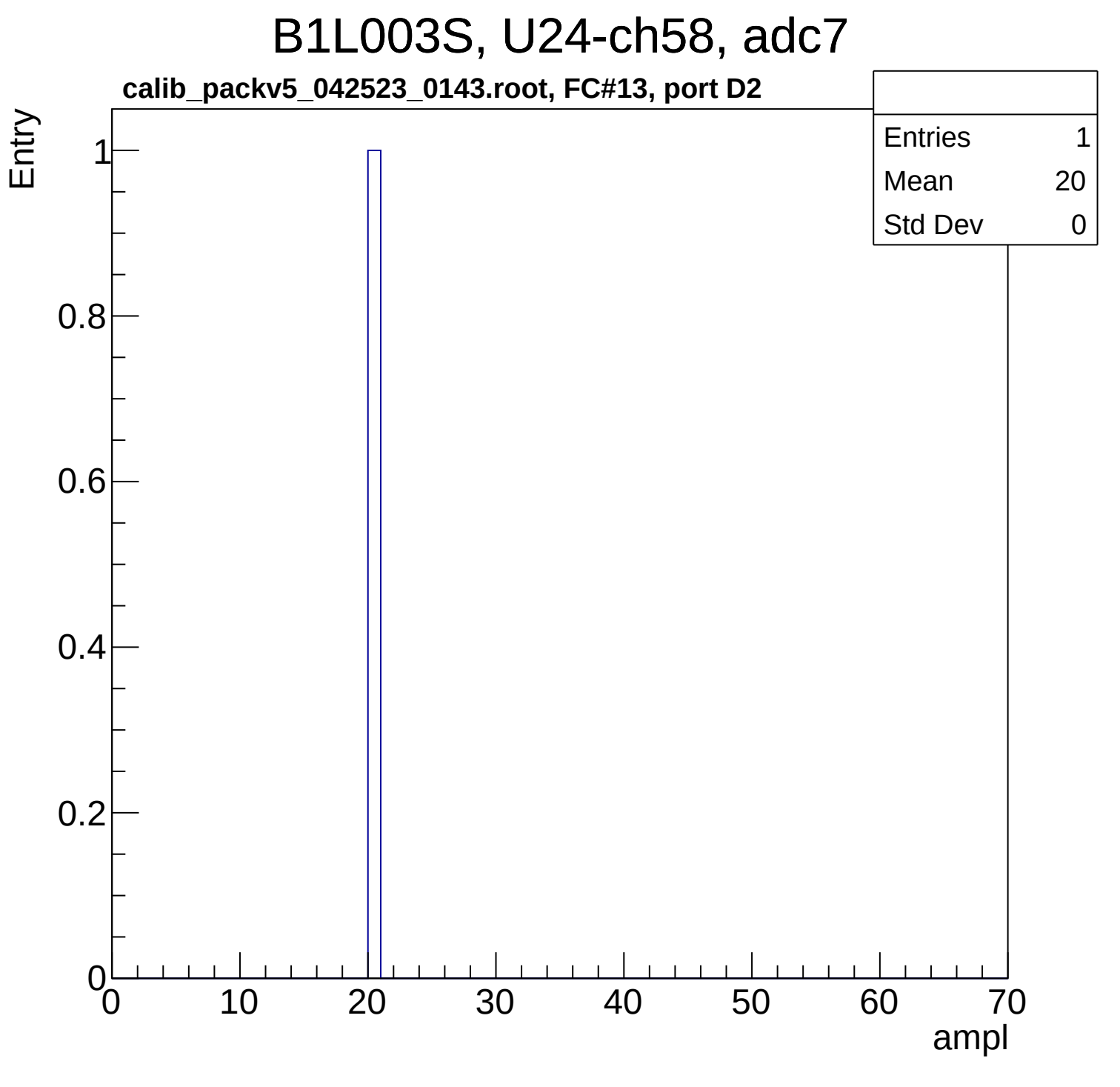

B1L003S, U24-ch58, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 54.91
Std Dev 2.974
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch58, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.83
Std Dev 9.125
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch58, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.83
Std Dev 1.462
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch58, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 20
Std Dev 0
Entry
1
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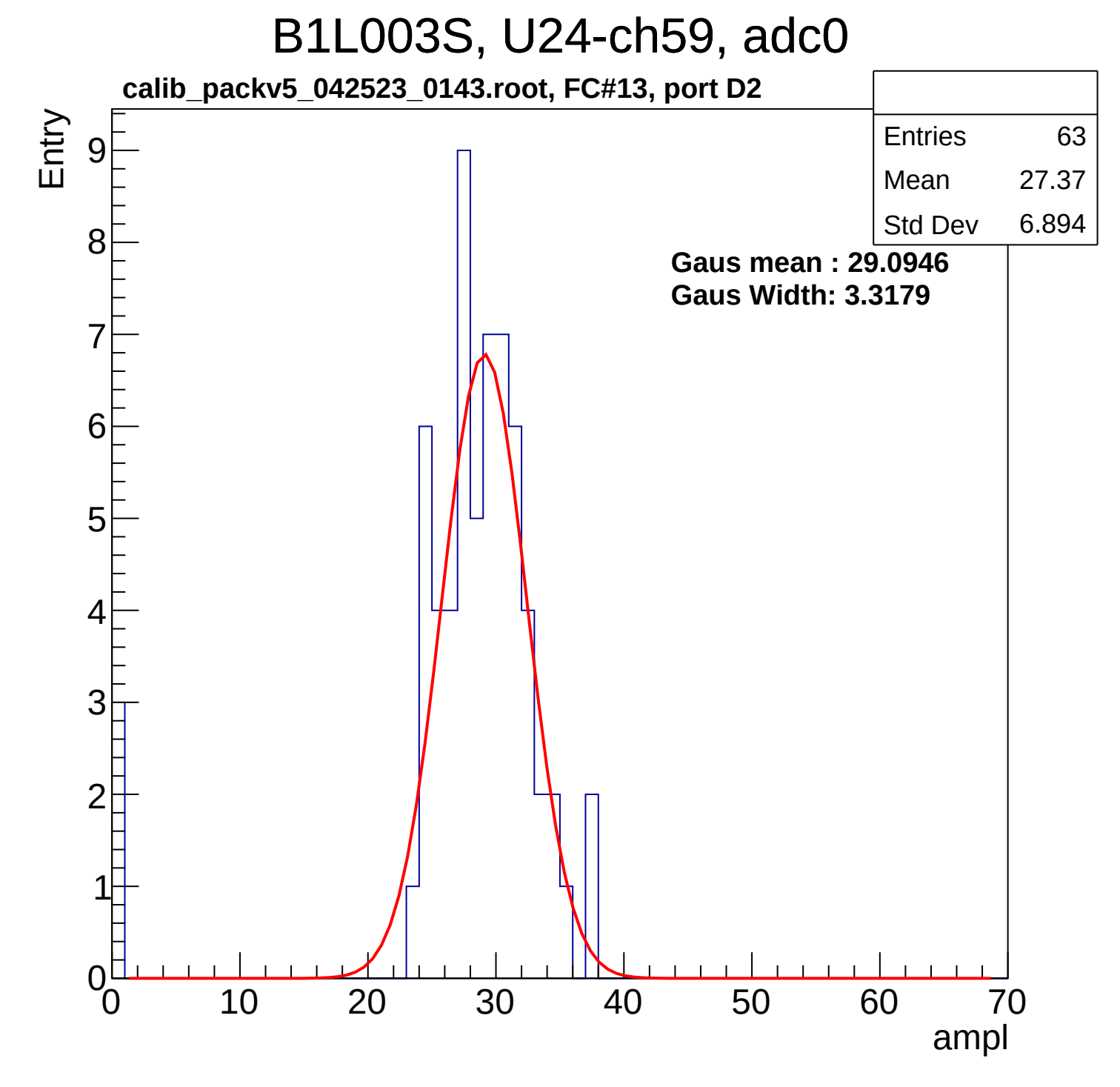
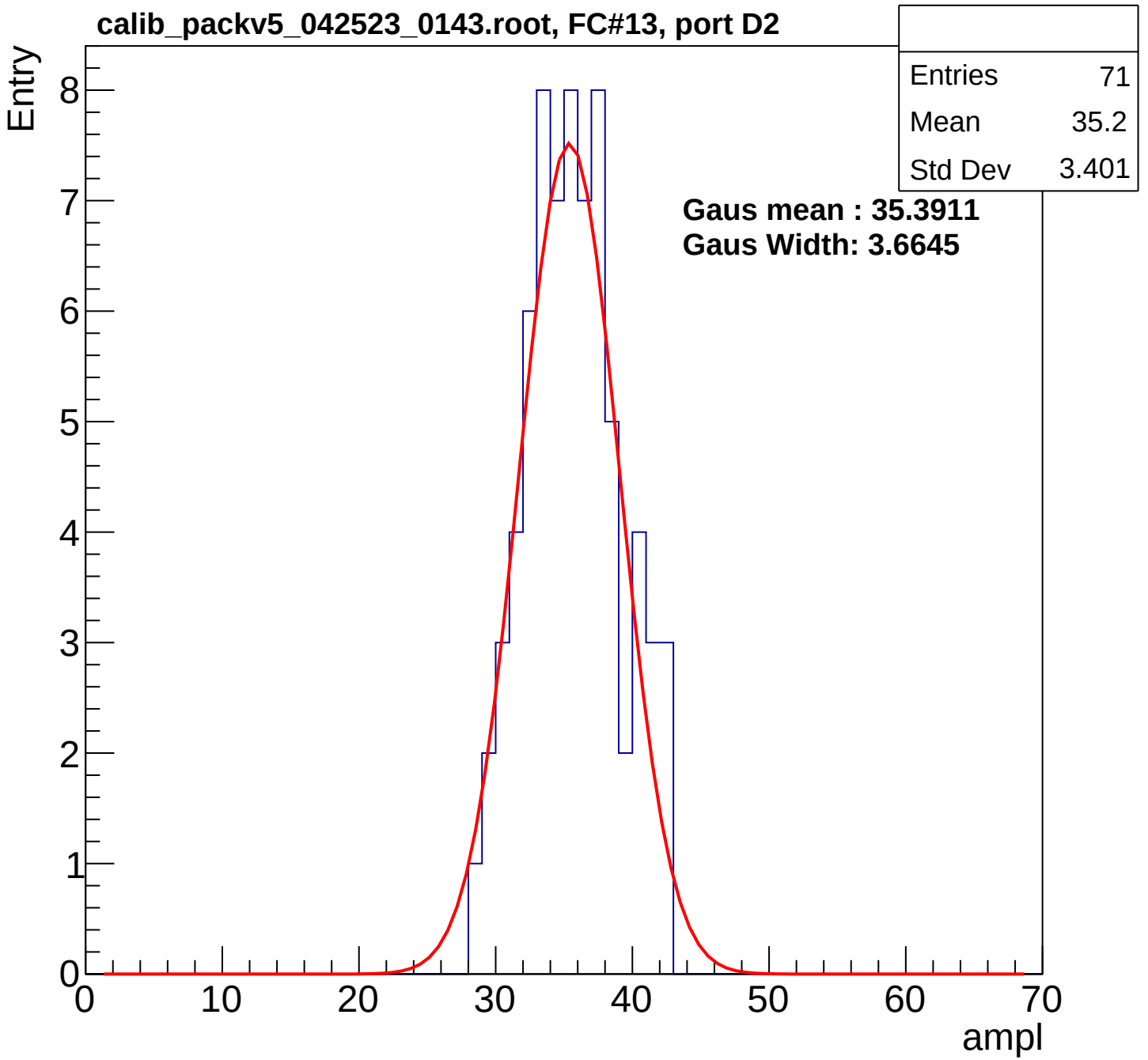
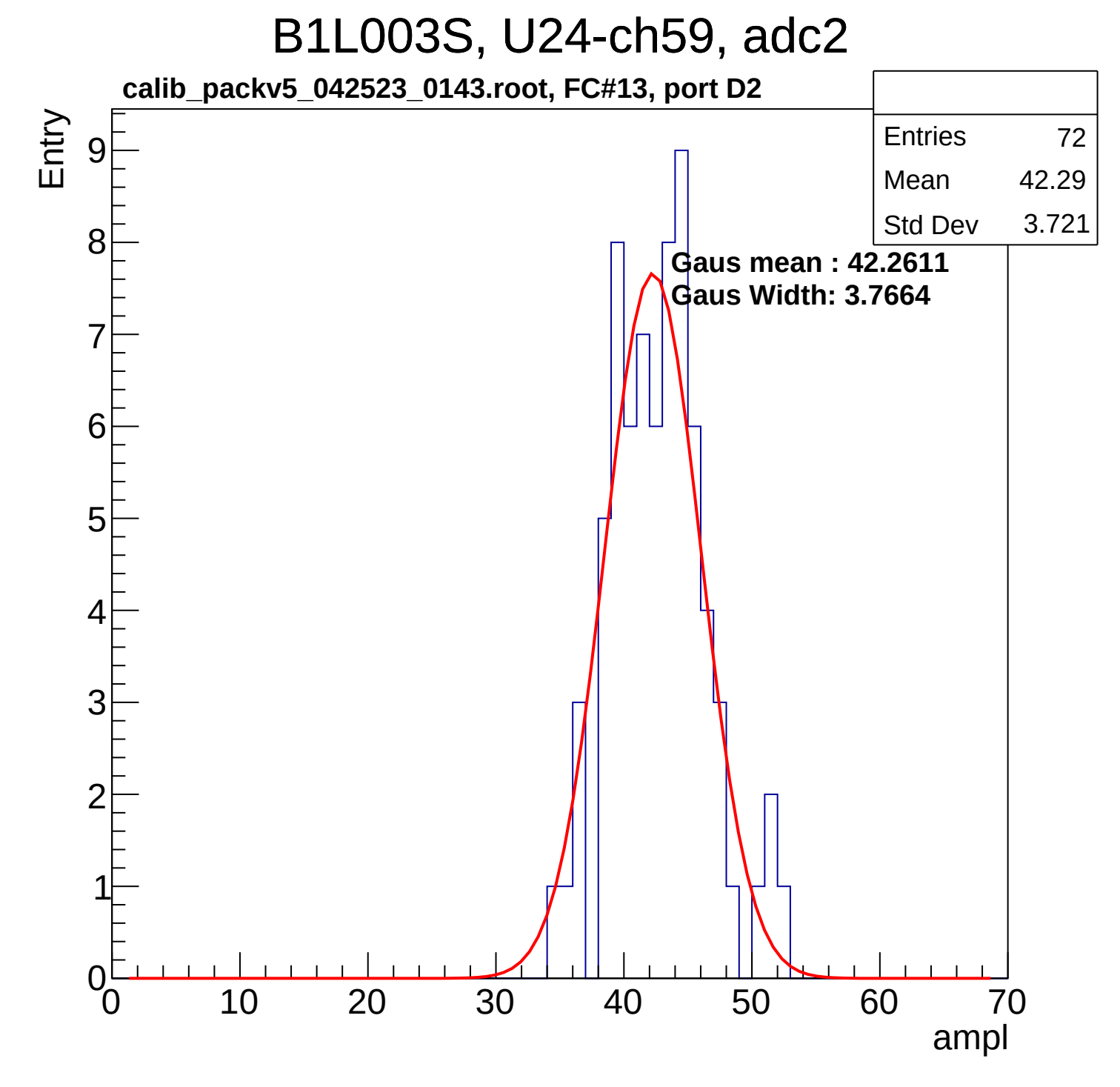
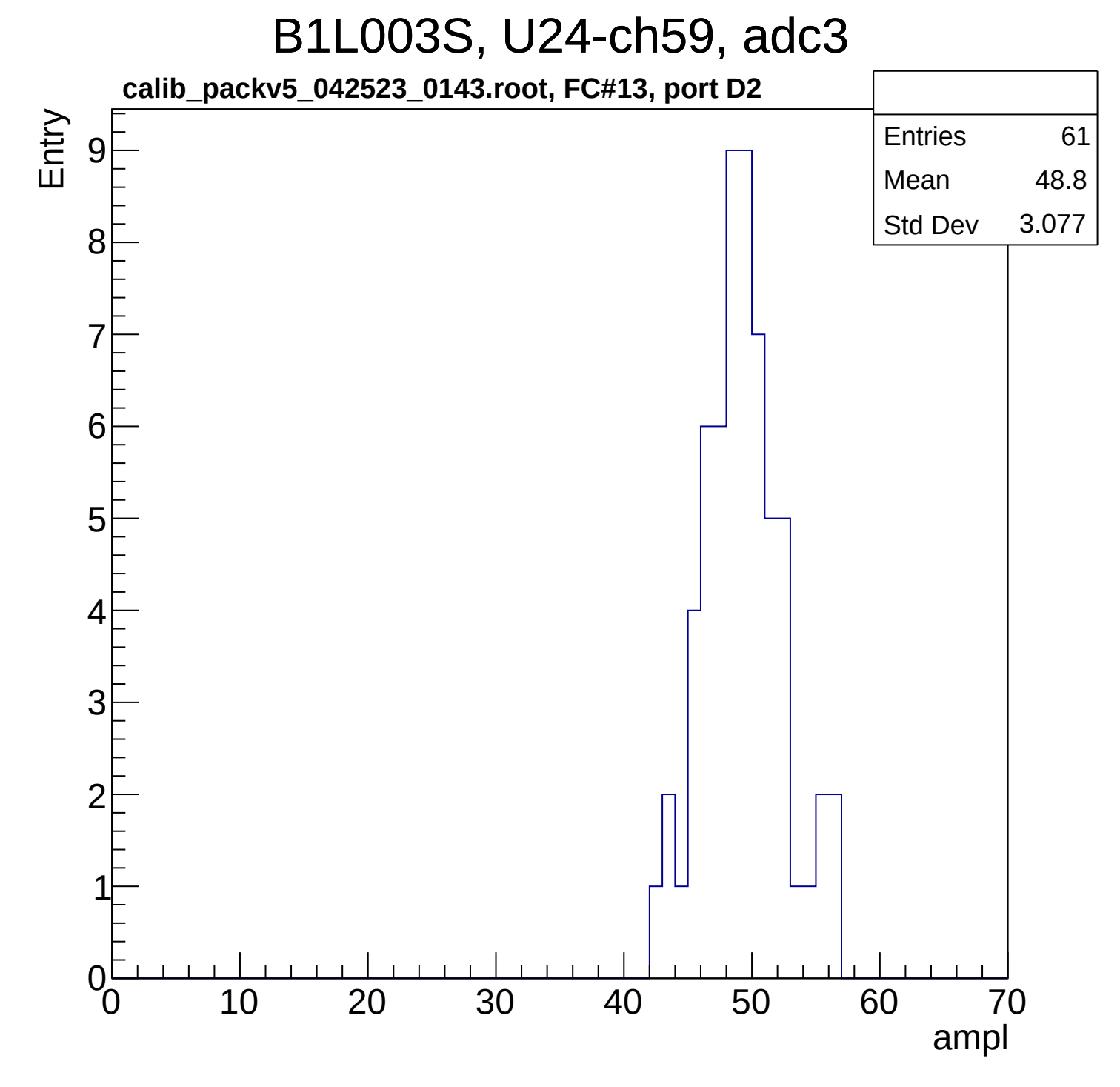
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch59, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 27.37
Std Dev 6.894
Gaus mean : 29.0946
Gaus Width: 3.3179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch59, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.2
Std Dev 3.401
Gaus mean : 35.3911
Gaus Width: 3.6645
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch59, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.29
Std Dev 3.721
Gaus mean : 42.2611
Gaus Width: 3.7664
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch59, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.8
Std Dev 3.077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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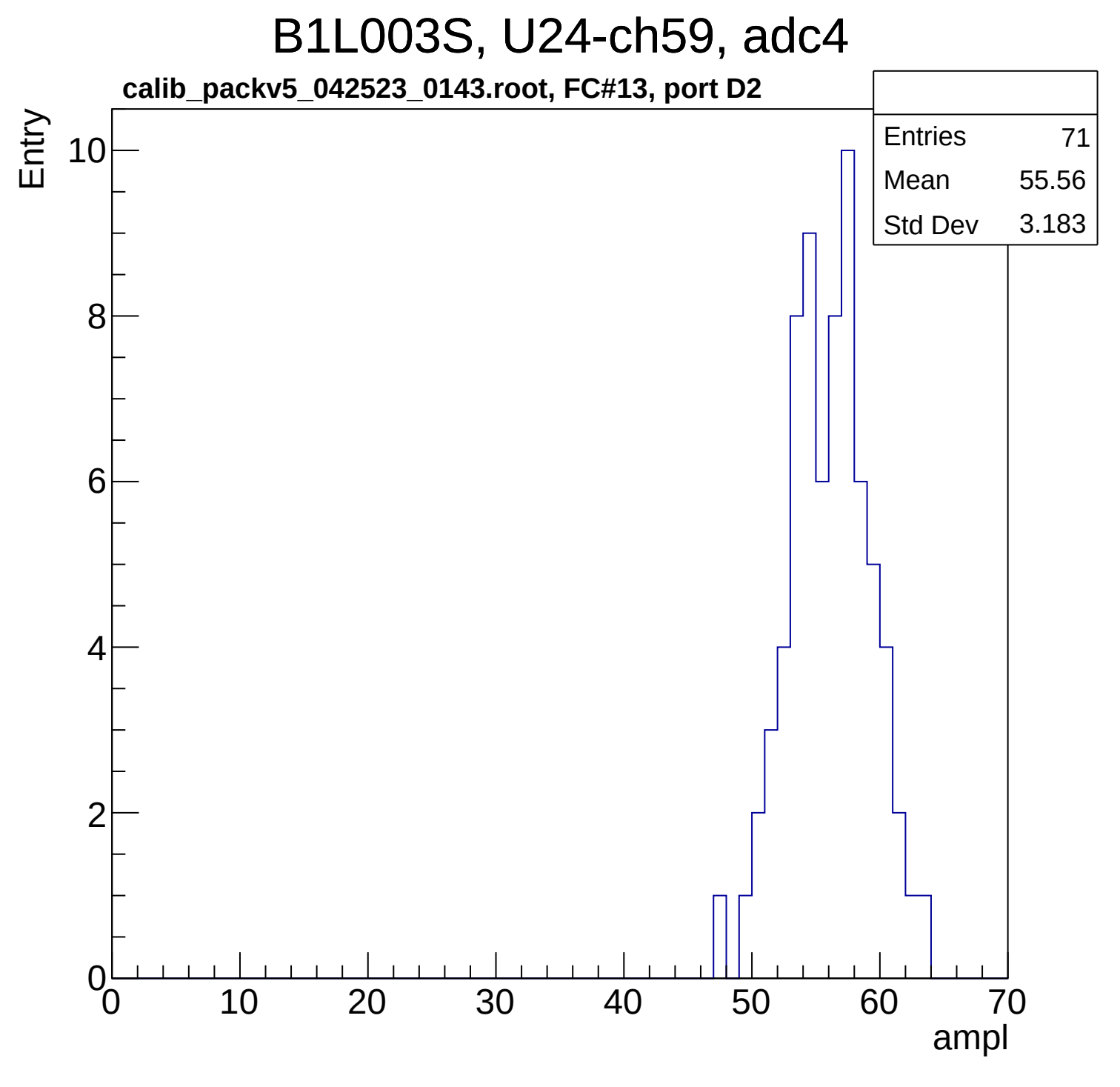
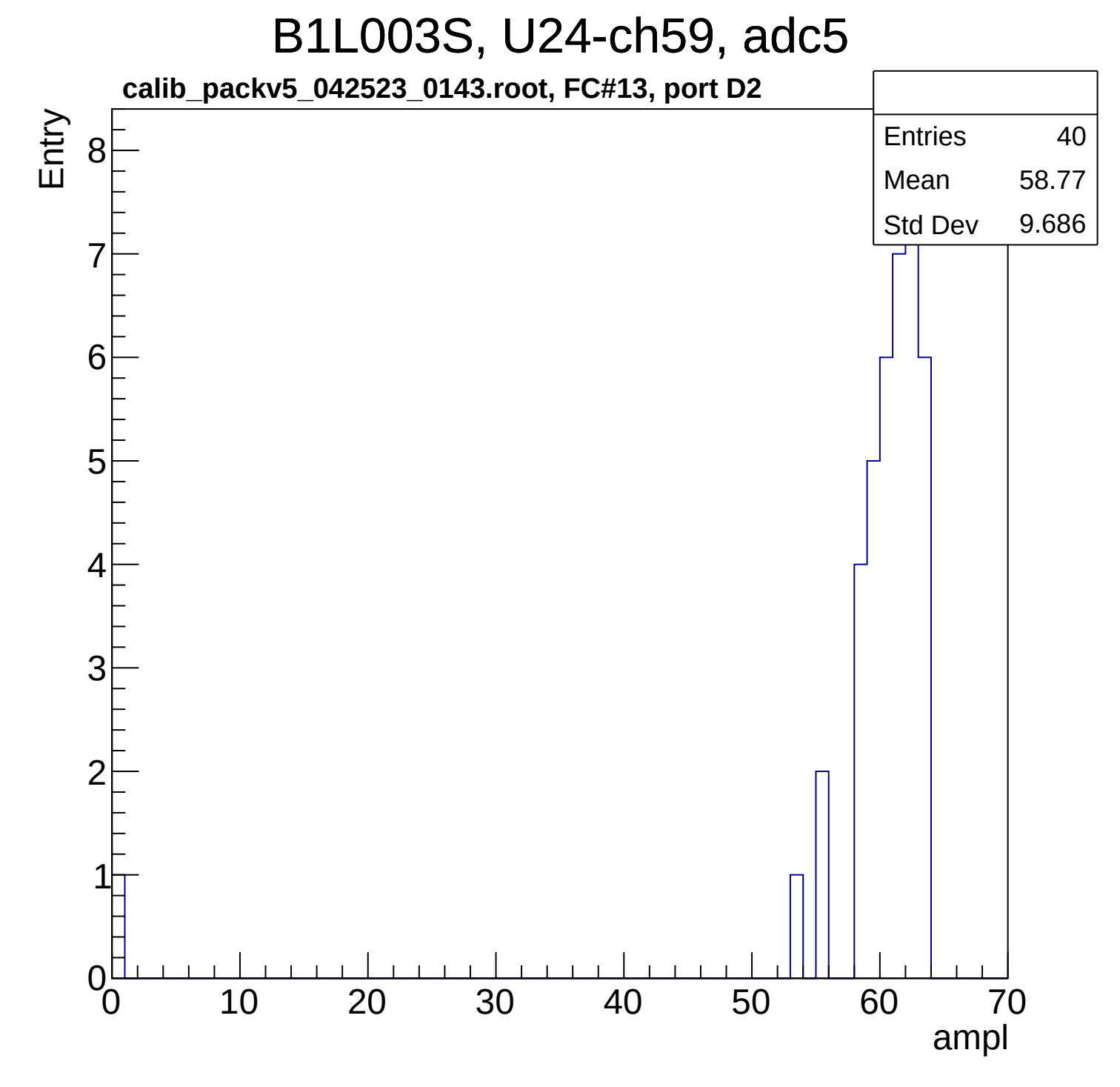
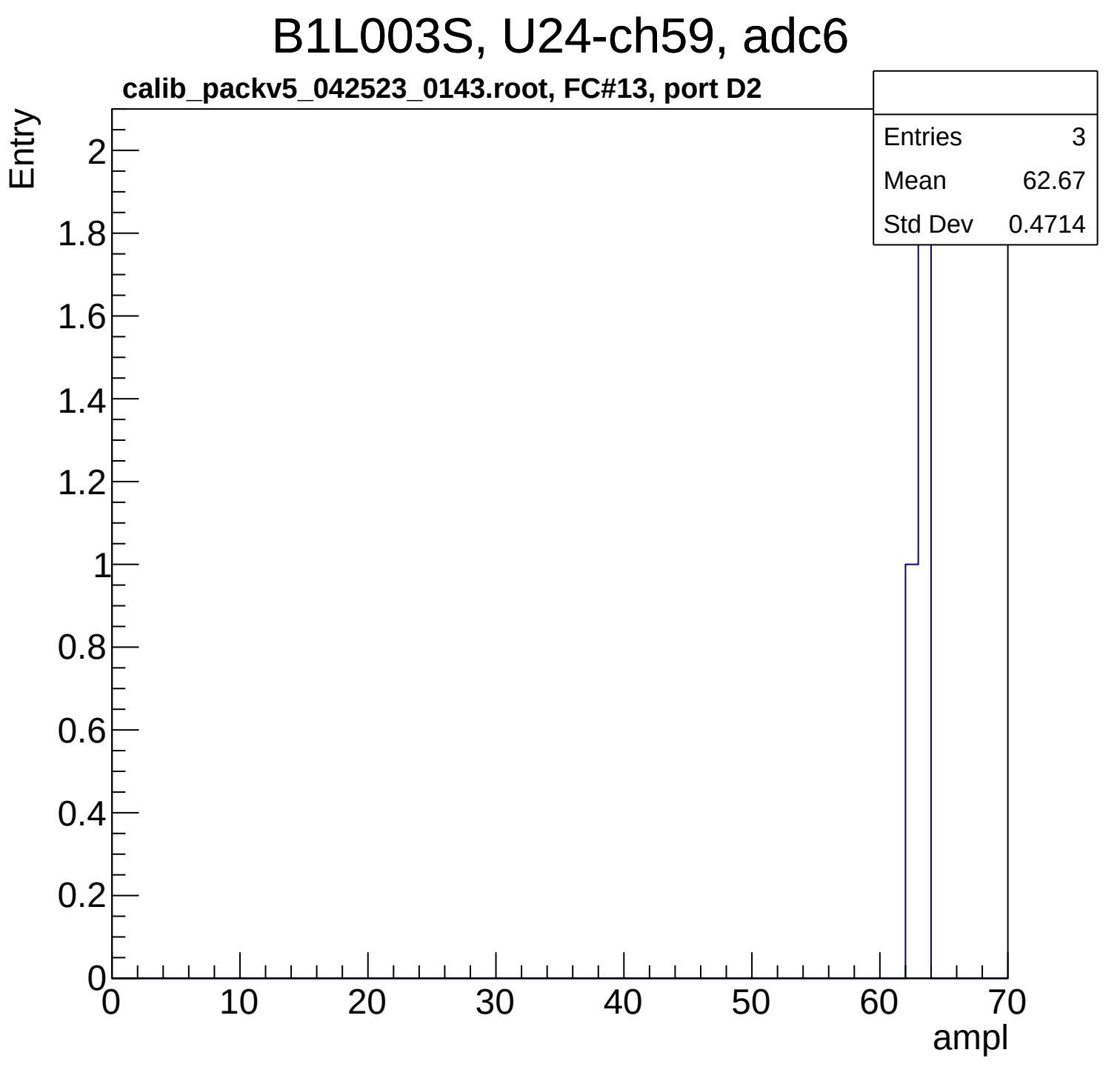
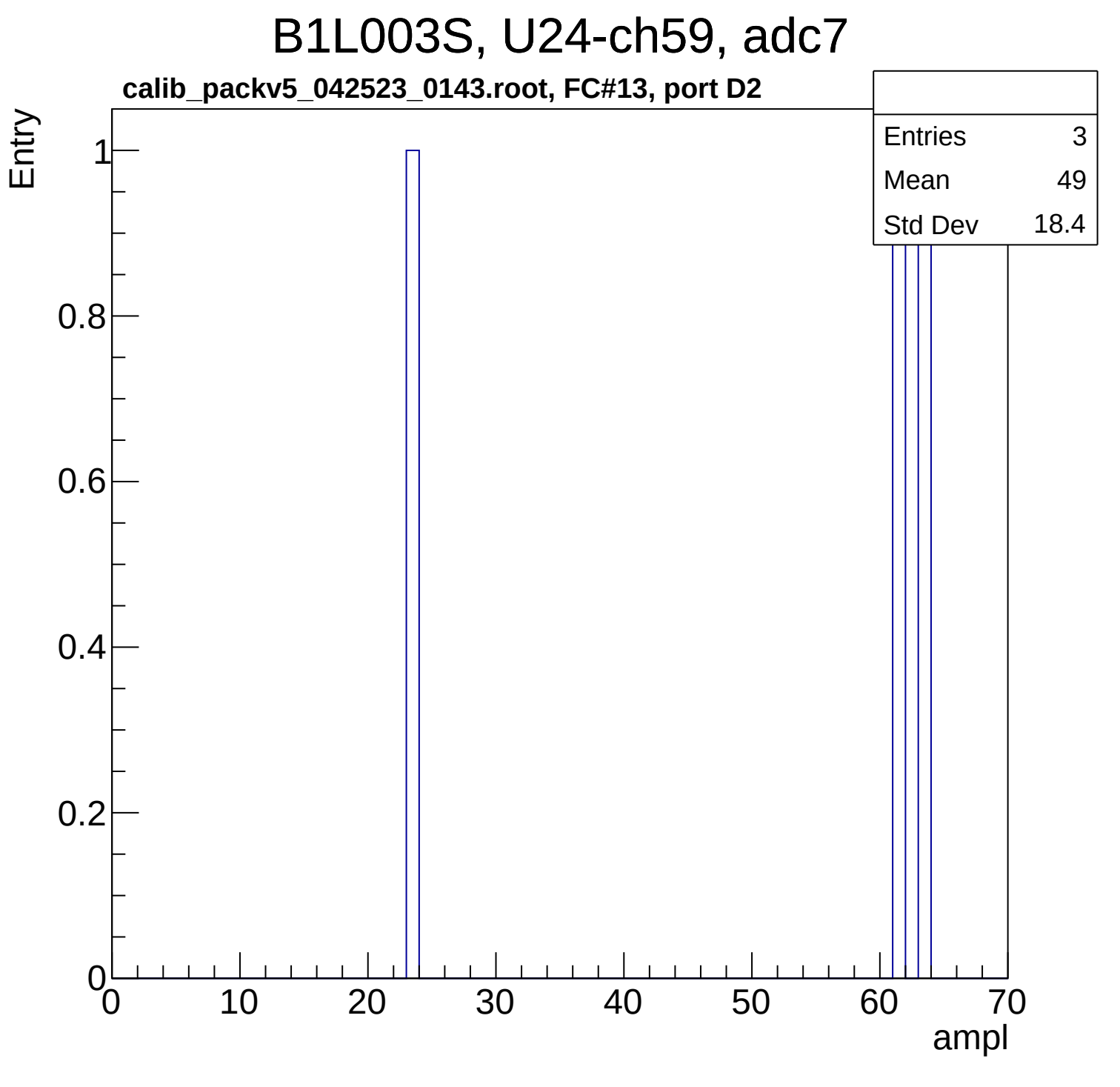
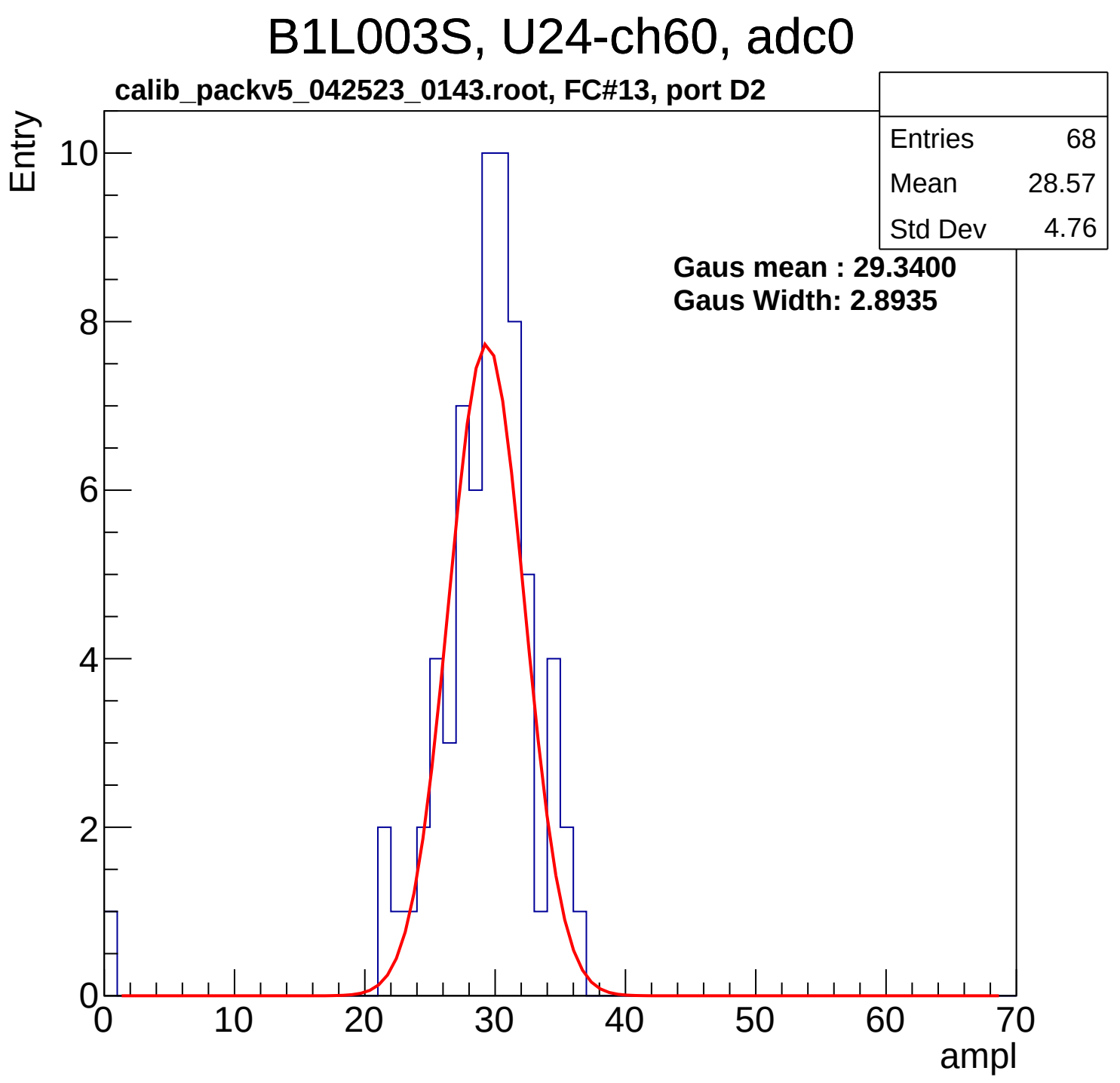
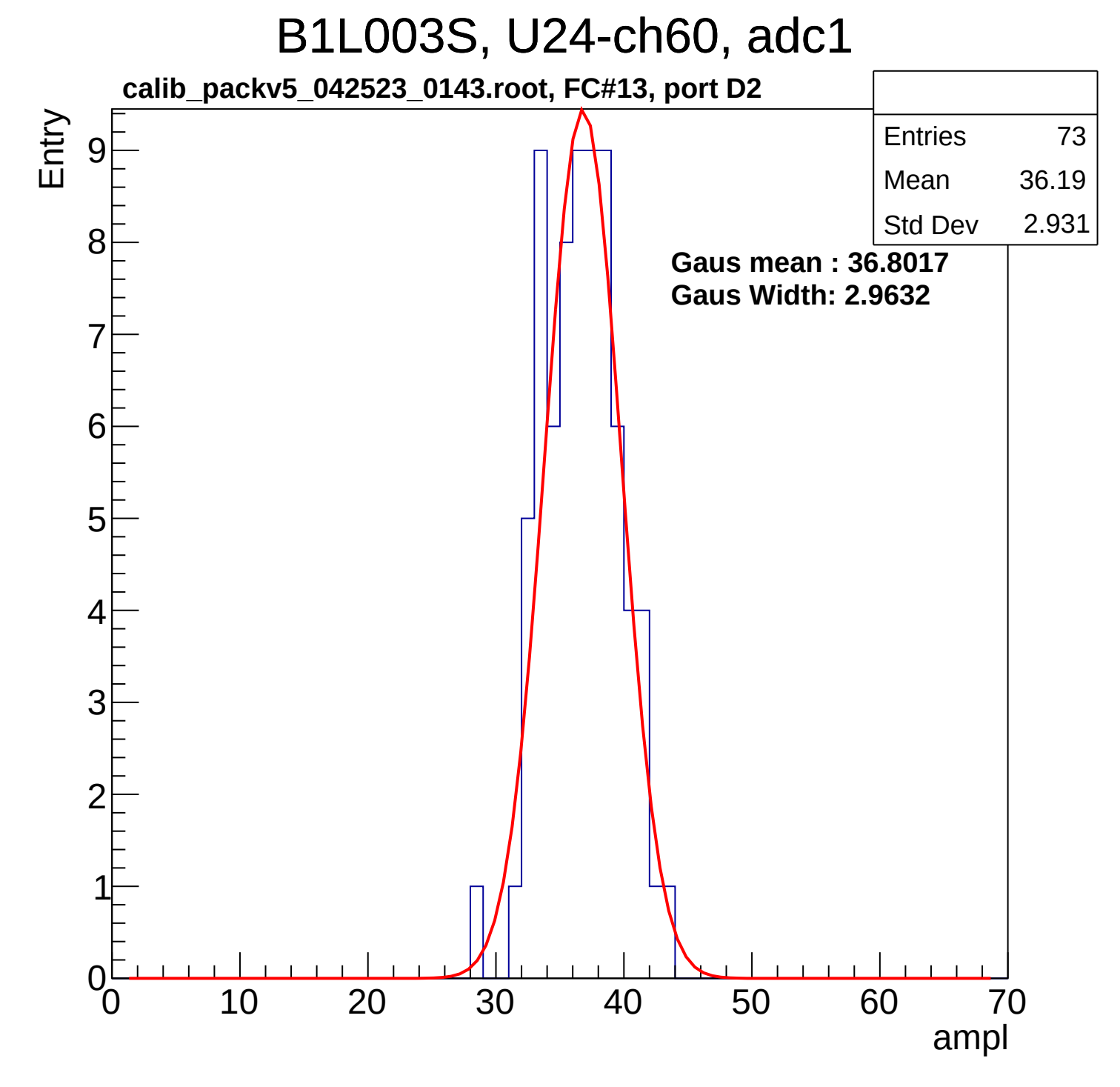

B1L003S, U24-ch59, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 55.56
Std Dev 3.183
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

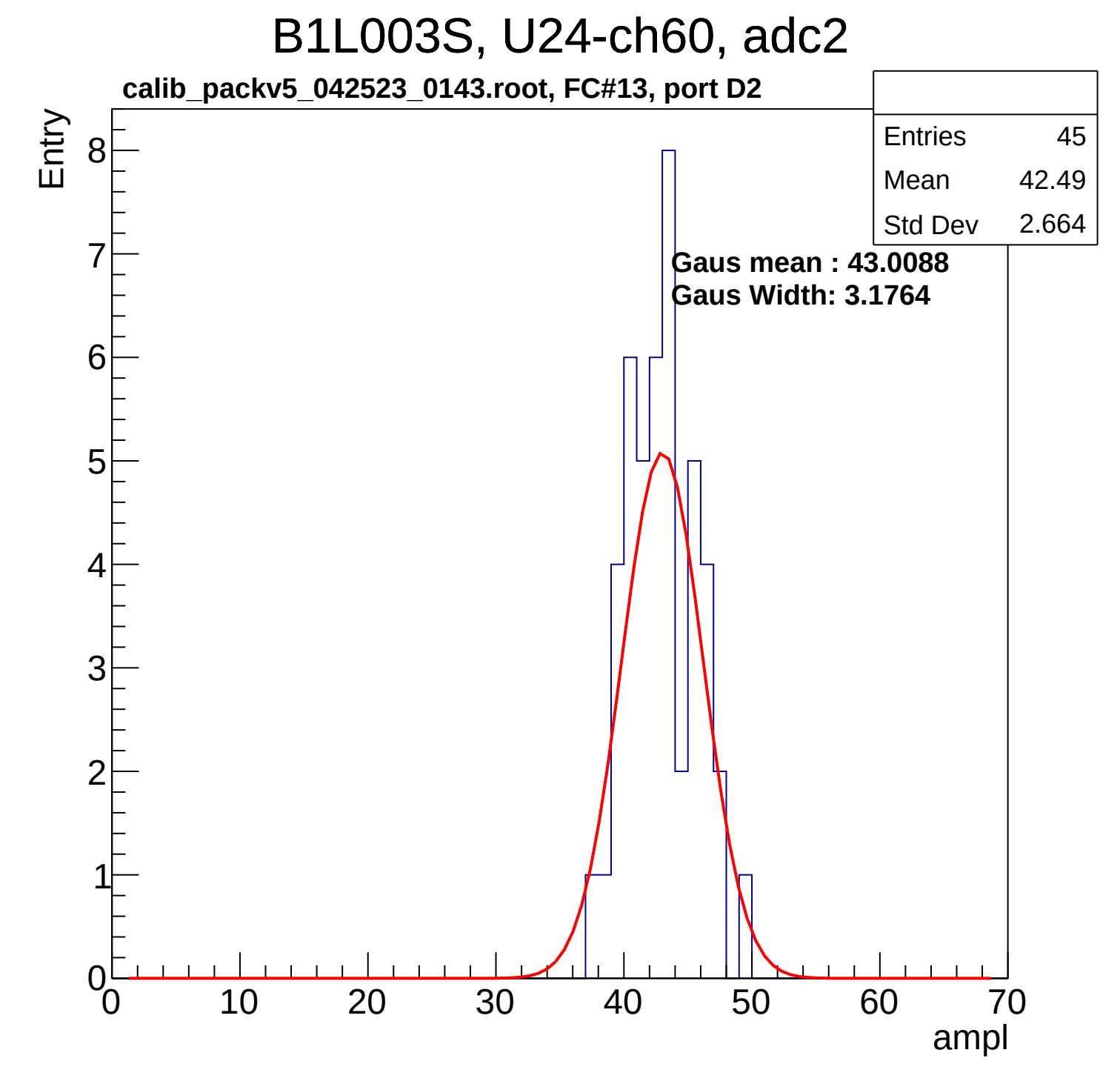

B1L003S, U24-ch59, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.77
Std Dev 9.686
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch59, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch59, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 49
Std Dev 18.4
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch60, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.57
Std Dev 4.76
Gaus mean : 29.3400
Gaus Width: 2.8935
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch60, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.19
Std Dev 2.931
Gaus mean : 36.8017
Gaus Width: 2.9632
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch60, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 42.49
Std Dev 2.664
Gaus mean : 43.0088
Gaus Width: 3.1764
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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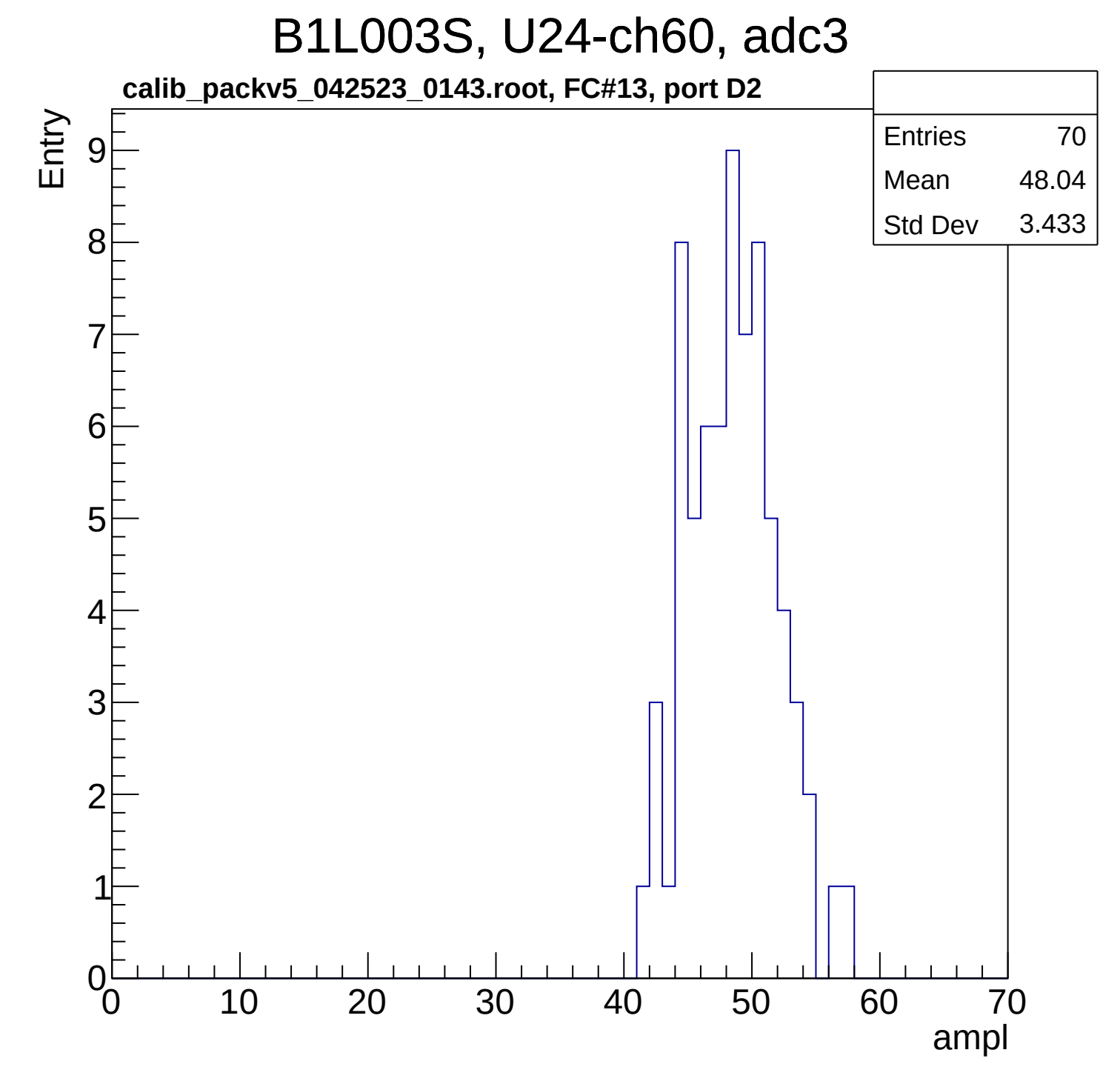
B1L003S, U24-ch60, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 48.04
Std Dev 3.433
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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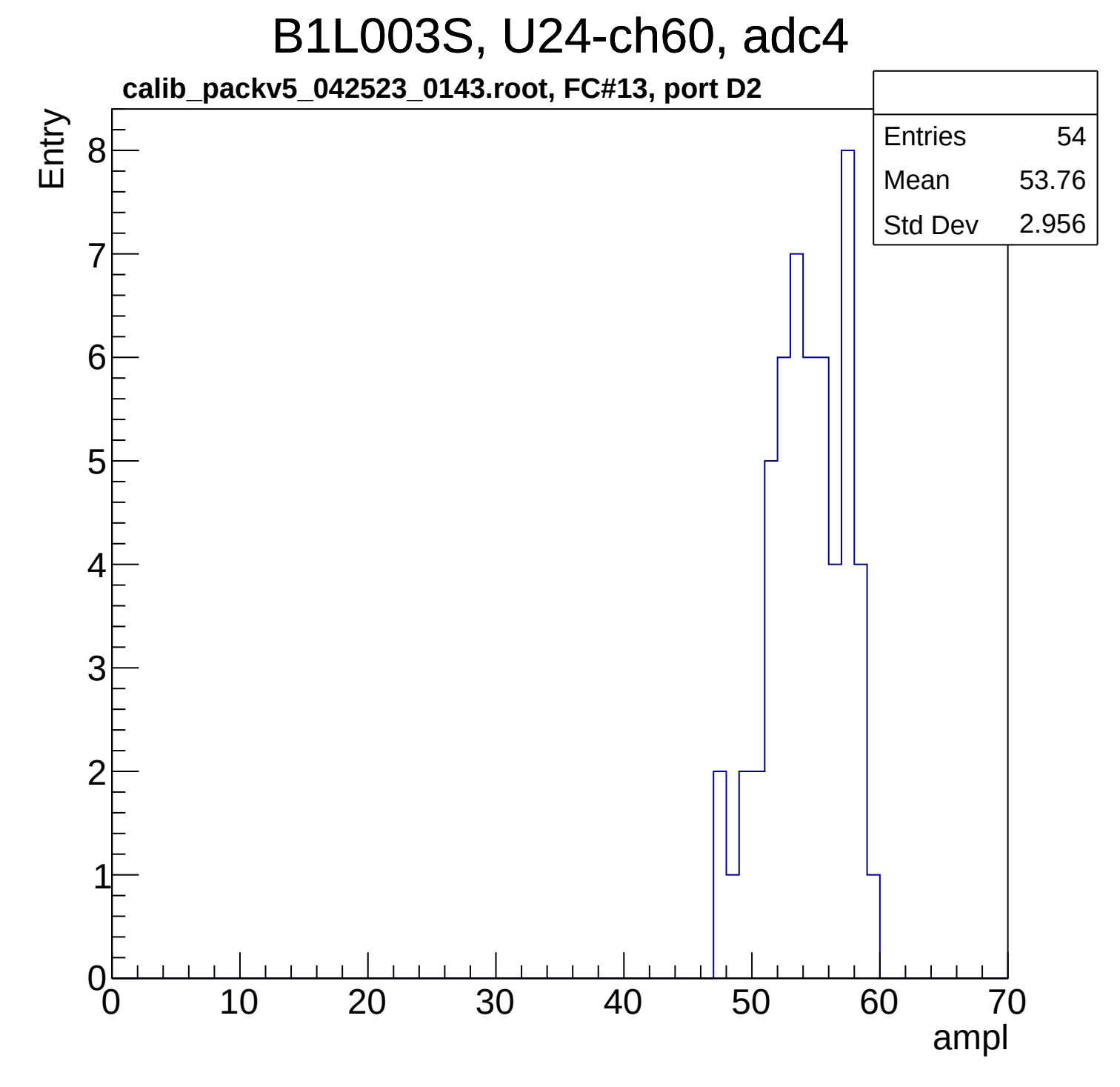
60
70


B1L003S, U24-ch60, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.76
Std Dev 2.956
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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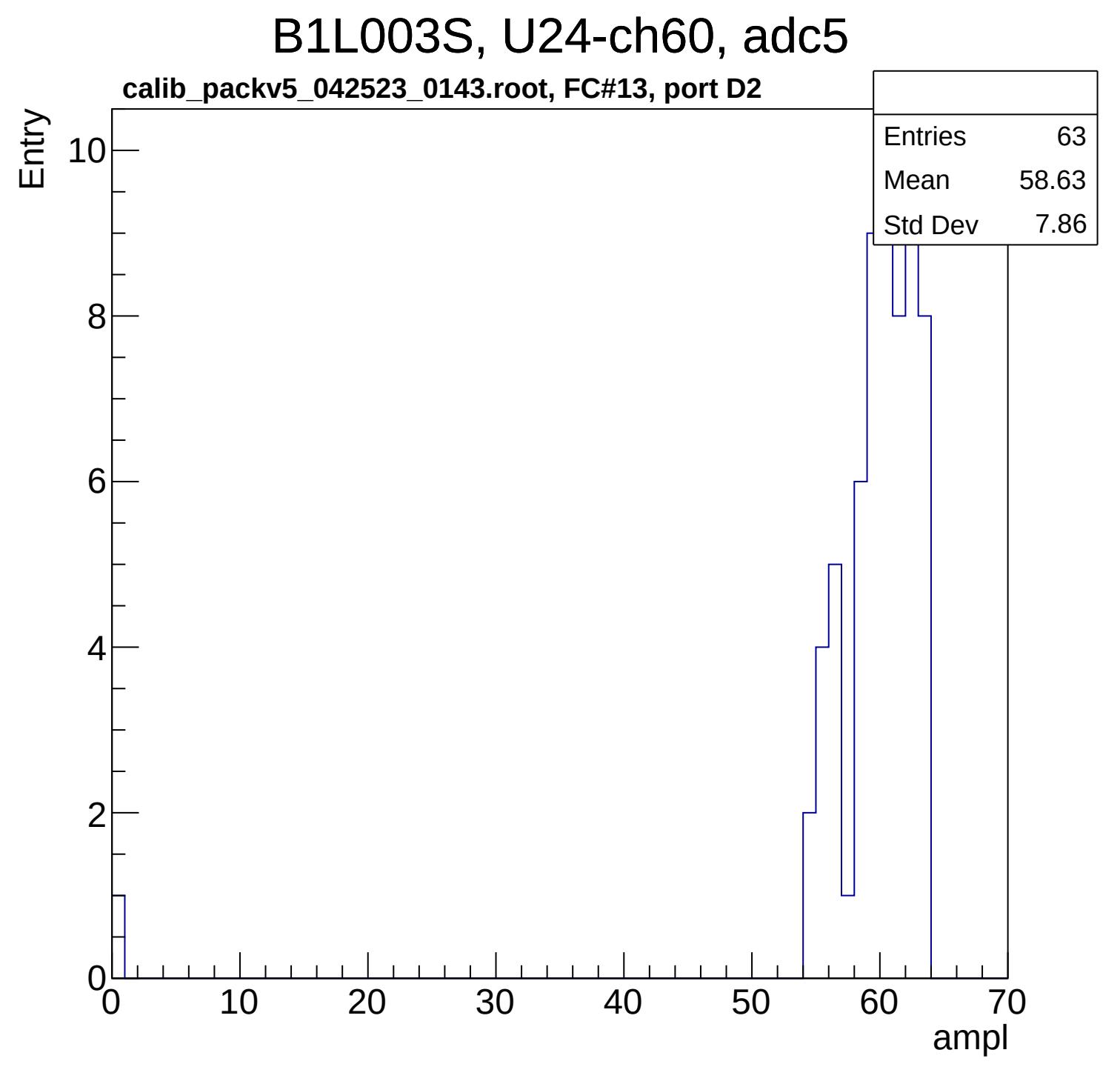
40
50
60
70


B1L003S, U24-ch60, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 58.63
Std Dev 7.86
Entry
0
2
4
6
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10
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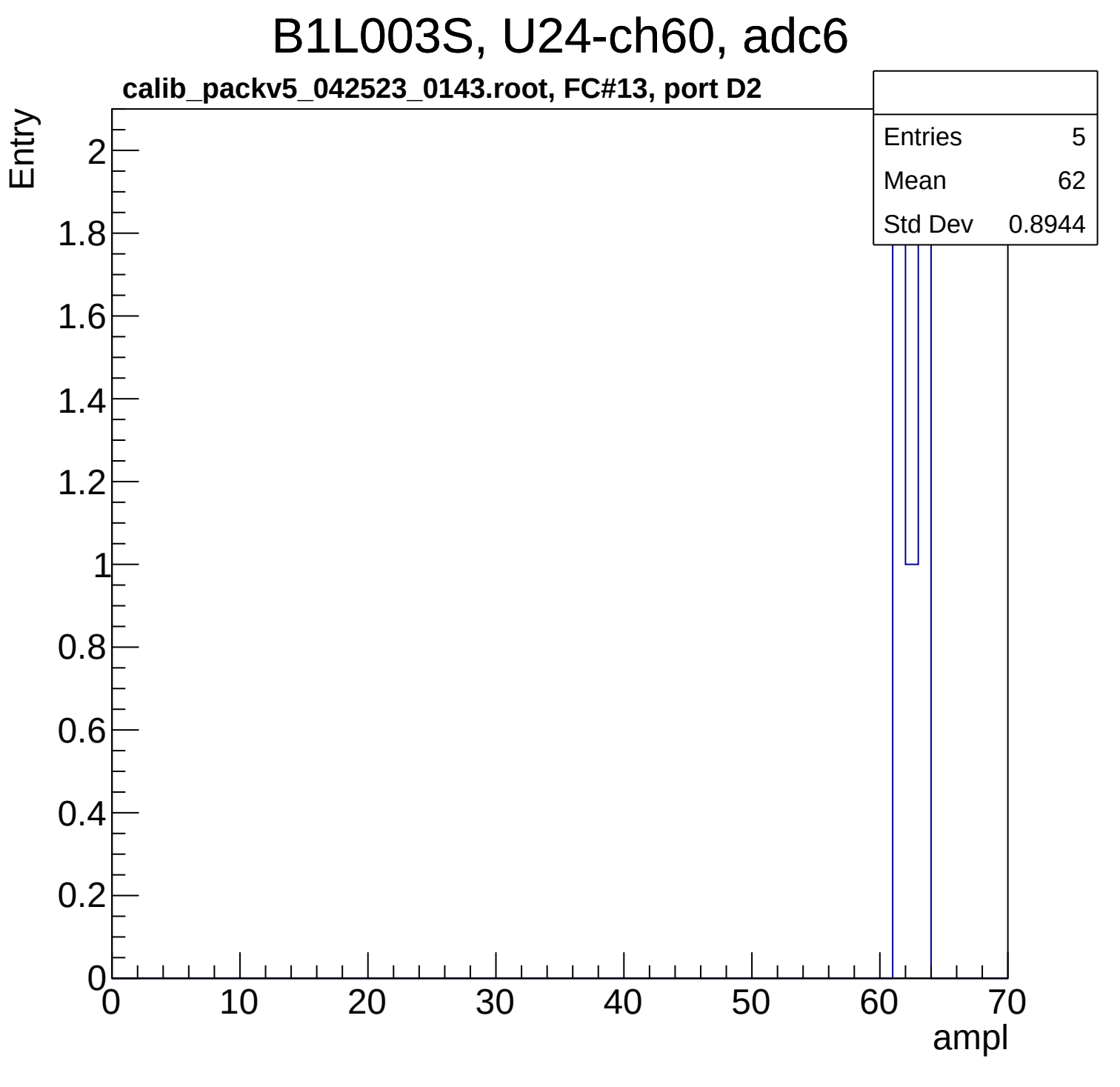
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch60, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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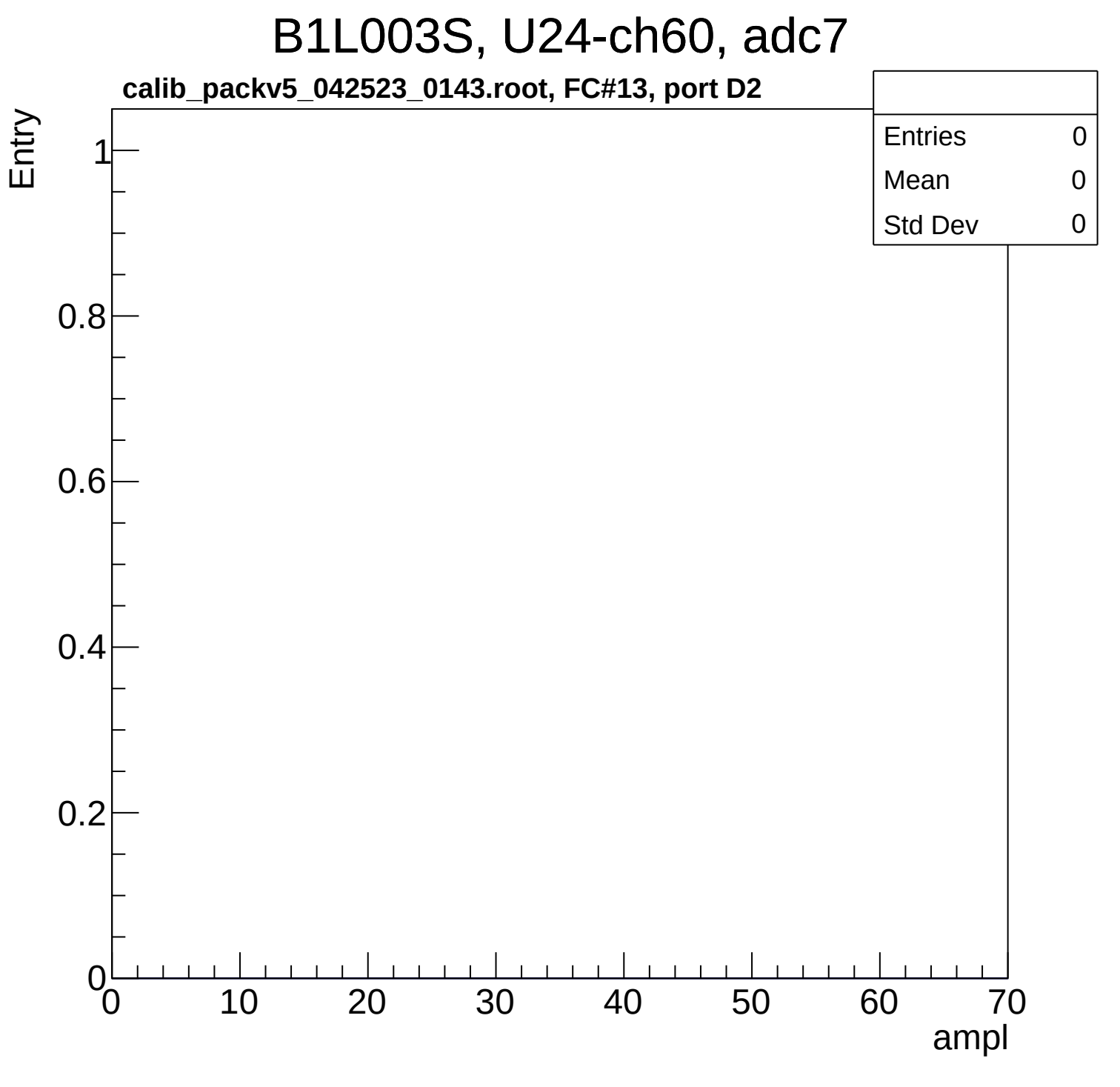
30
40
50
60
70


B1L003S, U24-ch60, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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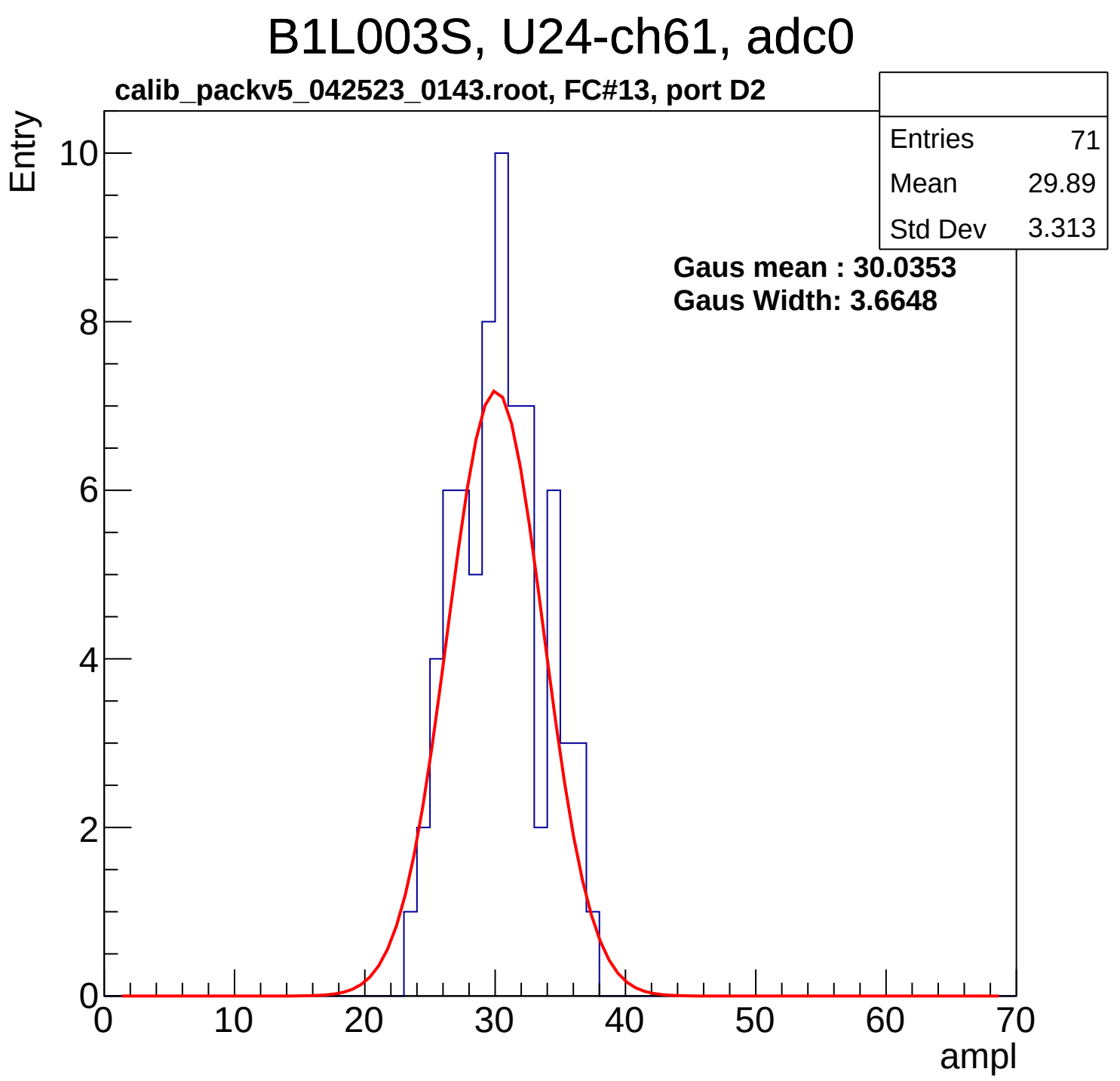
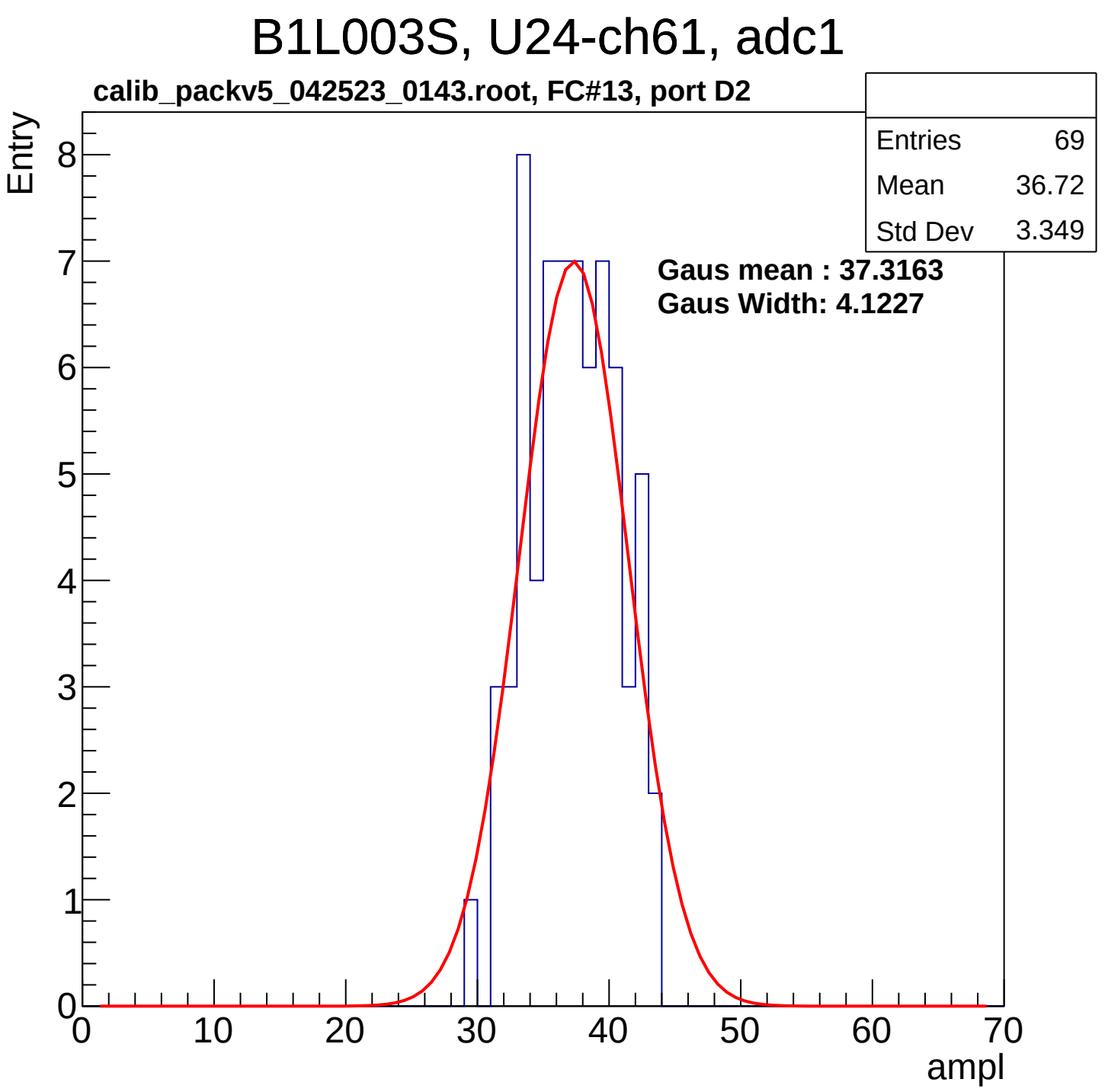
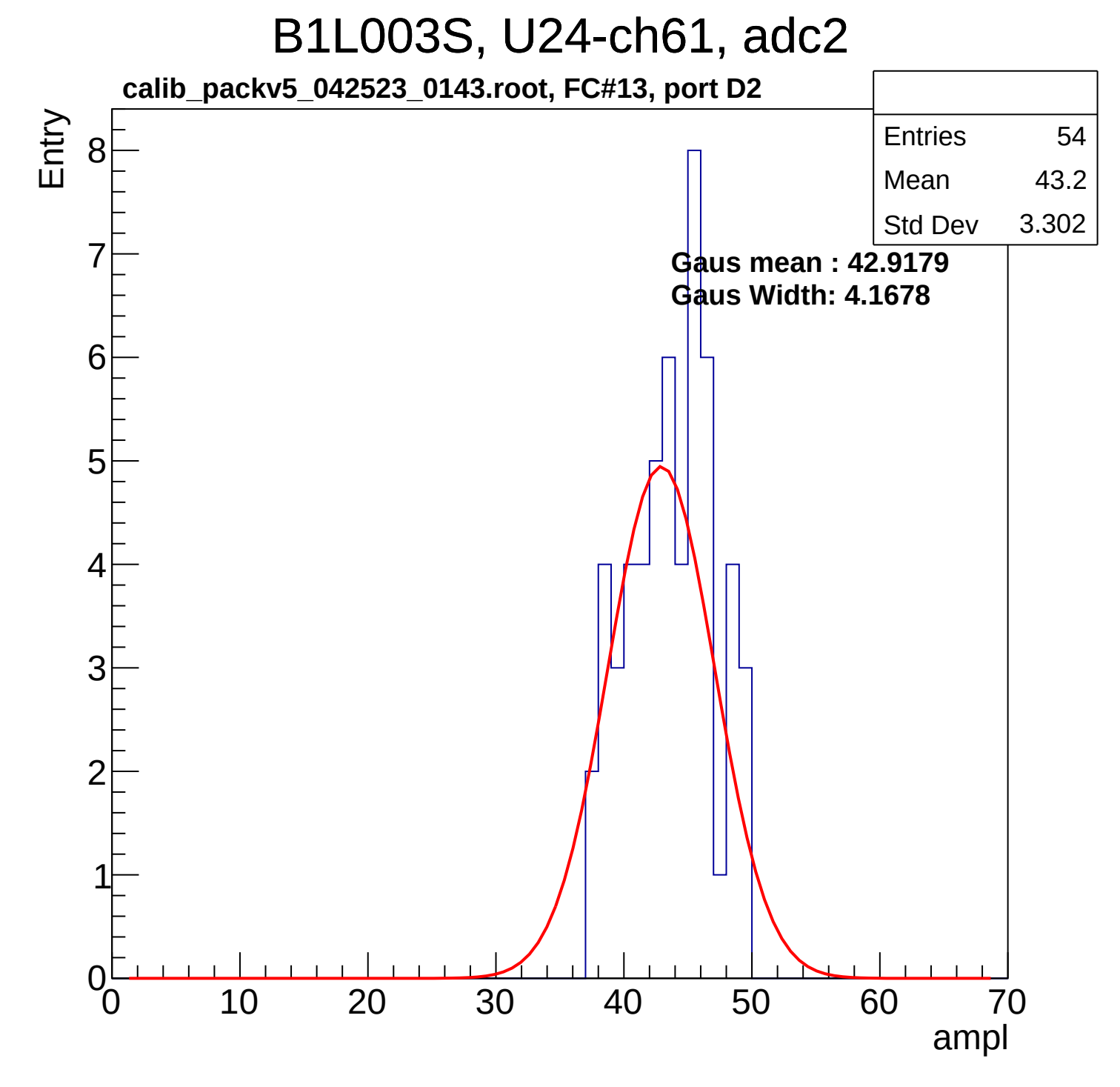
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch61, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.89
Std Dev 3.313
Gaus mean : 30.0353
Gaus Width: 3.6648
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch61, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 36.72
Std Dev 3.349
Gaus mean : 37.3163
Gaus Width: 4.1227
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch61, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 43.2
Std Dev 3.302
Gaus mean : 42.9179
Gaus Width: 4.1678
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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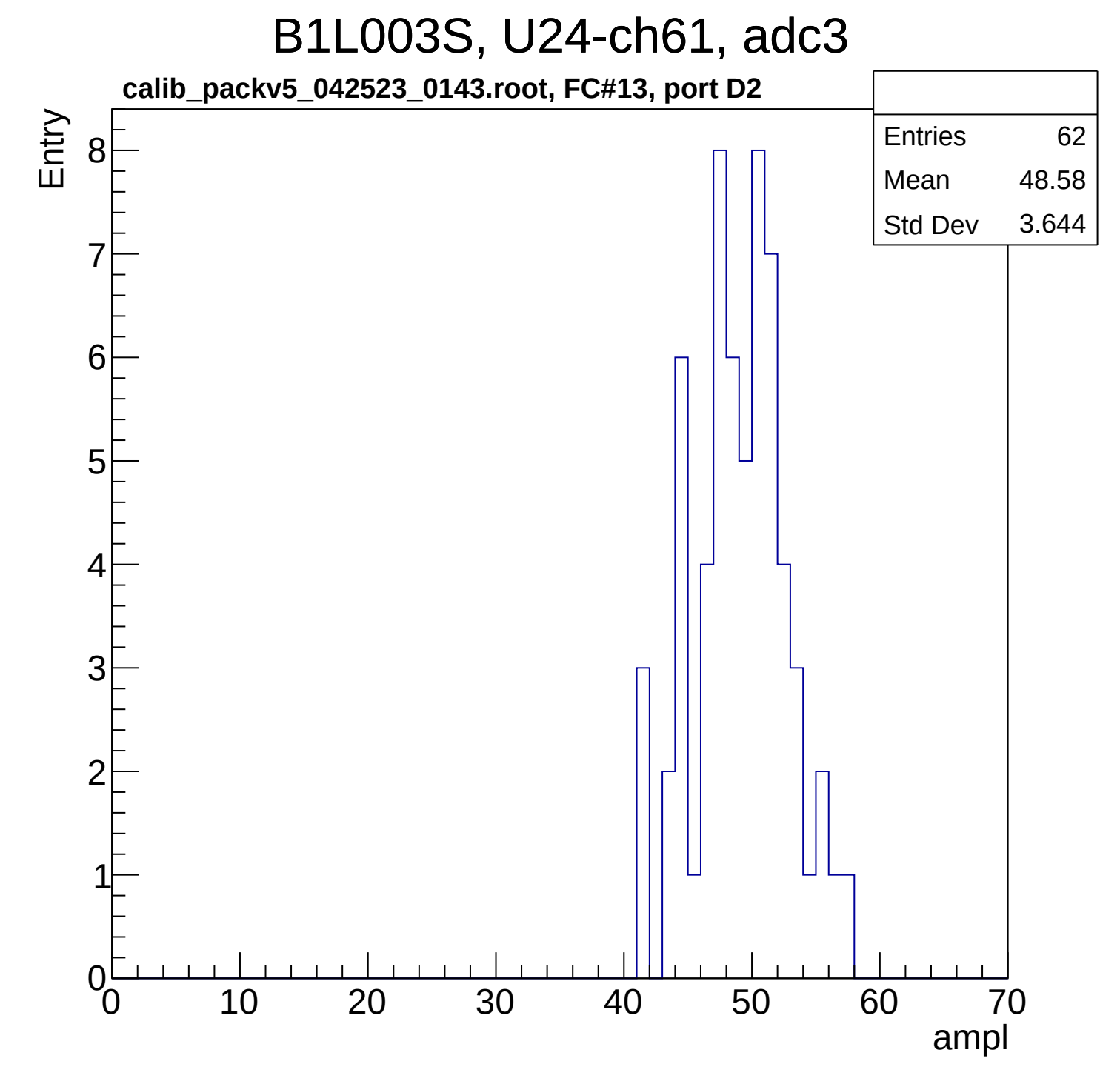
B1L003S, U24-ch61, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.58
Std Dev 3.644
Entry
ampl
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3
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60
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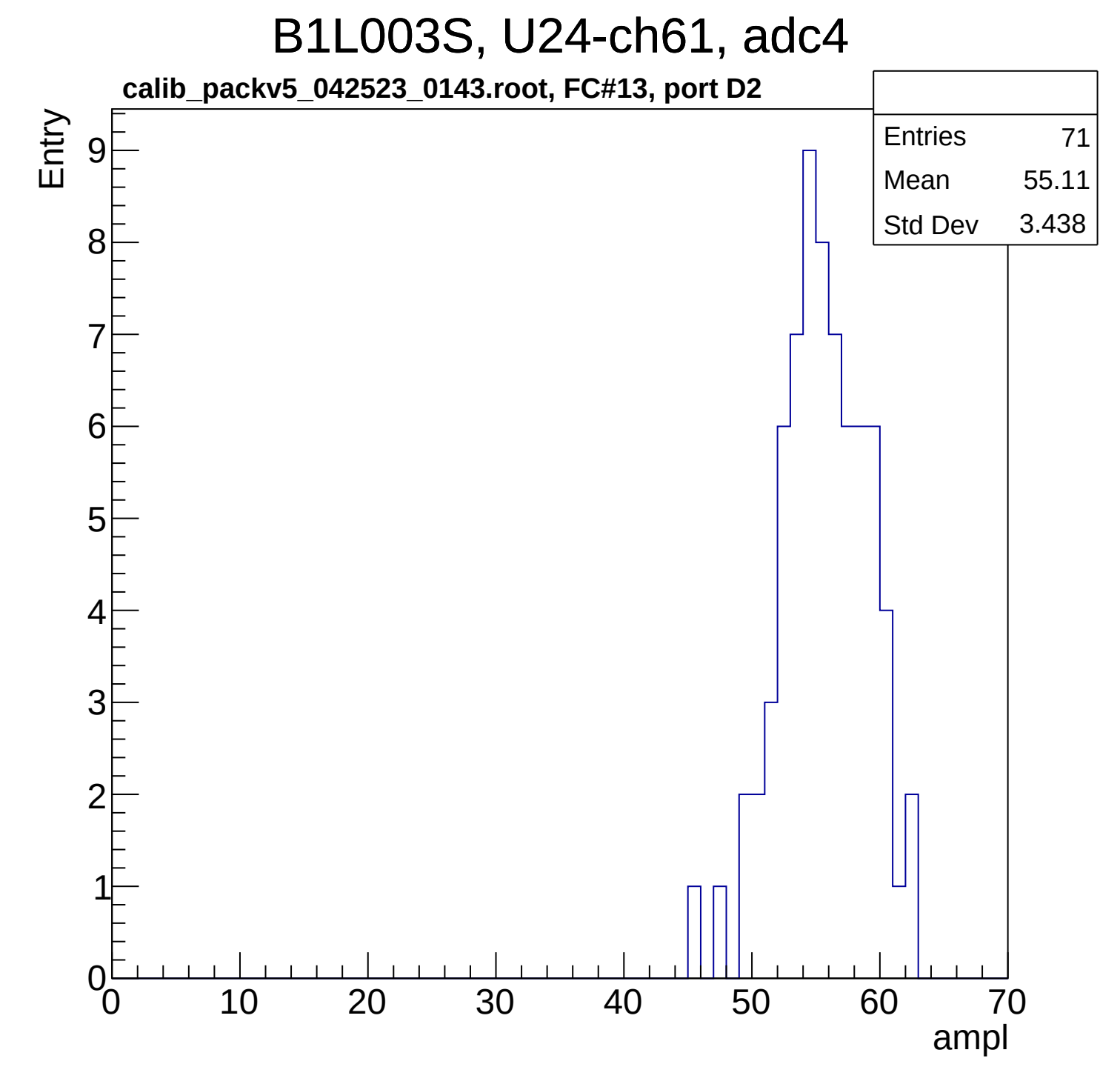
B1L003S, U24-ch61, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 55.11
Std Dev 3.438
Entry
ampl
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3
4
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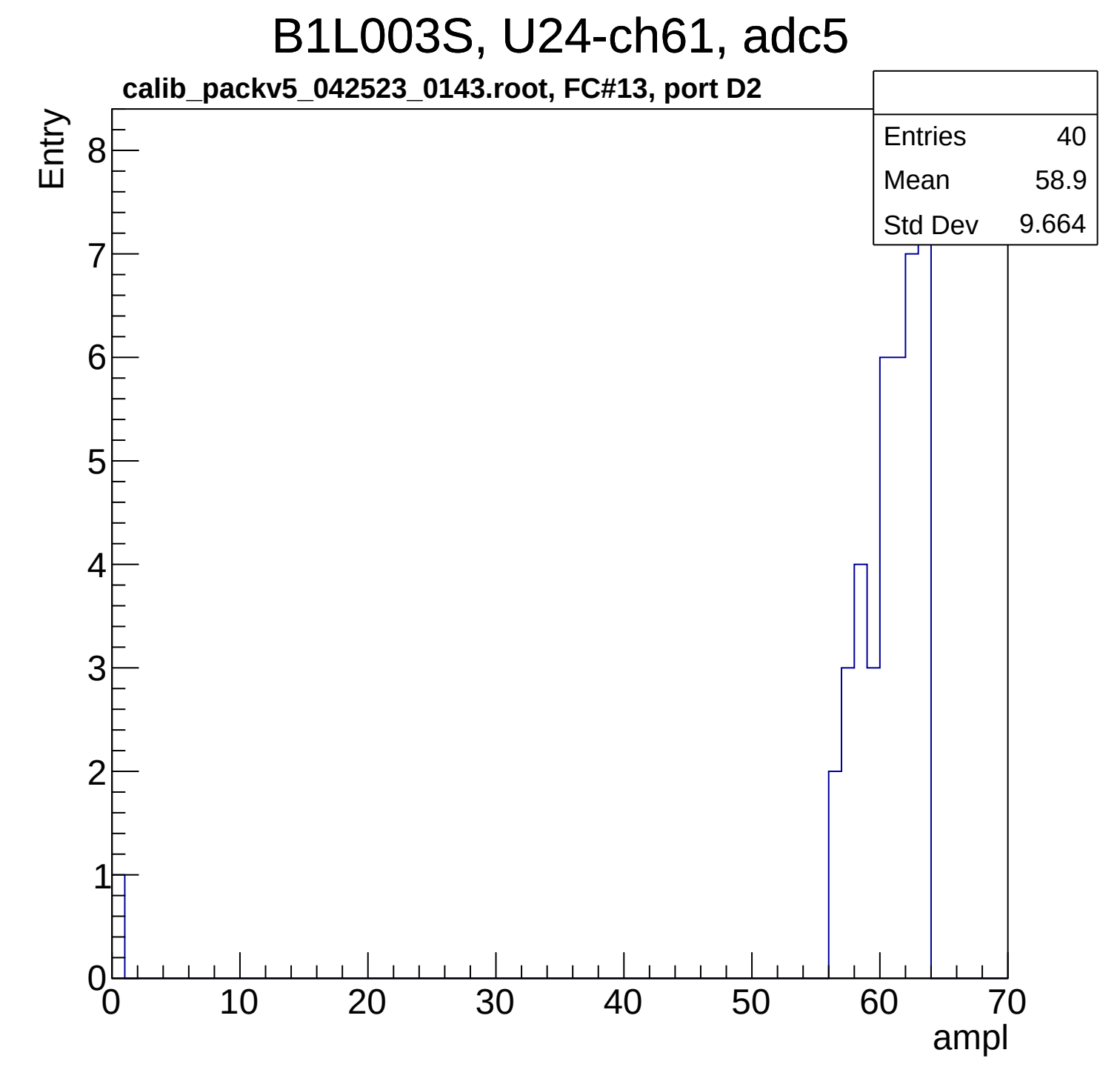
B1L003S, U24-ch61, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.9
Std Dev 9.664
Entry
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2
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5
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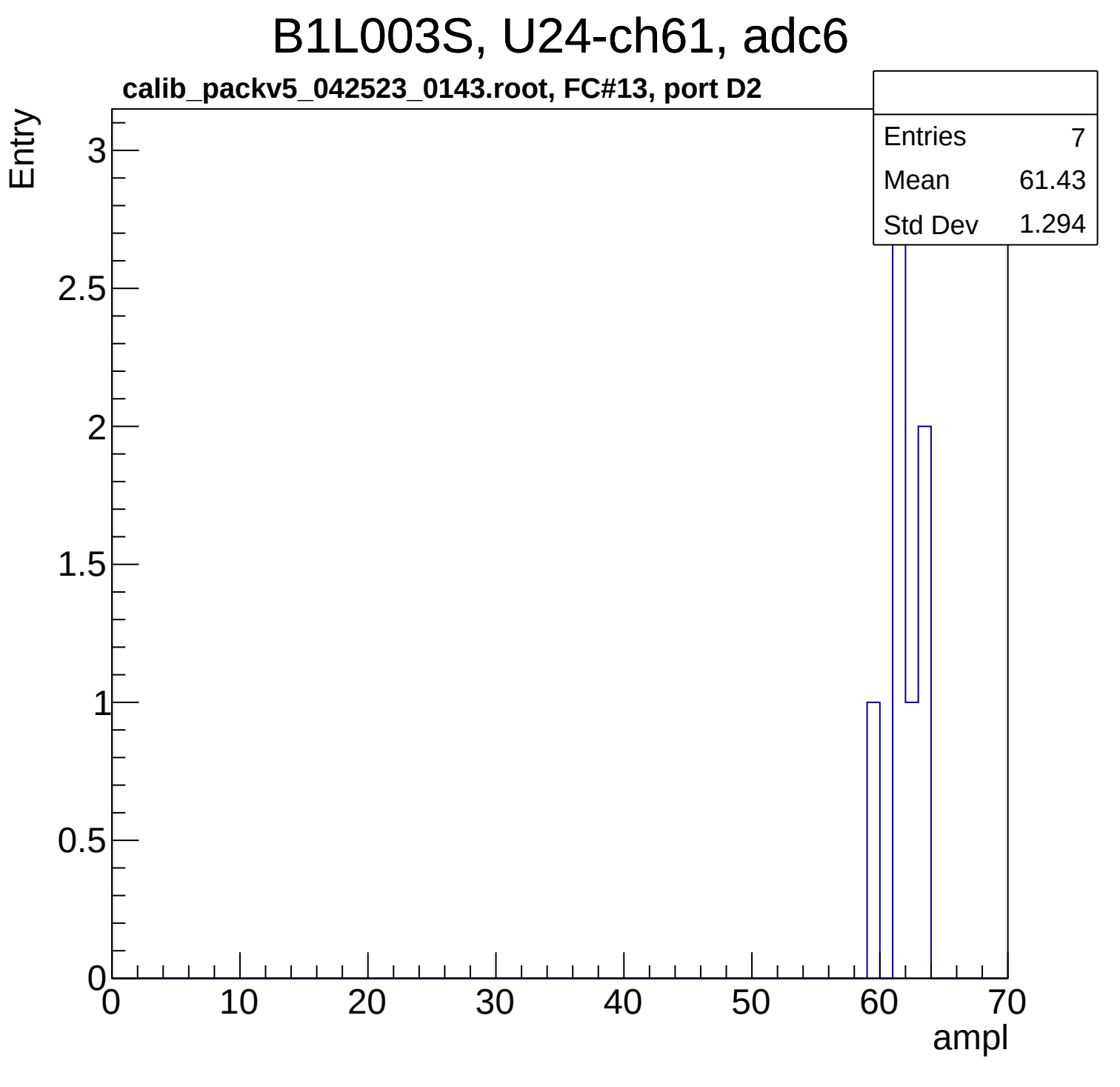
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch61, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.43
Std Dev 1.294
Entry
ampl
0
0.5
1
1.5
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2.5
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60
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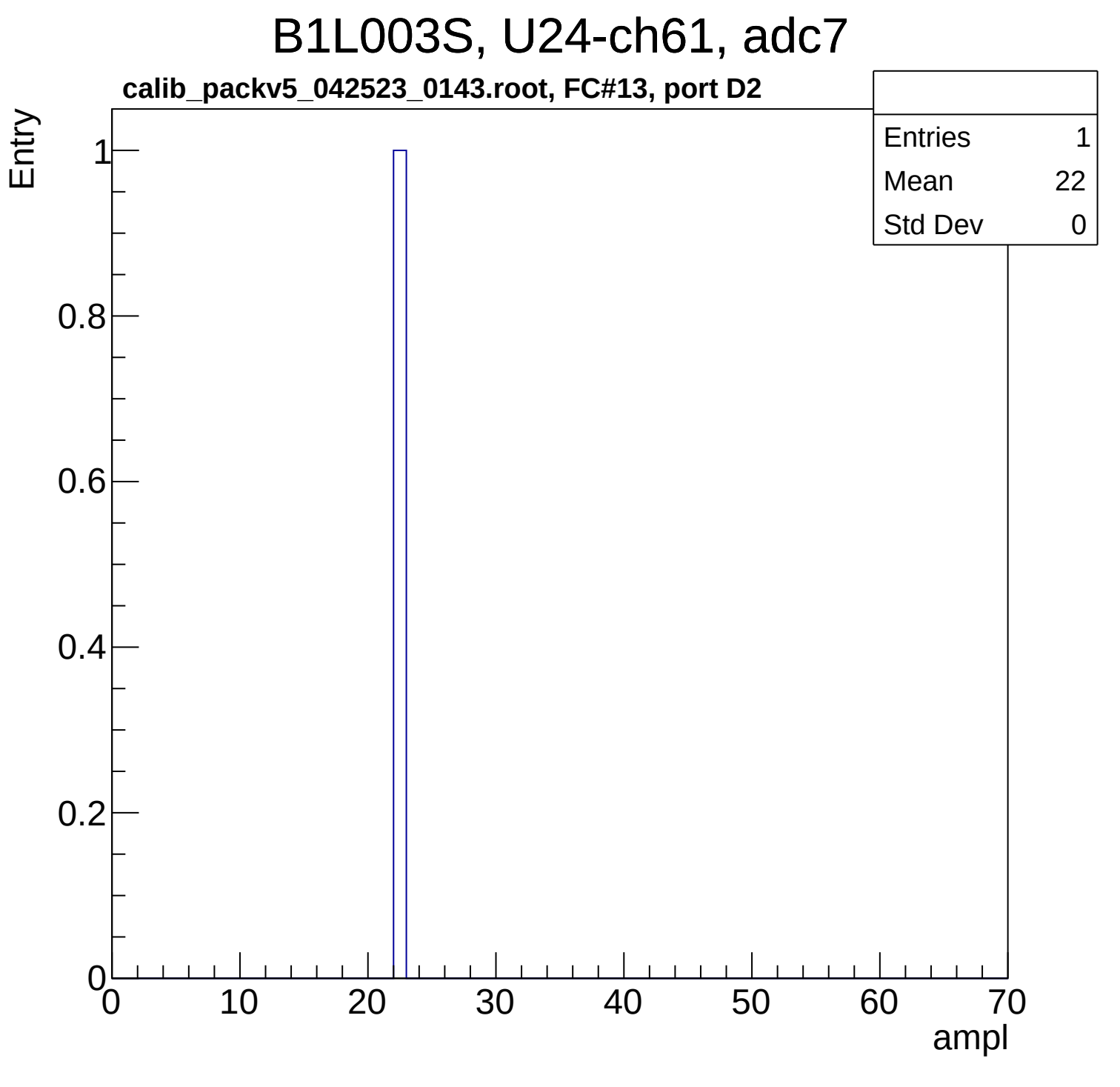
B1L003S, U24-ch61, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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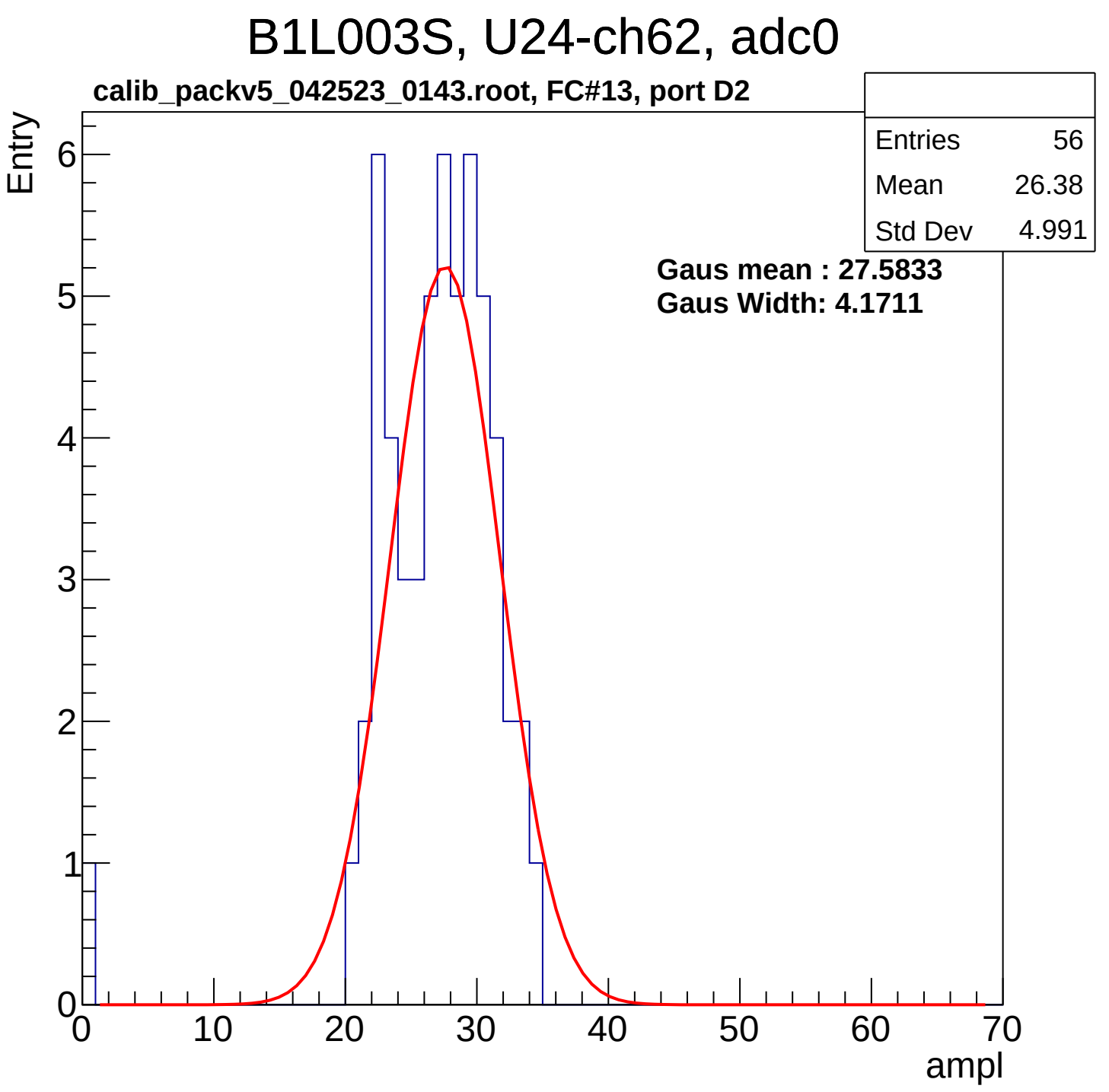
70
ampl


B1L003S, U24-ch62, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 26.38
Std Dev 4.991
Gaus mean : 27.5833
Gaus Width: 4.1711
Entry
ampl
0
1
2
3
4
5
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0
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60
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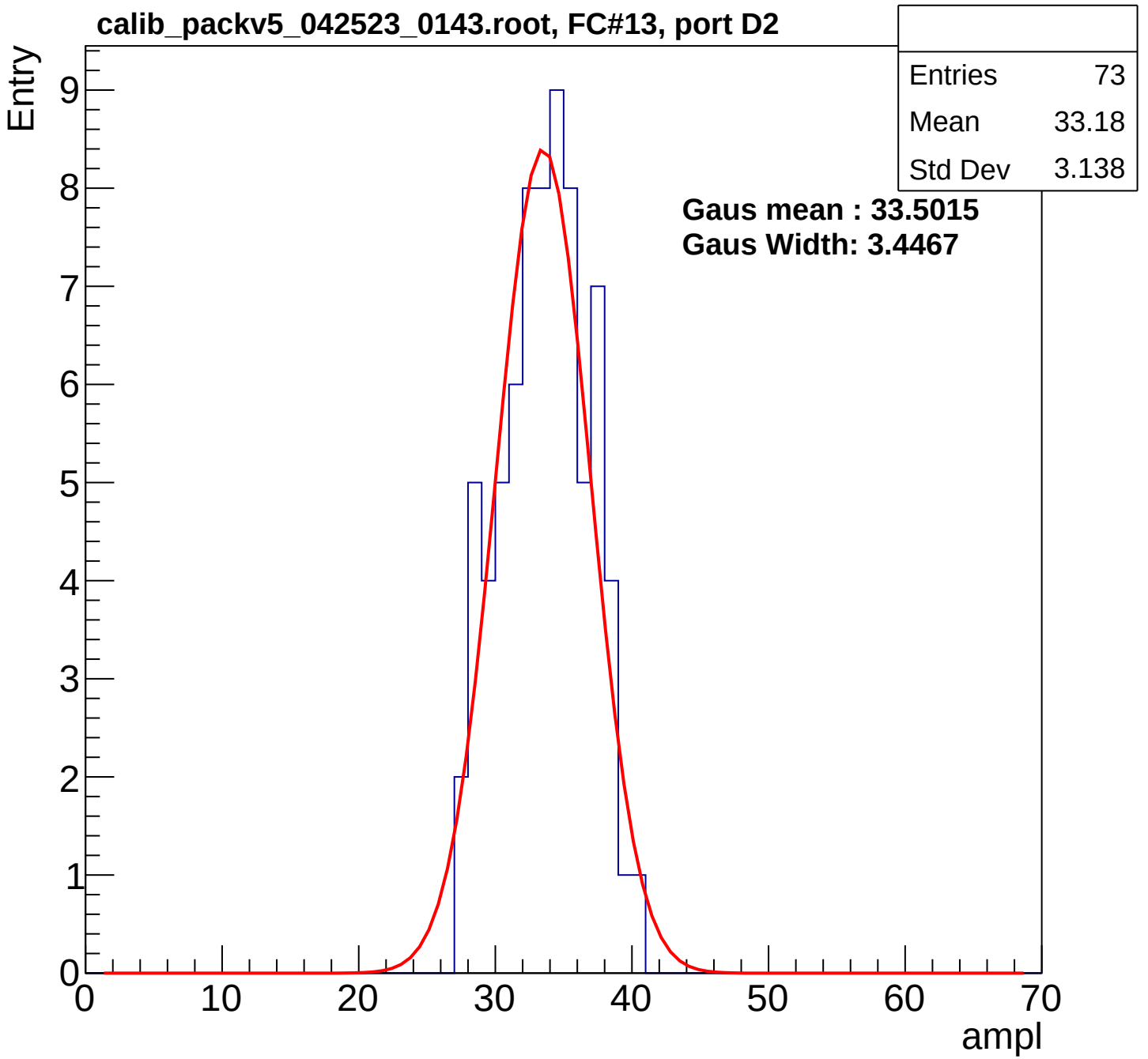
B1L003S, U24-ch62, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 33.18
Std Dev 3.138
Gaus mean : 33.5015
Gaus Width: 3.4467
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
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6
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8
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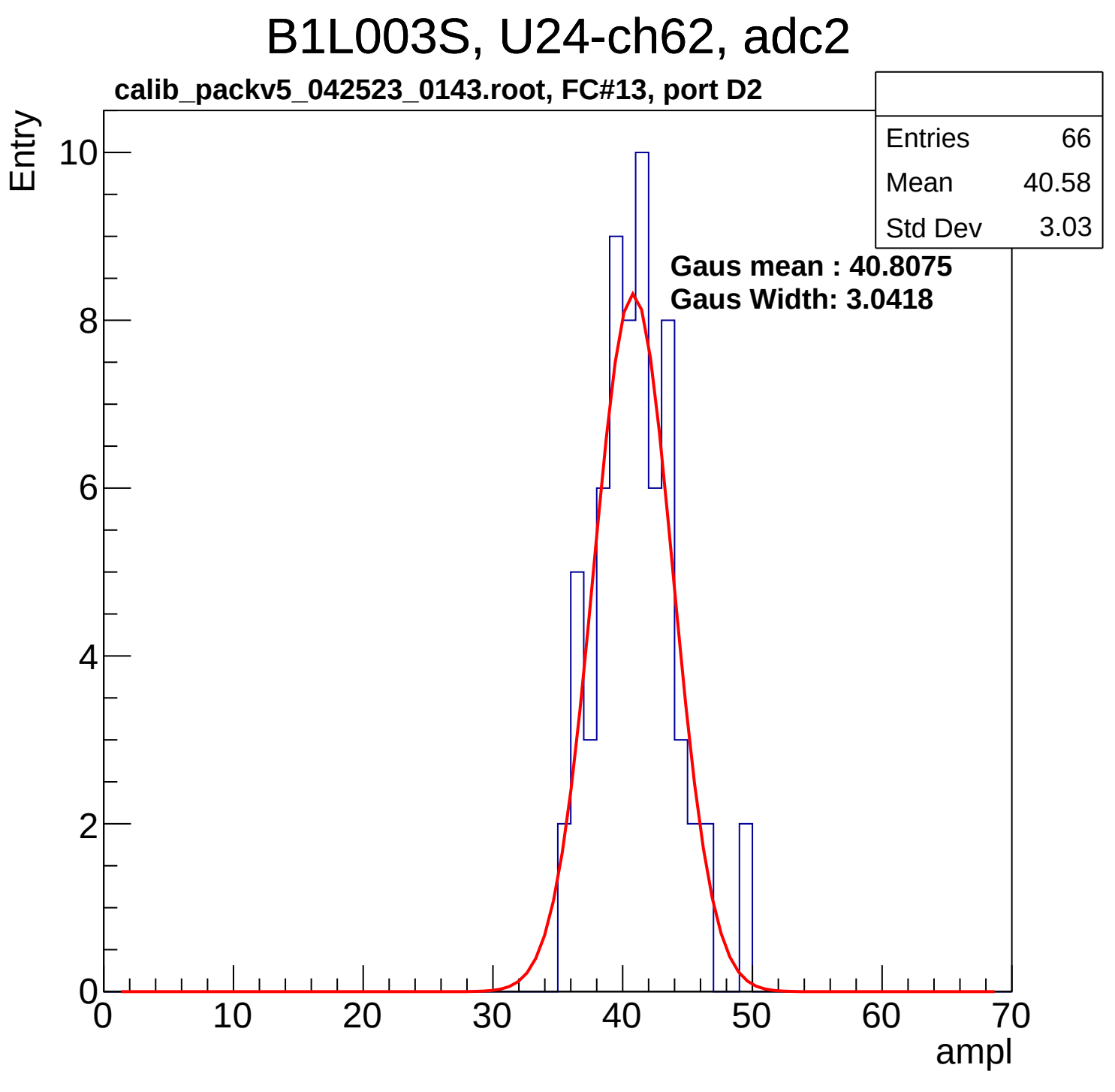
B1L003S, U24-ch62, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 40.58
Std Dev 3.03
Gaus mean : 40.8075
Gaus Width: 3.0418
Entry
ampl
0
2
4
6
8
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0
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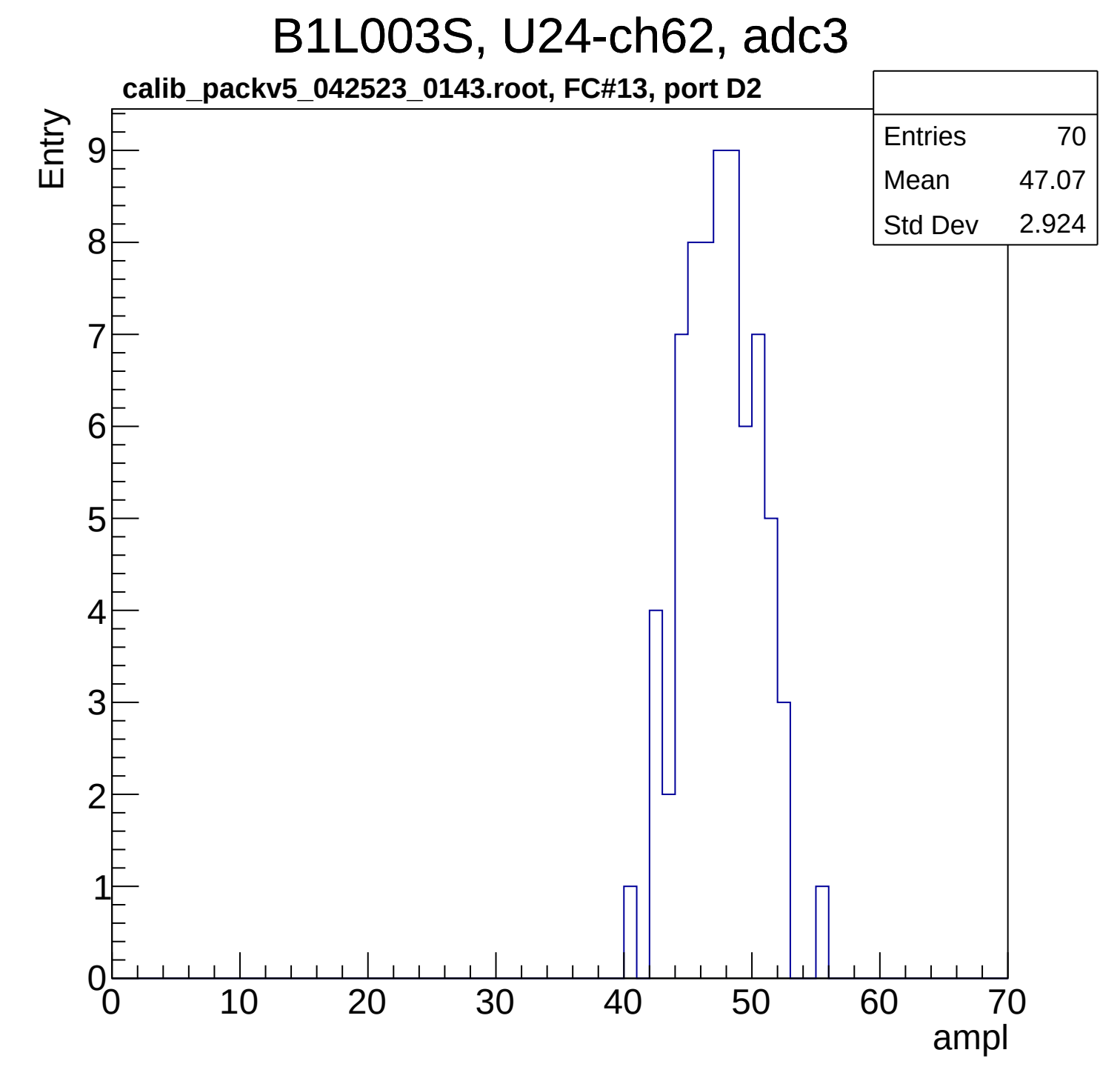
B1L003S, U24-ch62, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 47.07
Std Dev 2.924
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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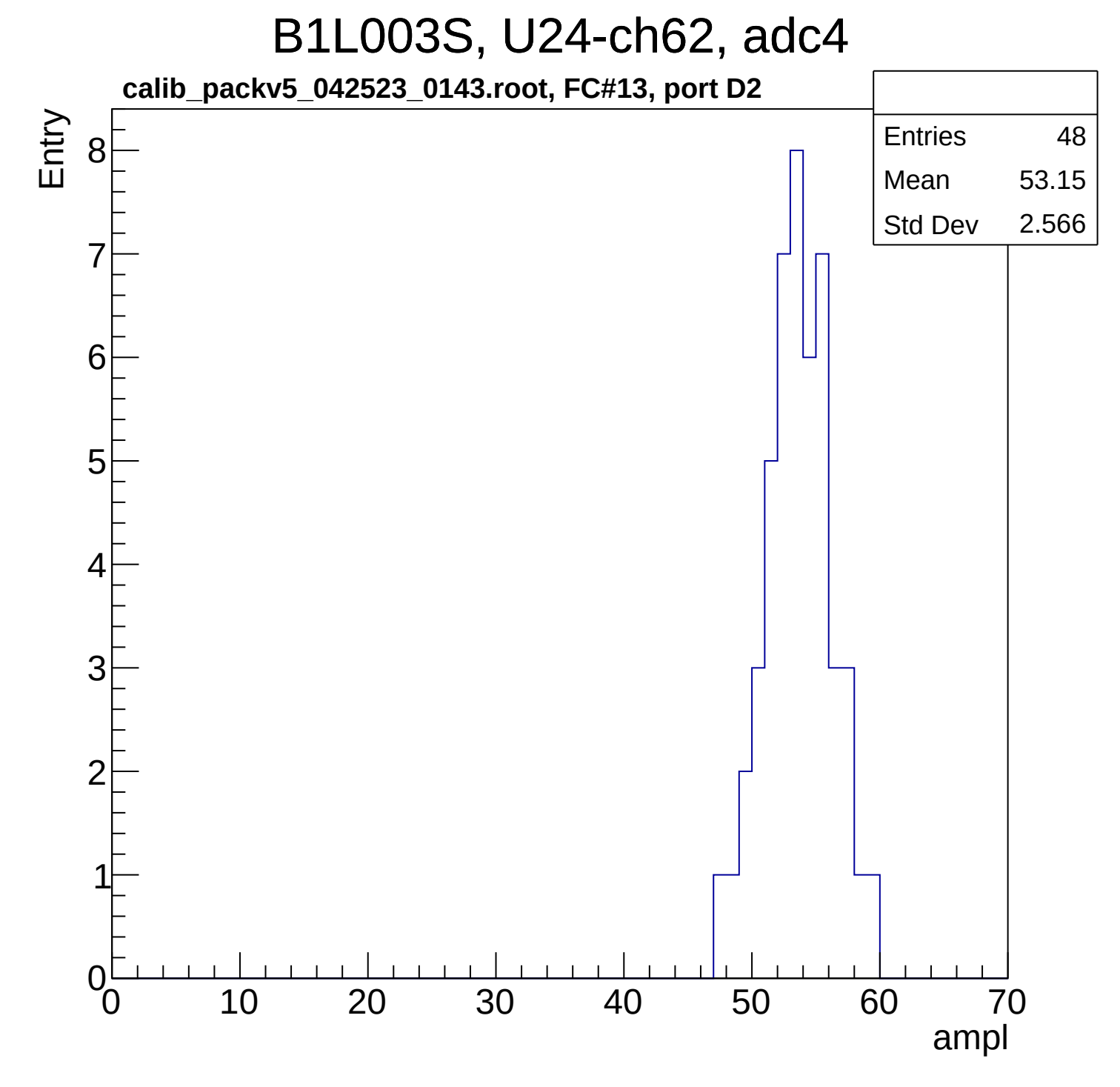
B1L003S, U24-ch62, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 53.15
Std Dev 2.566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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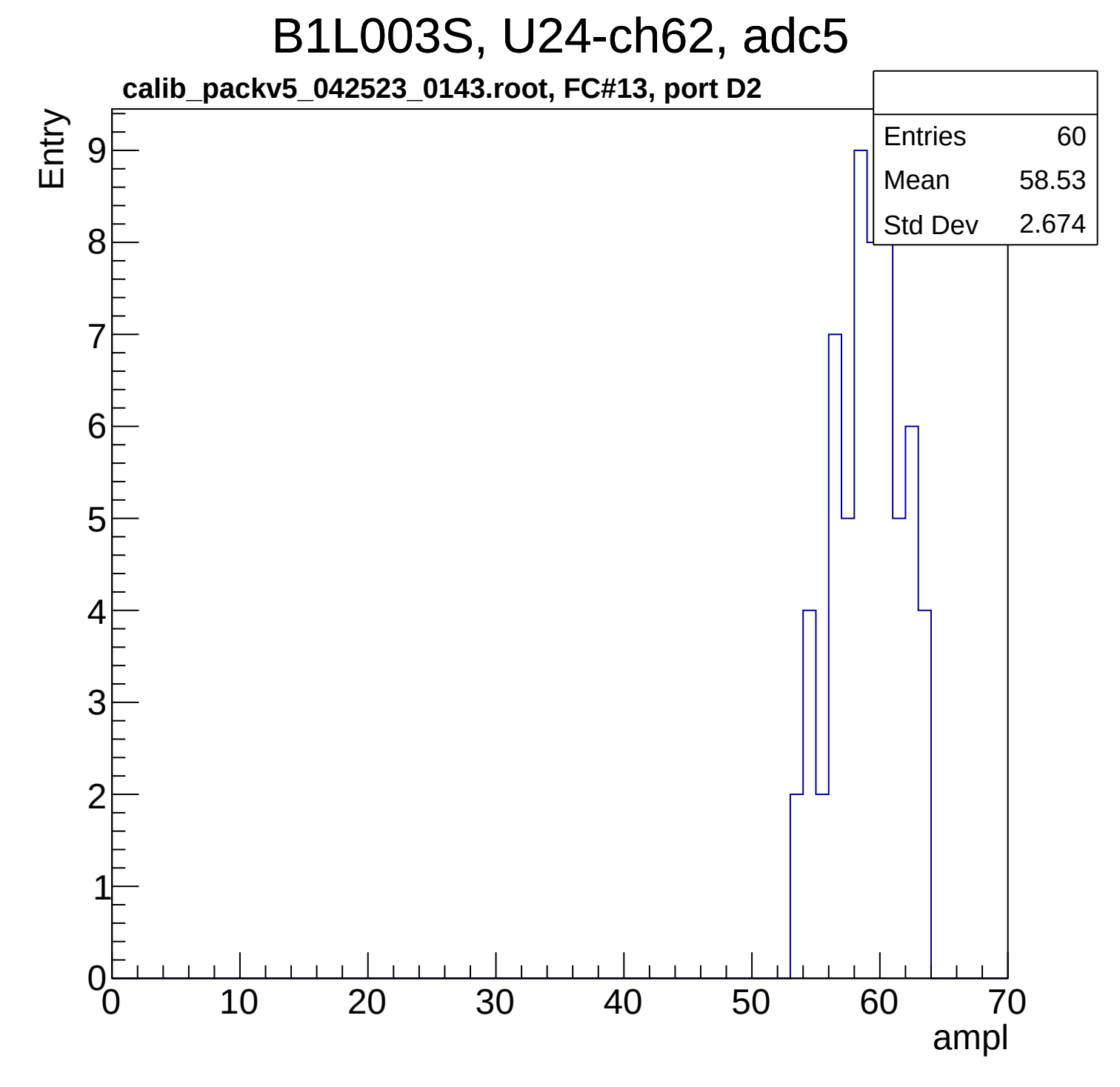
B1L003S, U24-ch62, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.53
Std Dev 2.674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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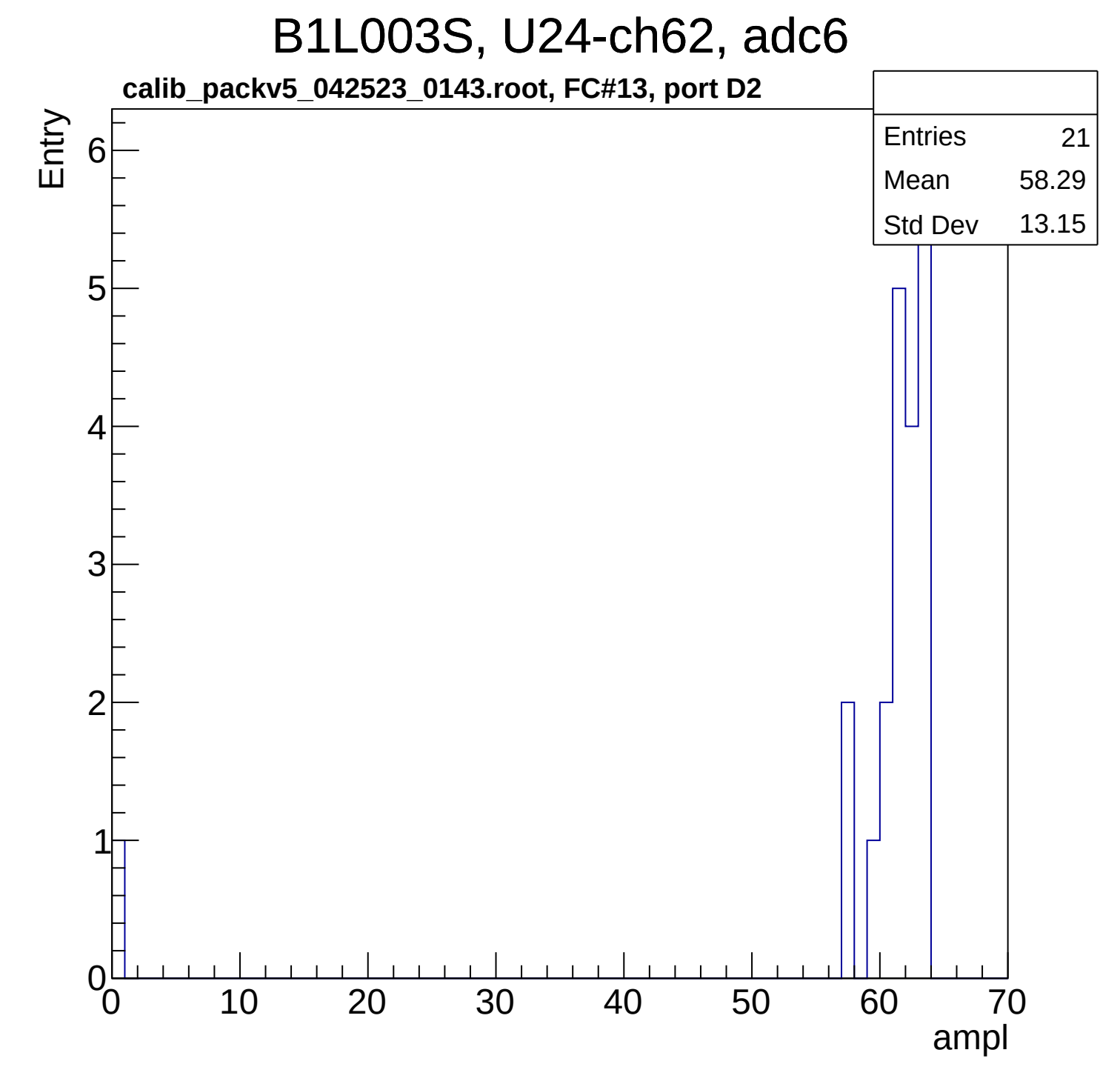
B1L003S, U24-ch62, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 58.29
Std Dev 13.15
Entry
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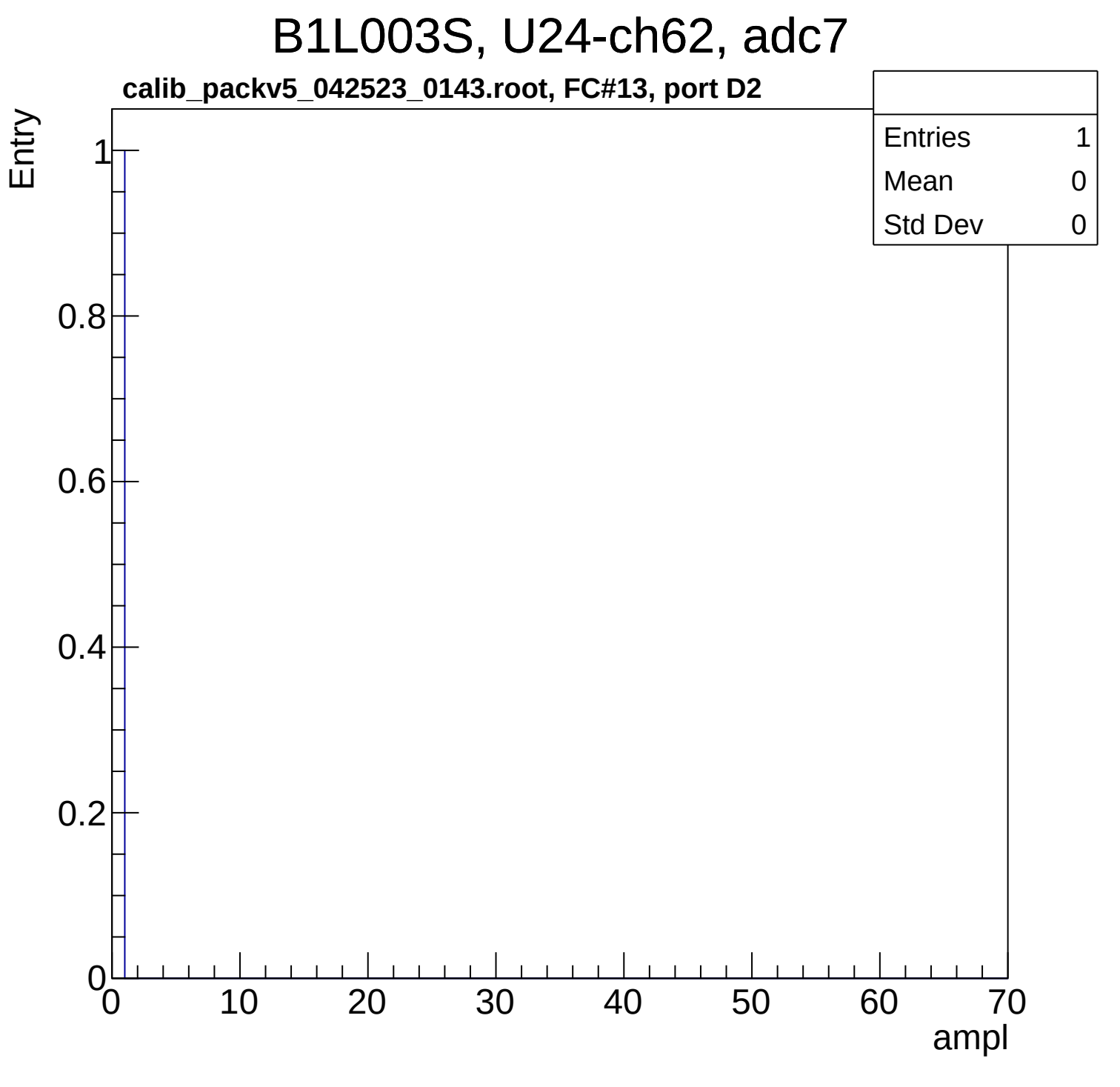
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch62, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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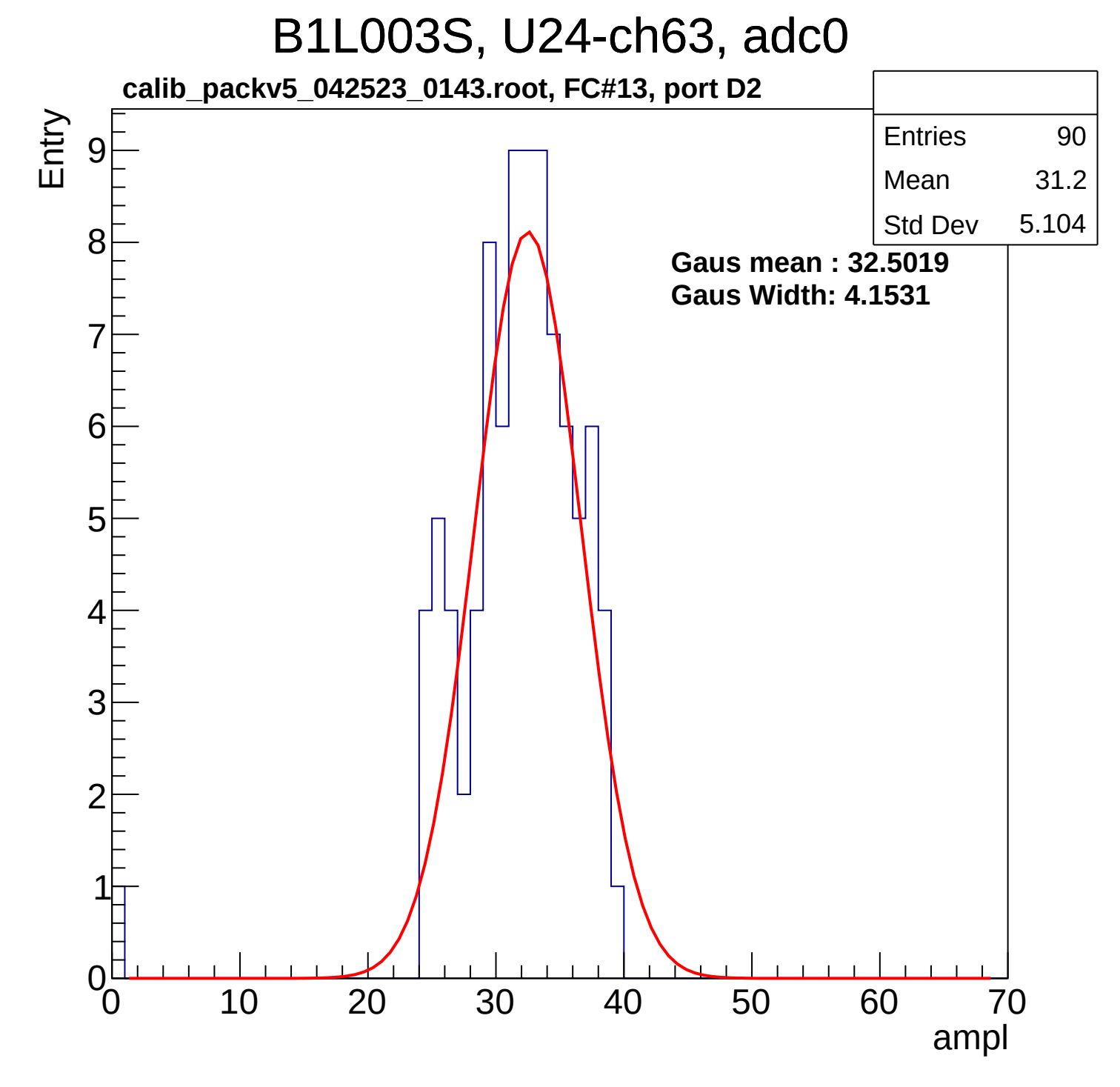
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch63, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 90
Mean 31.2
Std Dev 5.104
Gaus mean : 32.5019
Gaus Width: 4.1531
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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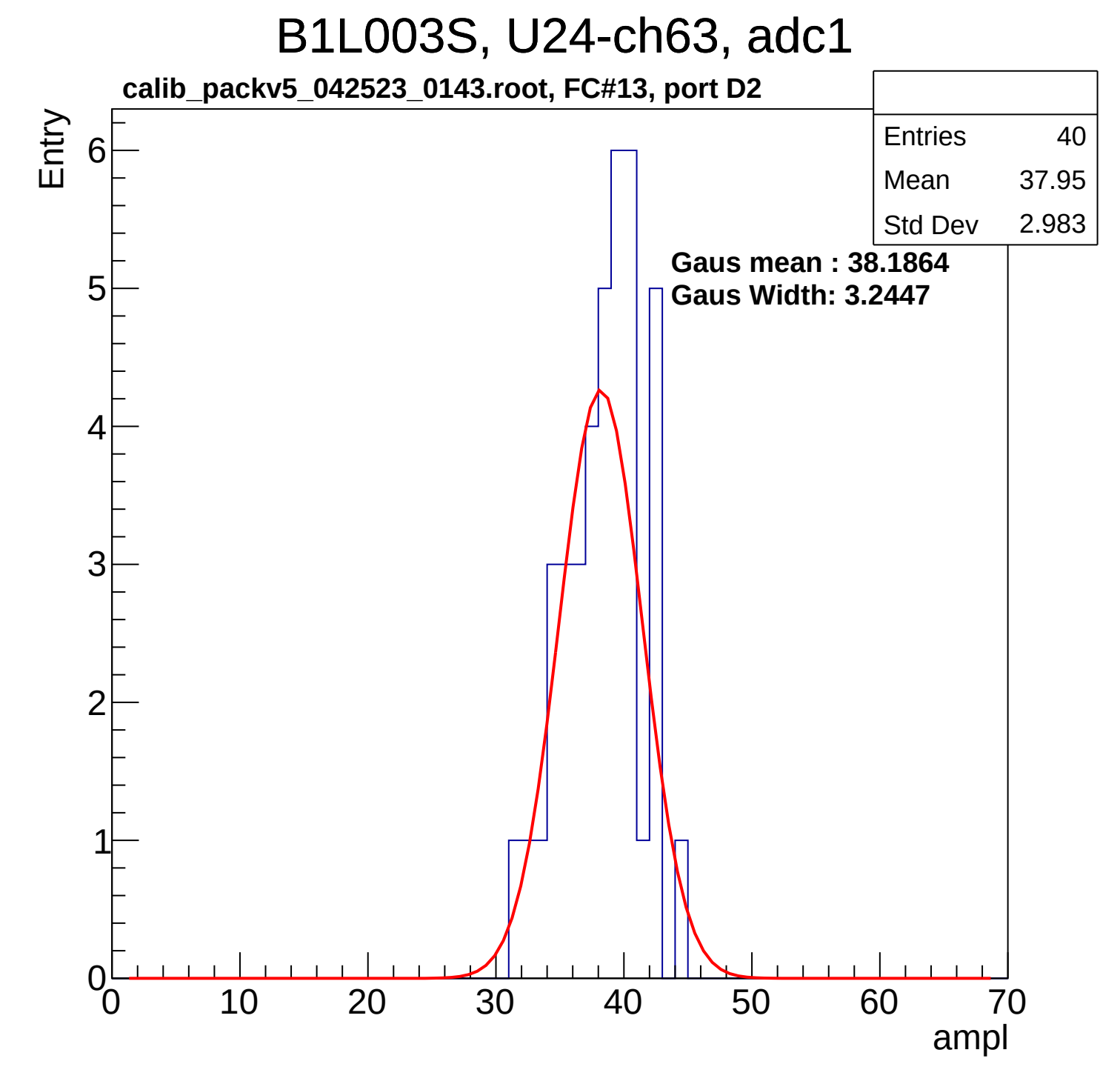
B1L003S, U24-ch63, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 37.95
Std Dev 2.983
Gaus mean : 38.1864
Gaus Width: 3.2447
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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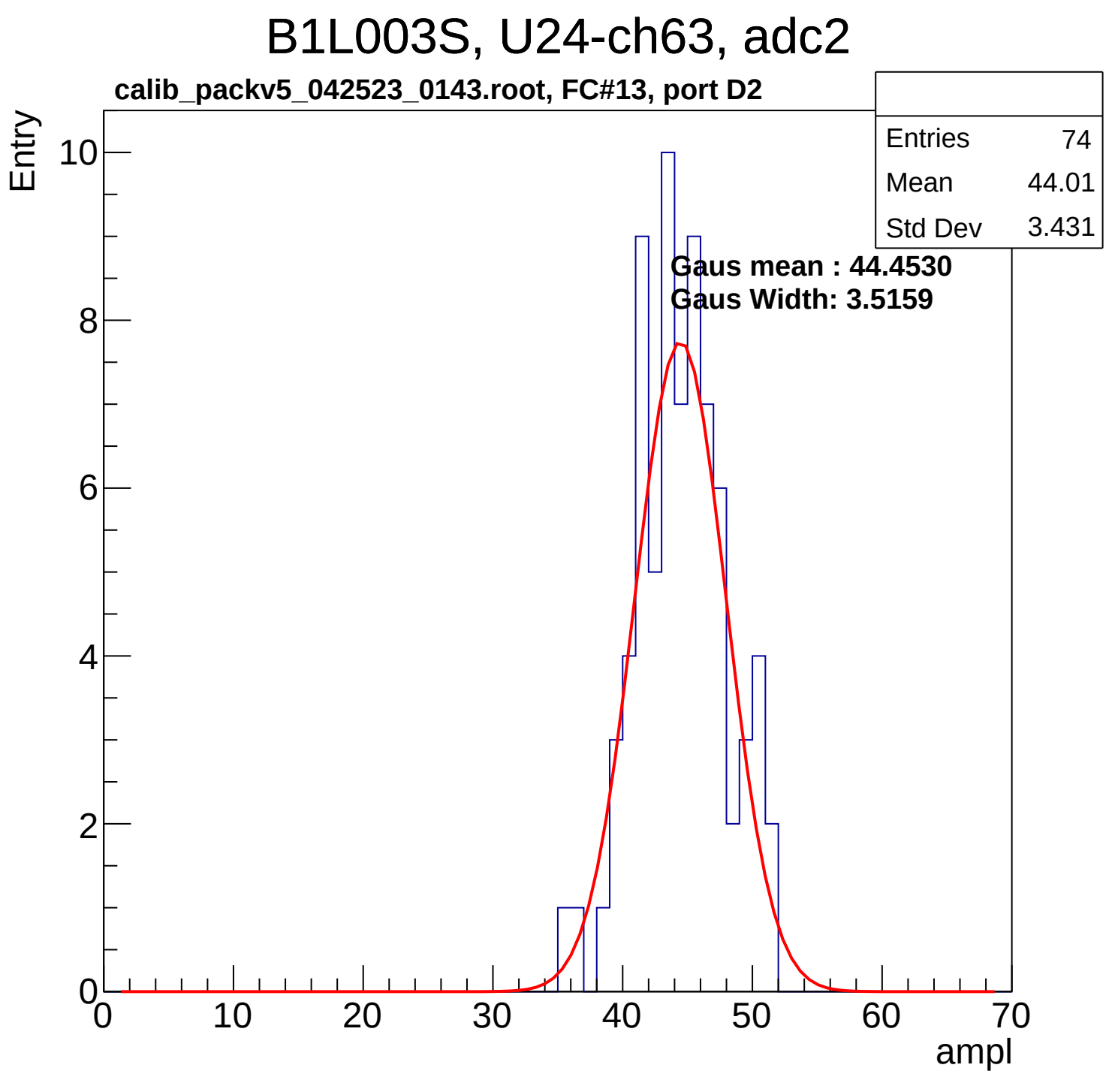
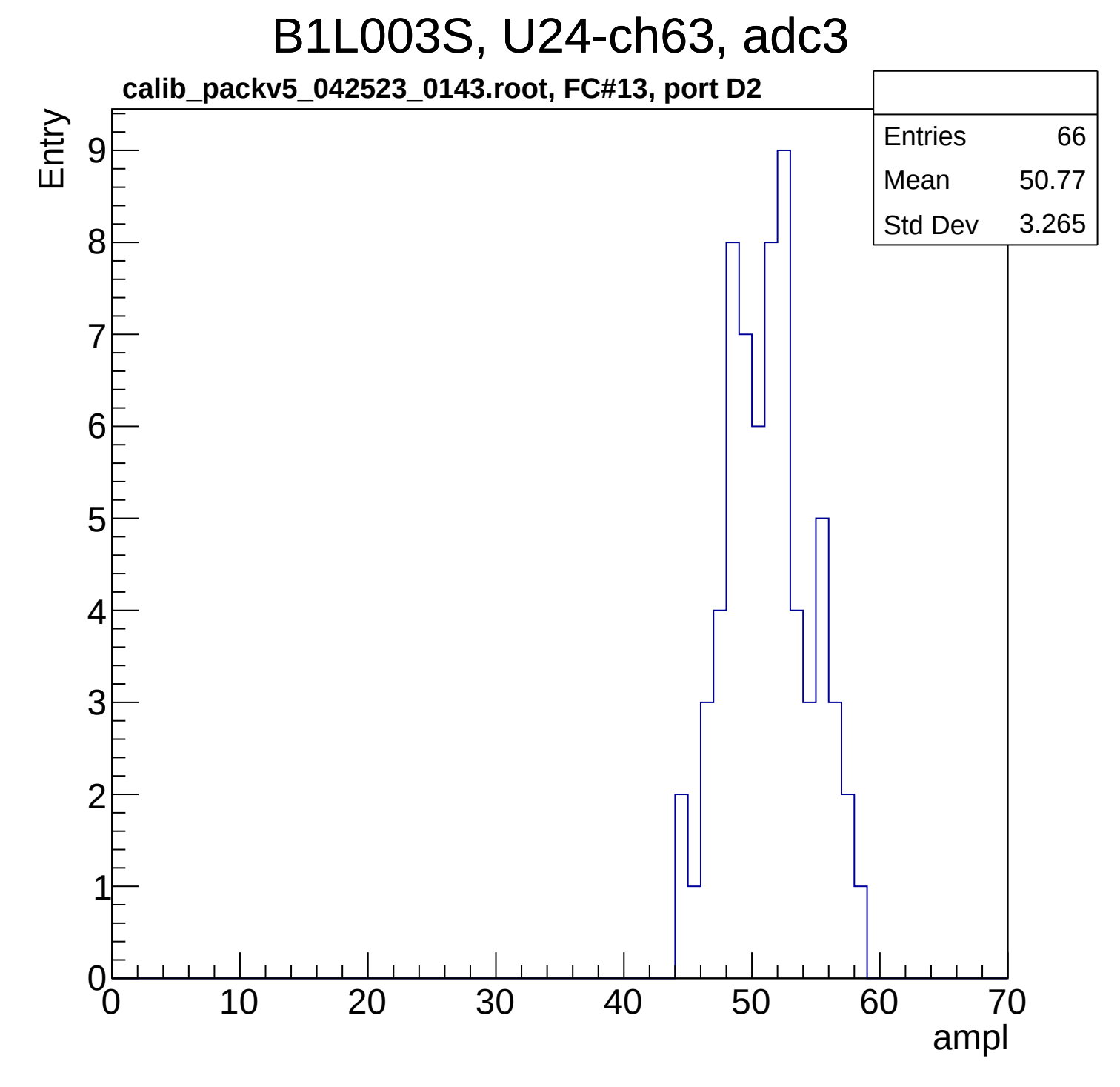

B1L003S, U24-ch63, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 44.01
Std Dev 3.431
Gaus mean : 44.4530
Gaus Width: 3.5159
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch63, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 50.77
Std Dev 3.265
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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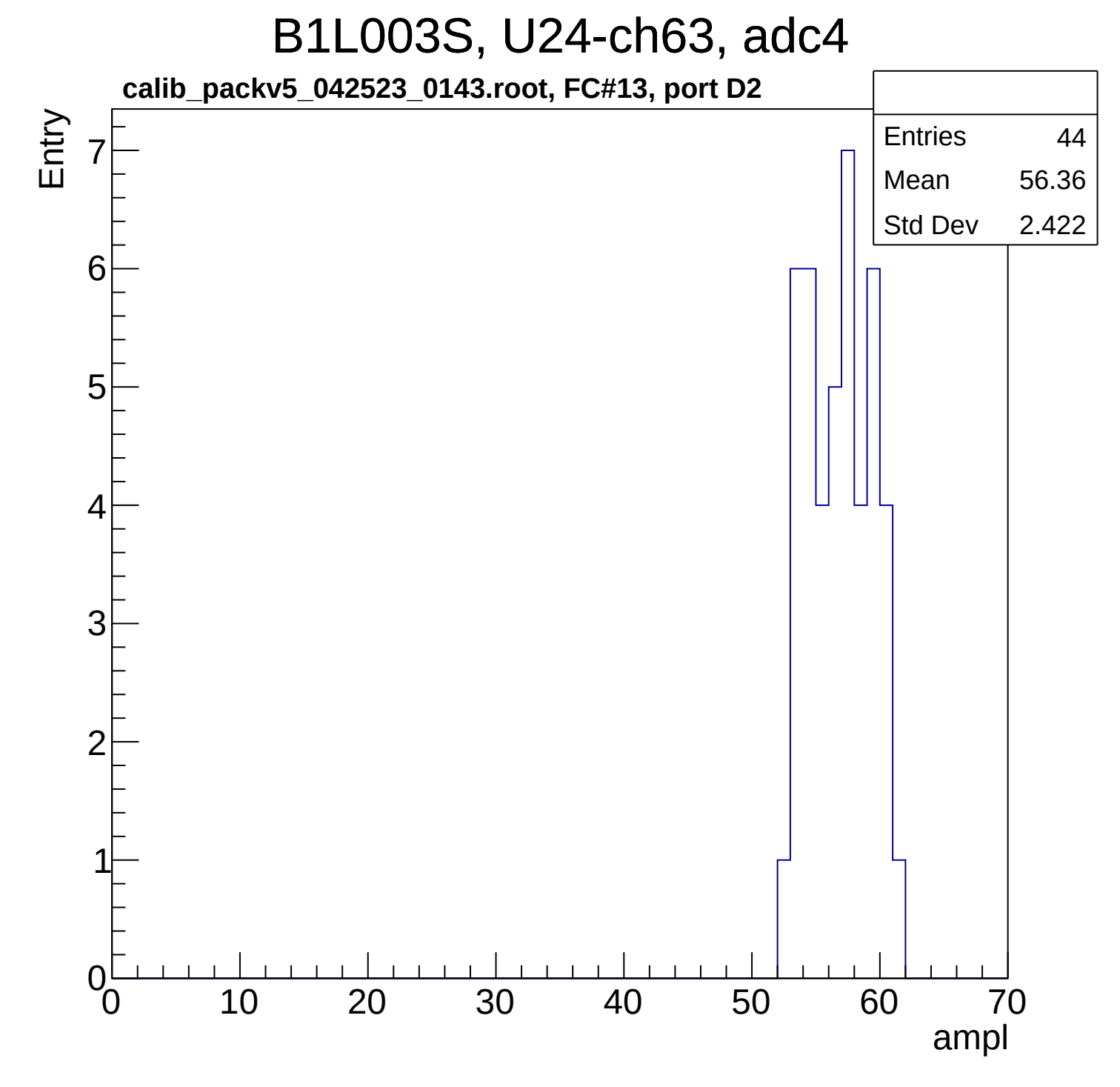
B1L003S, U24-ch63, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 56.36
Std Dev 2.422
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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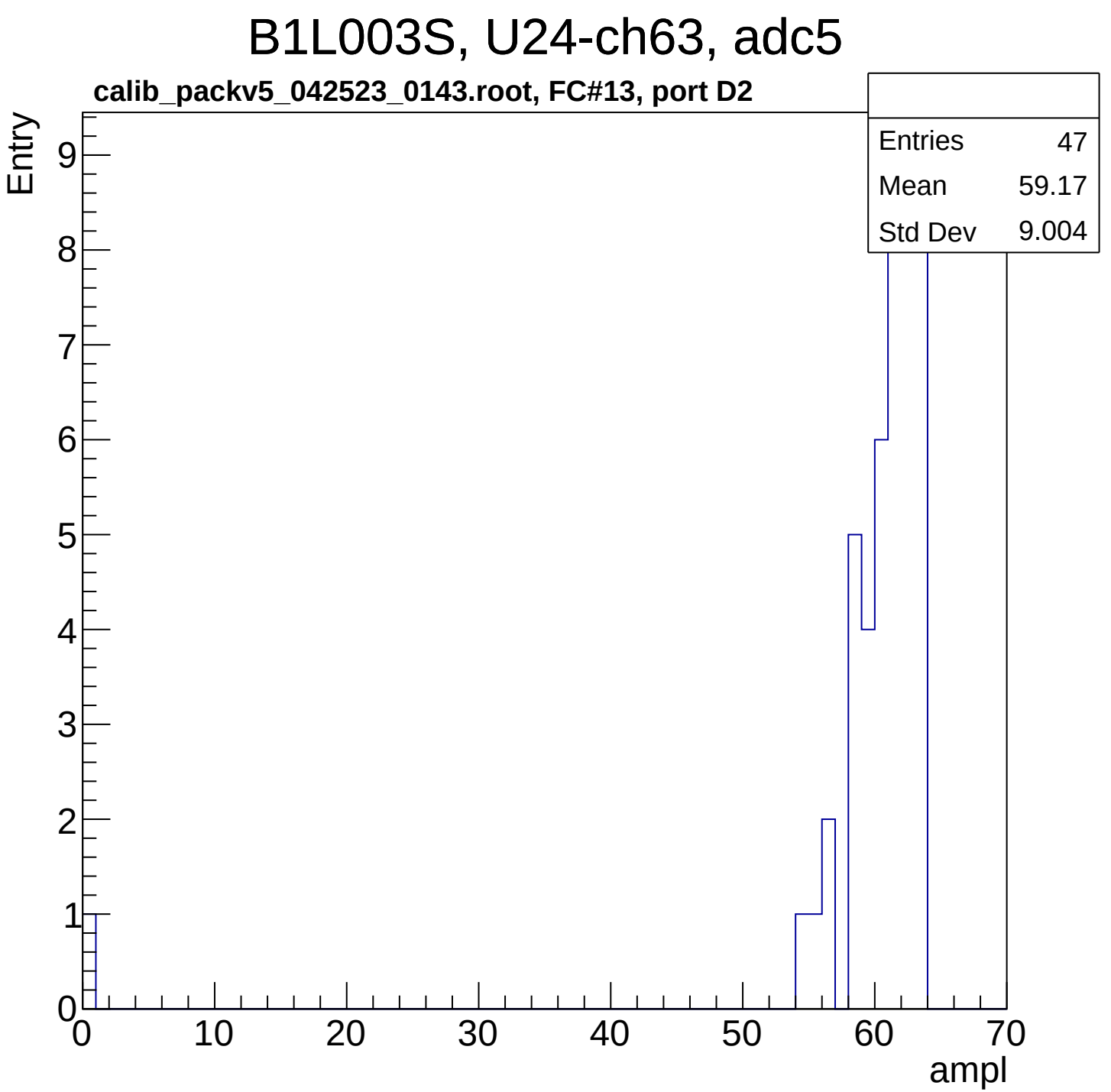
B1L003S, U24-ch63, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59.17
Std Dev 9.004
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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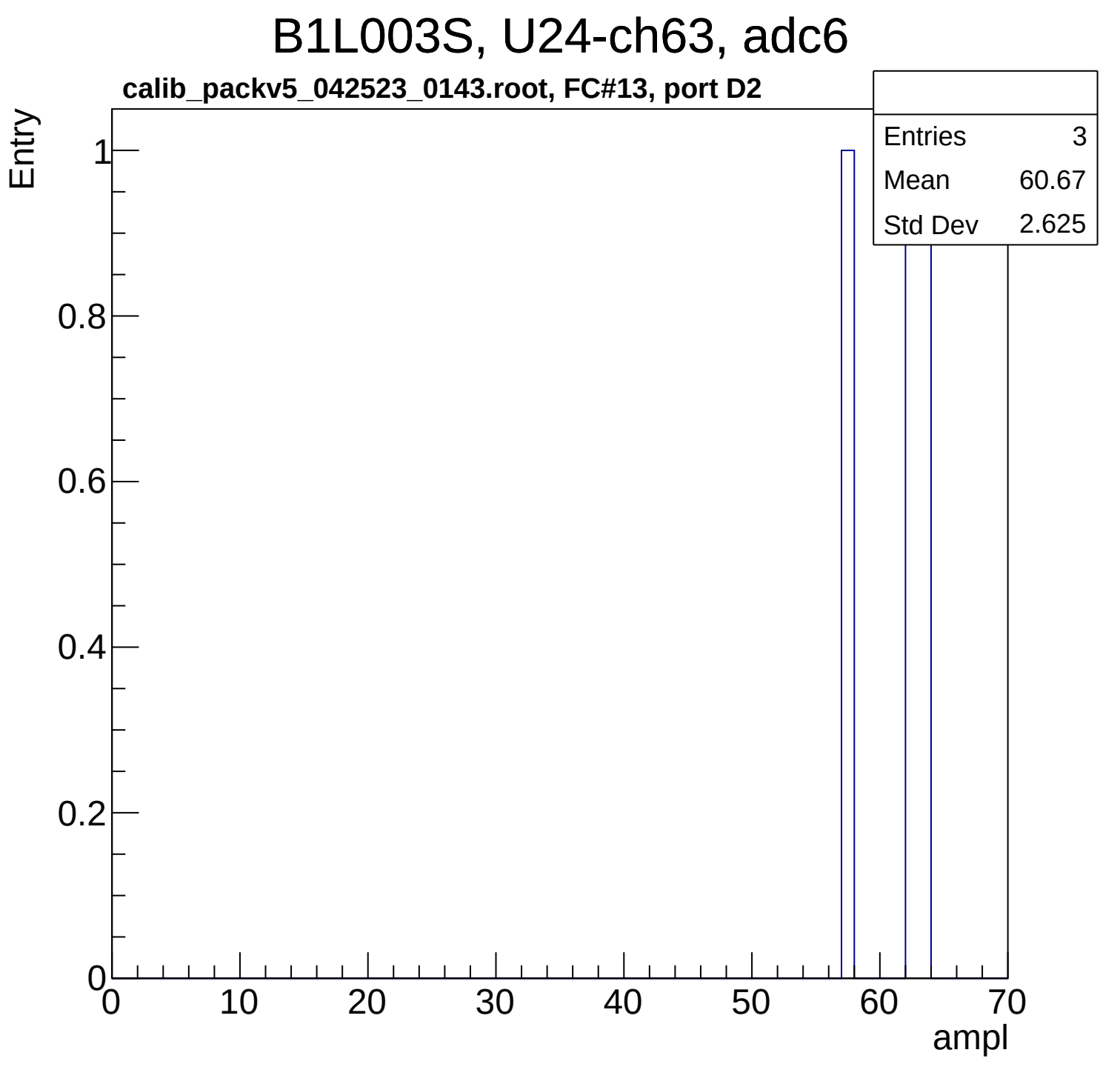
B1L003S, U24-ch63, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 60.67
Std Dev 2.625
Entry
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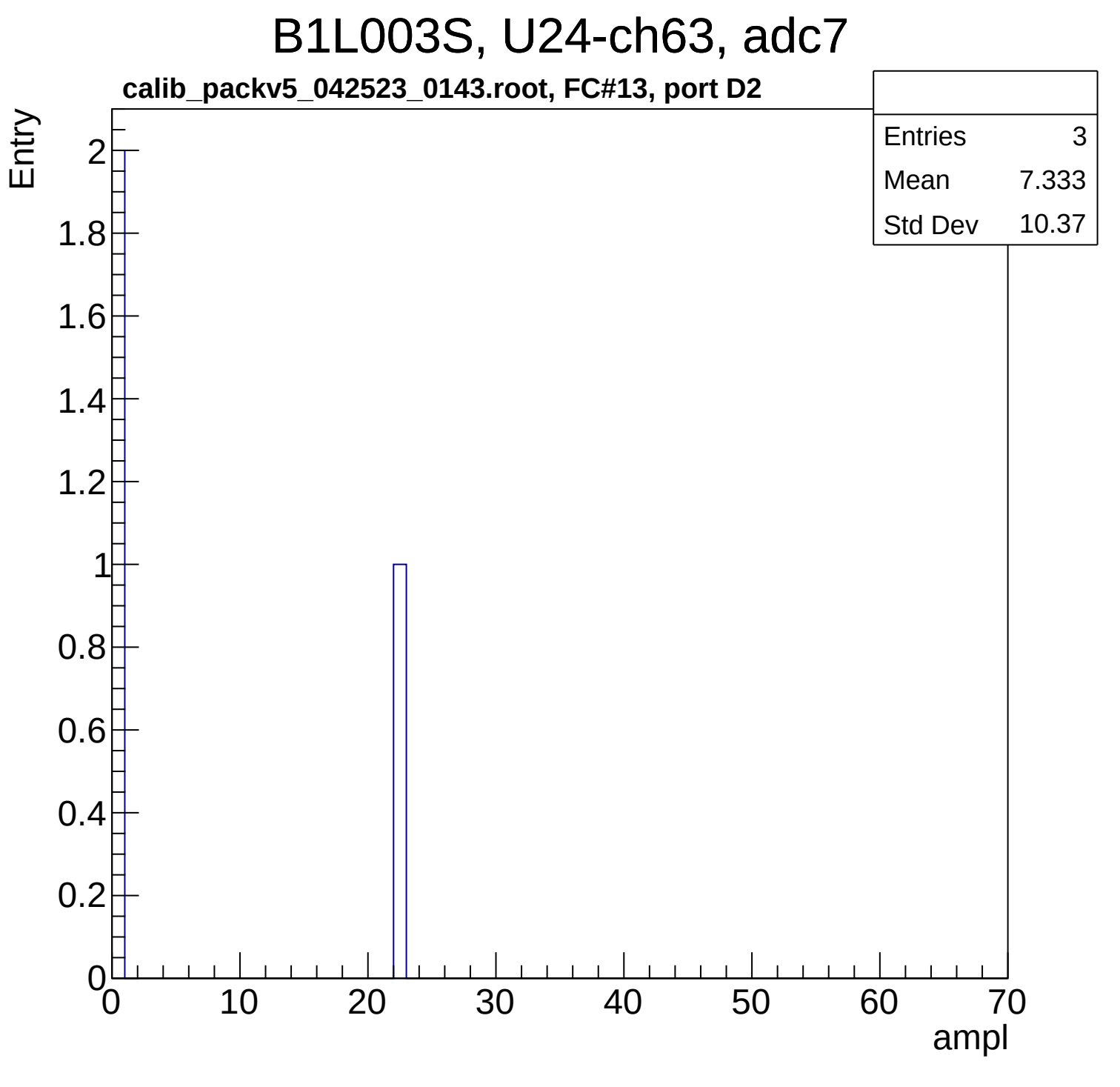
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch63, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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60
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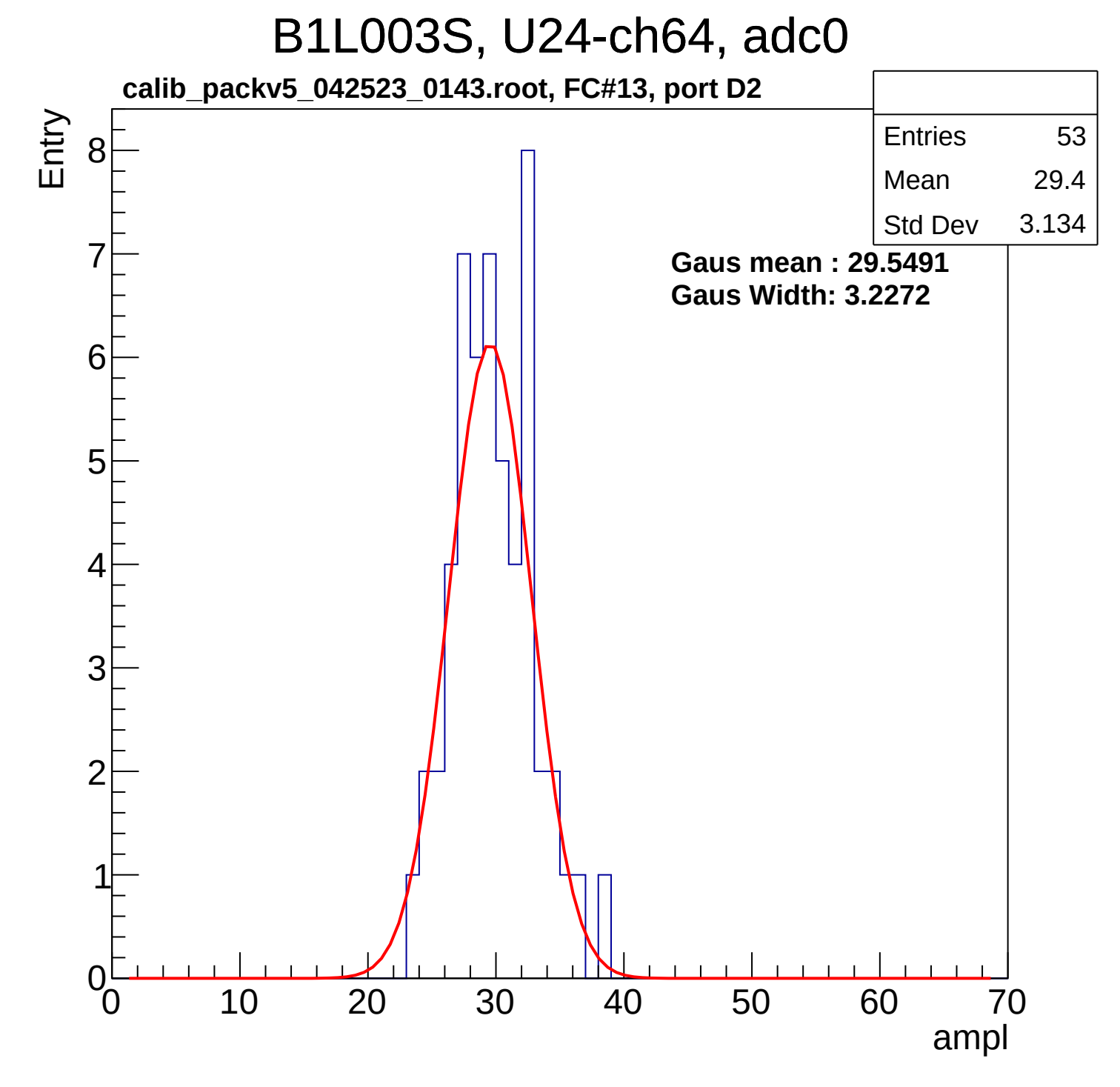
B1L003S, U24-ch64, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 29.4
Std Dev 3.134
Gaus mean : 29.5491
Gaus Width: 3.2272
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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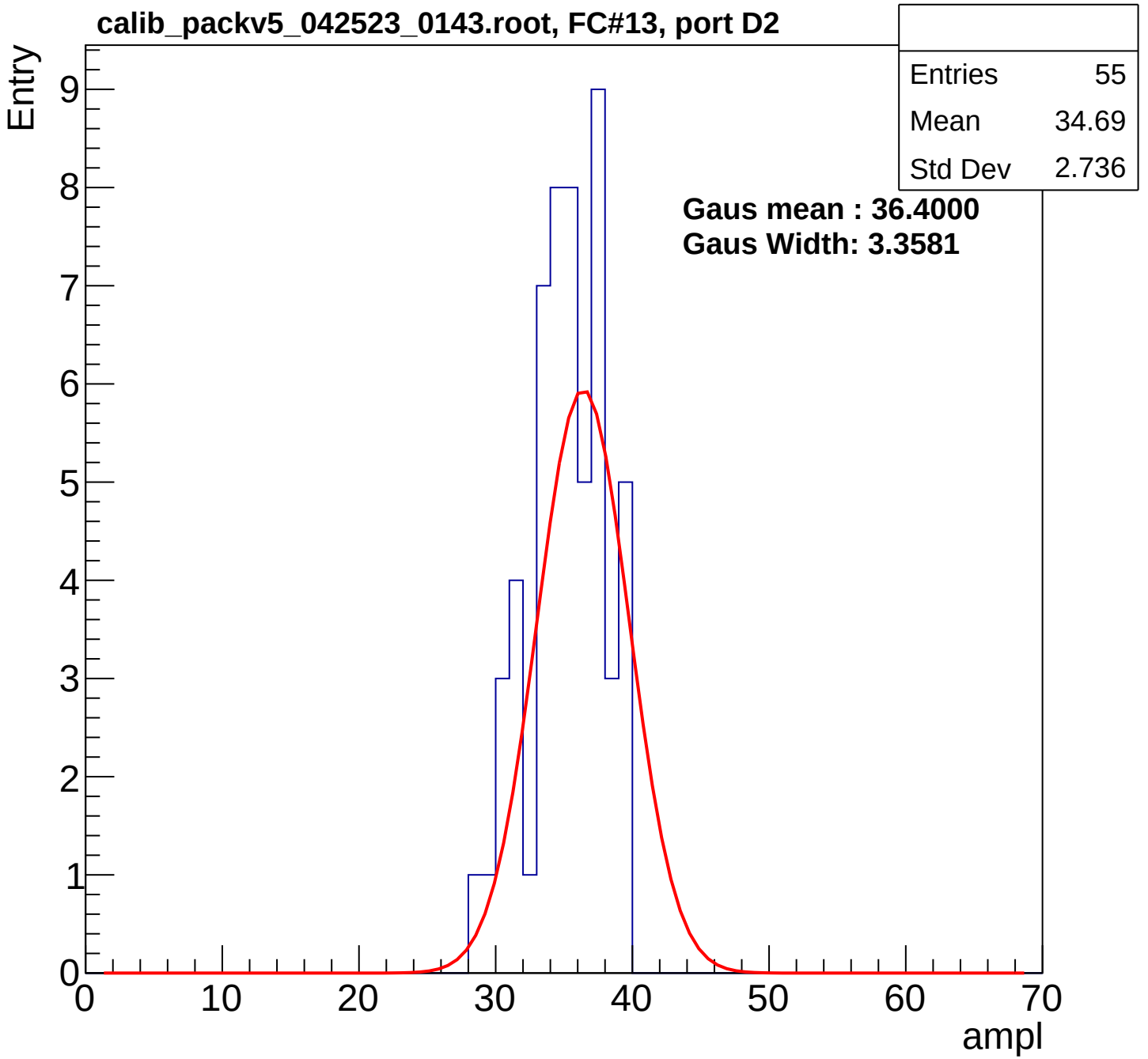
B1L003S, U24-ch64, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 34.69
Std Dev 2.736
Gaus mean : 36.4000
Gaus Width: 3.3581
Entry
ampl
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10
20
30
40
50
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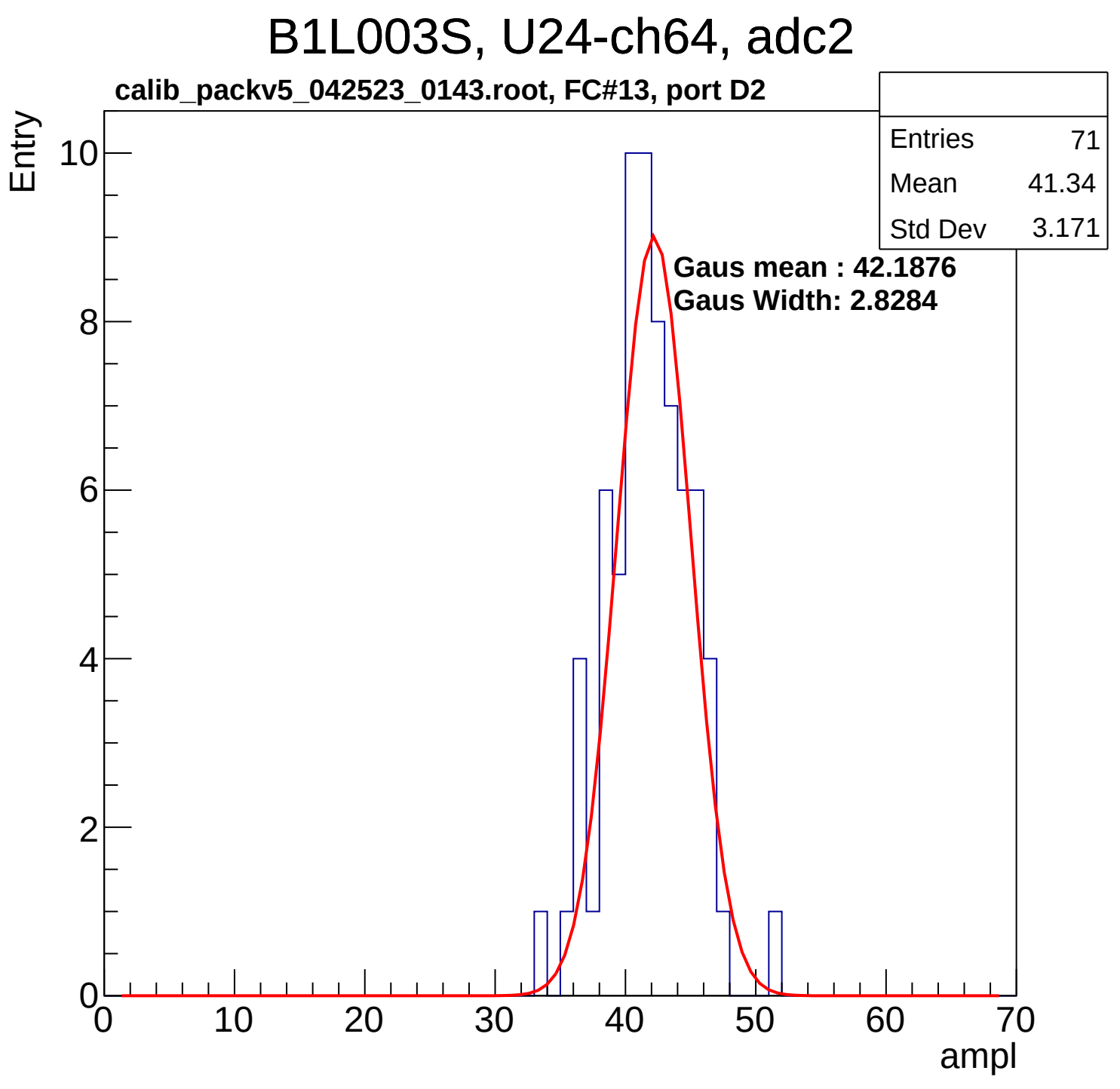
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch64, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 41.34
Std Dev 3.171
Gaus mean : 42.1876
Gaus Width: 2.8284
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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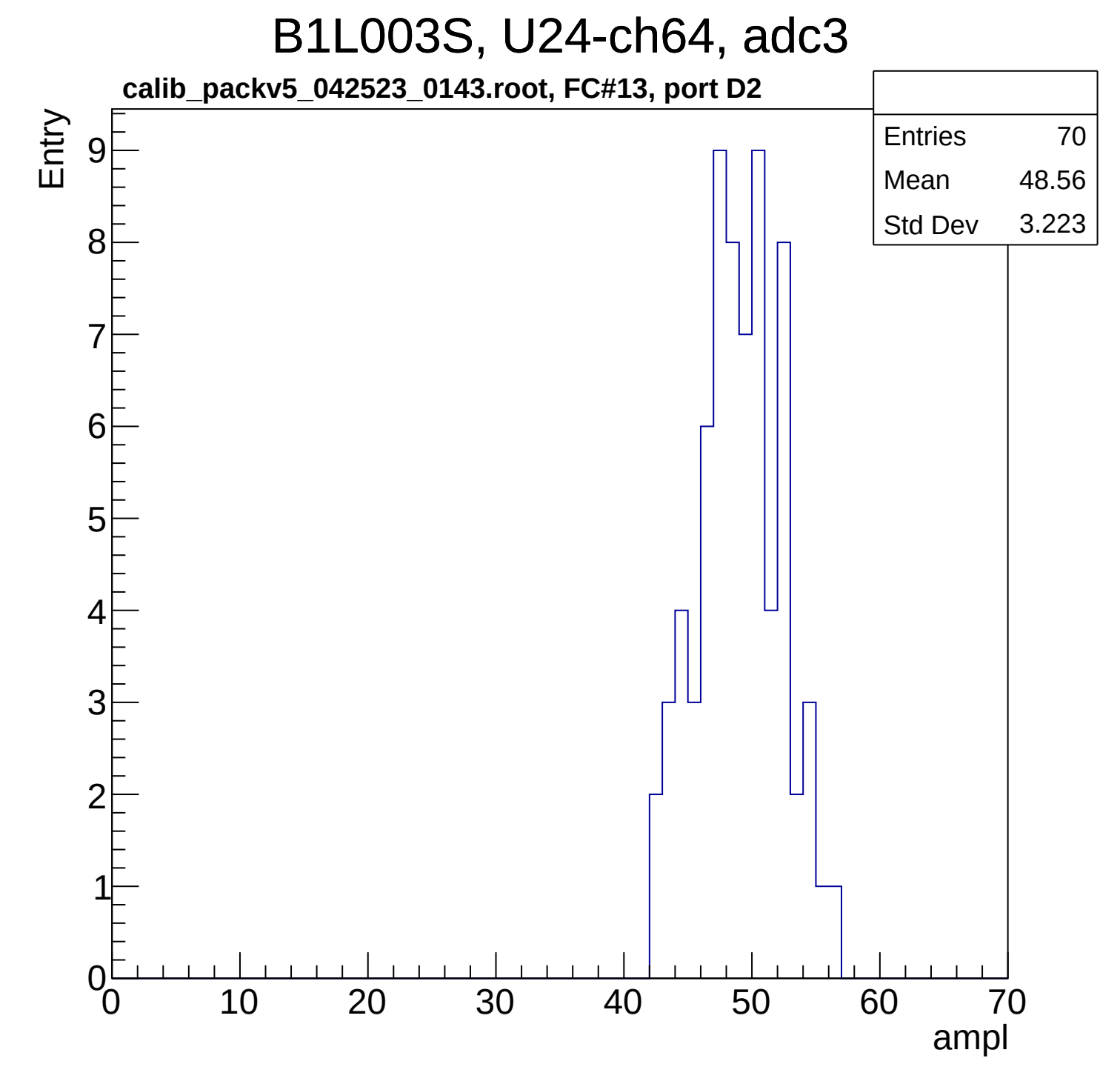
B1L003S, U24-ch64, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 48.56
Std Dev 3.223
Entry
ampl
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3
4
5
6
7
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0
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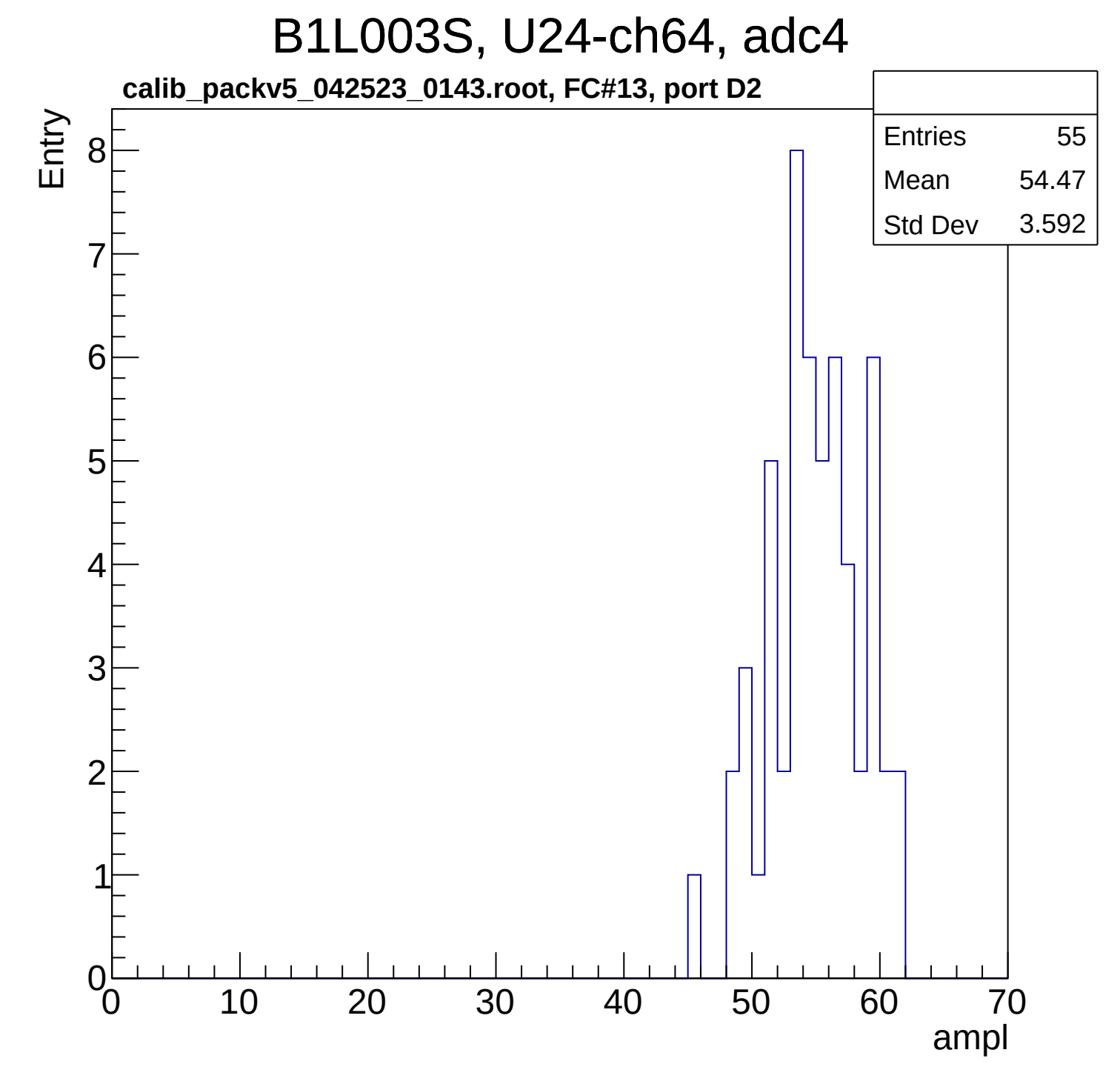
B1L003S, U24-ch64, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 54.47
Std Dev 3.592
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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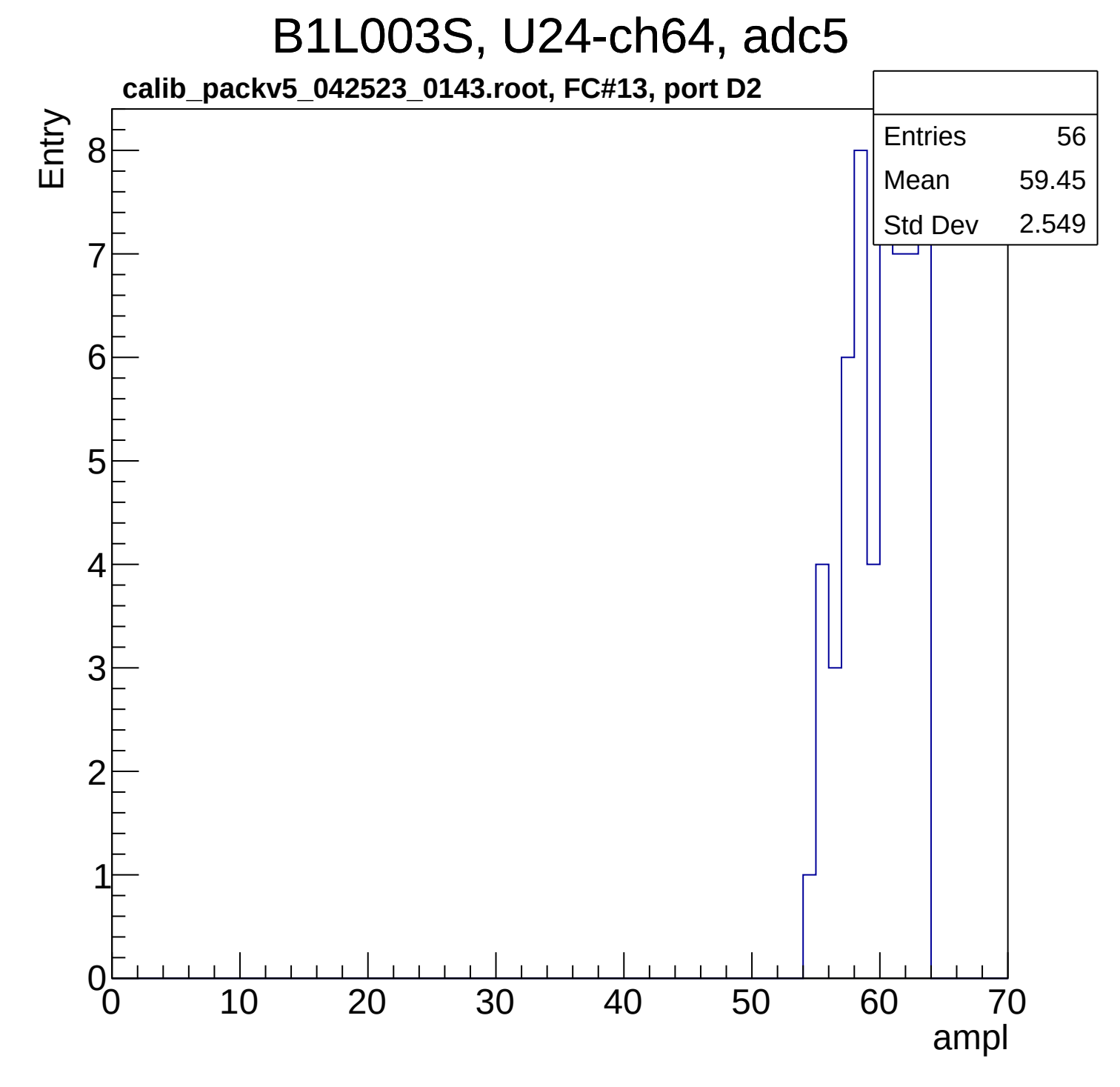
B1L003S, U24-ch64, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.45
Std Dev 2.549
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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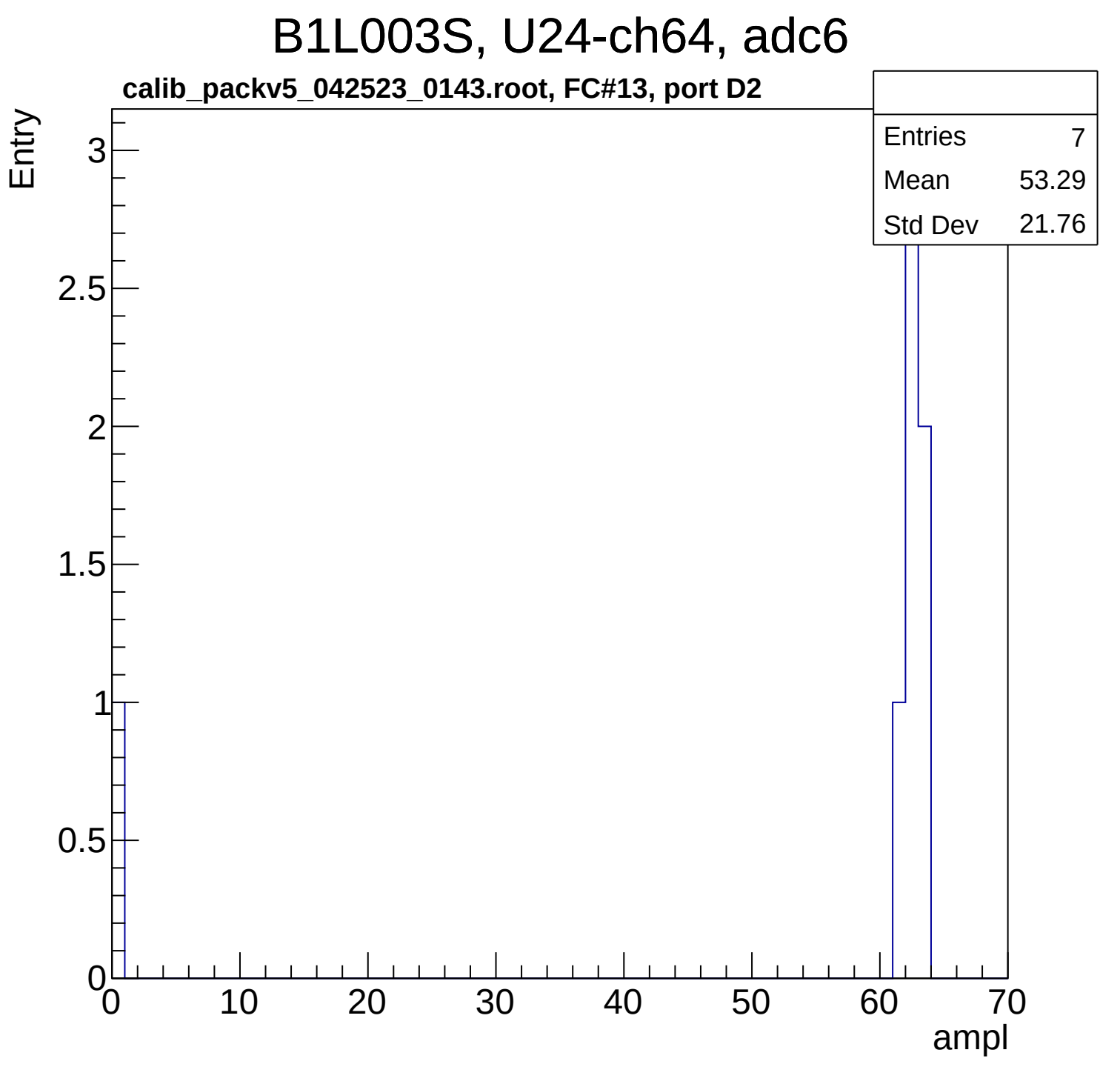
B1L003S, U24-ch64, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.29
Std Dev 21.76
Entry
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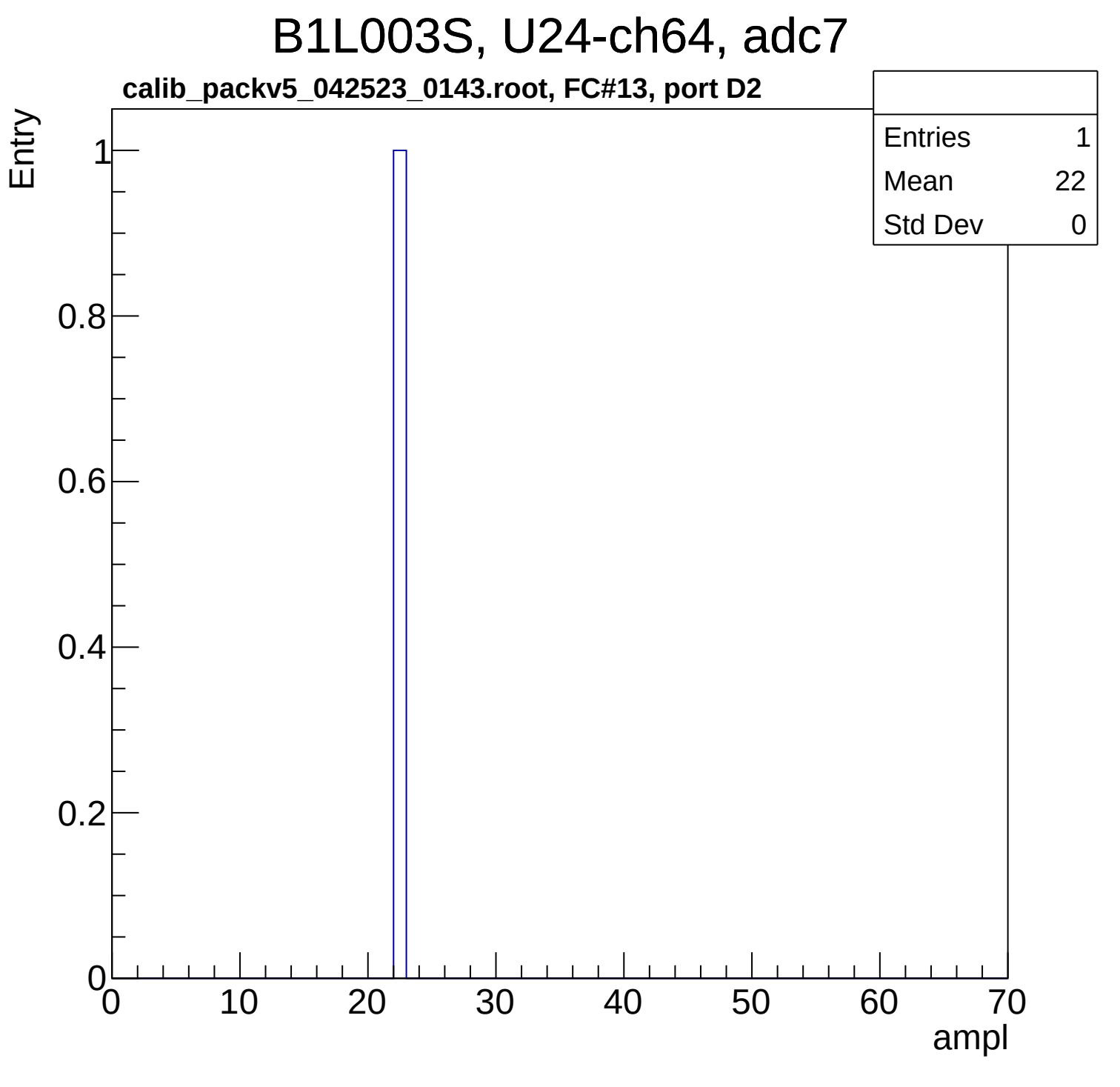
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch64, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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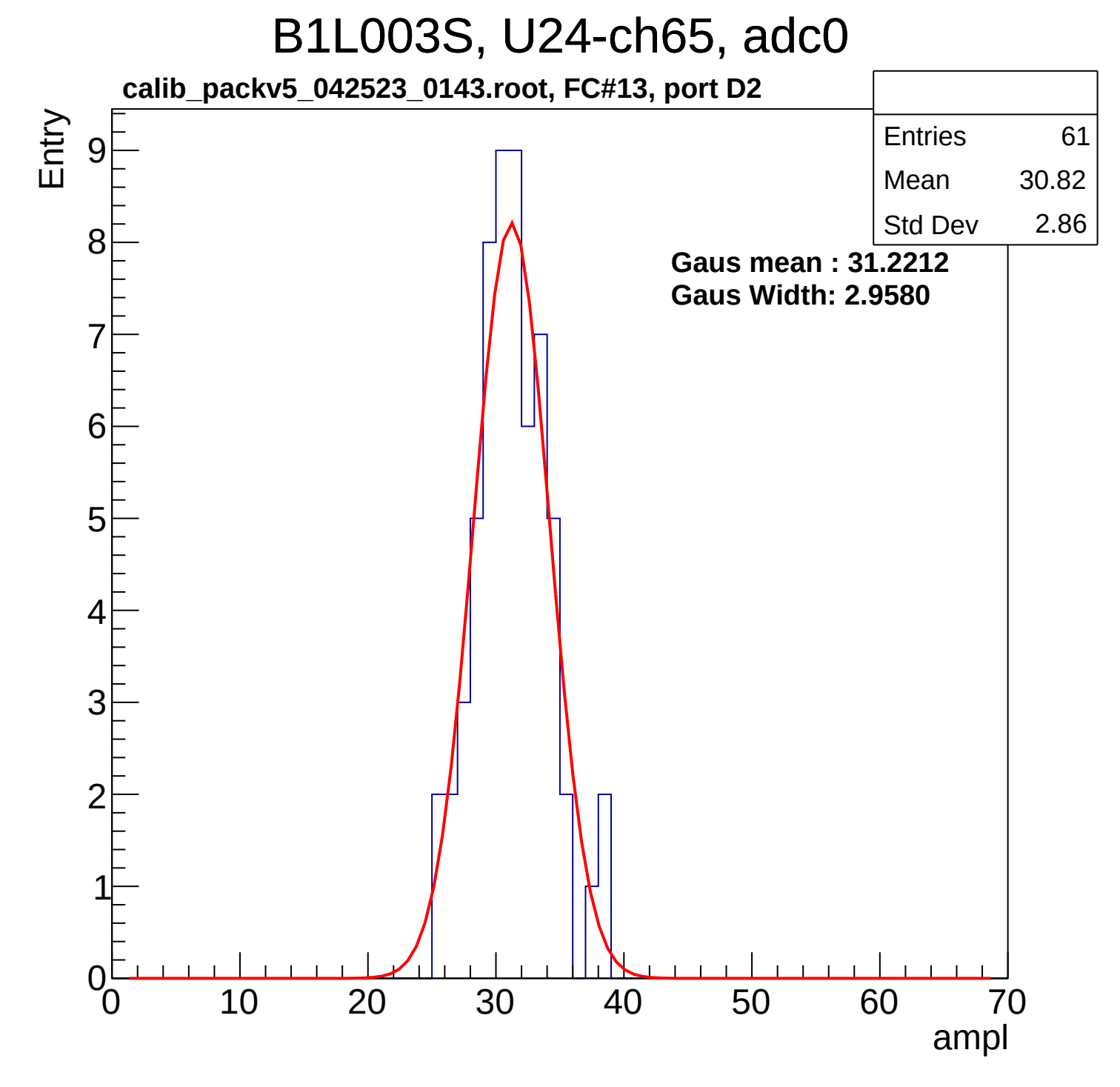
50
60
70
ampl


B1L003S, U24-ch65, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 30.82
Std Dev 2.86
Gaus mean : 31.2212
Gaus Width: 2.9580
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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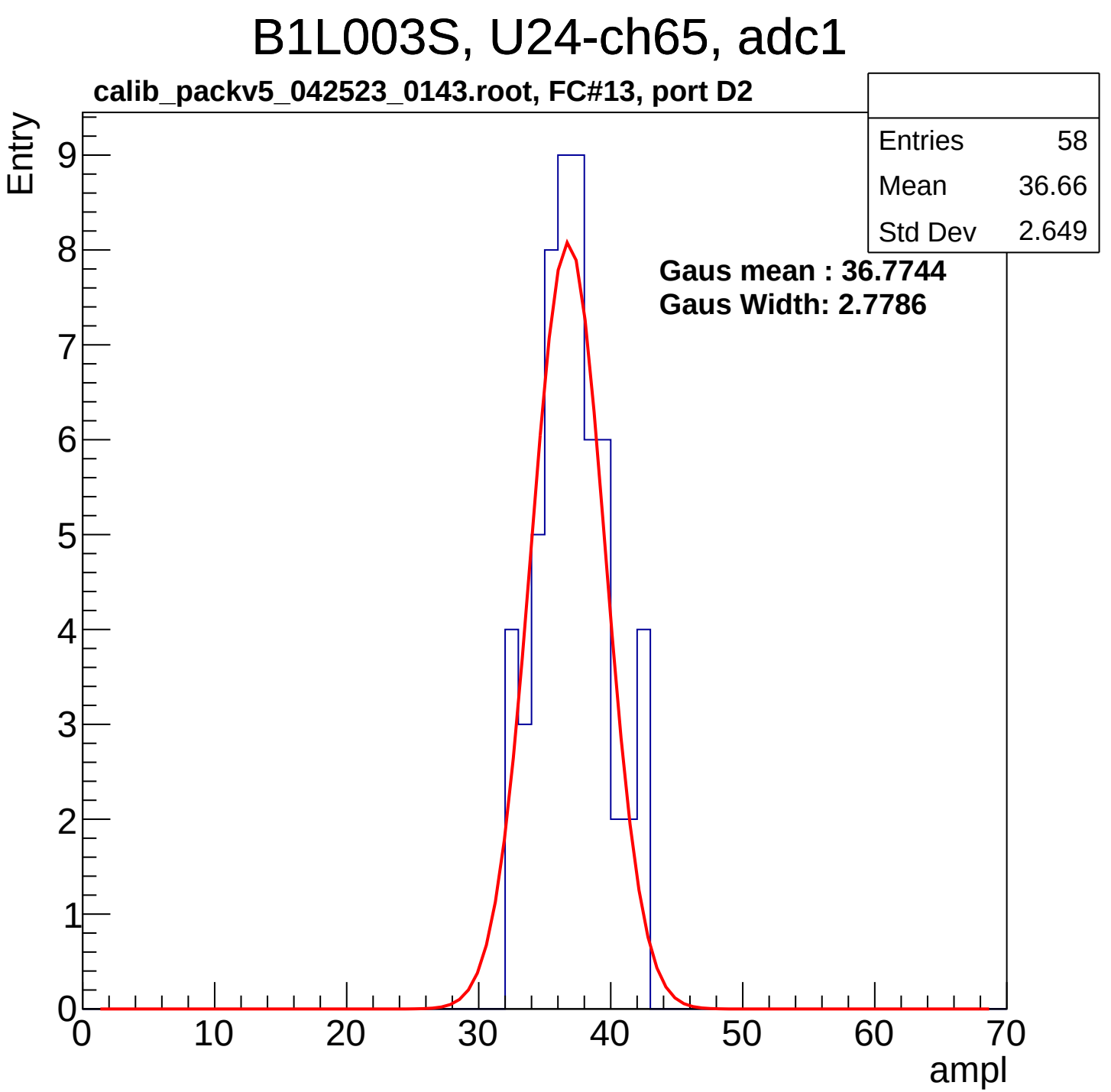
B1L003S, U24-ch65, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 36.66
Std Dev 2.649
Gaus mean : 36.7744
Gaus Width: 2.7786
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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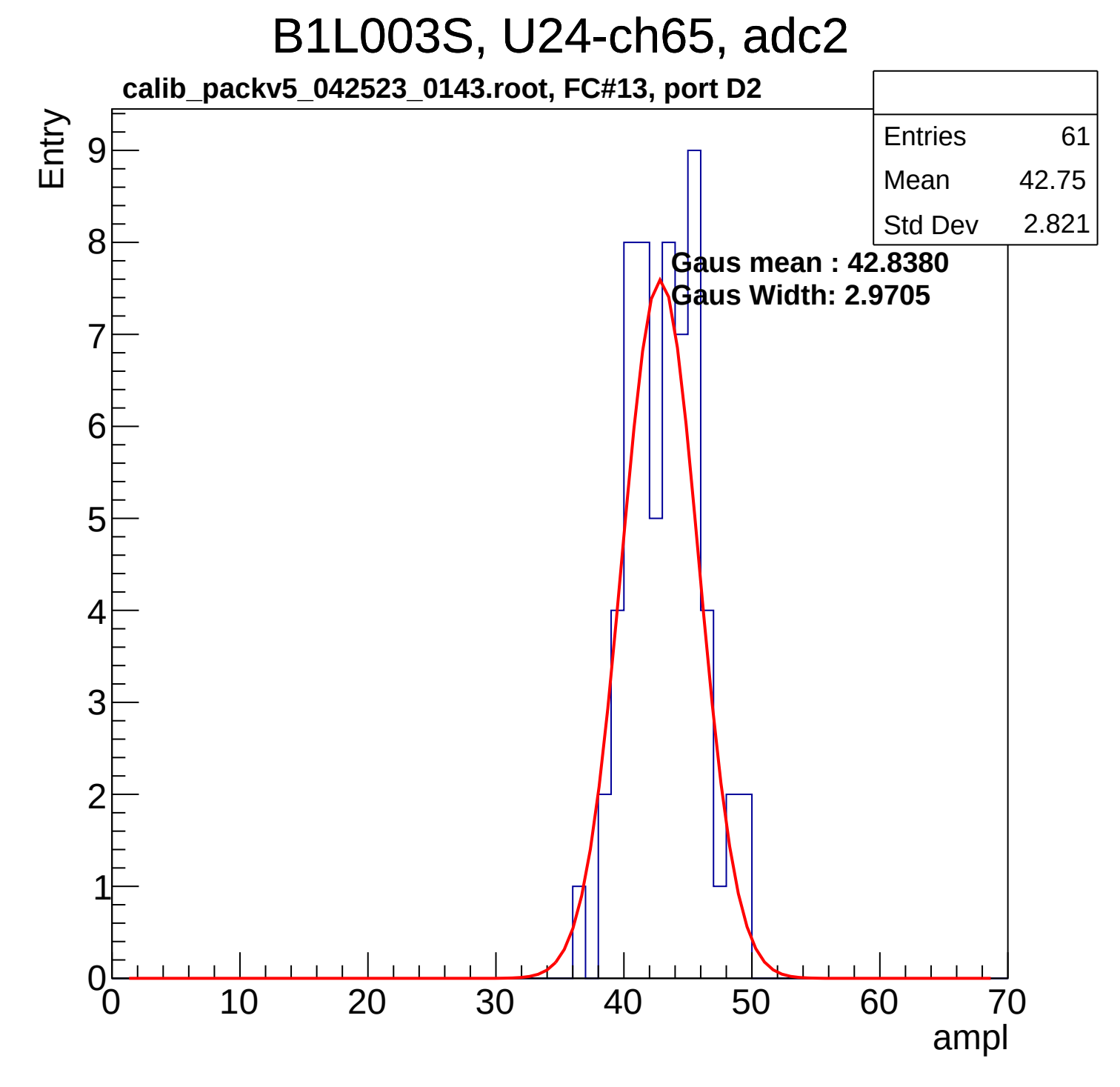
B1L003S, U24-ch65, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.75
Std Dev 2.821
Gaus mean : 42.8380
Gaus Width: 2.9705
Entry
ampl
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10
20
30
40
50
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1
2
3
4
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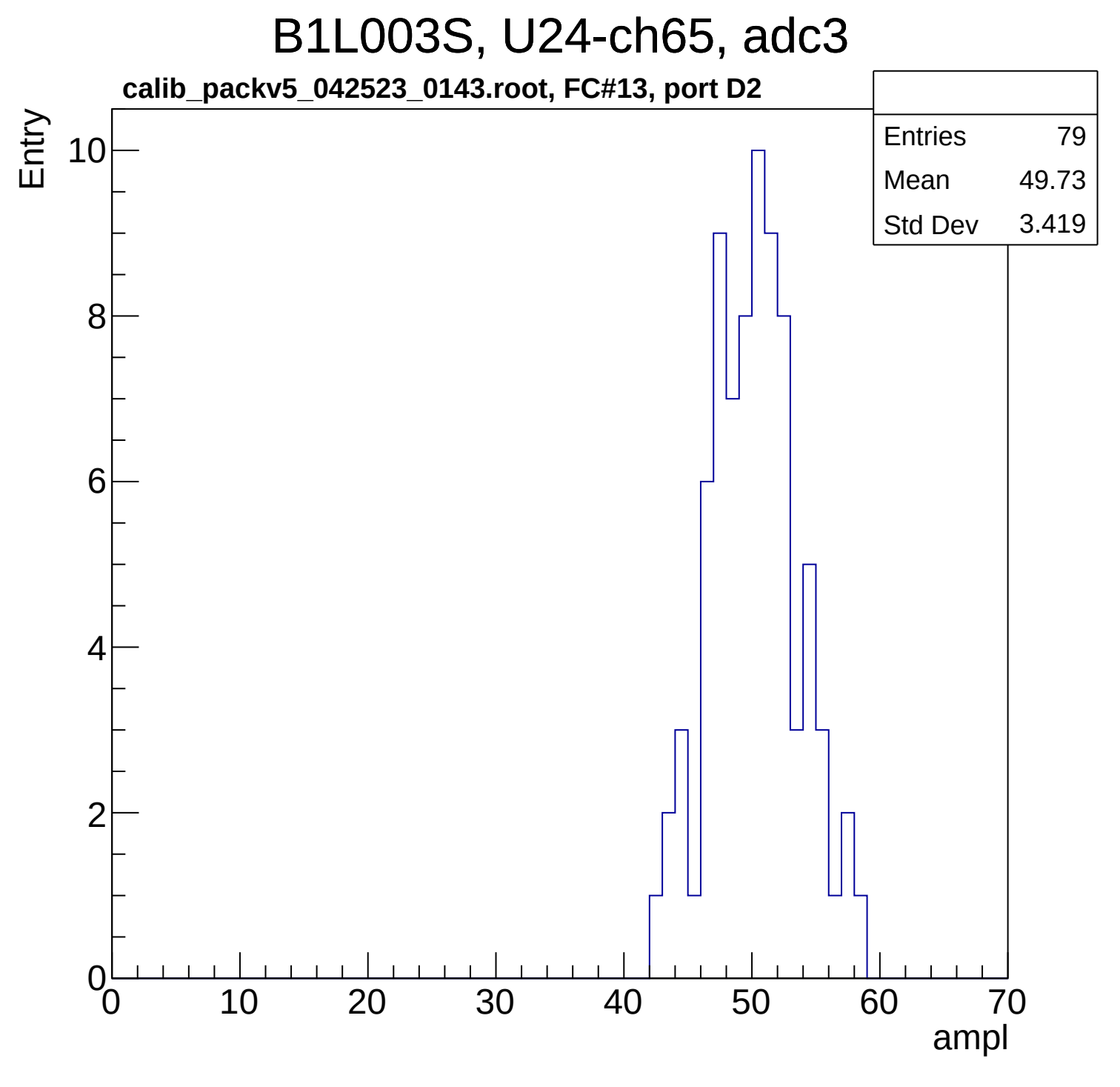
6
7
8
9


B1L003S, U24-ch65, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 49.73
Std Dev 3.419
Entry
ampl
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4
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10
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10
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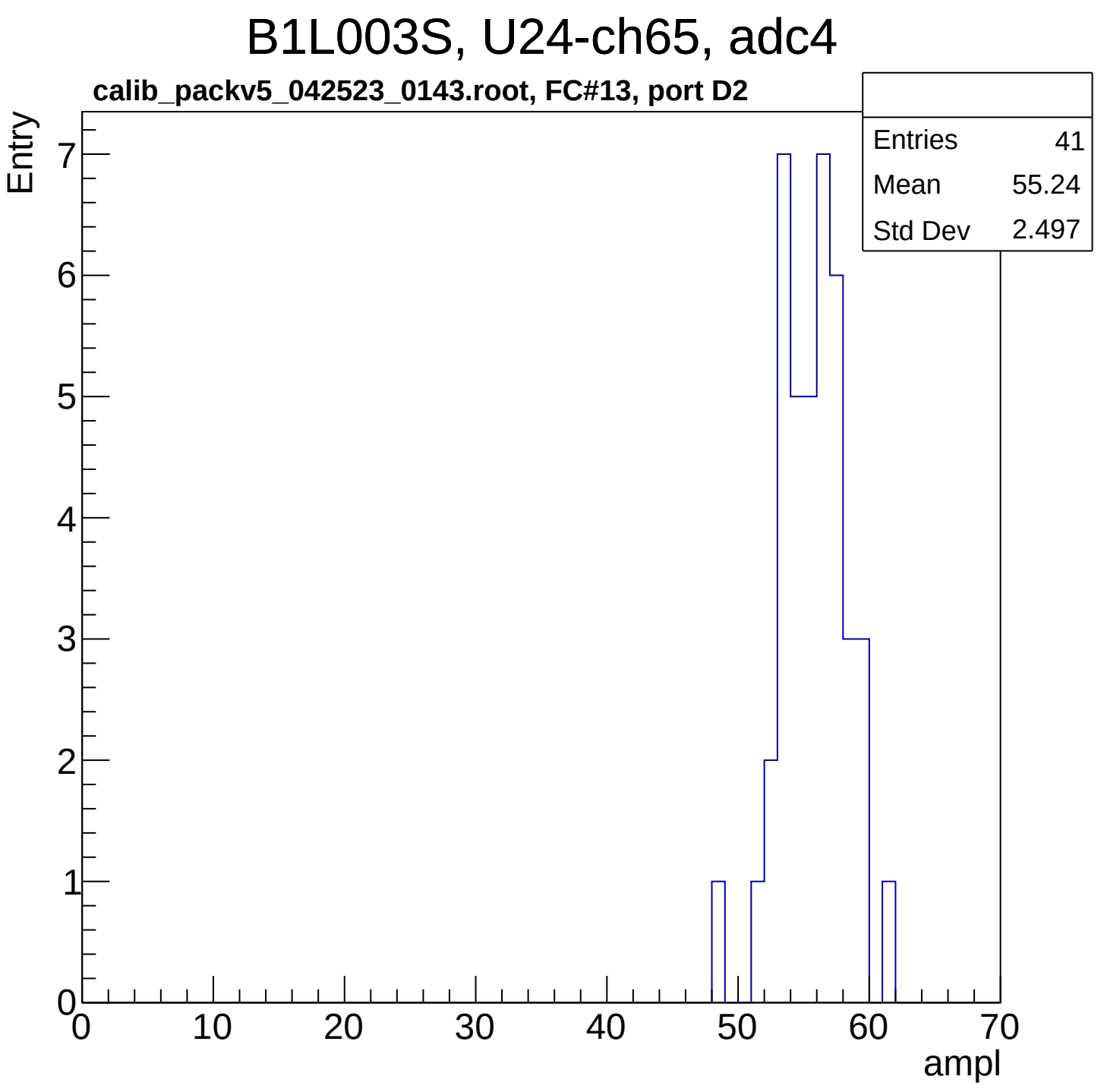
60
70


B1L003S, U24-ch65, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 55.24
Std Dev 2.497
Entry
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2
3
4
5
6
7
0
10
20
30
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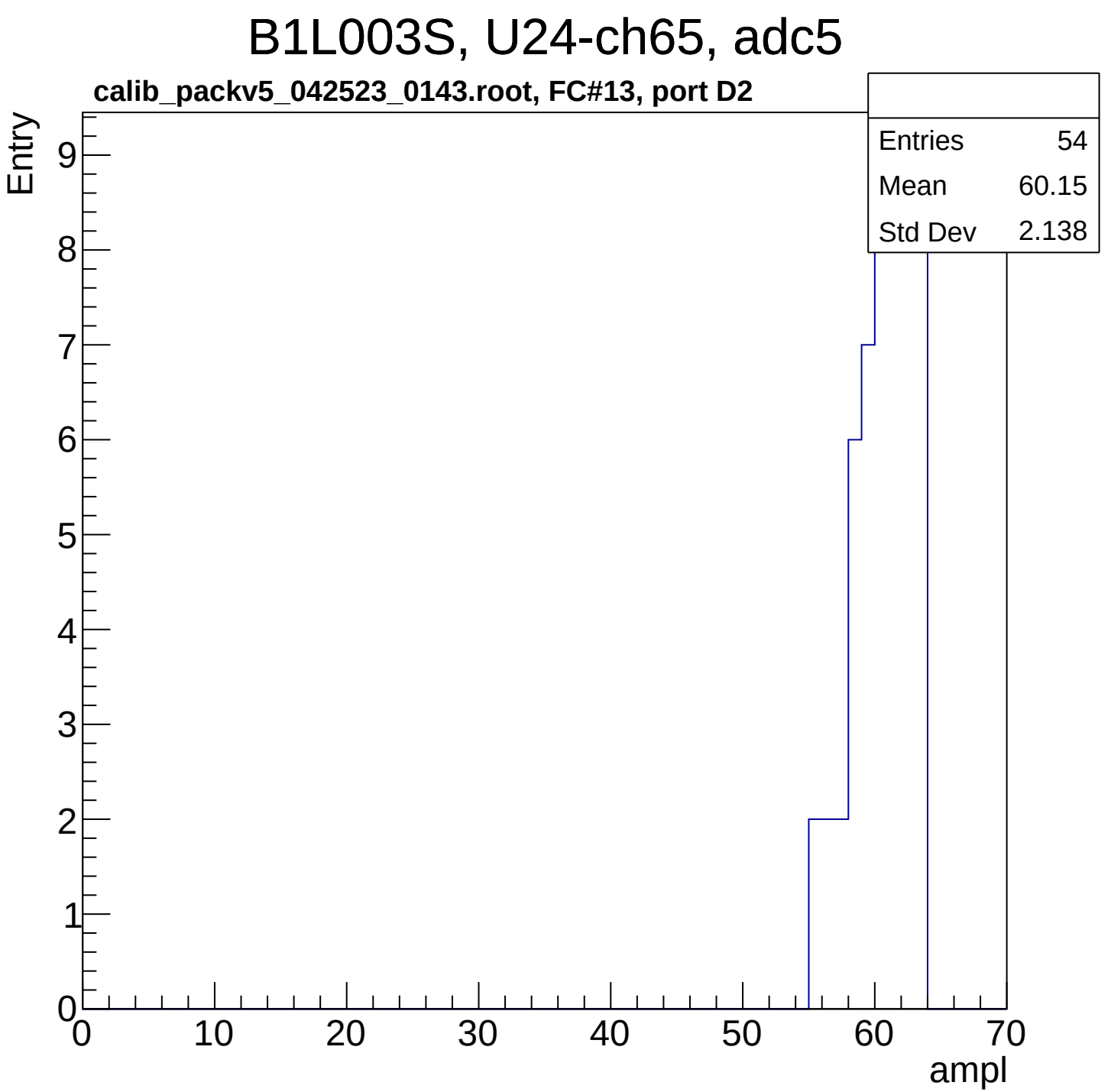
50
60
70
ampl


B1L003S, U24-ch65, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 60.15
Std Dev 2.138
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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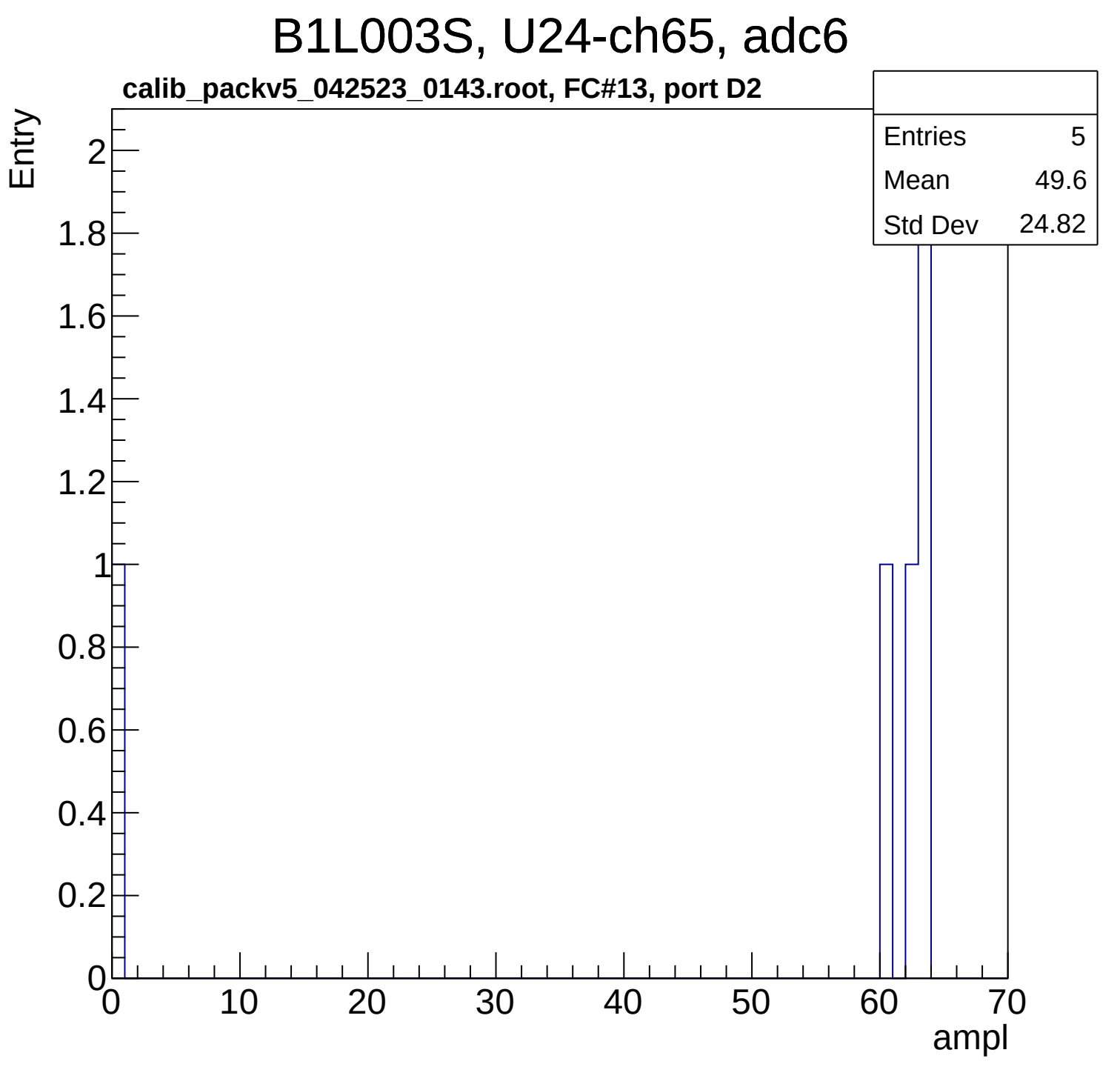
B1L003S, U24-ch65, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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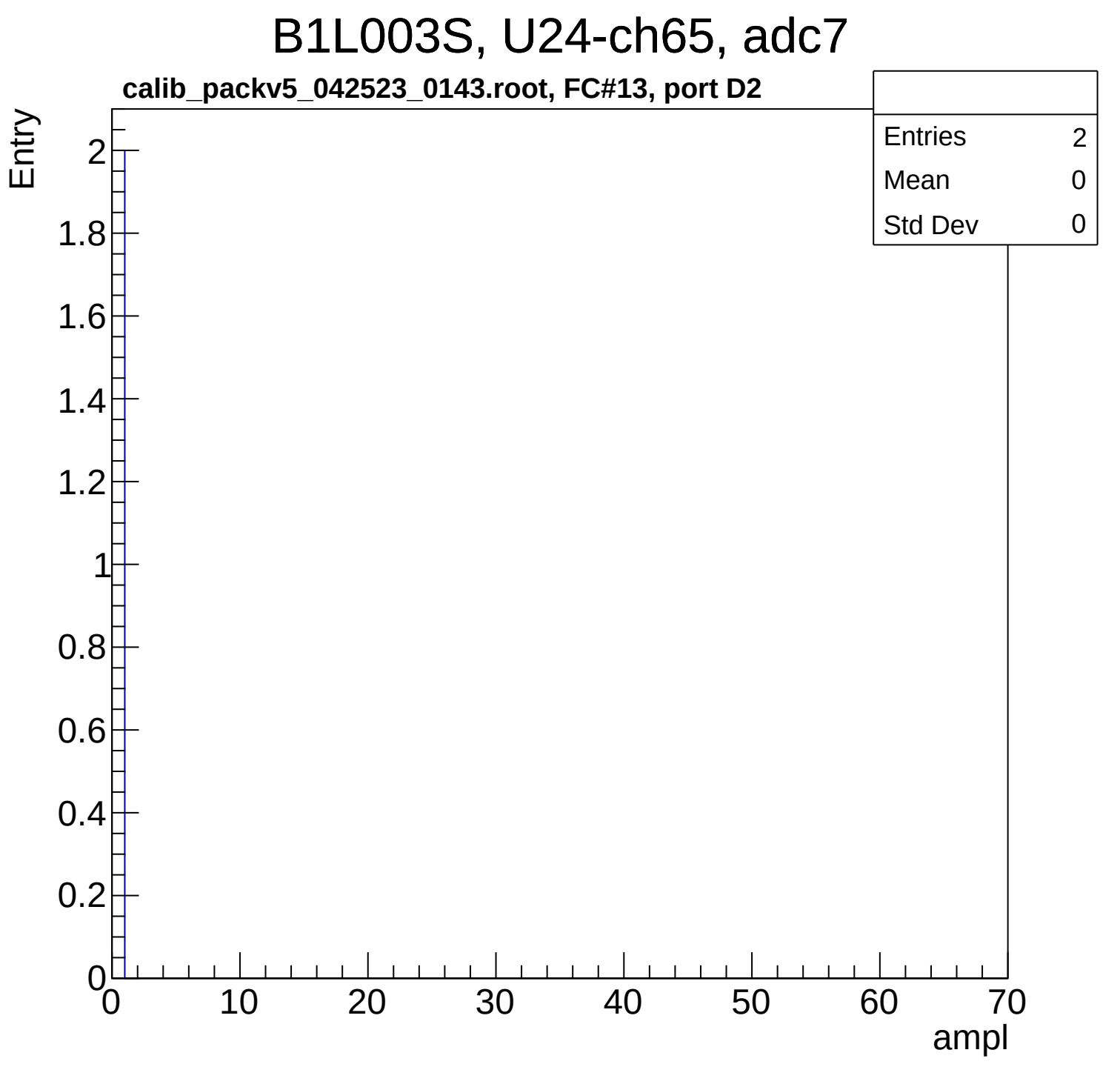
B1L003S, U24-ch65, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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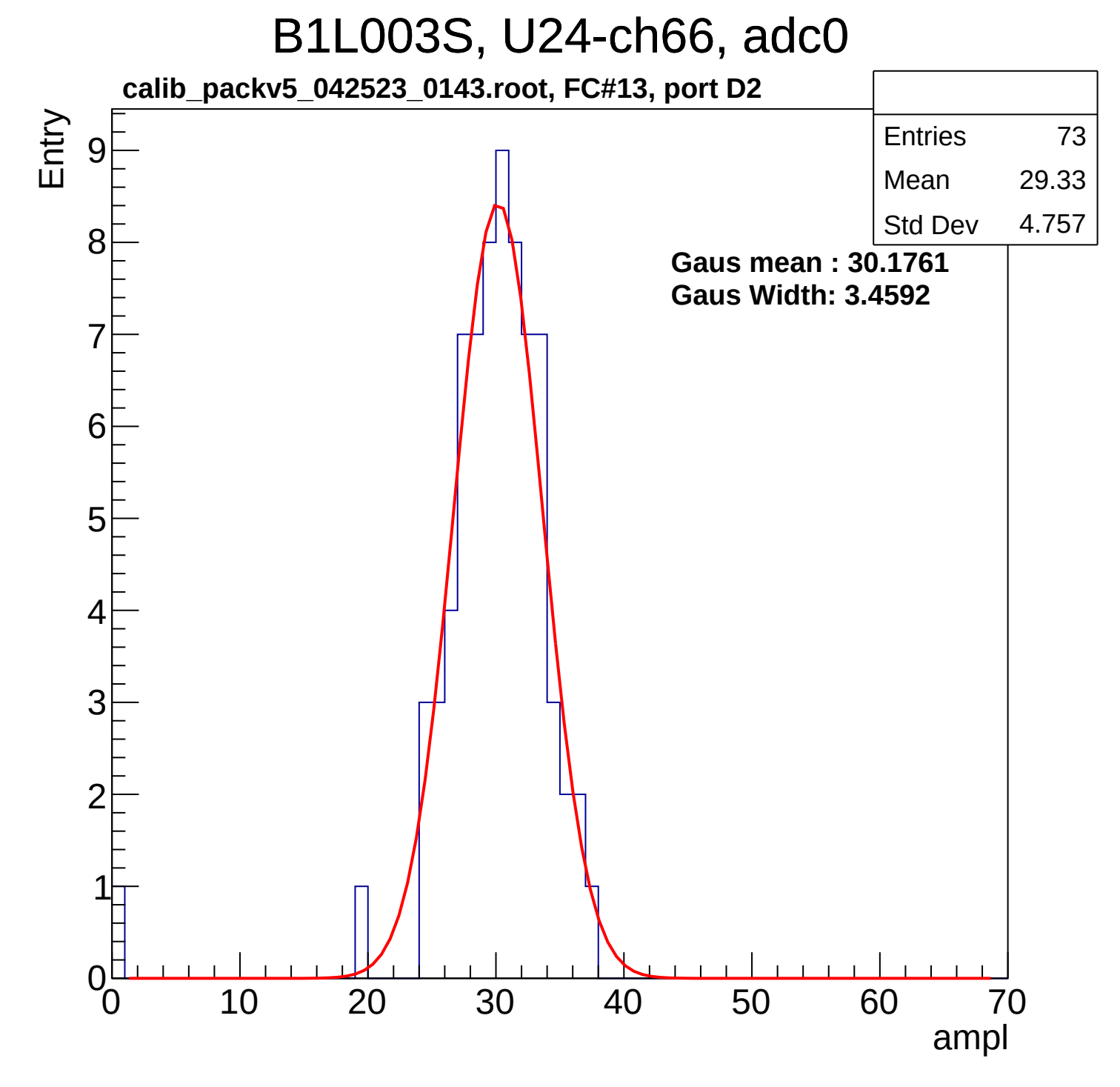
B1L003S, U24-ch66, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 29.33
Std Dev 4.757
Gaus mean : 30.1761
Gaus Width: 3.4592
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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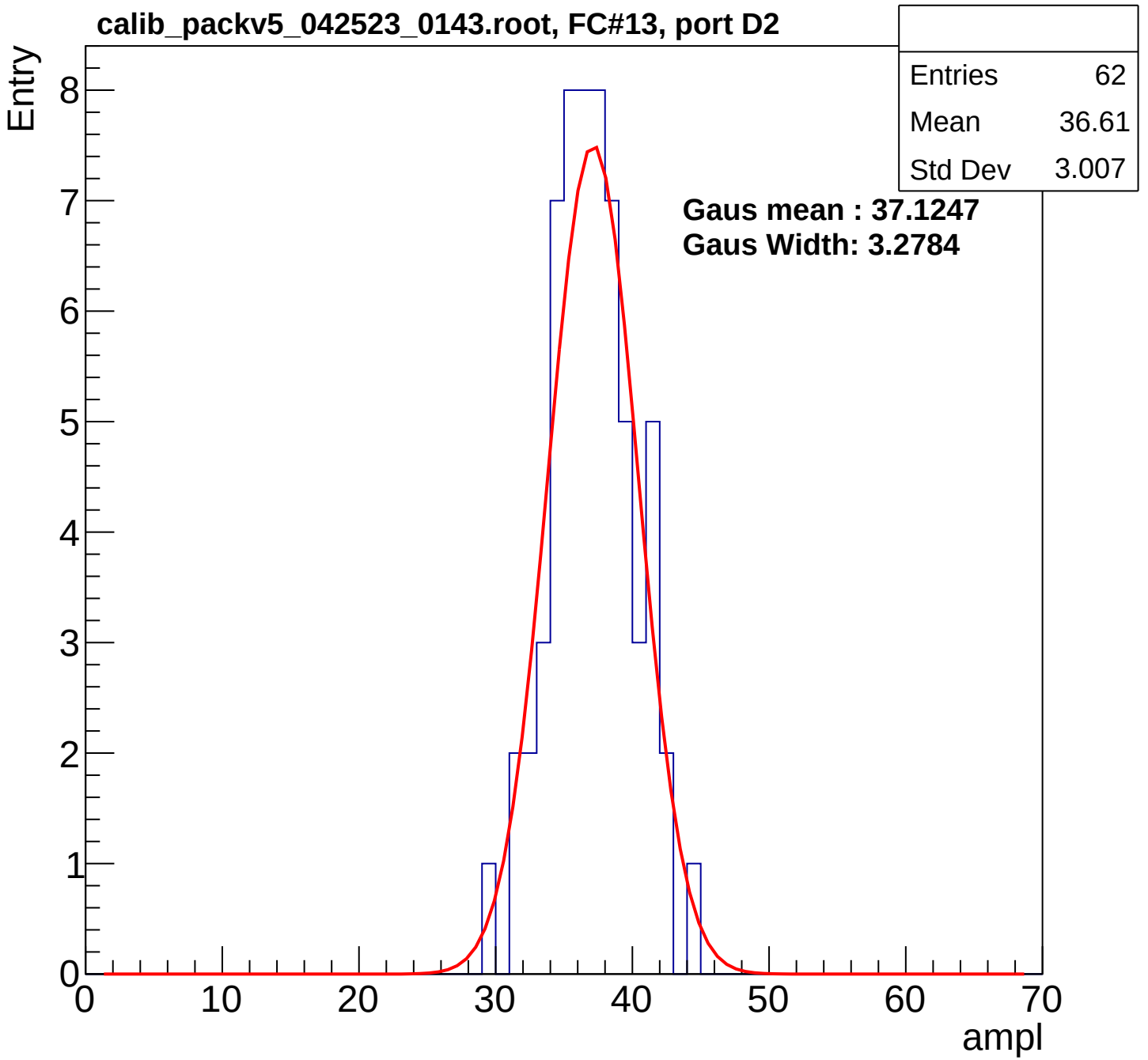
B1L003S, U24-ch66, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.61
Std Dev 3.007
Gaus mean : 37.1247
Gaus Width: 3.2784
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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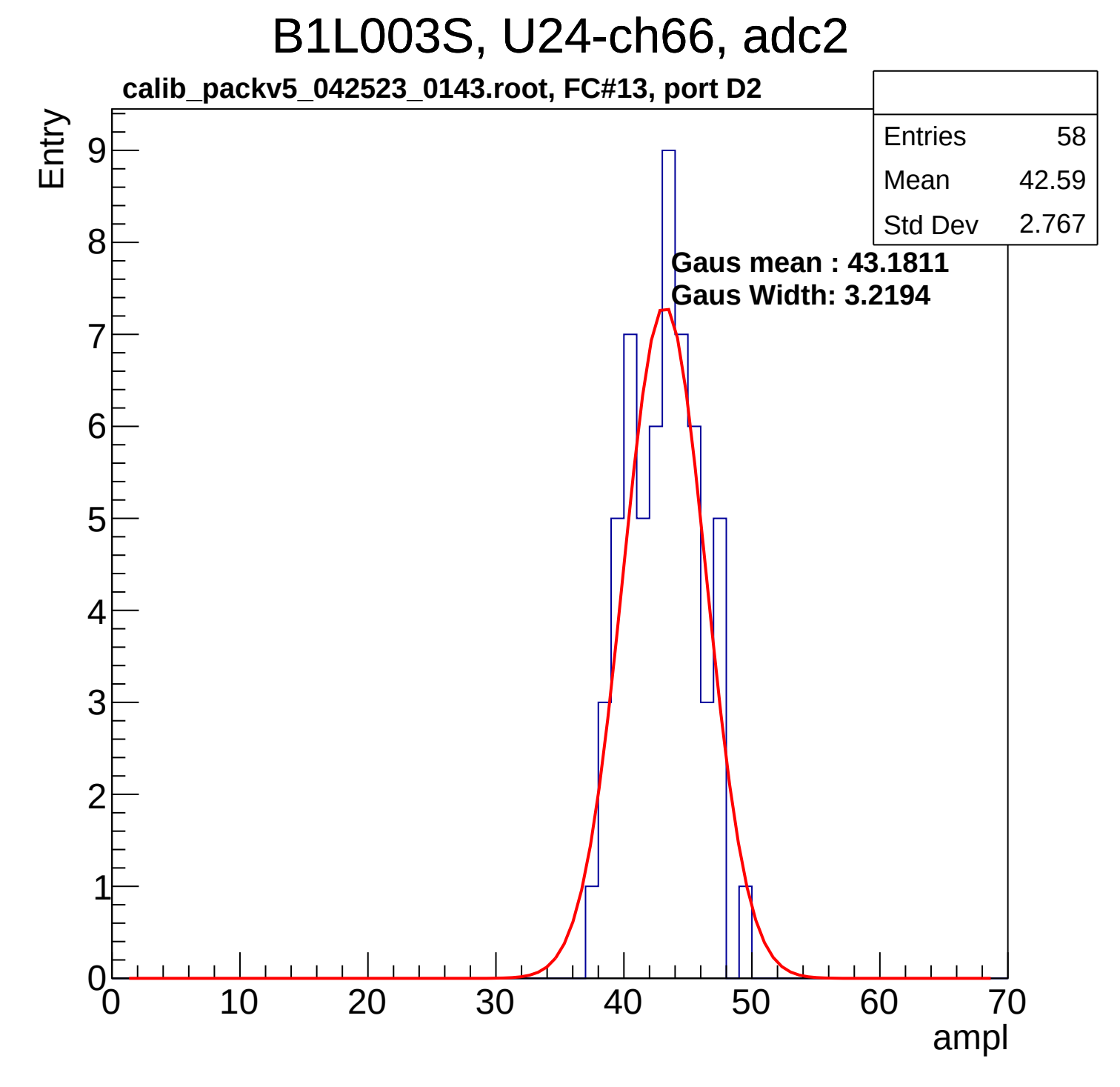
B1L003S, U24-ch66, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.59
Std Dev 2.767
Gaus mean : 43.1811
Gaus Width: 3.2194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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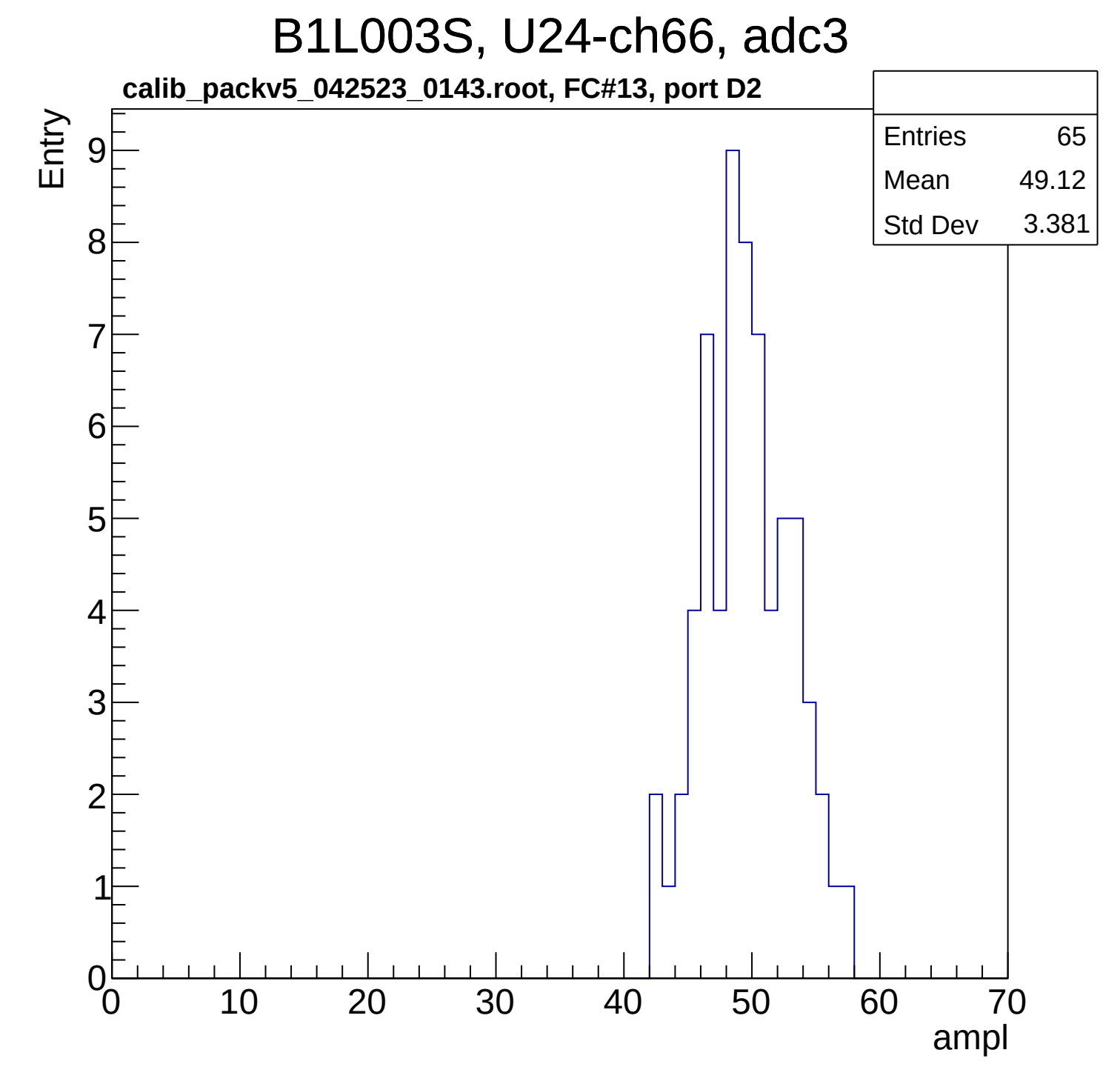
B1L003S, U24-ch66, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 49.12
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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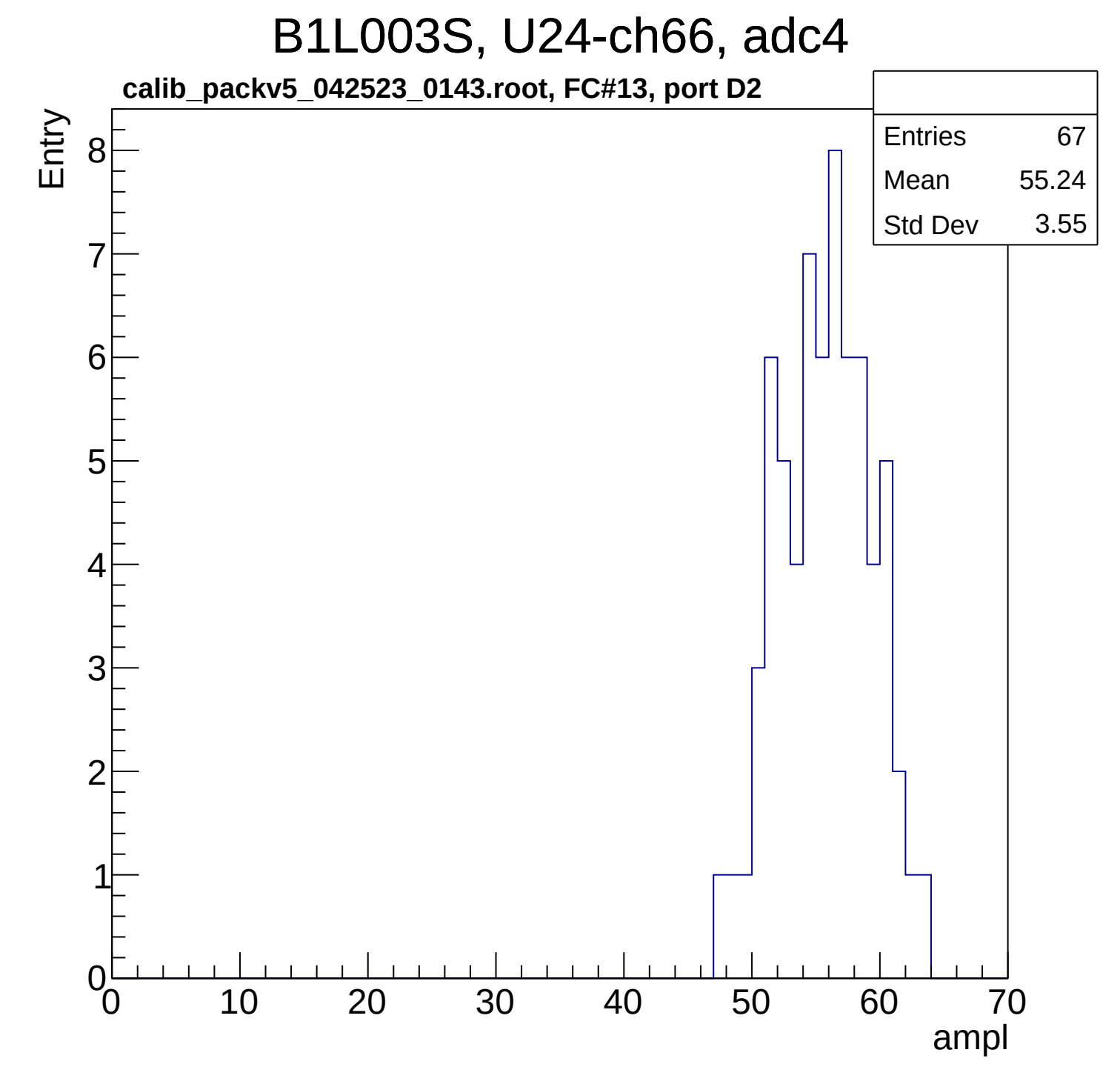
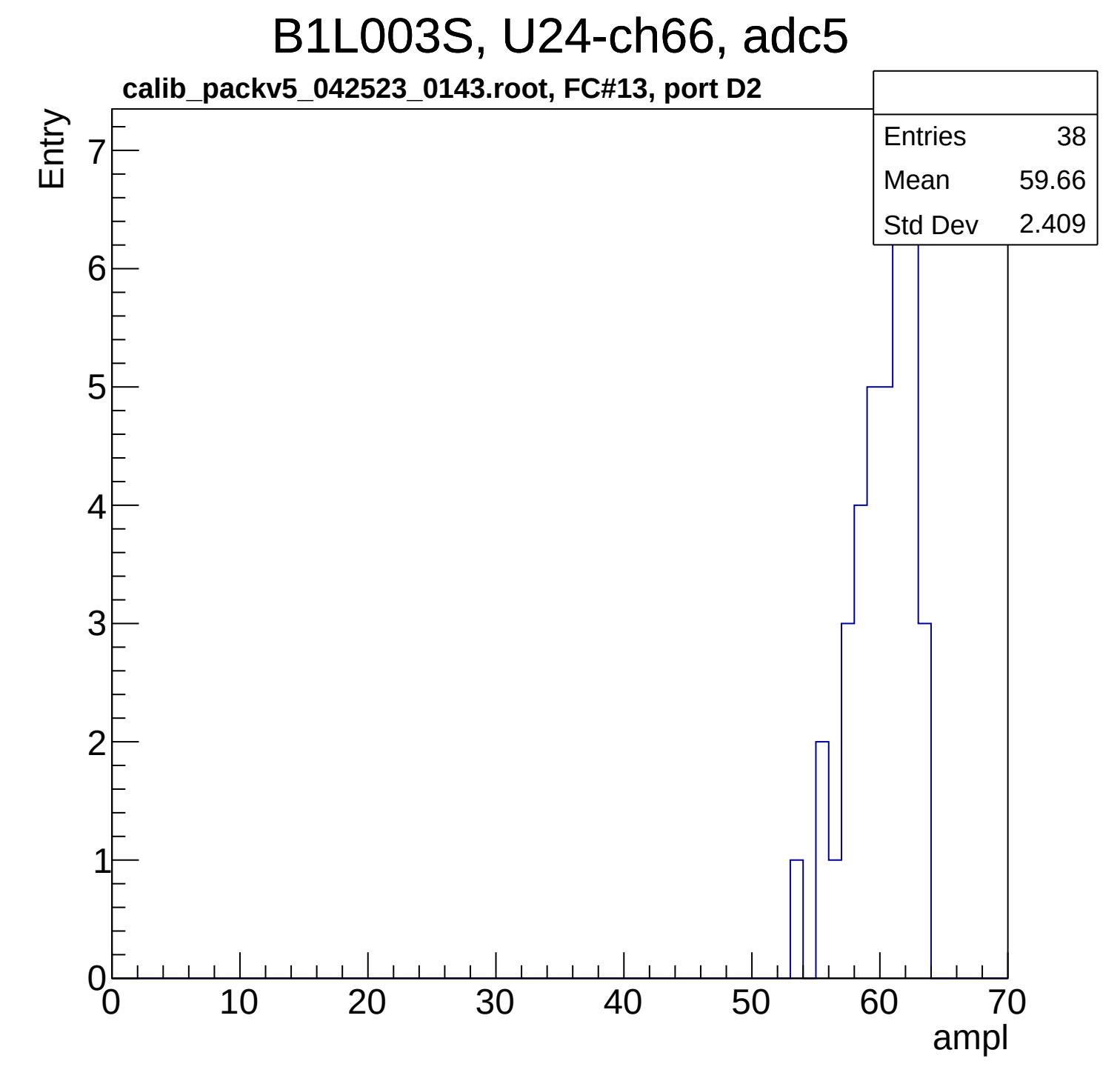
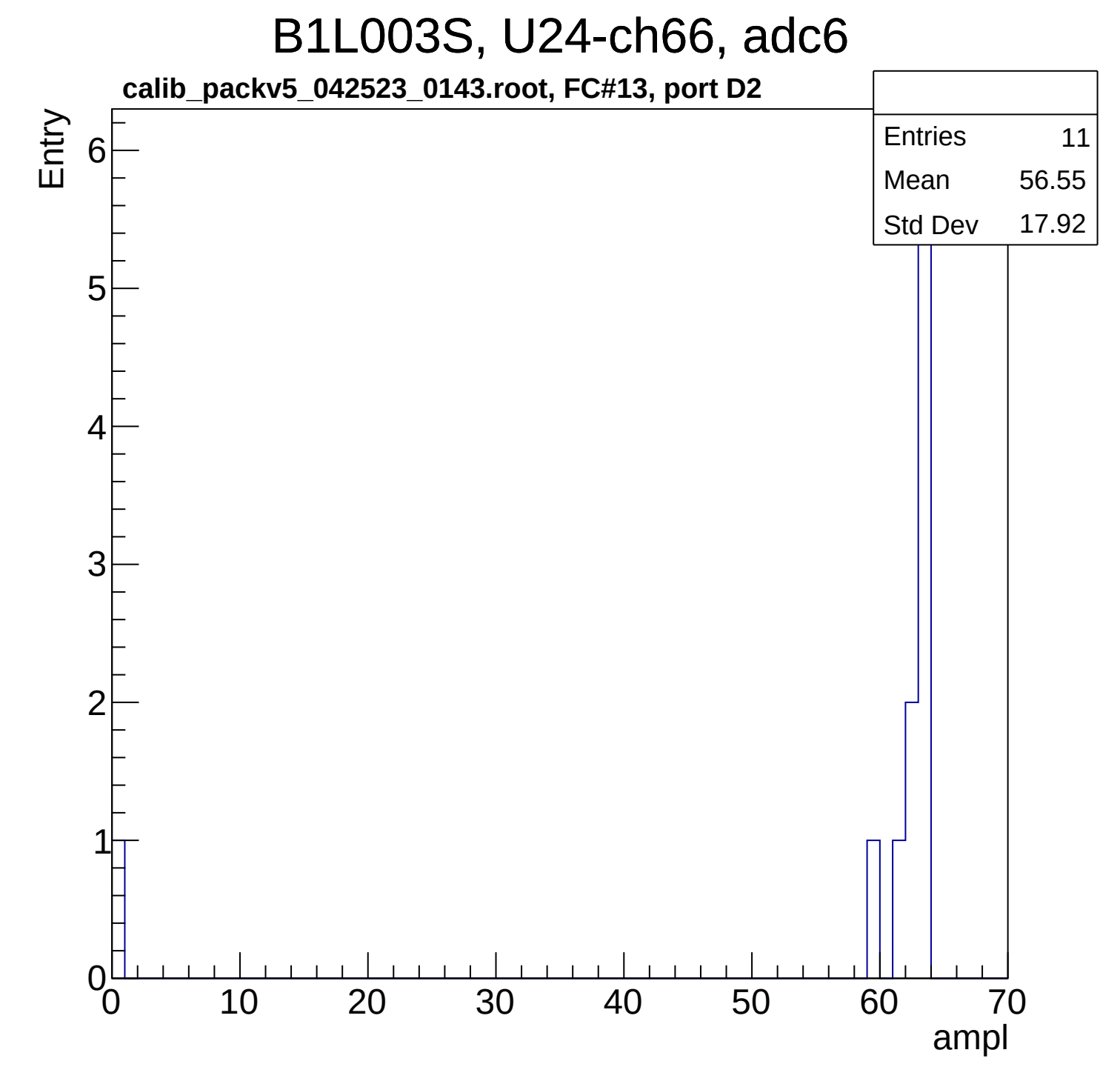
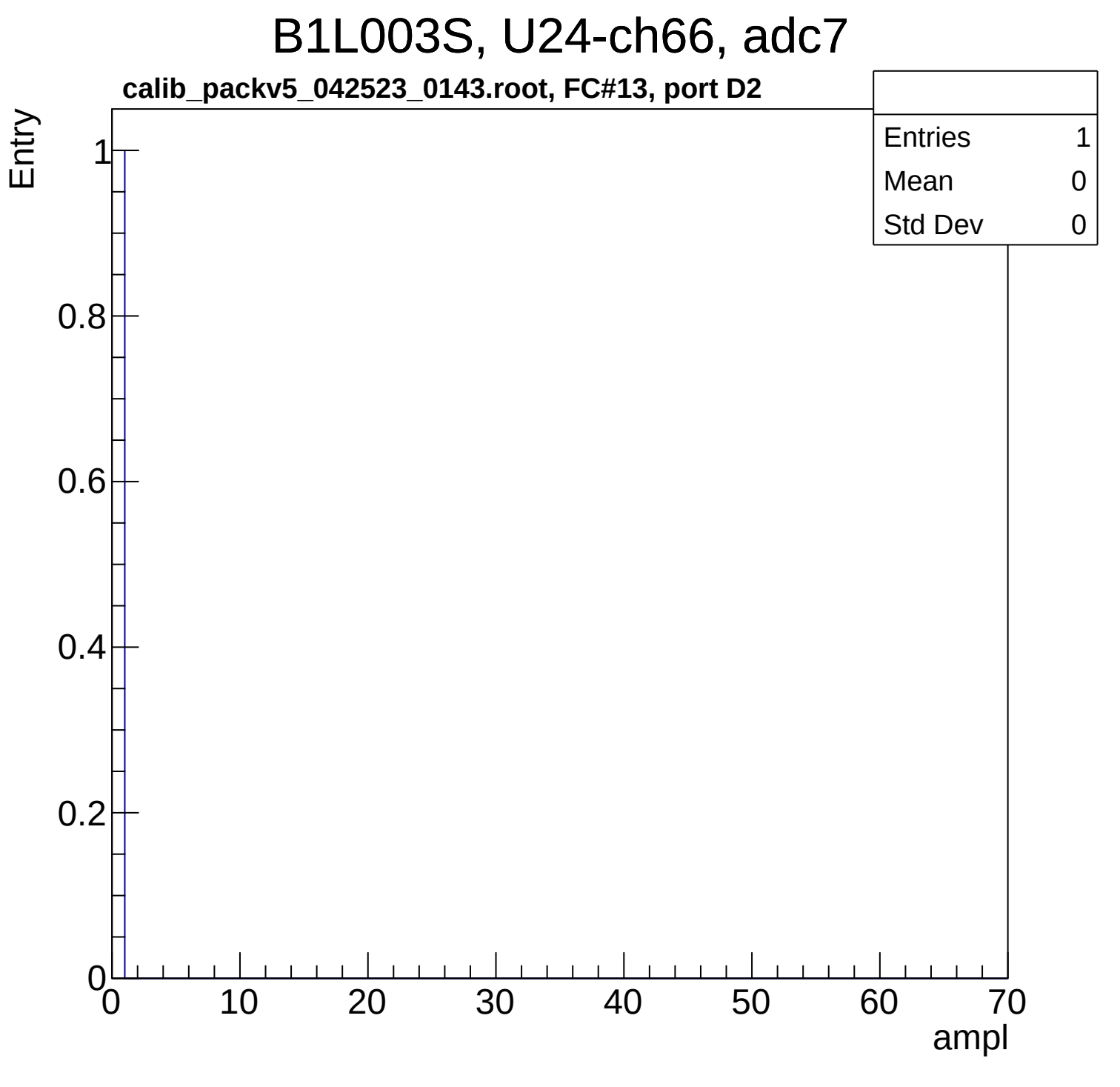

B1L003S, U24-ch66, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 55.24
Std Dev 3.55
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch66, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 59.66
Std Dev 2.409
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch66, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.55
Std Dev 17.92
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch66, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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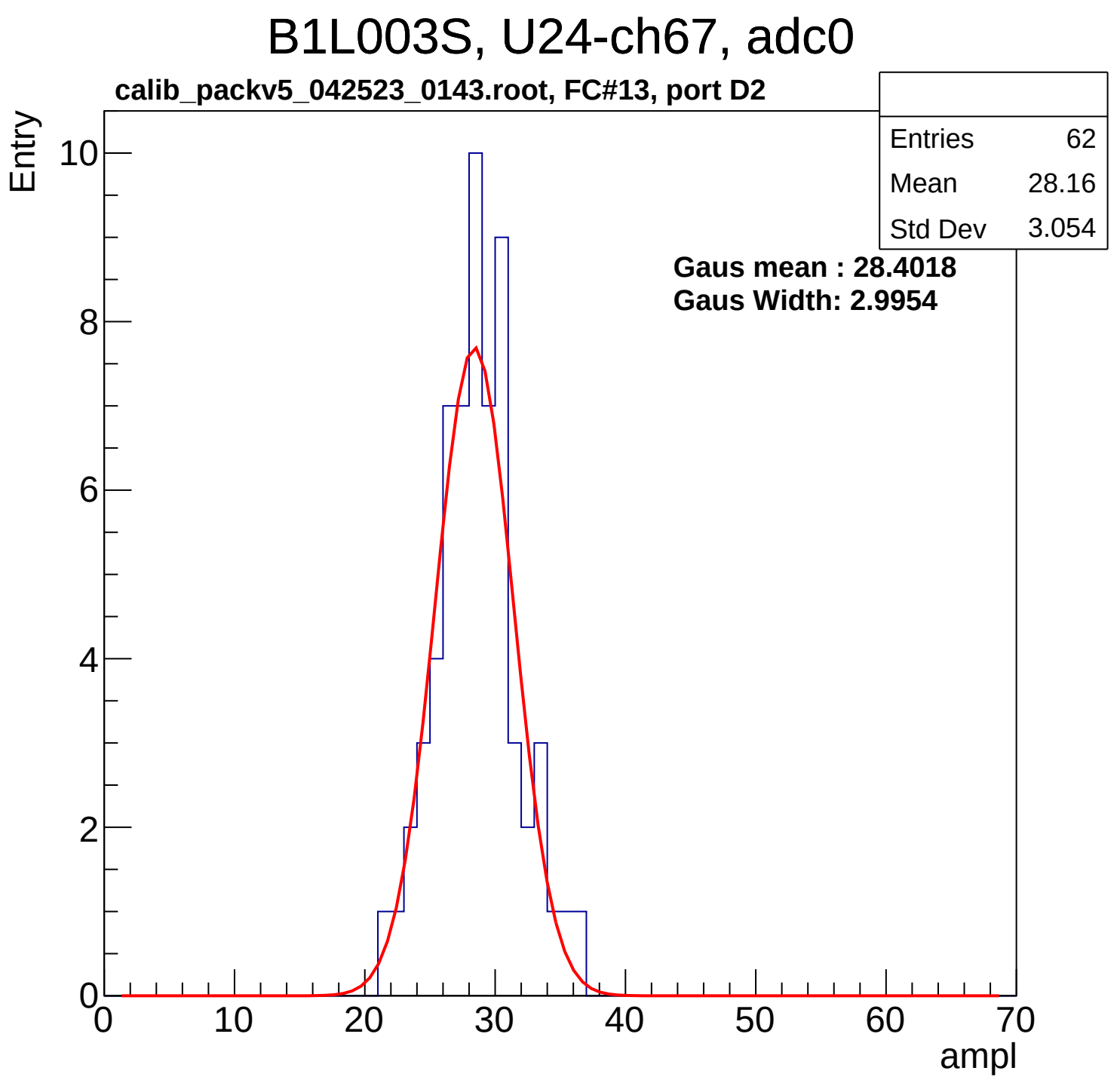
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch67, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.16
Std Dev 3.054
Gaus mean : 28.4018
Gaus Width: 2.9954
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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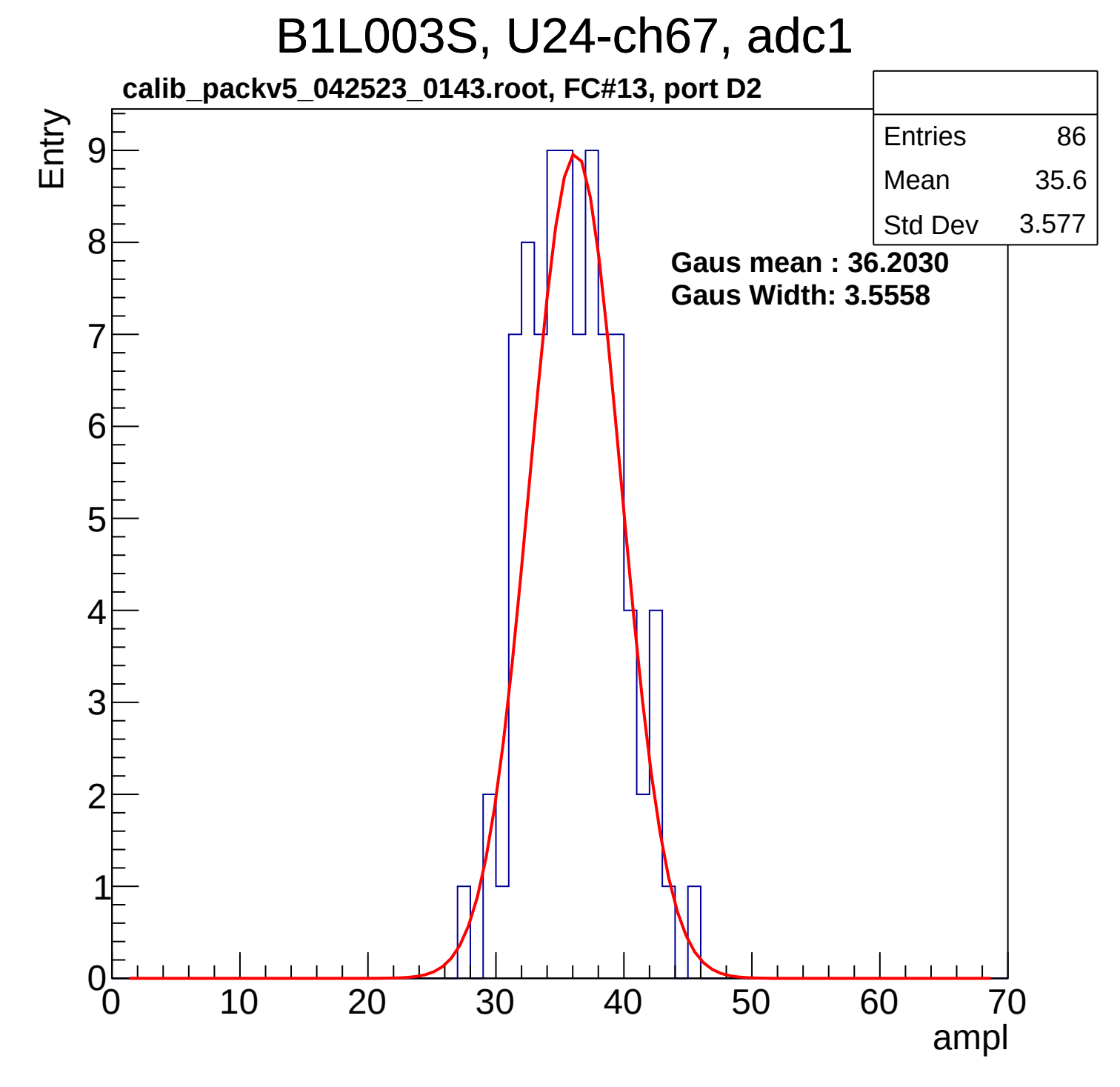
B1L003S, U24-ch67, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 35.6
Std Dev 3.577
Gaus mean : 36.2030
Gaus Width: 3.5558
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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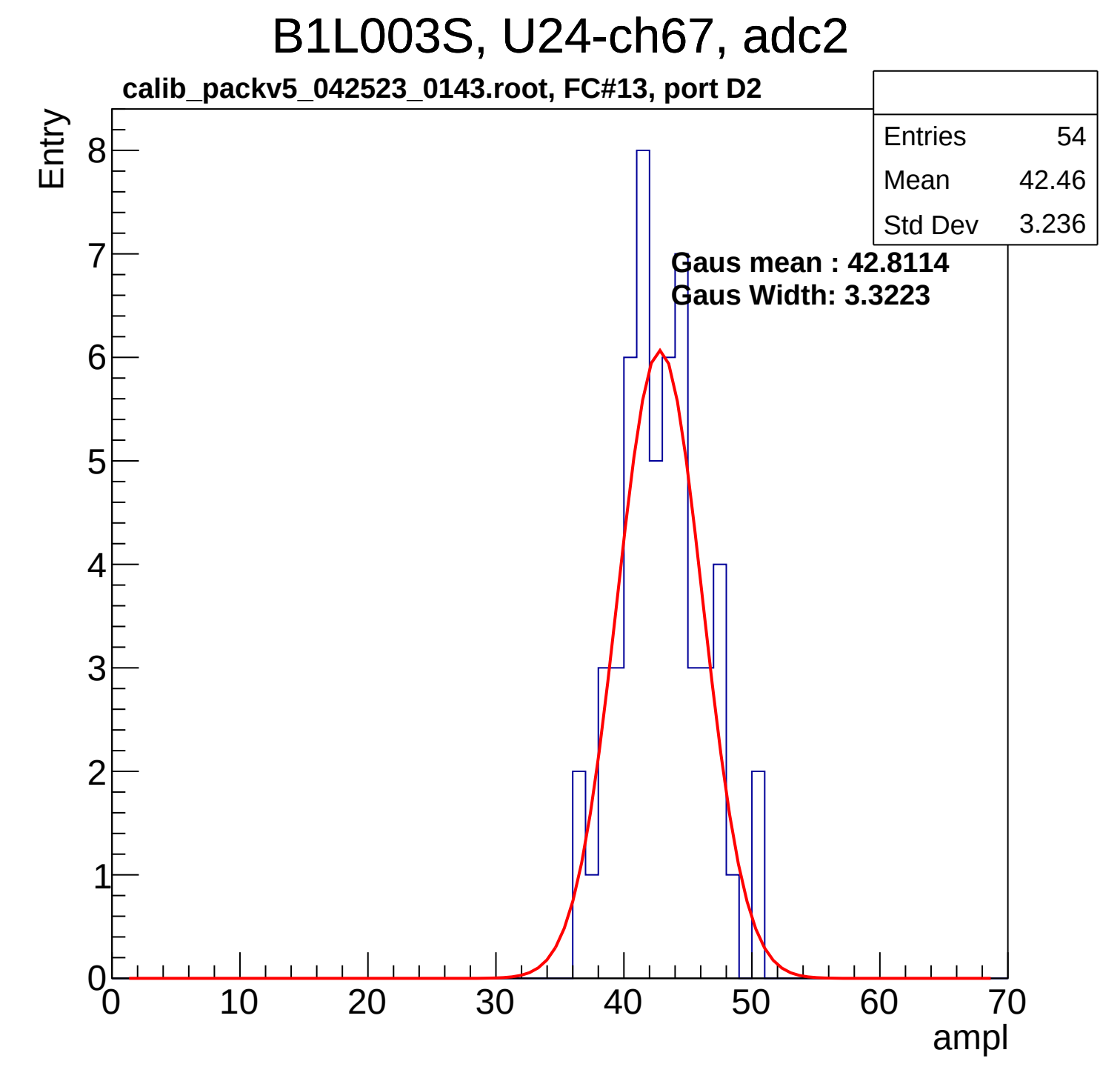
B1L003S, U24-ch67, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.46
Std Dev 3.236
Gaus mean : 42.8114
Gaus Width: 3.3223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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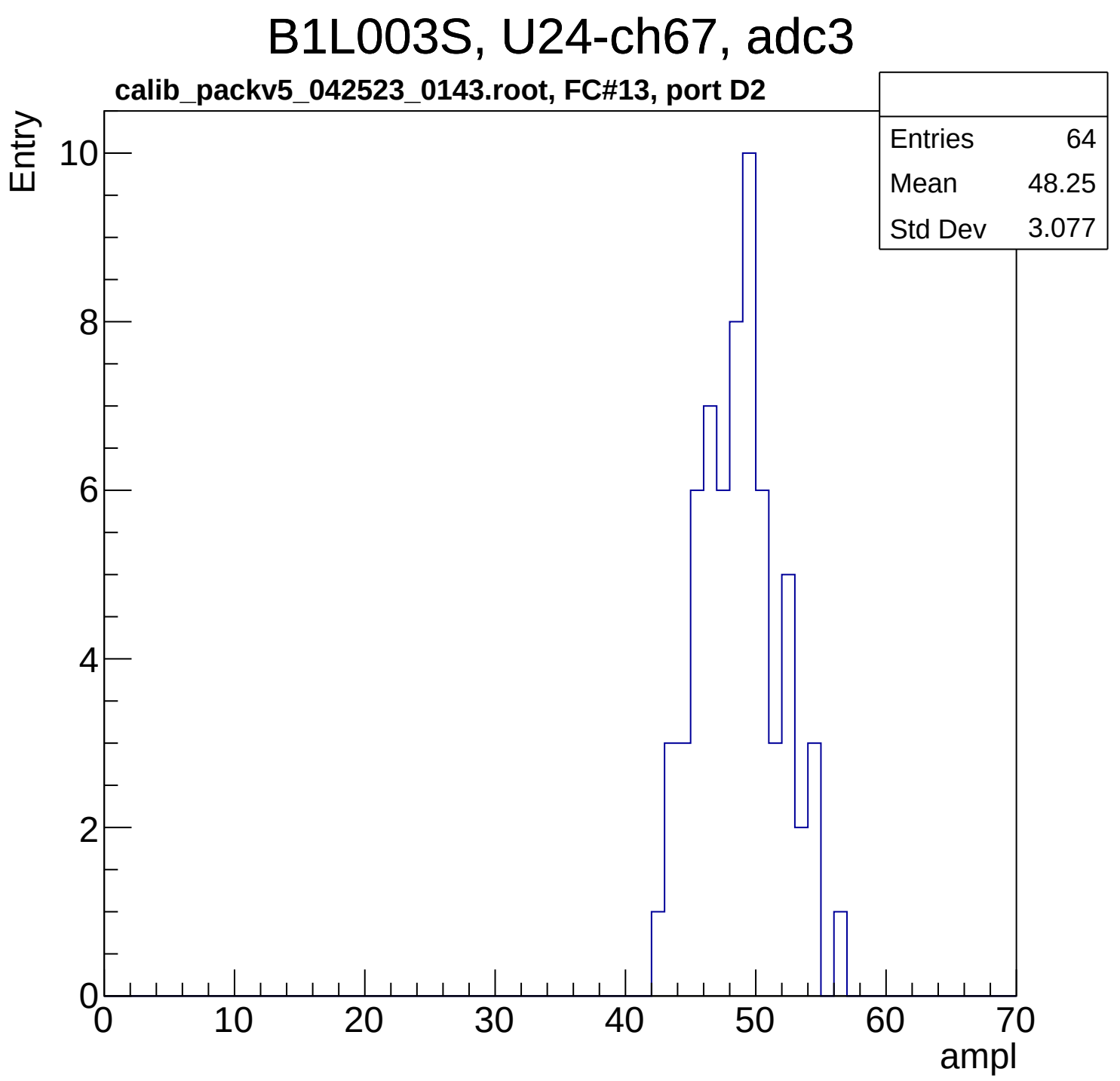
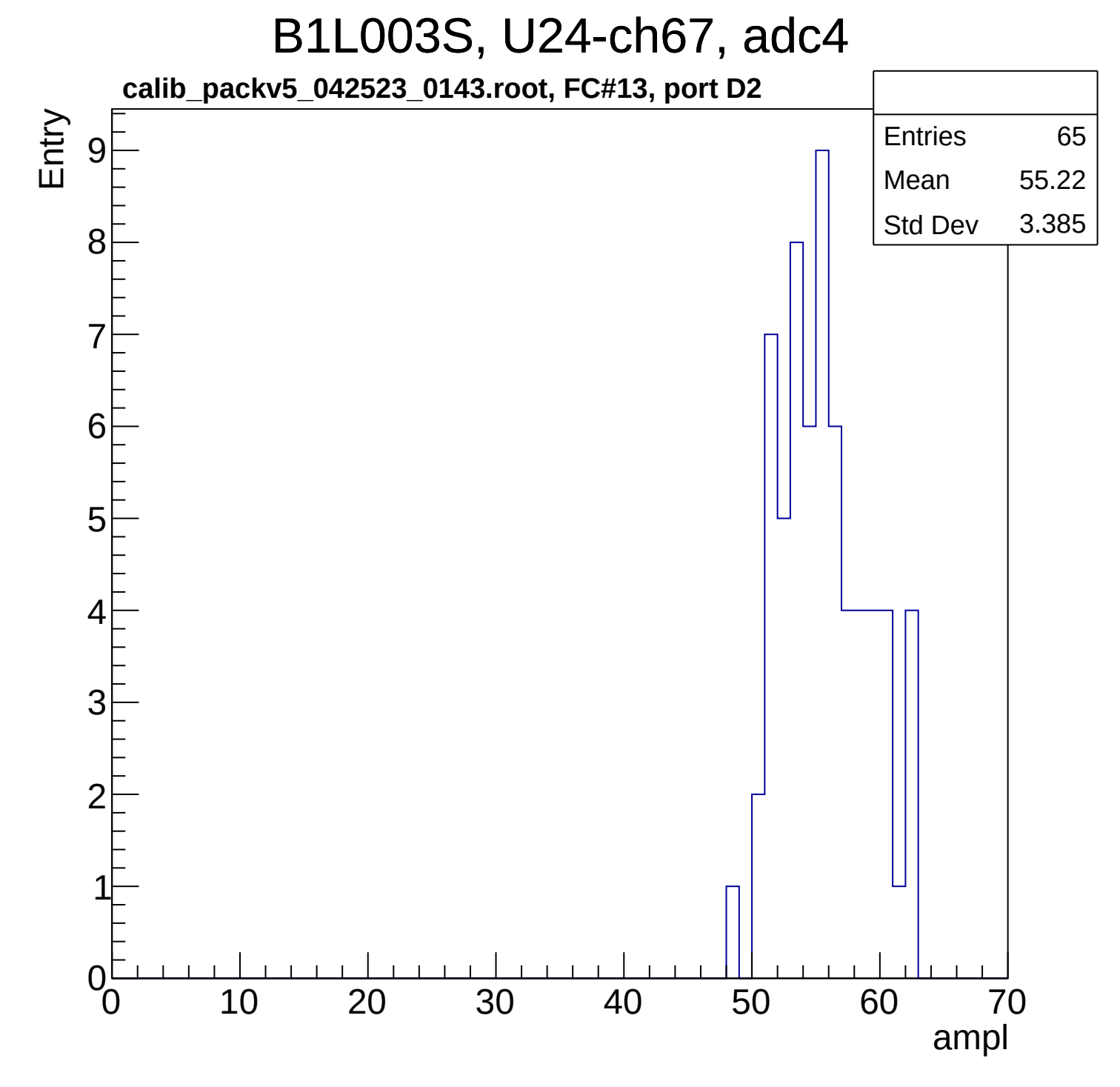

B1L003S, U24-ch67, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 48.25
Std Dev 3.077
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch67, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 55.22
Std Dev 3.385
Entry
ampl
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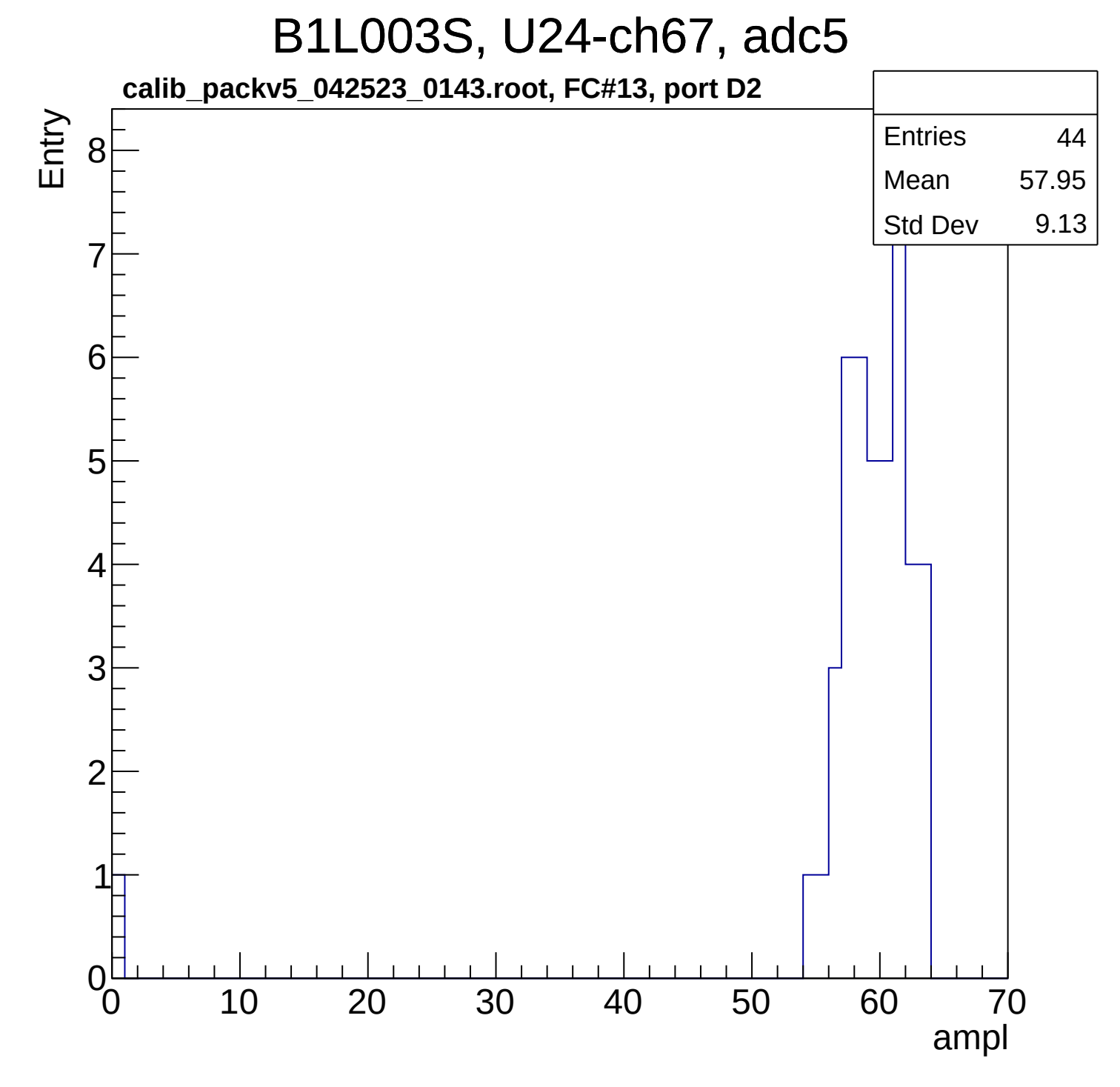
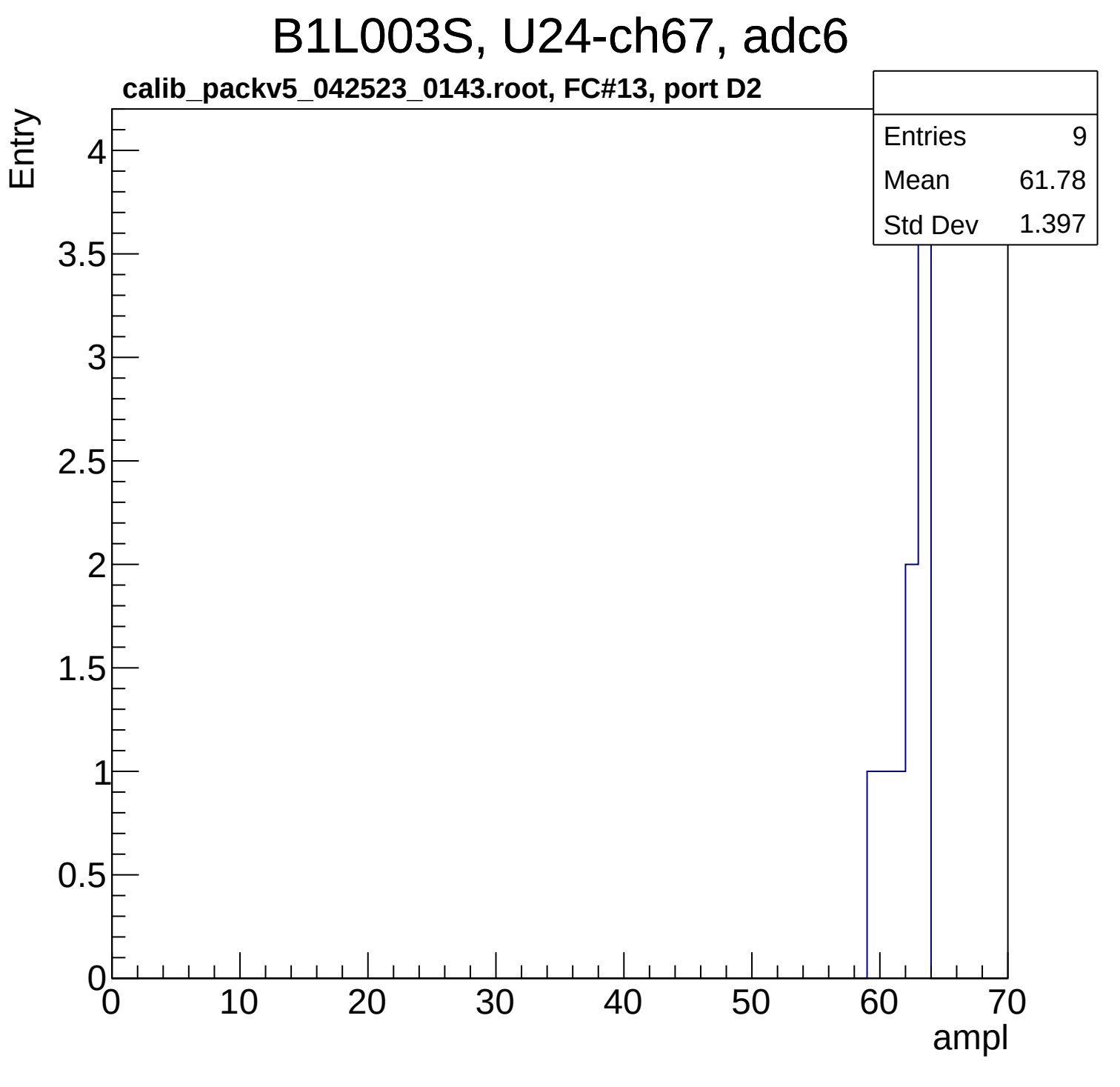
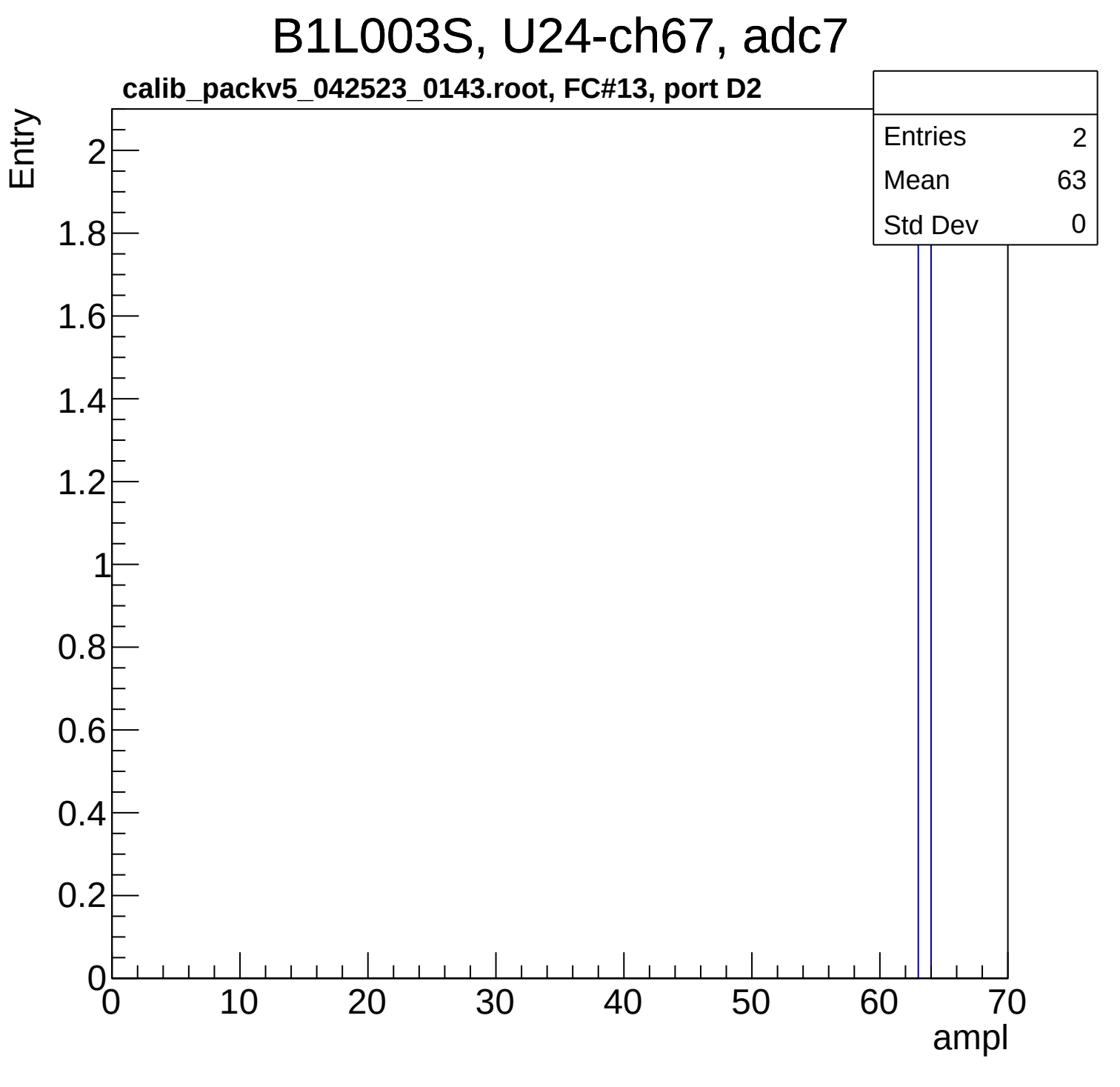
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch67, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 57.95
Std Dev 9.13
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch67, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.78
Std Dev 1.397
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U24-ch67, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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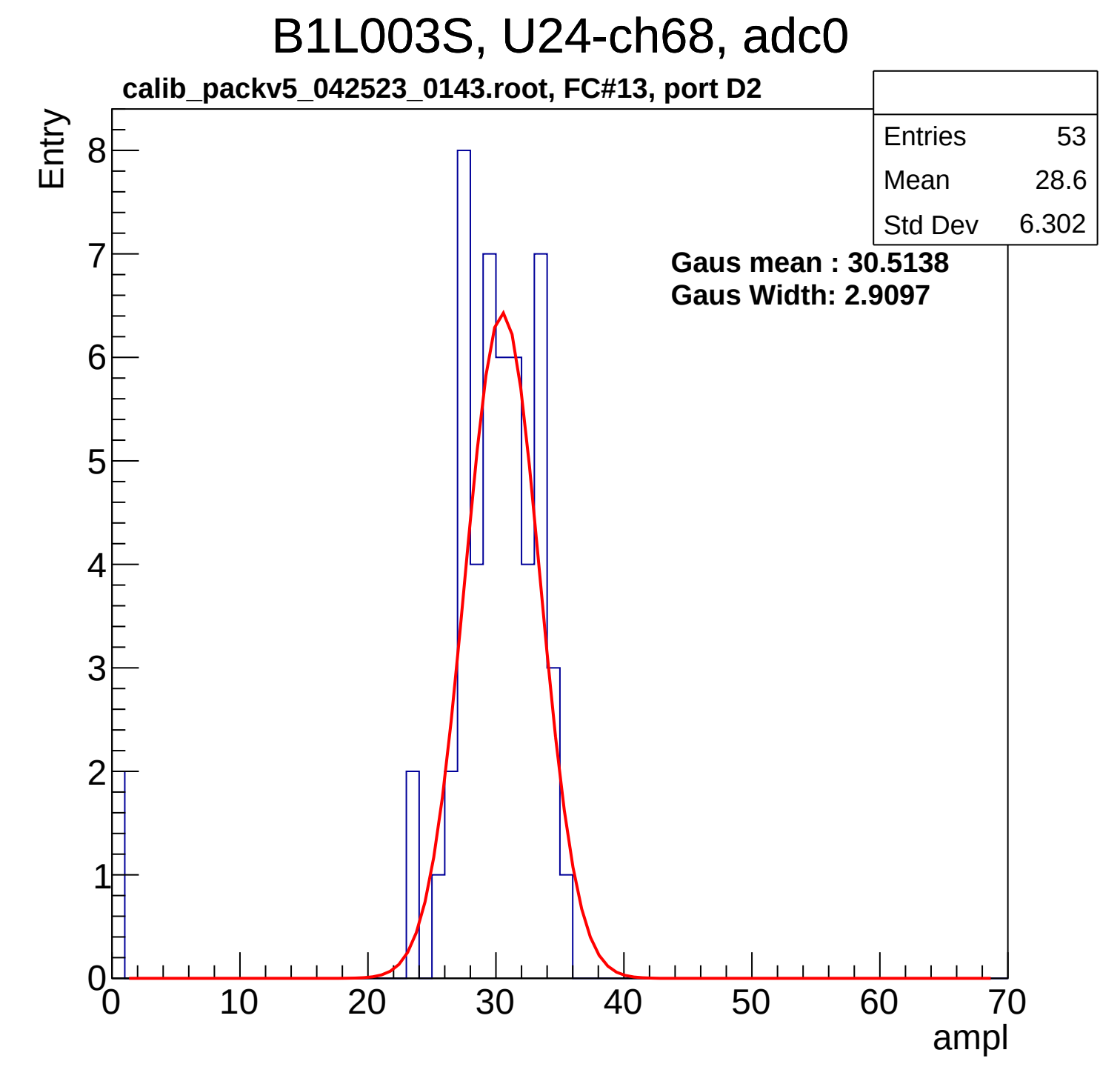
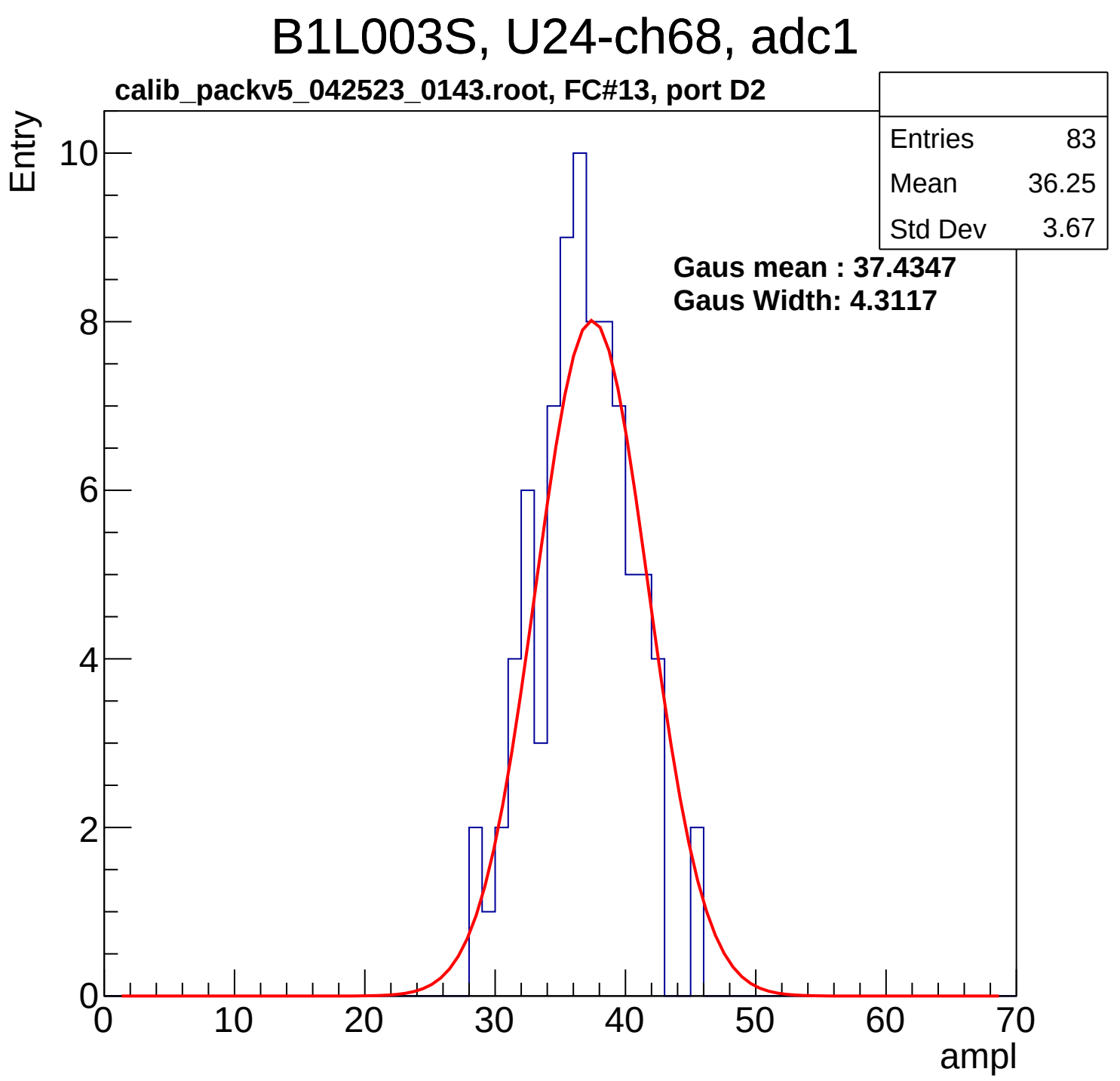
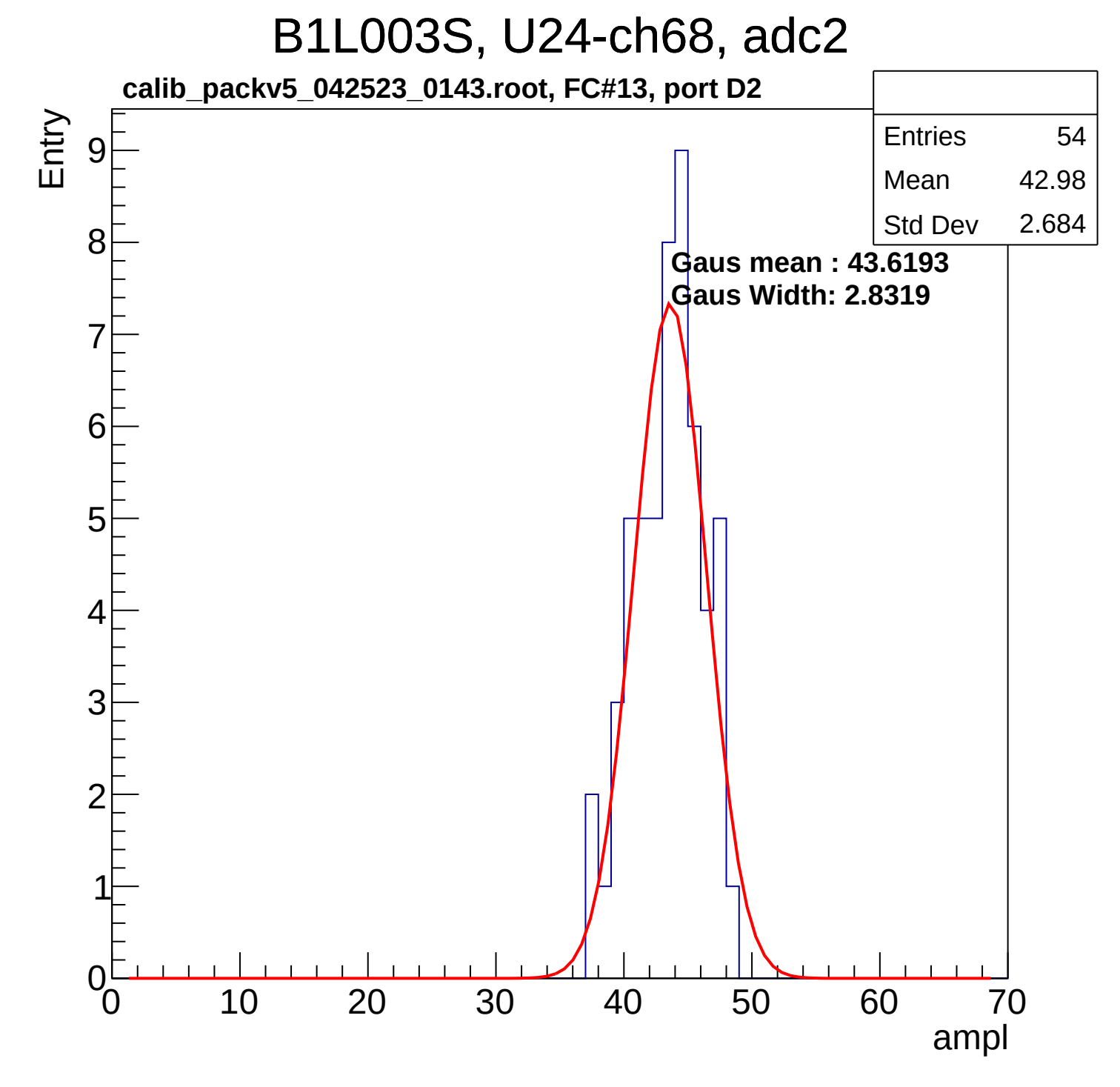
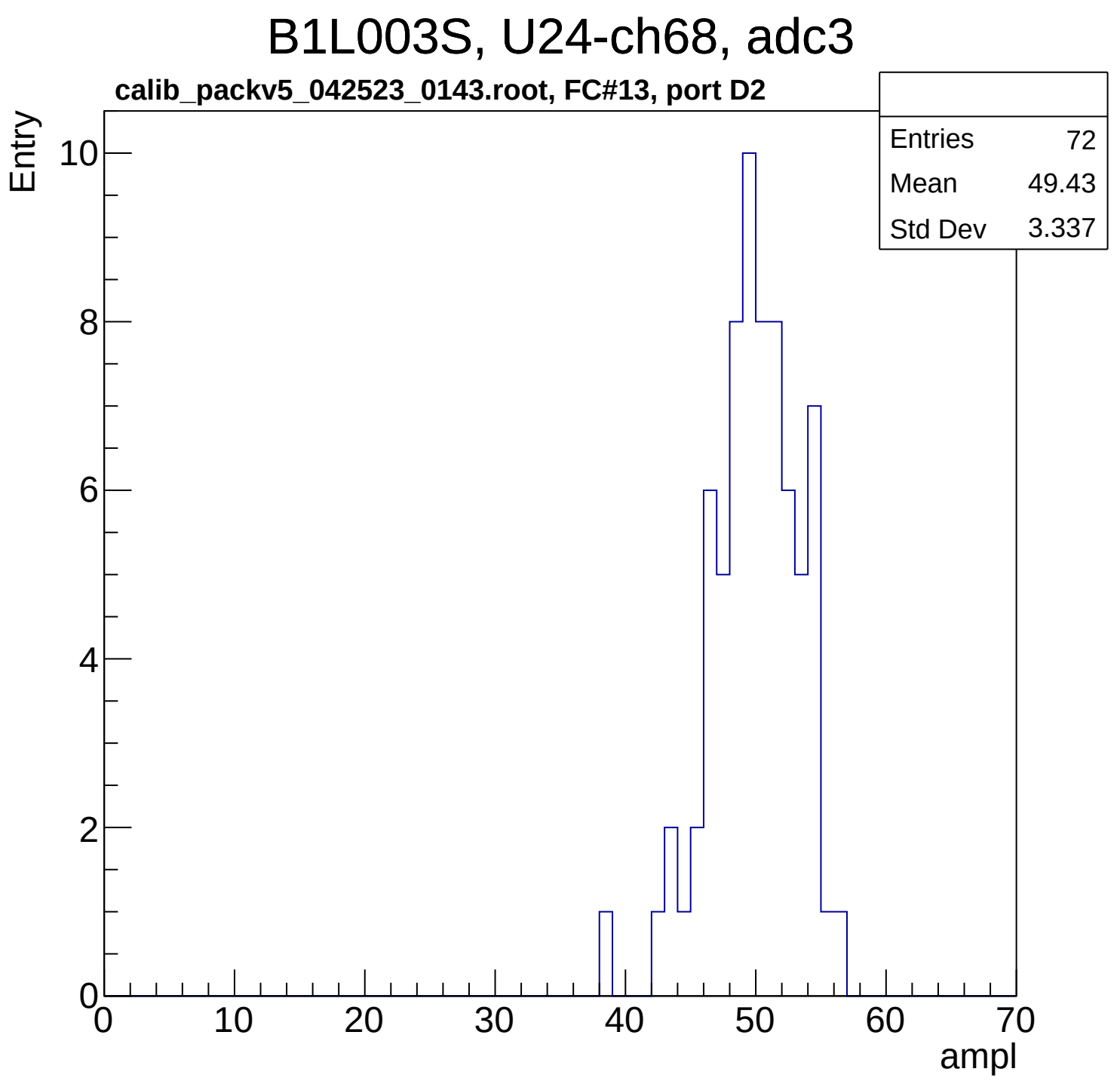

B1L003S, U24-ch68, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 28.6
Std Dev 6.302
Gaus mean : 30.5138
Gaus Width: 2.9097
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch68, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 36.25
Std Dev 3.67
Gaus mean : 37.4347
Gaus Width: 4.3117
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch68, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.98
Std Dev 2.684
Gaus mean : 43.6193
Gaus Width: 2.8319
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch68, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.43
Std Dev 3.337
Entry
0
2
4
6
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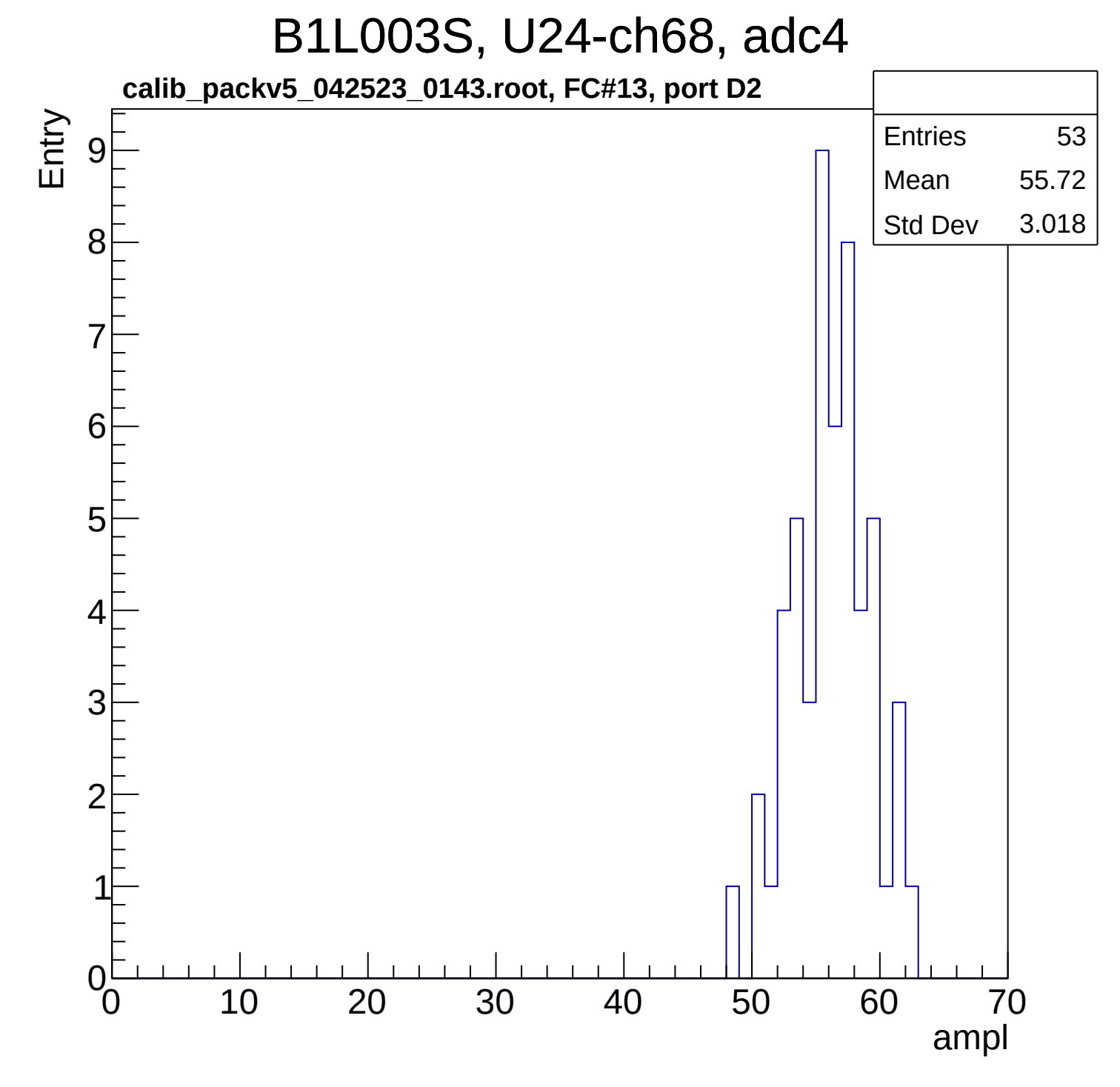
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch68, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.72
Std Dev 3.018
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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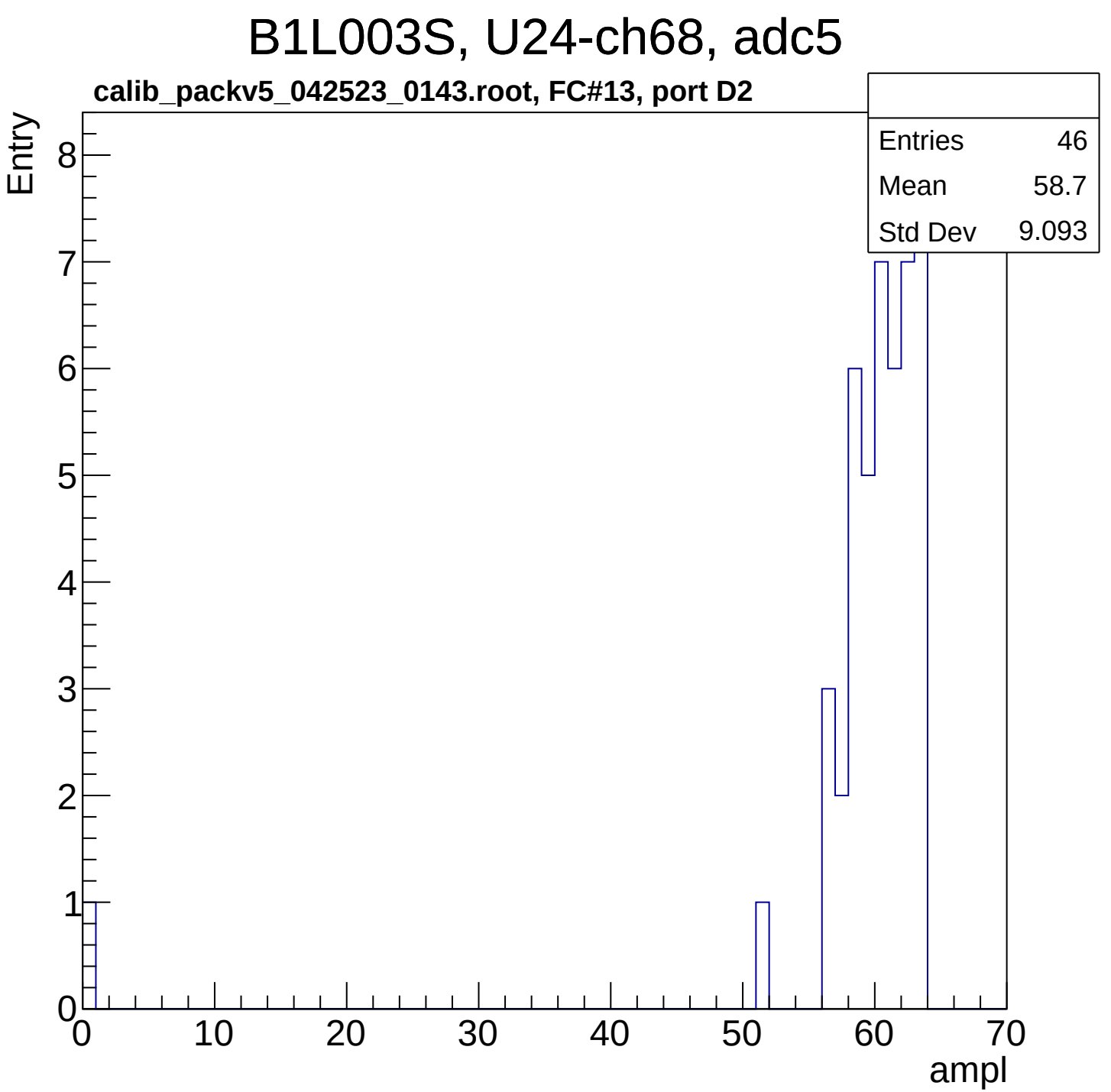
40
50
60
70


B1L003S, U24-ch68, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.7
Std Dev 9.093
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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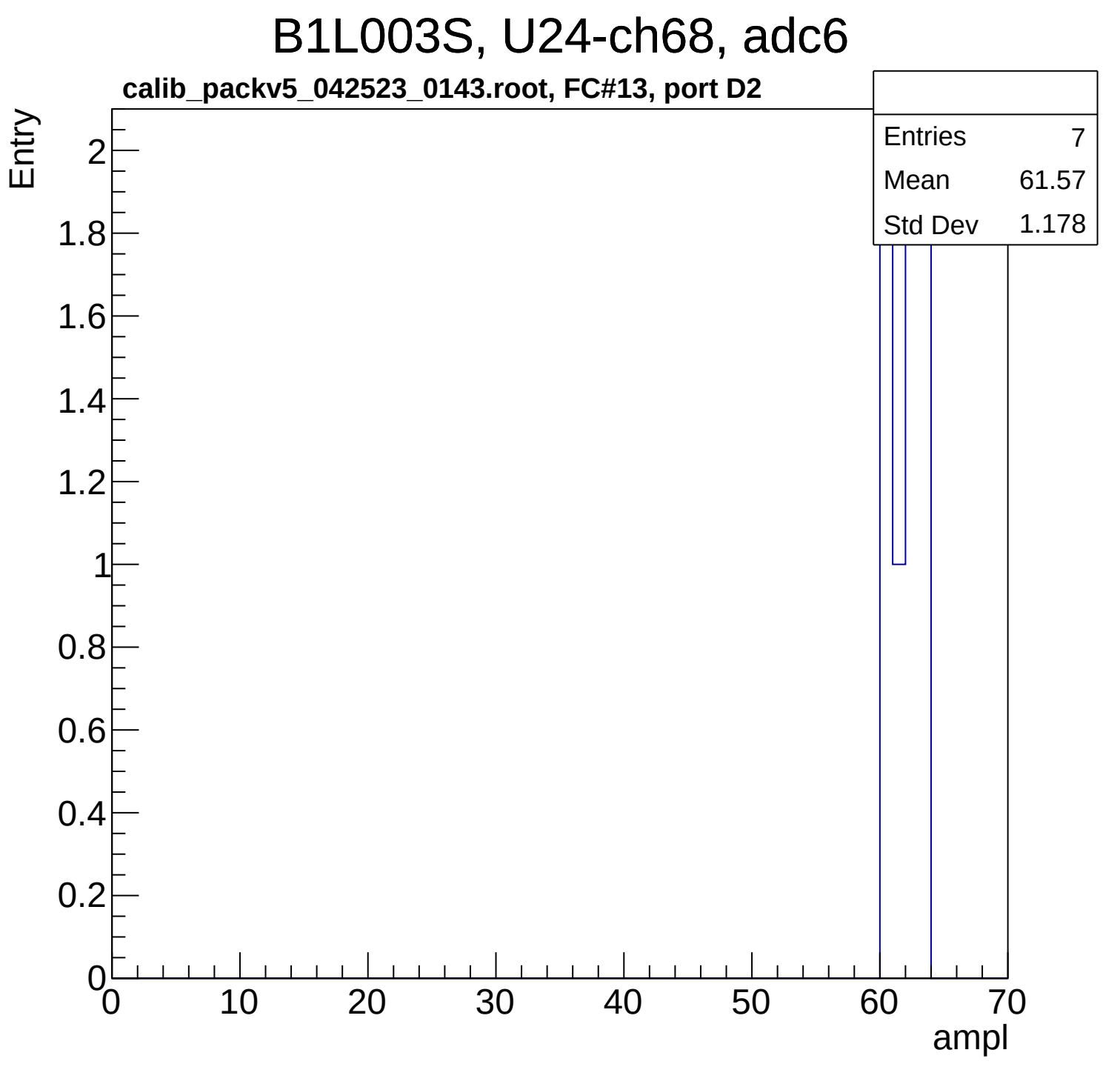
B1L003S, U24-ch68, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.57
Std Dev 1.178
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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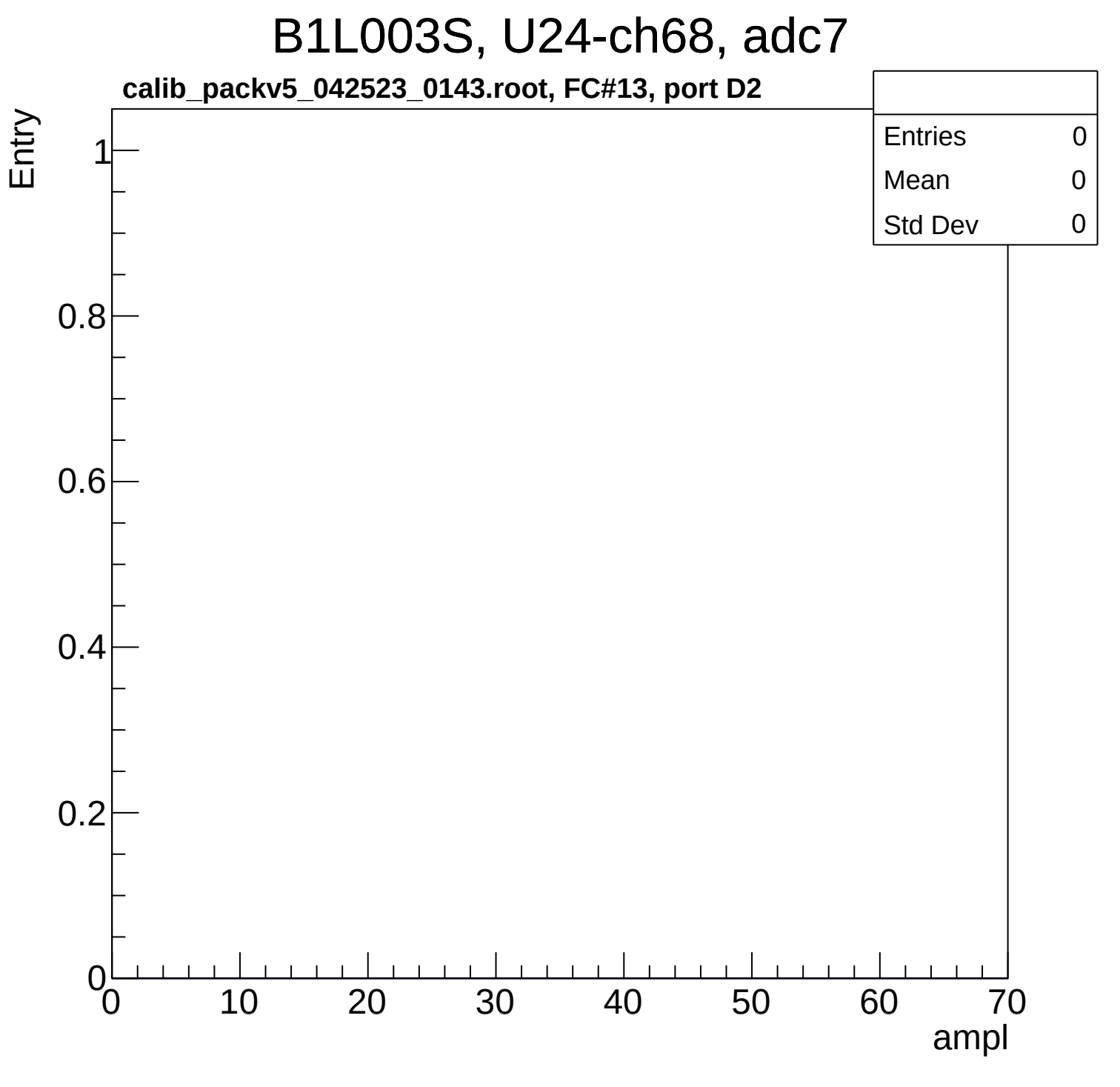
40
50
60
70


B1L003S, U24-ch68, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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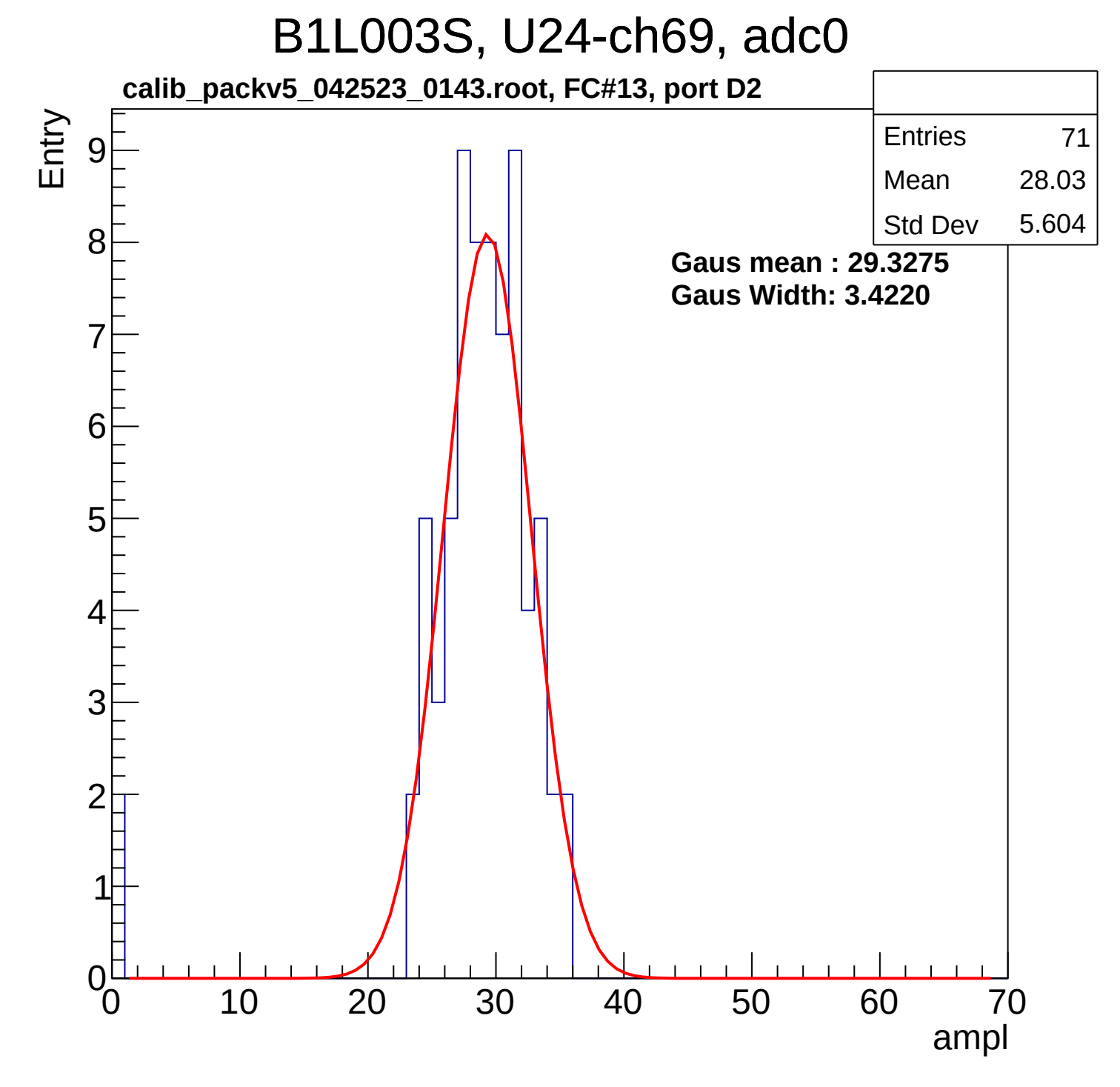
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch69, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 28.03
Std Dev 5.604
Gaus mean : 29.3275
Gaus Width: 3.4220
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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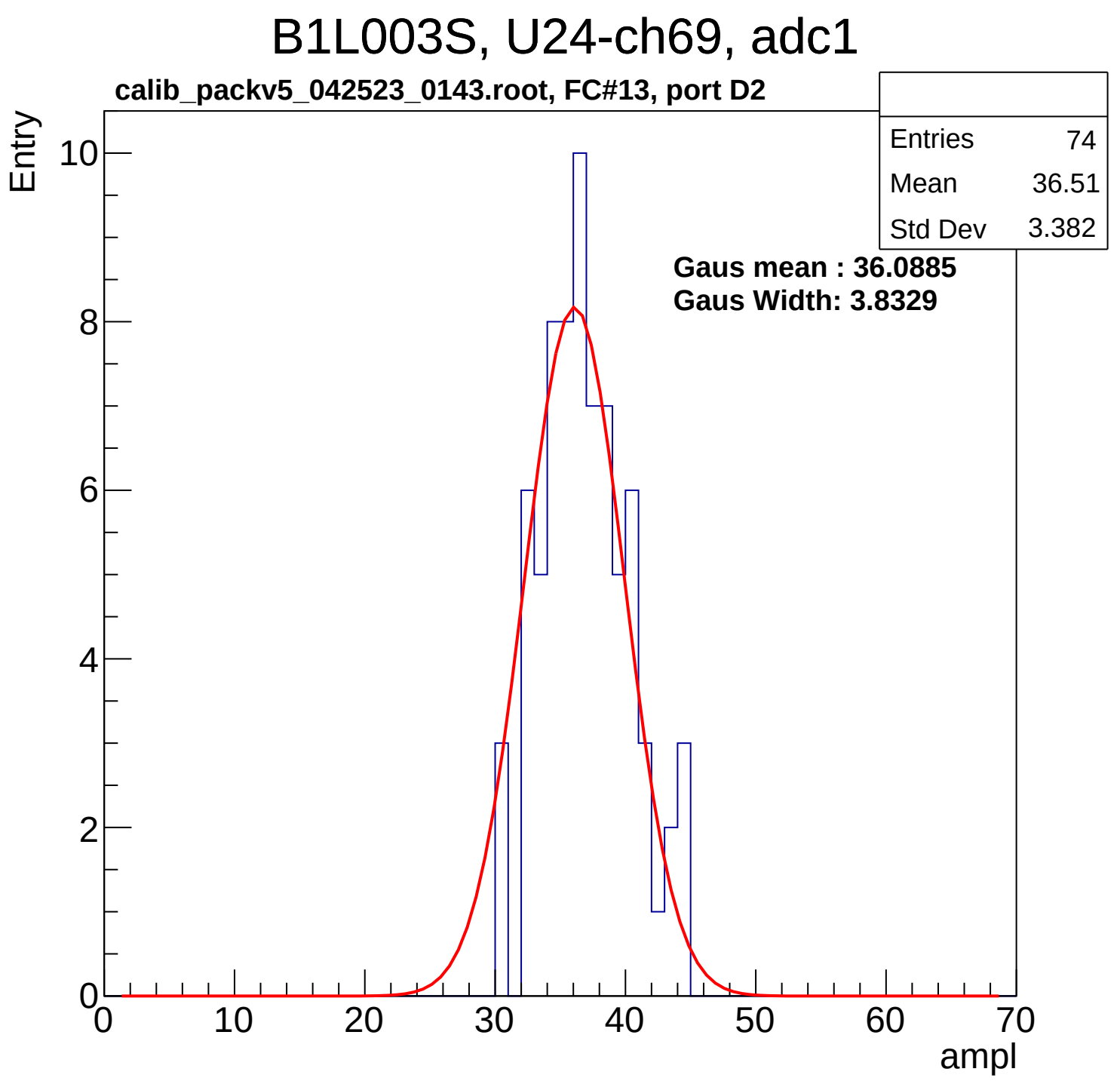
B1L003S, U24-ch69, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.51
Std Dev 3.382
Gaus mean : 36.0885
Gaus Width: 3.8329
Entry
ampl
0
2
4
6
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0
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20
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40
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60
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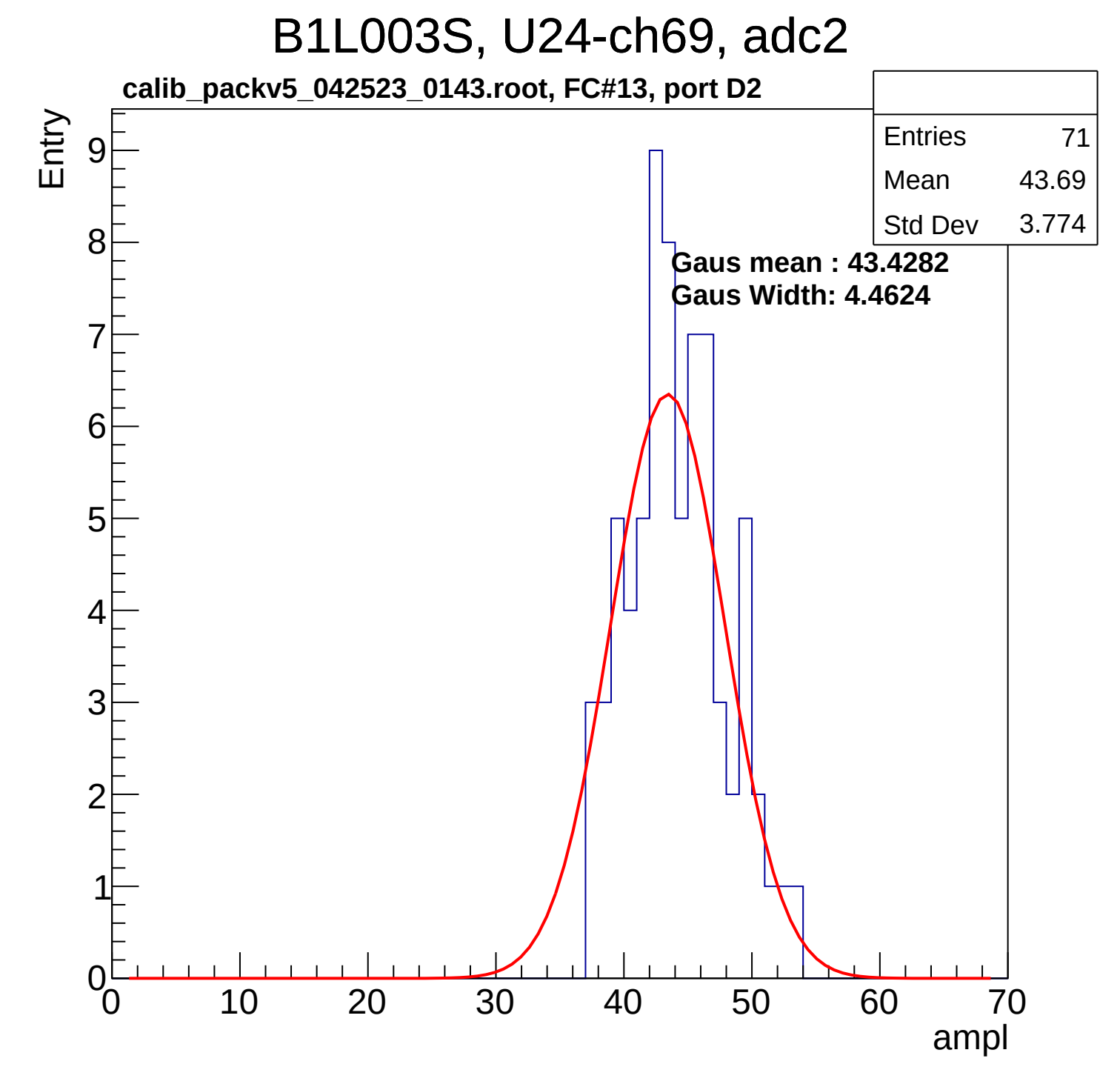
B1L003S, U24-ch69, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 43.69
Std Dev 3.774
Gaus mean : 43.4282
Gaus Width: 4.4624
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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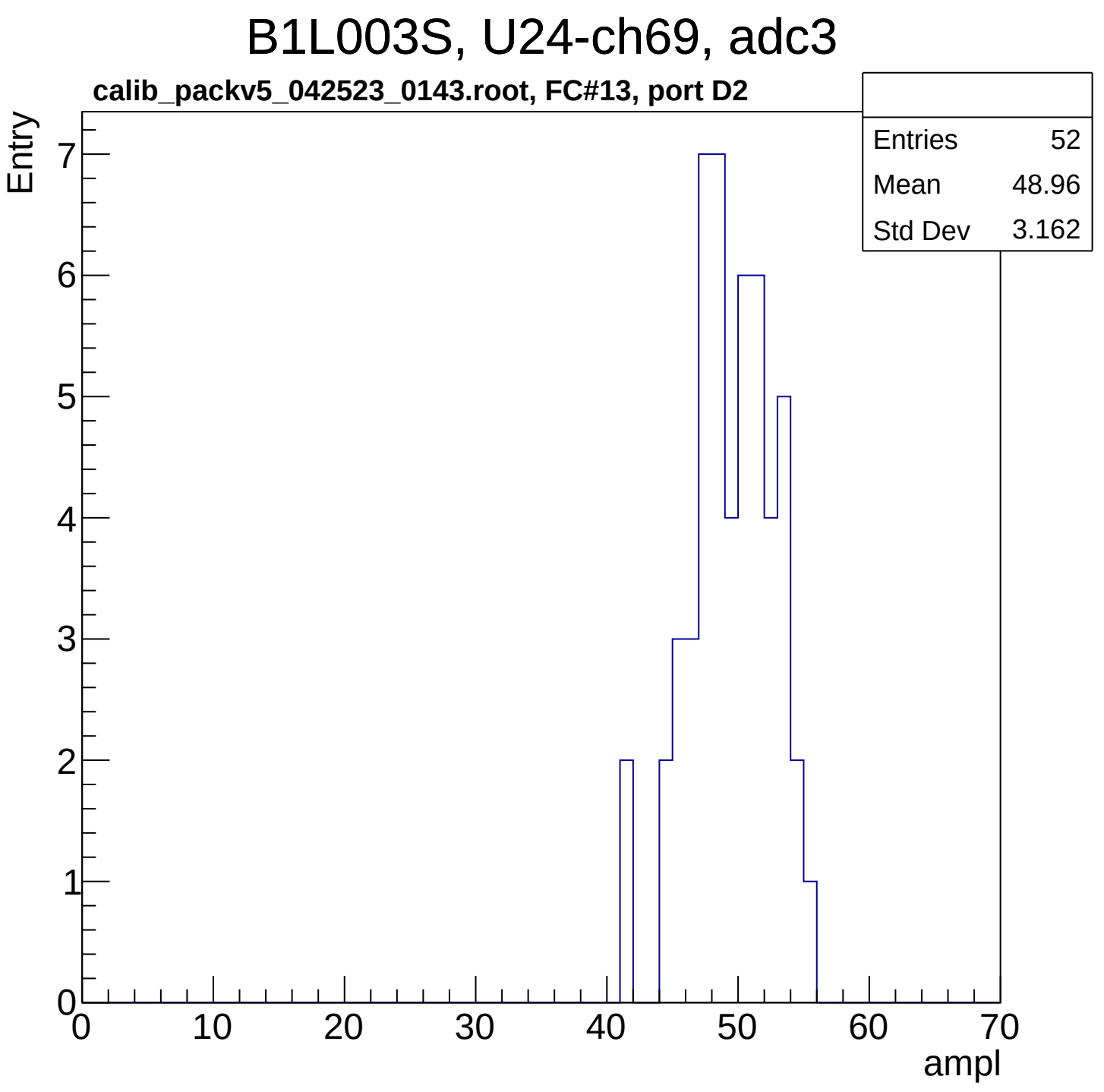
B1L003S, U24-ch69, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 48.96
Std Dev 3.162
Entry
ampl
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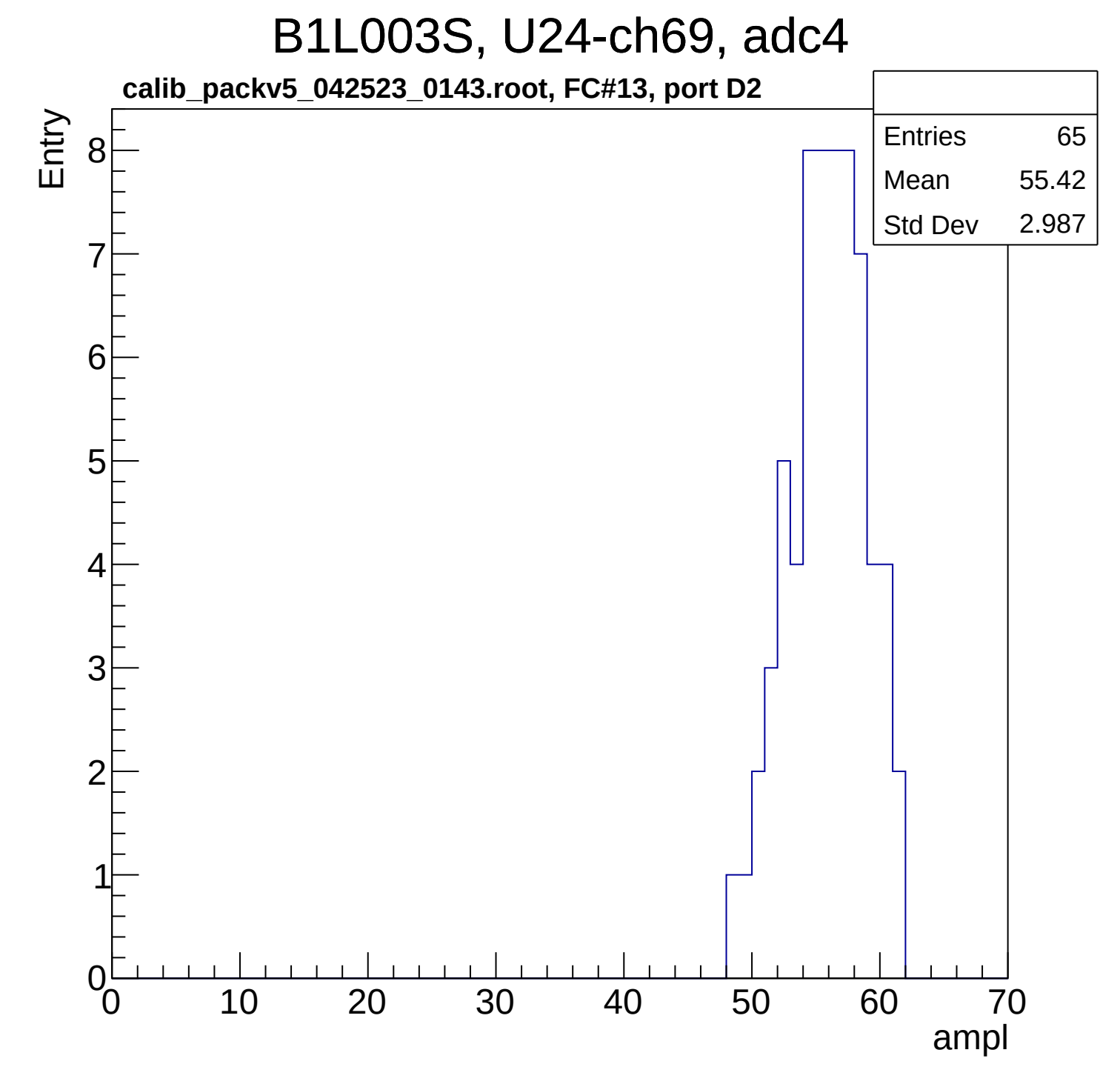
B1L003S, U24-ch69, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 55.42
Std Dev 2.987
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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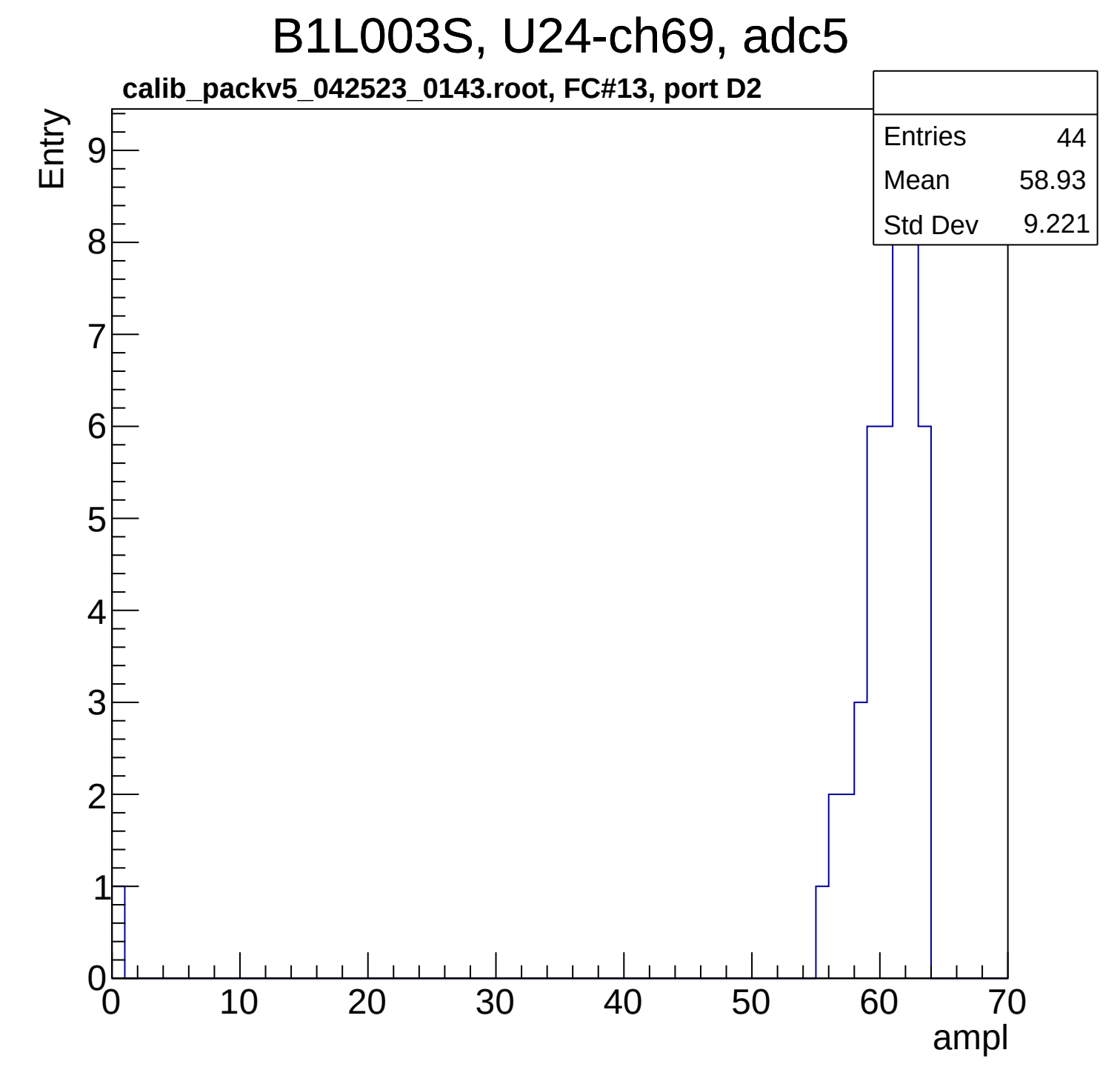
B1L003S, U24-ch69, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.93
Std Dev 9.221
Entry
ampl
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2
3
4
5
6
7
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0
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60
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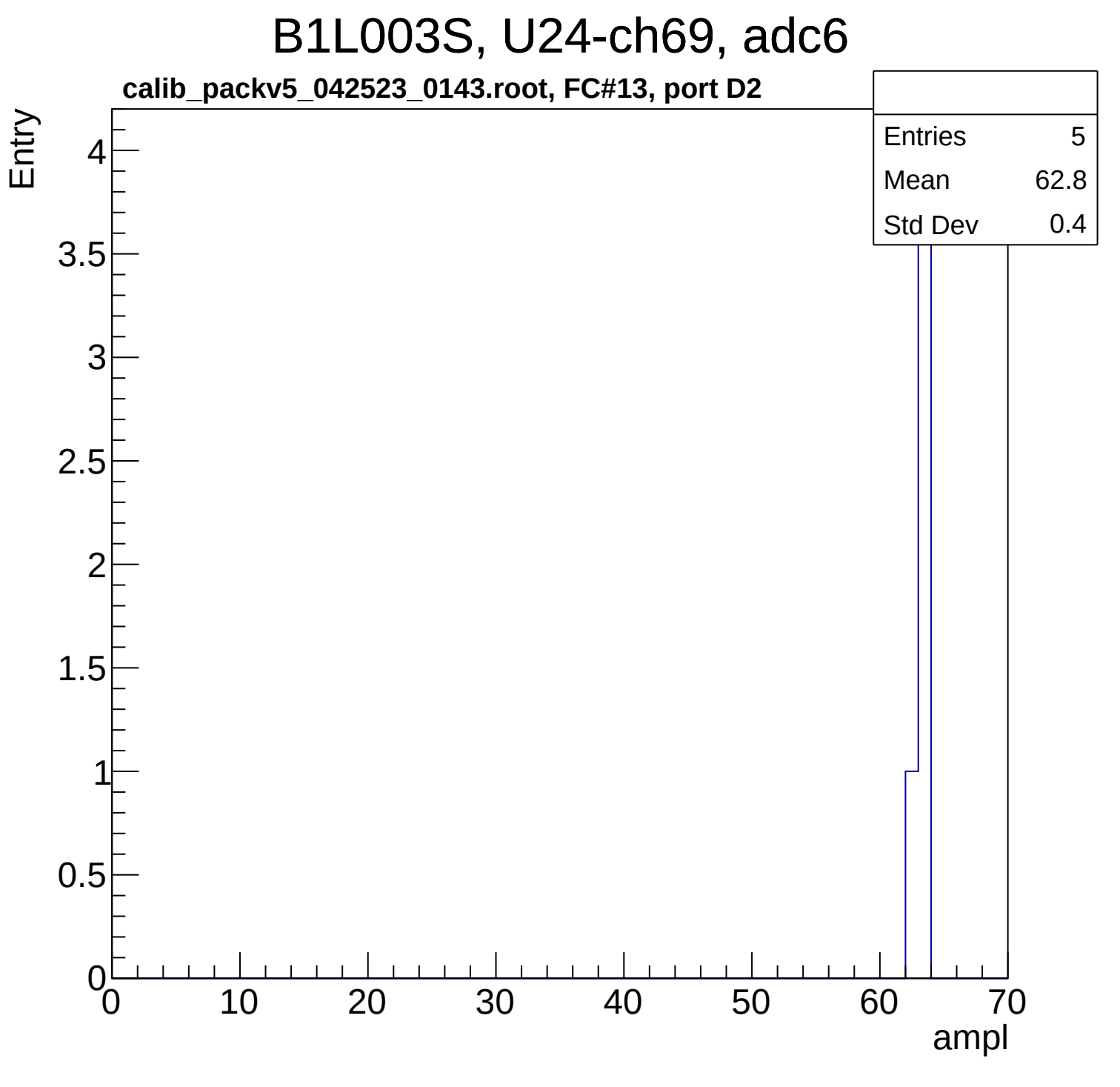
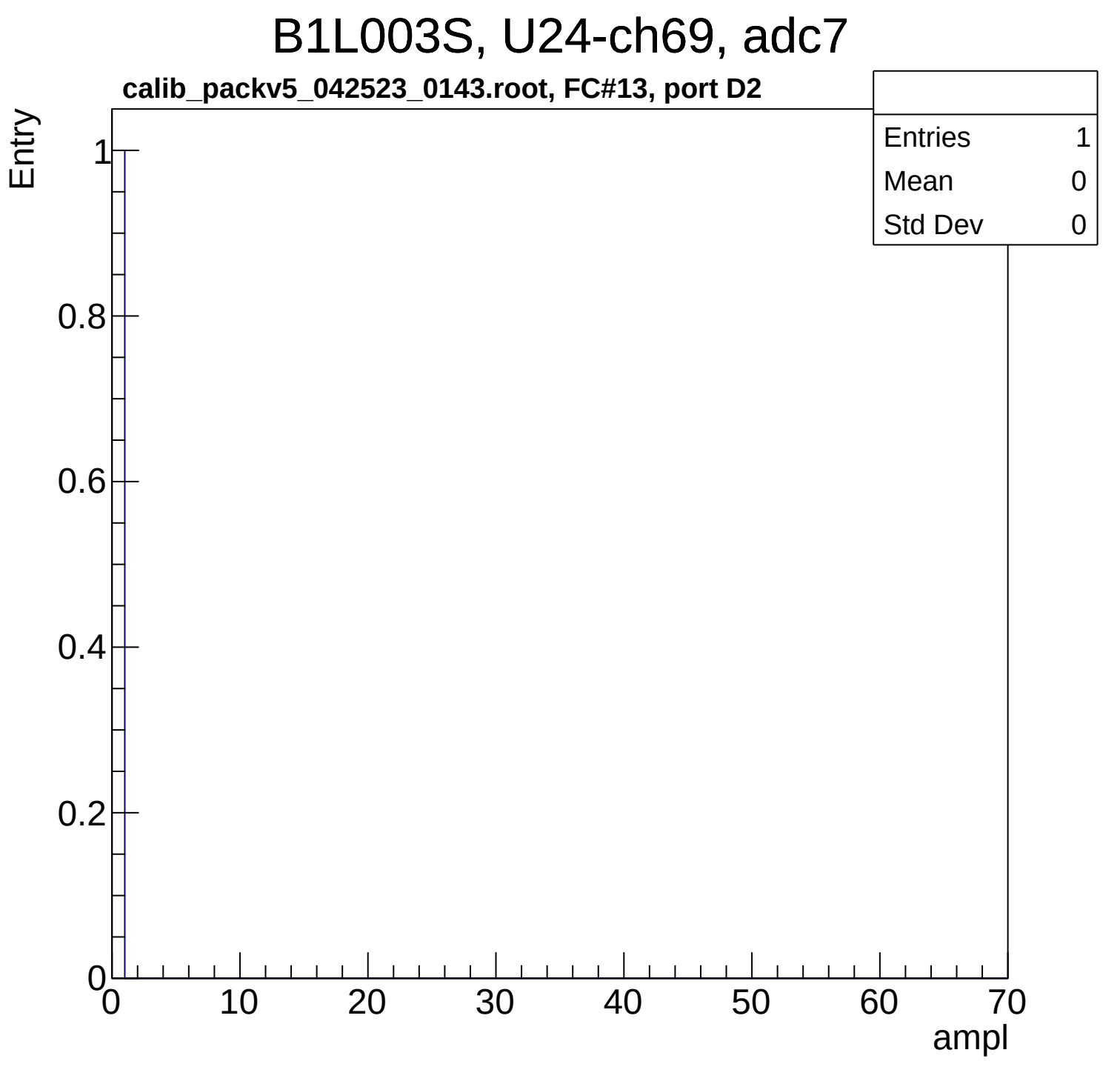
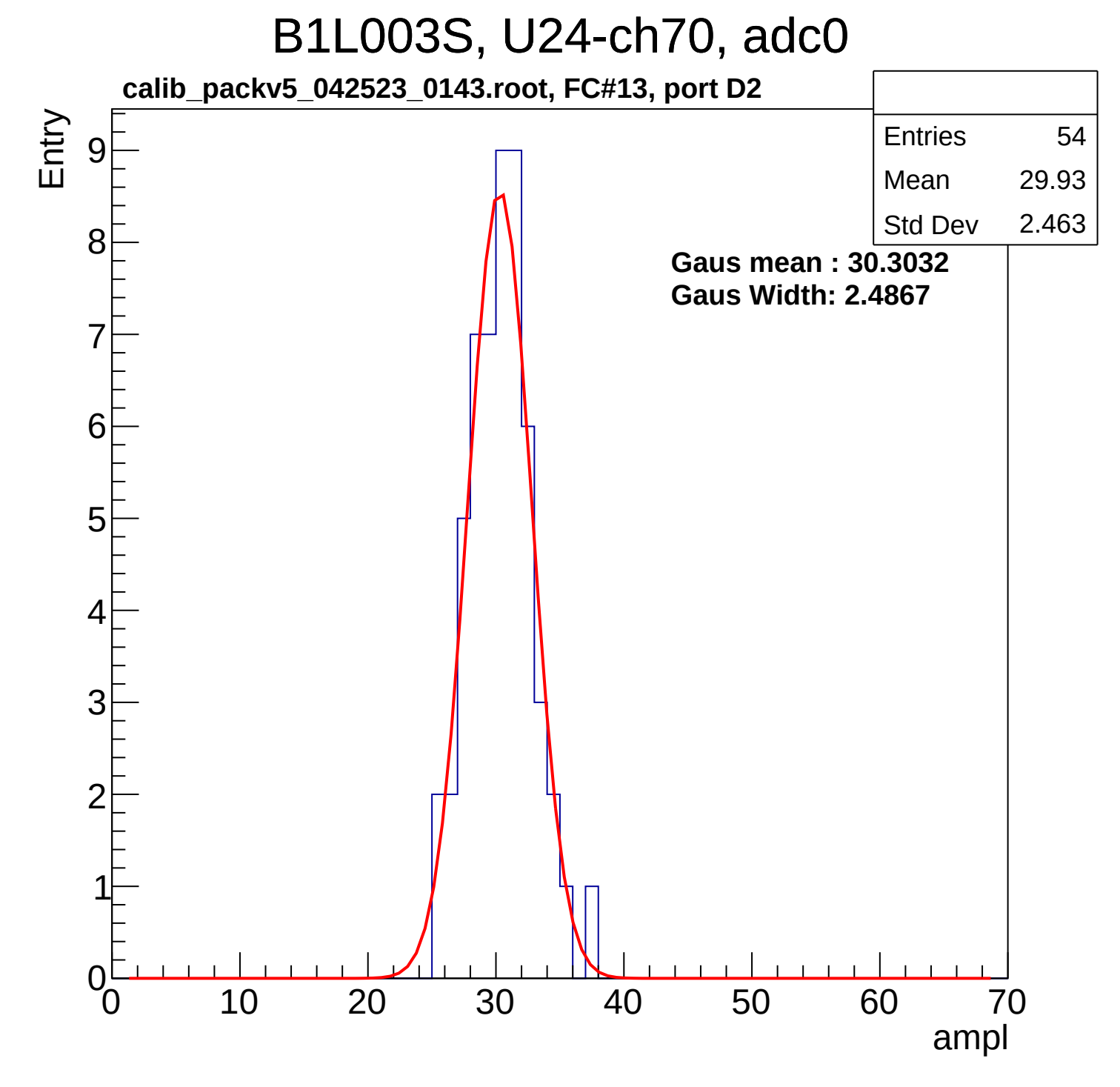

B1L003S, U24-ch69, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.8
Std Dev 0.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch69, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch70, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.93
Std Dev 2.463
Gaus mean : 30.3032
Gaus Width: 2.4867
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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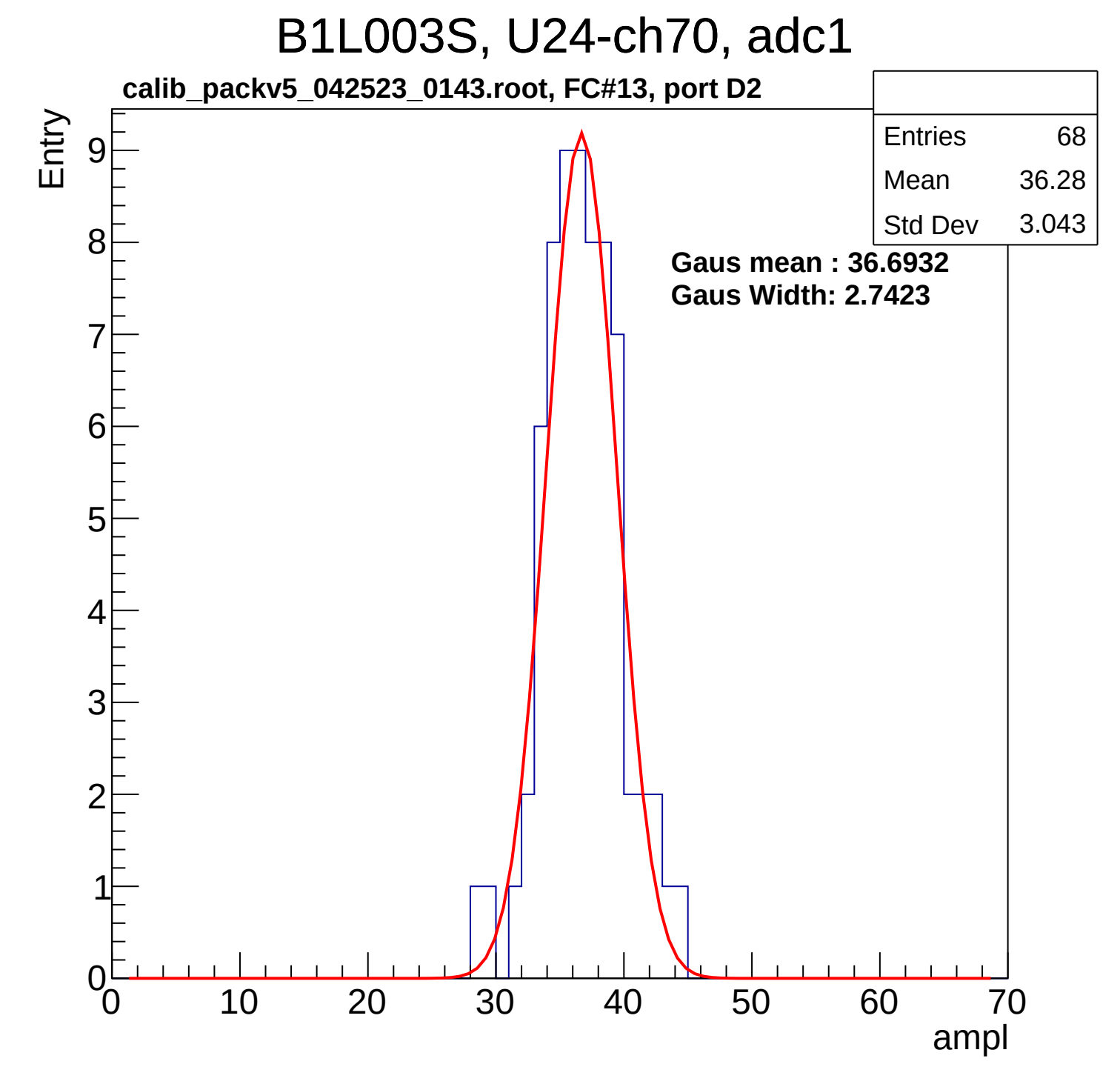
B1L003S, U24-ch70, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.28
Std Dev 3.043
Gaus mean : 36.6932
Gaus Width: 2.7423
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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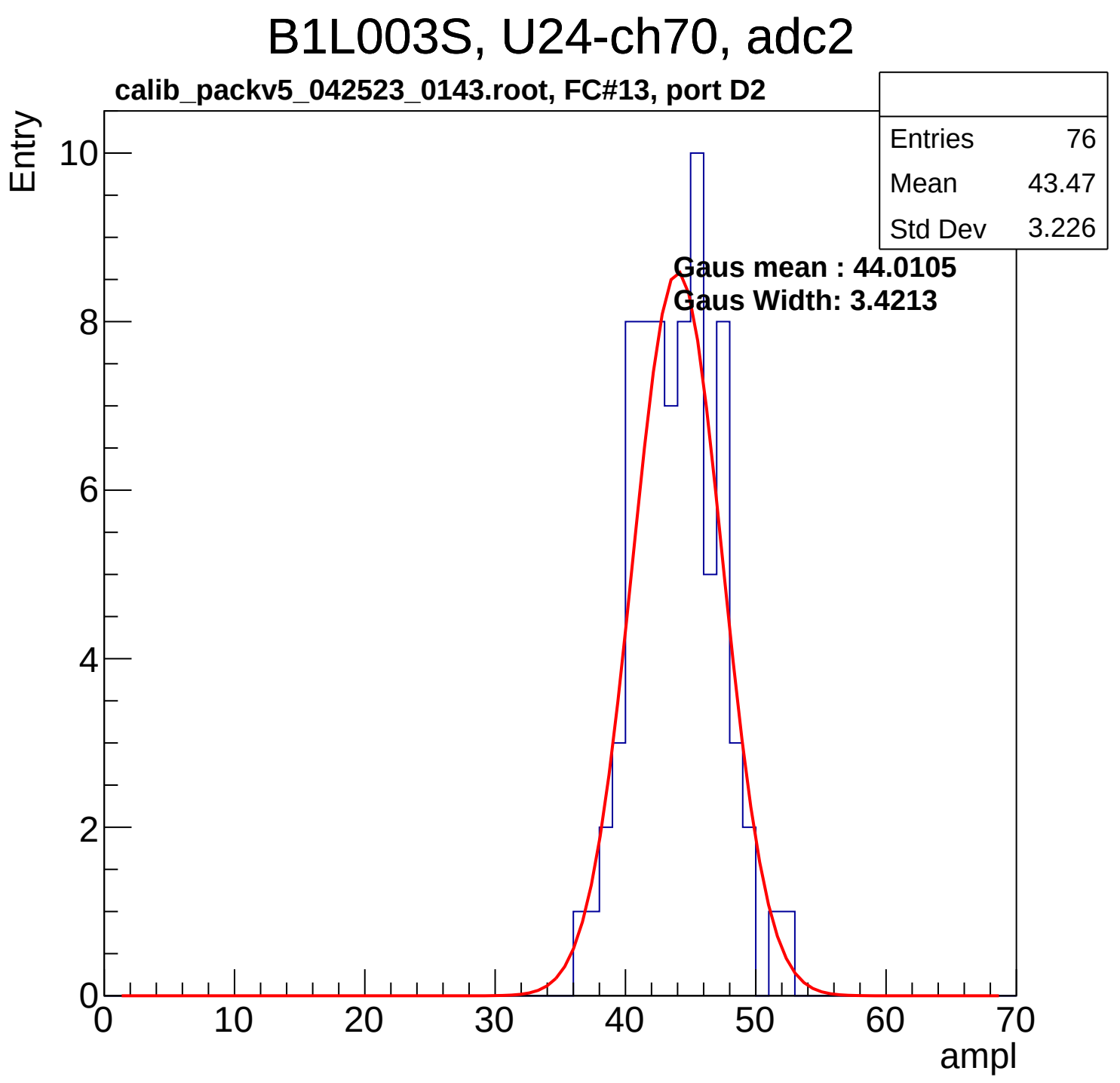
B1L003S, U24-ch70, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 43.47
Std Dev 3.226
Gaus mean : 44.0105
Gaus Width: 3.4213
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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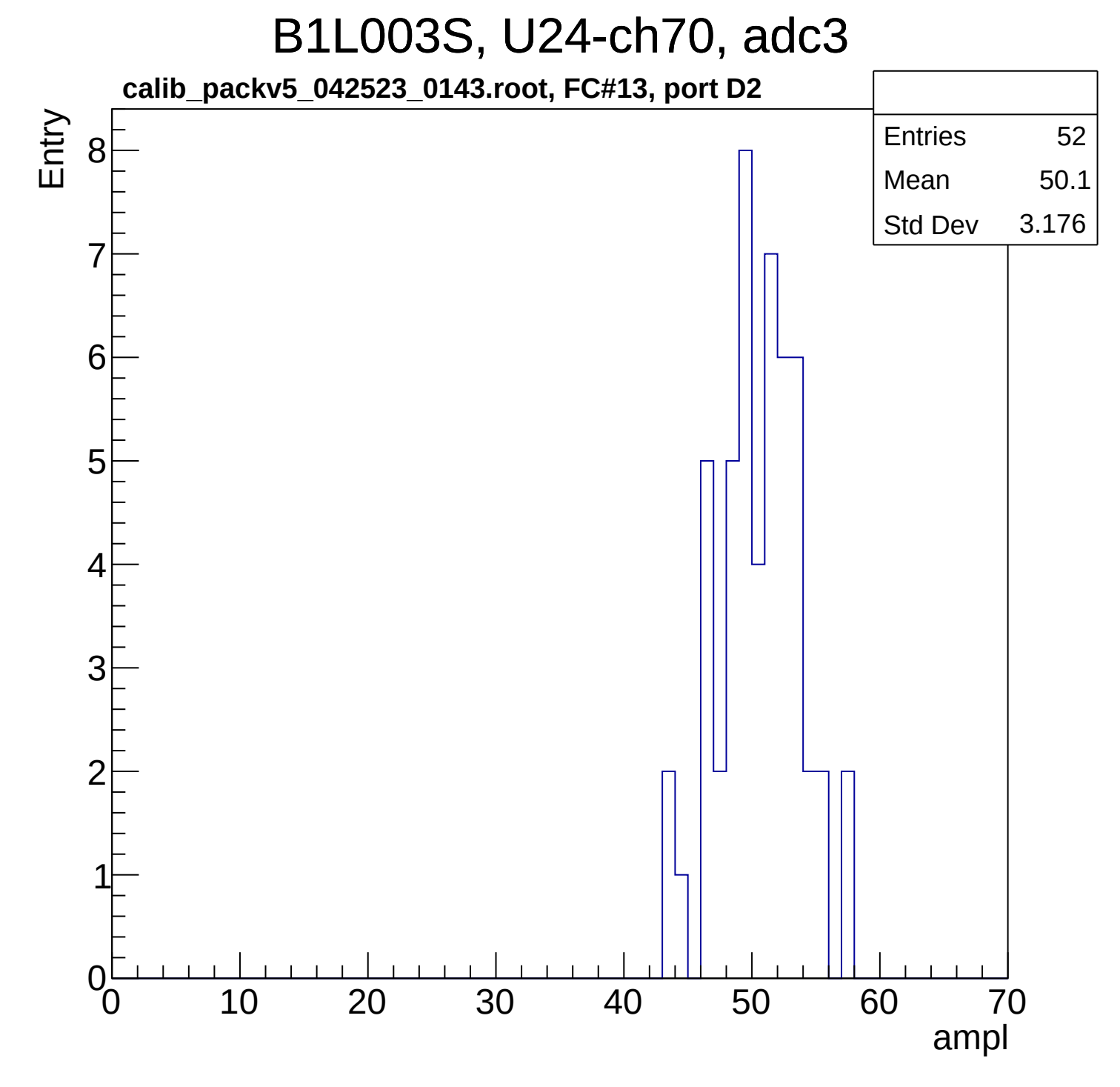
B1L003S, U24-ch70, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 50.1
Std Dev 3.176
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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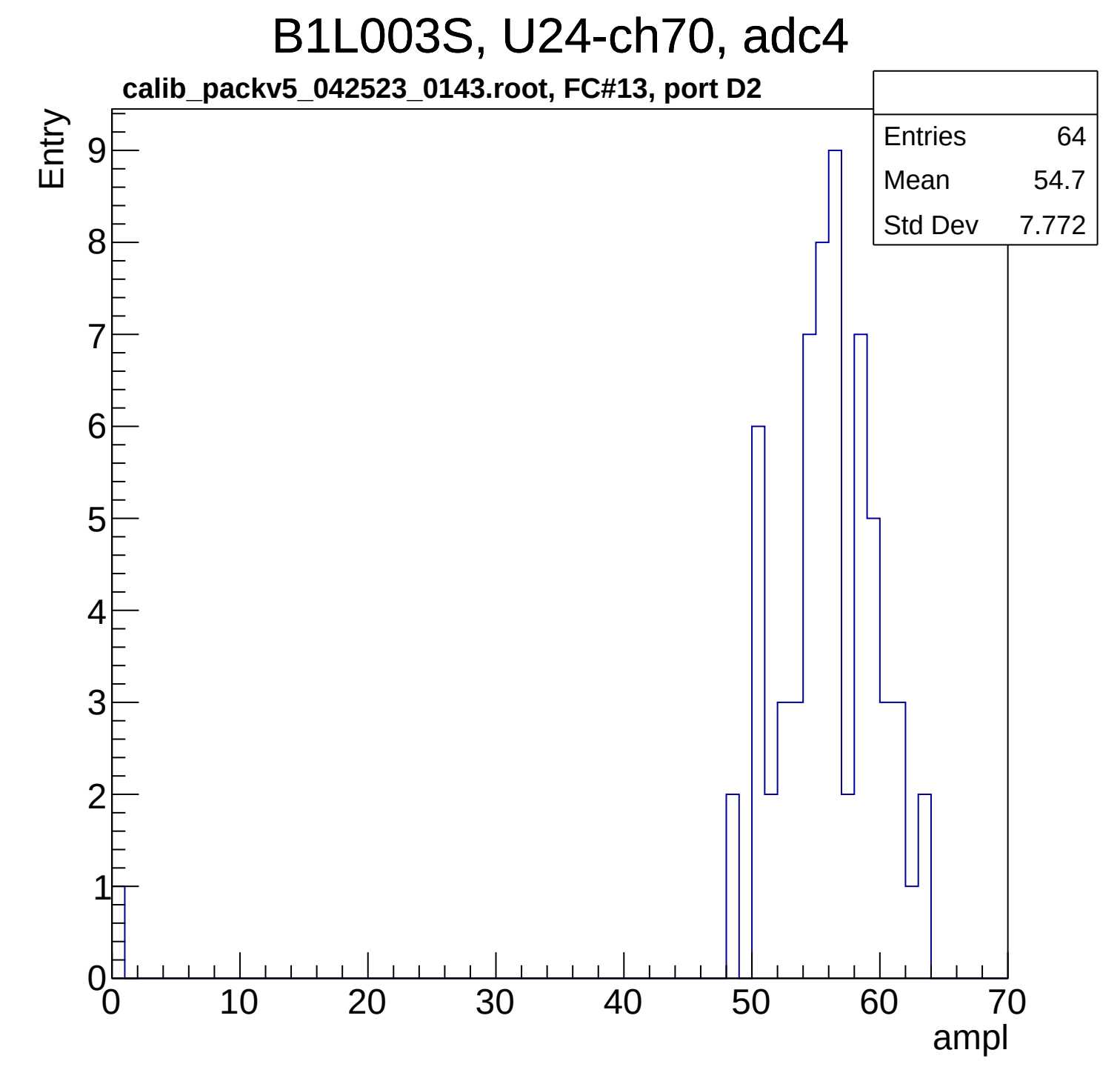
B1L003S, U24-ch70, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 54.7
Std Dev 7.772
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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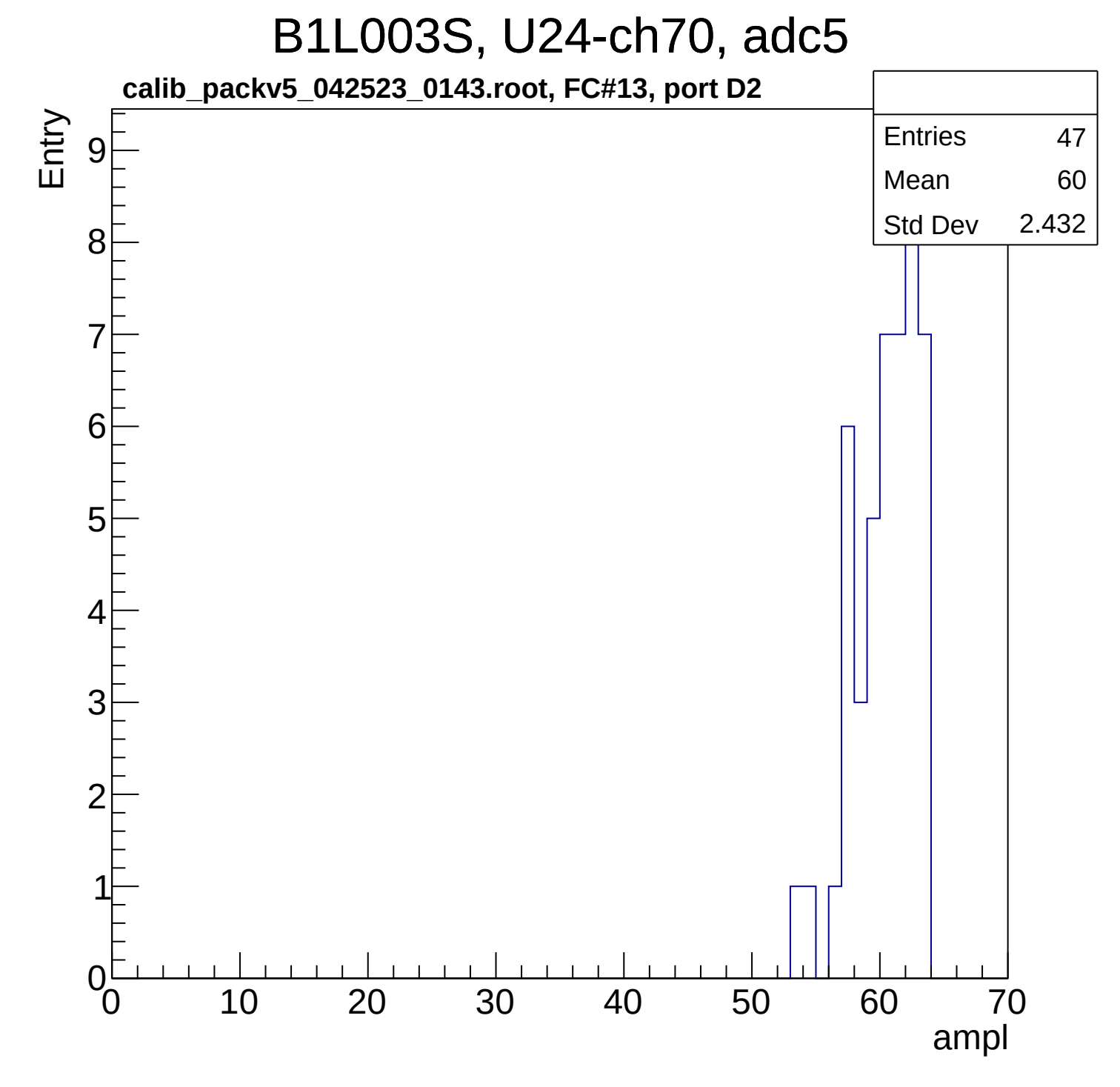
B1L003S, U24-ch70, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 60
Std Dev 2.432
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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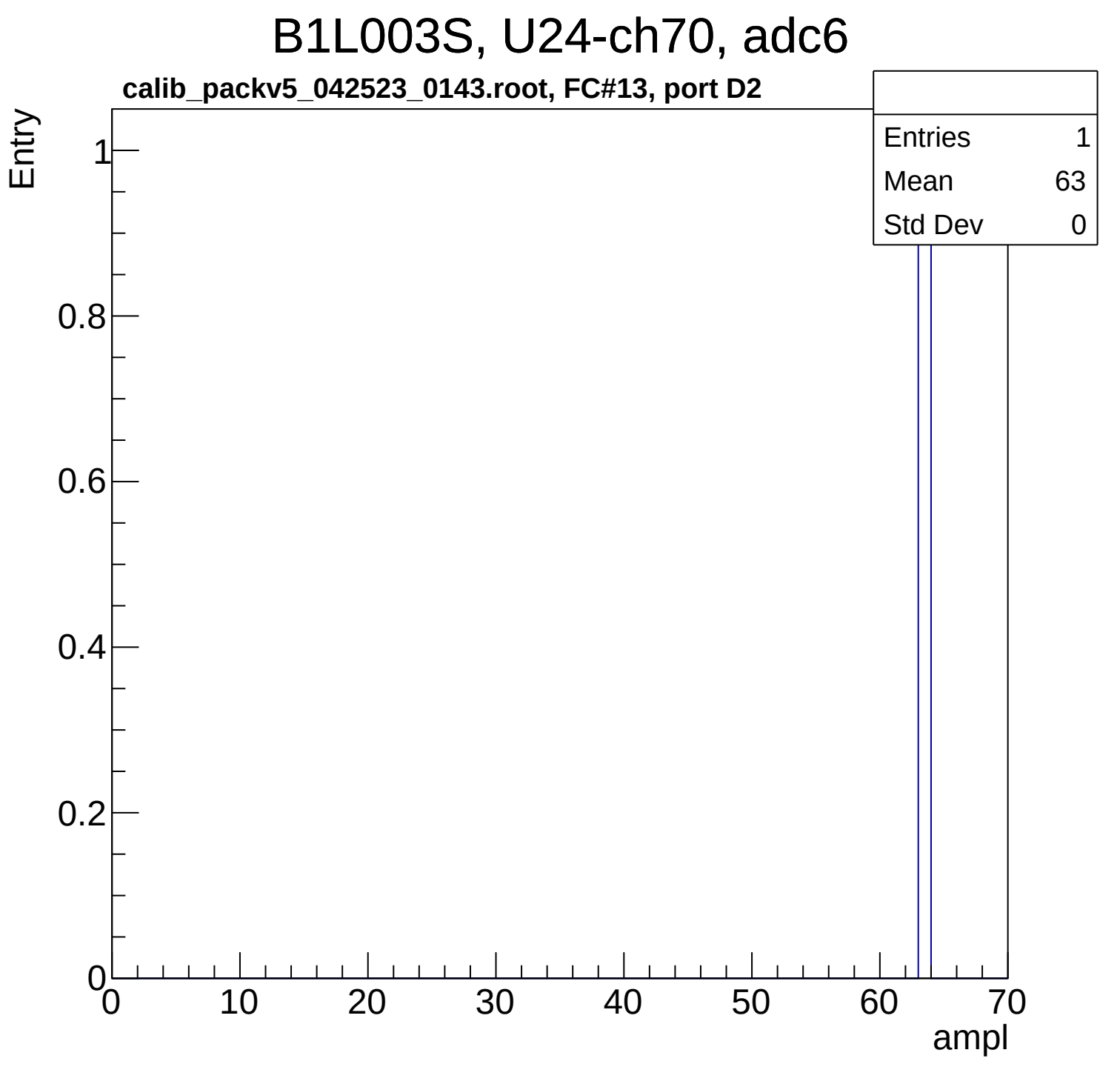
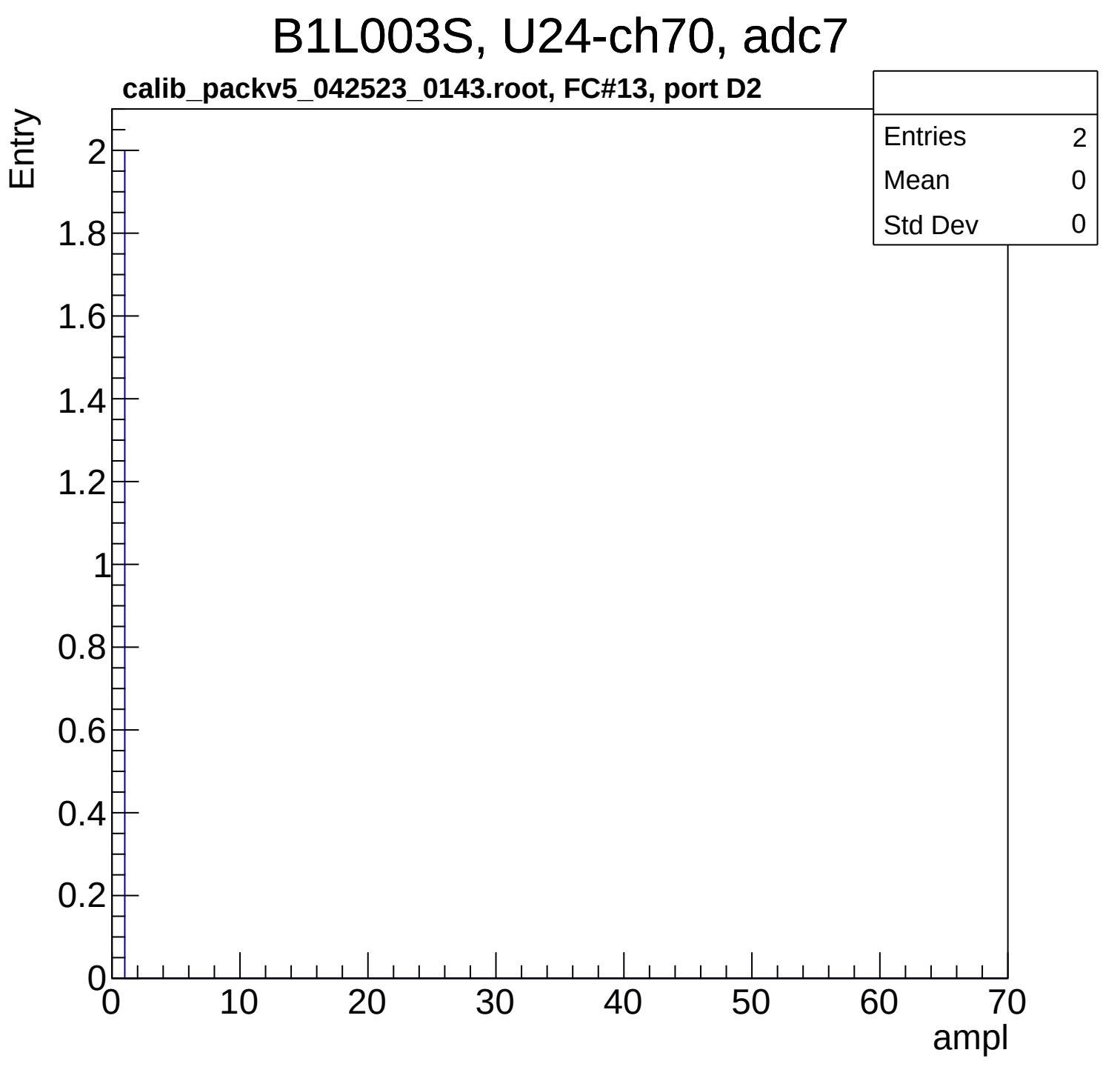

B1L003S, U24-ch70, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch70, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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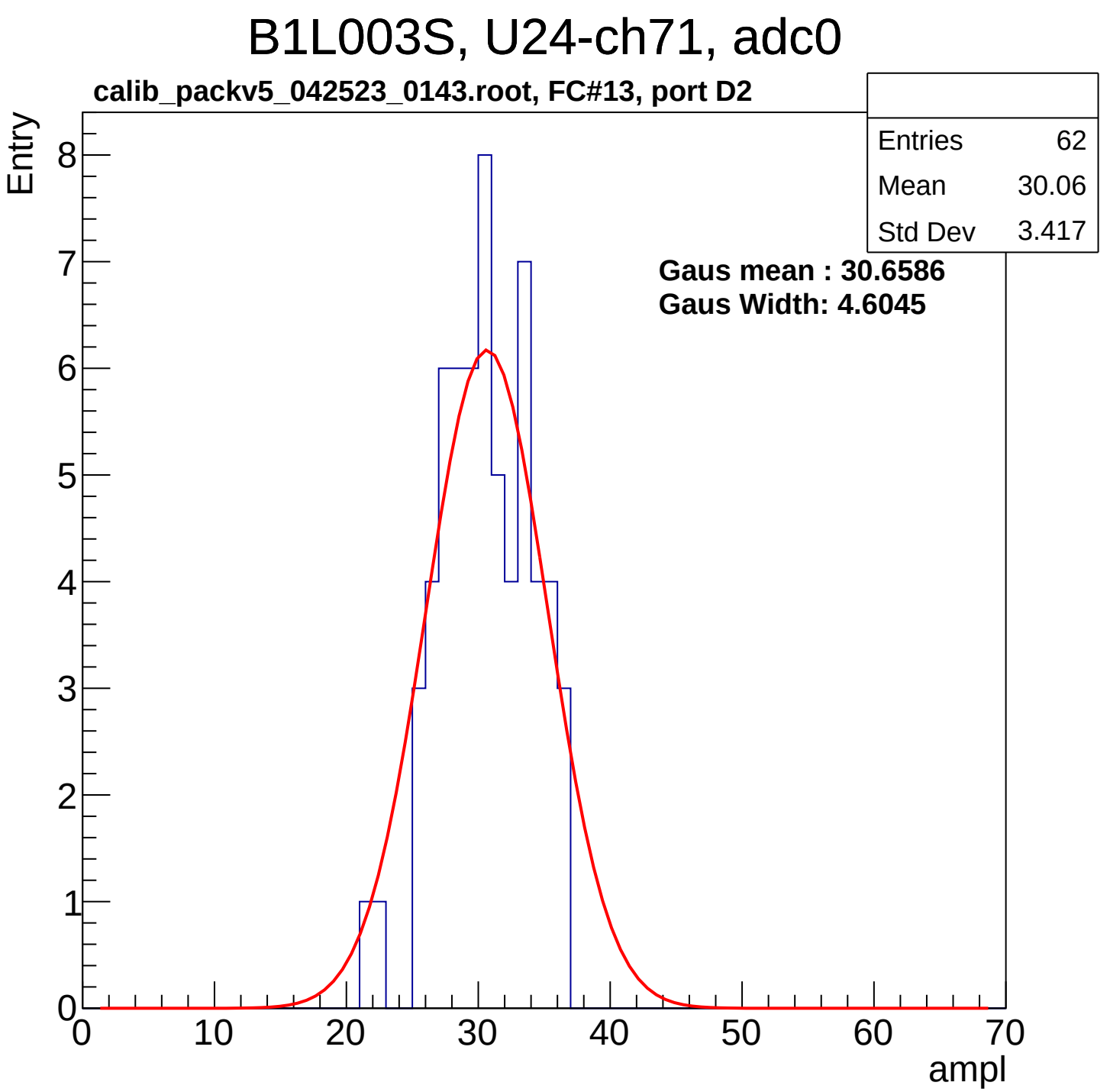
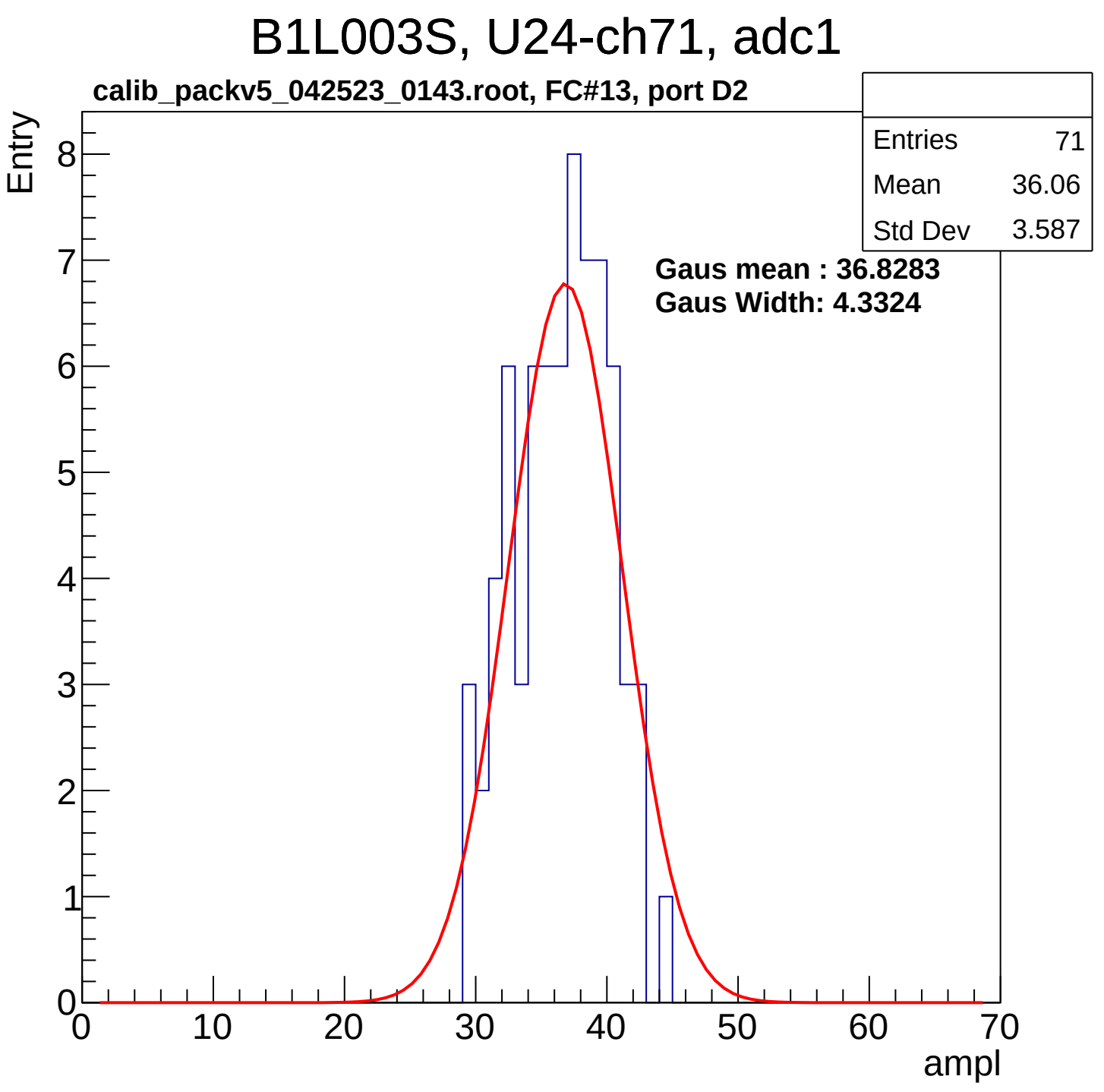

B1L003S, U24-ch71, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 30.06
Std Dev 3.417
Gaus mean : 30.6586
Gaus Width: 4.6045
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch71, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.06
Std Dev 3.587
Gaus mean : 36.8283
Gaus Width: 4.3324
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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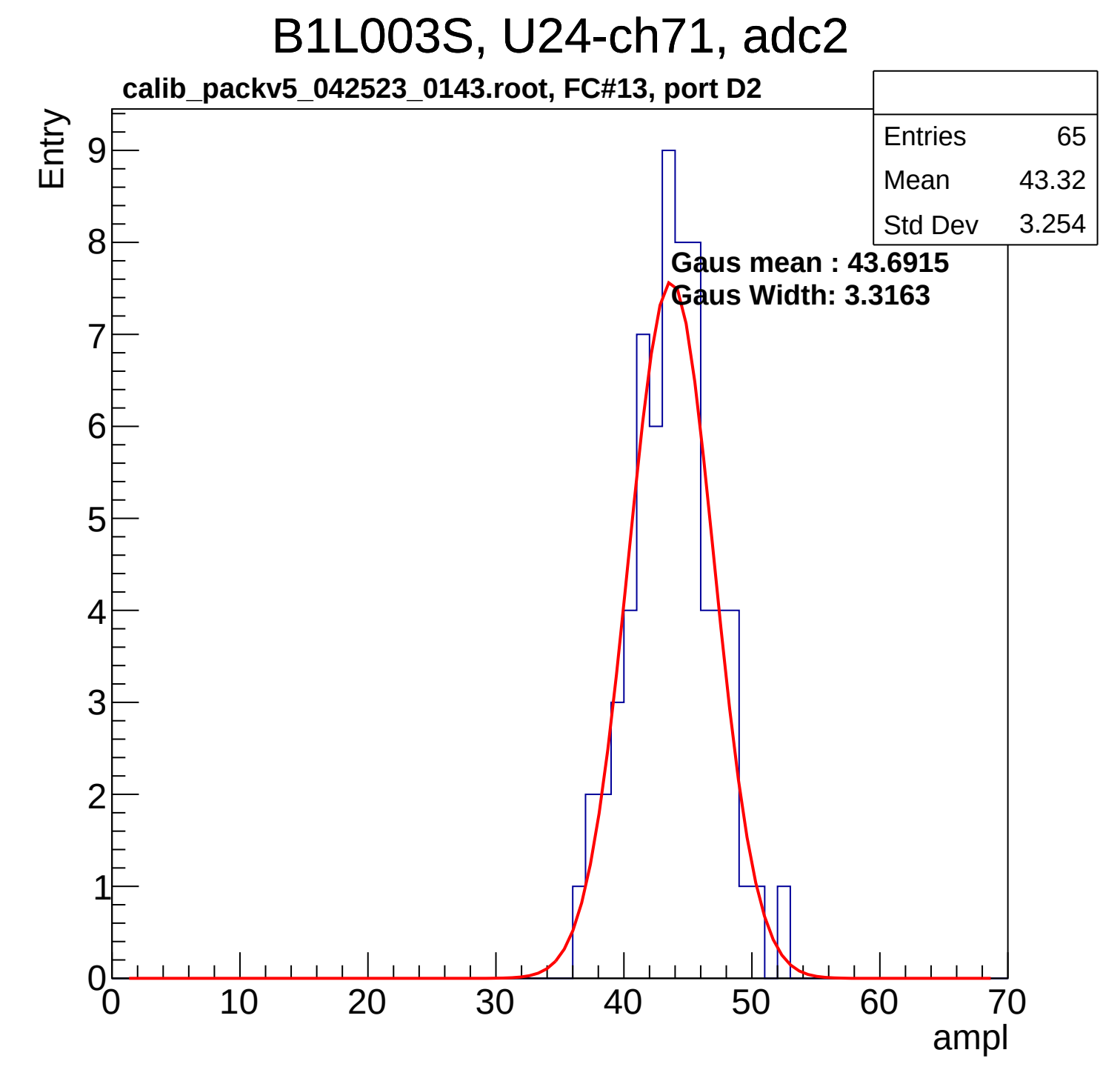
B1L003S, U24-ch71, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.32
Std Dev 3.254
Gaus mean : 43.6915
Gaus Width: 3.3163
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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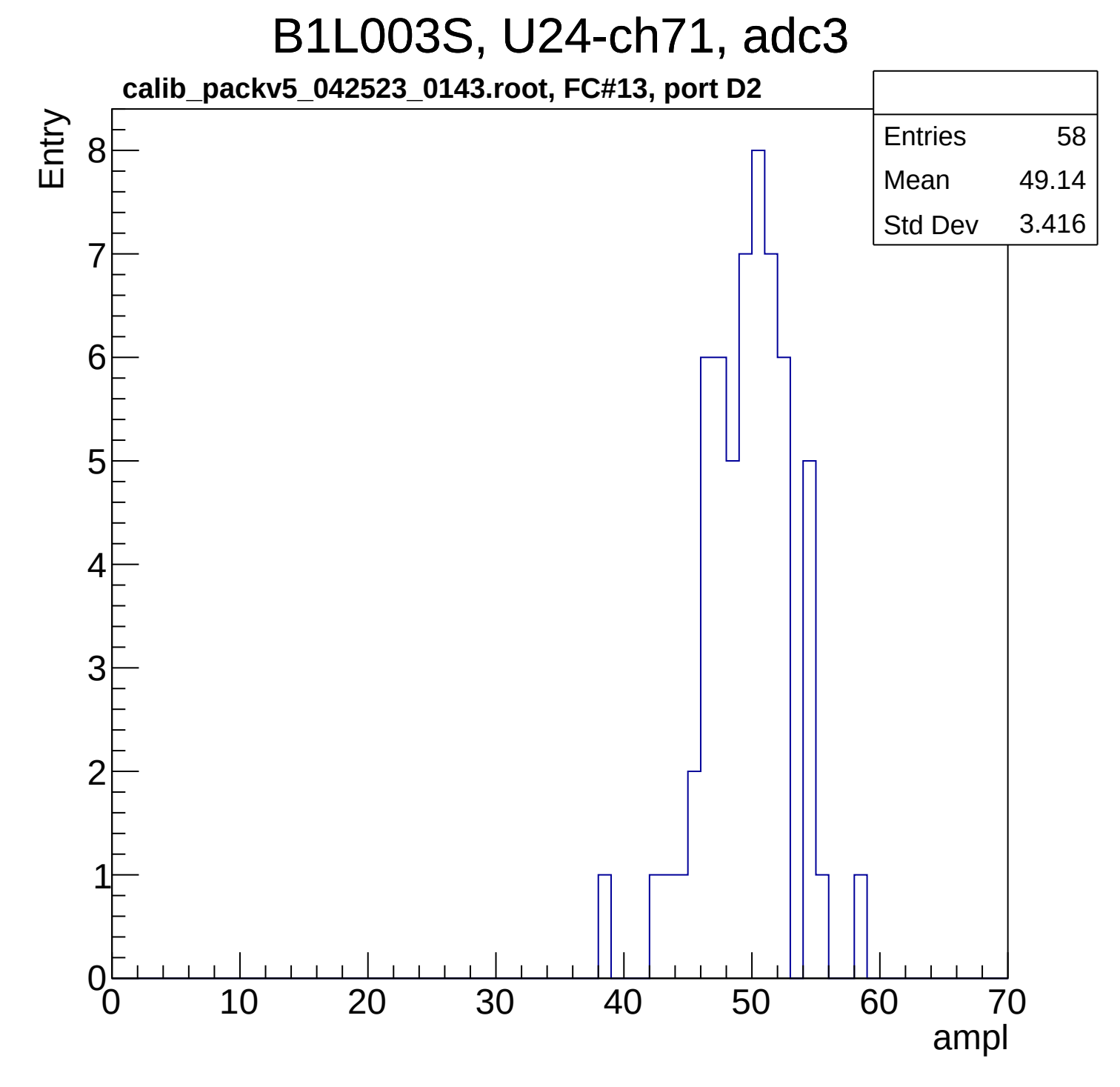
B1L003S, U24-ch71, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 49.14
Std Dev 3.416
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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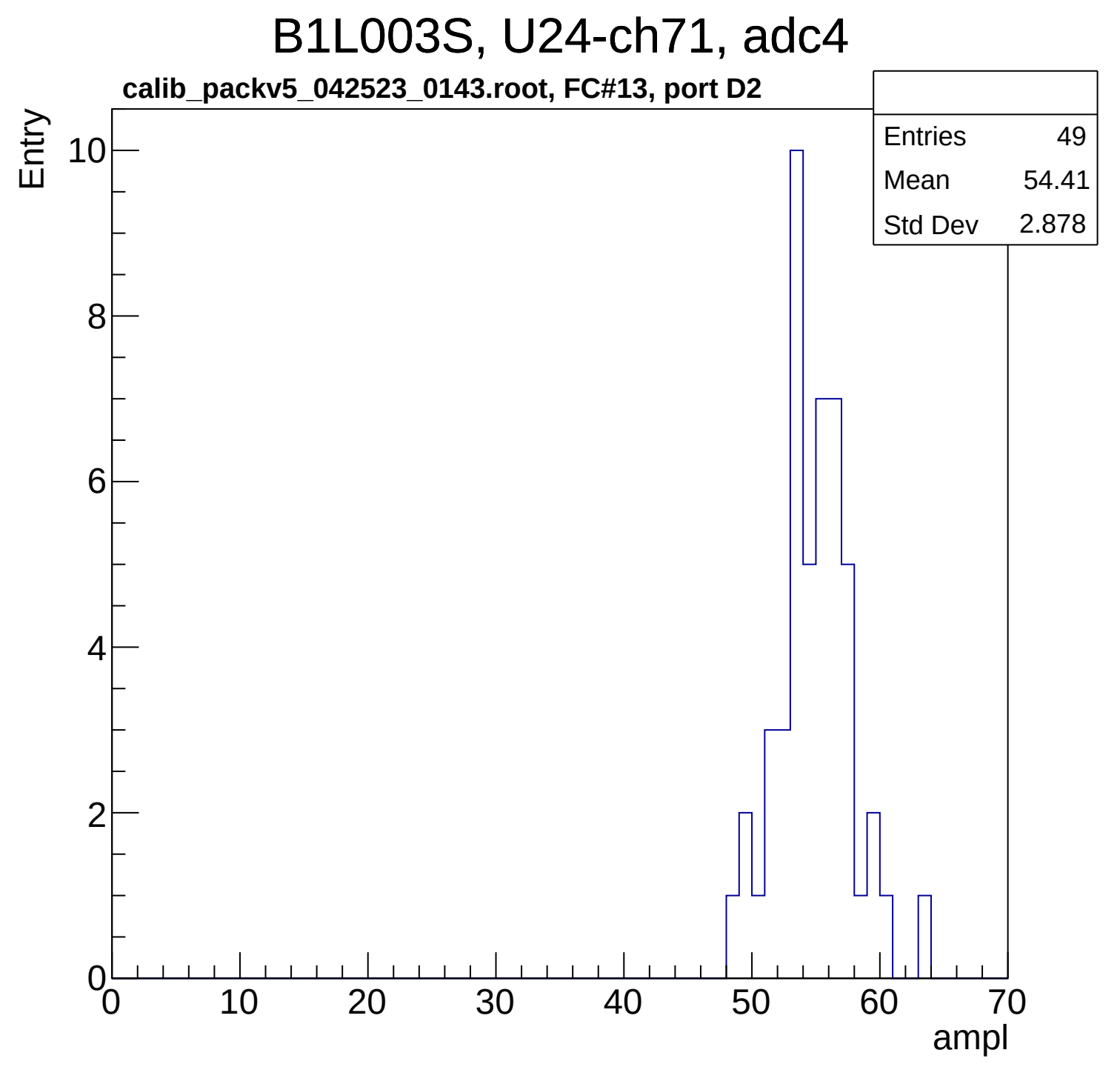
B1L003S, U24-ch71, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.41
Std Dev 2.878
Entry
ampl
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0
10
20
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40
50
60
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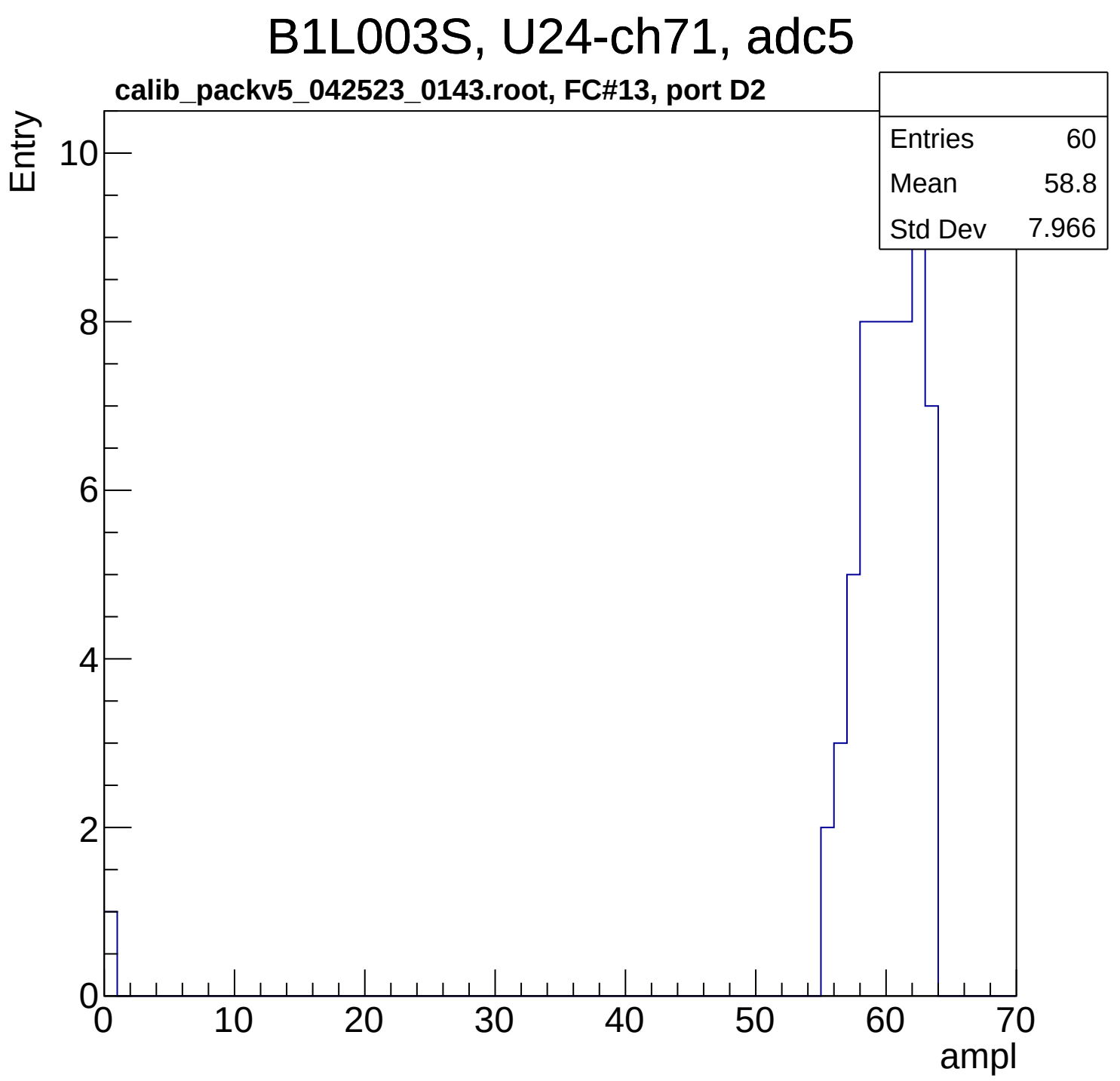
B1L003S, U24-ch71, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.8
Std Dev 7.966
Entry
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2
4
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10
0
10
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30
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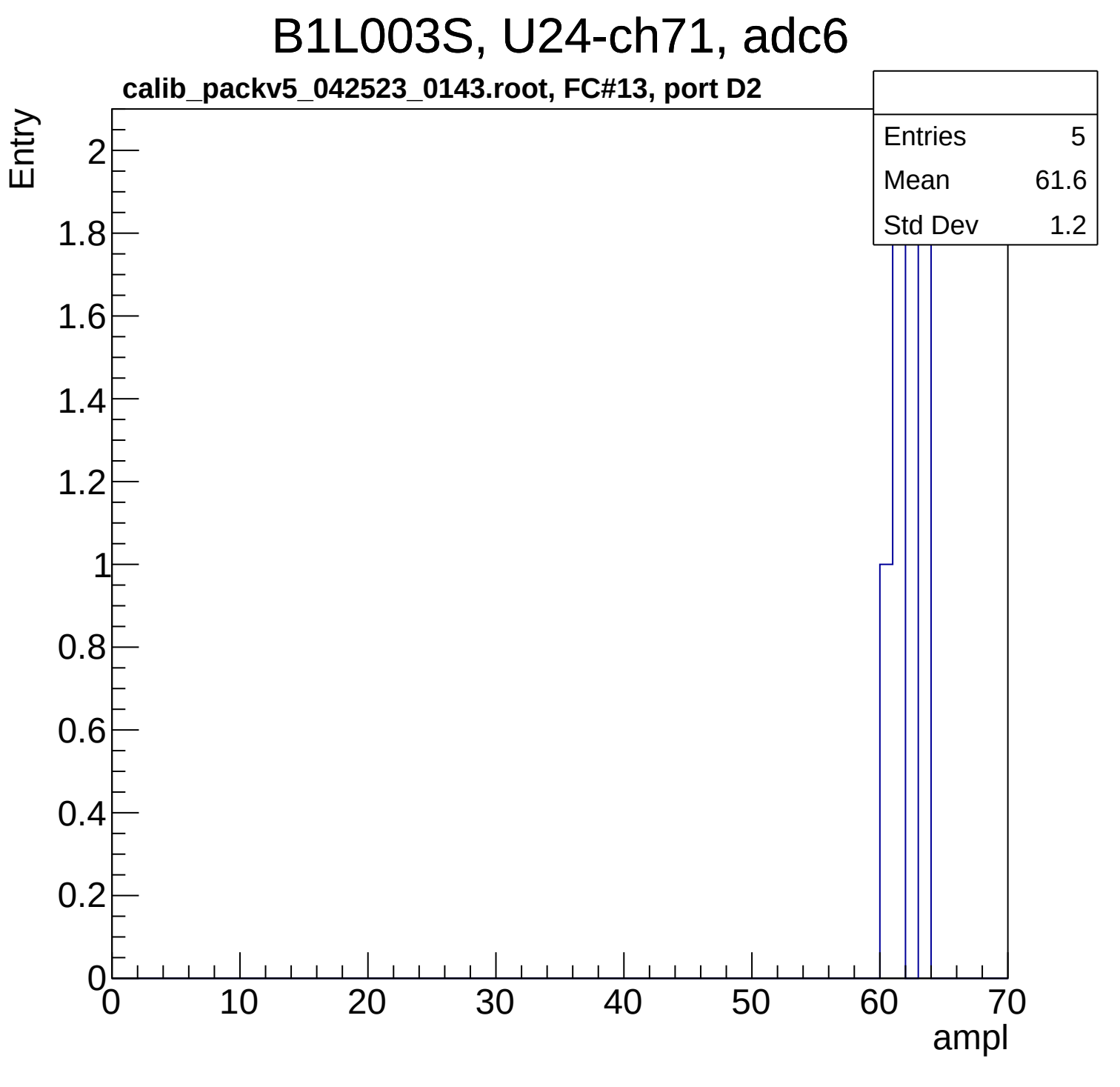
50
60
70
ampl


B1L003S, U24-ch71, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.6
Std Dev 1.2
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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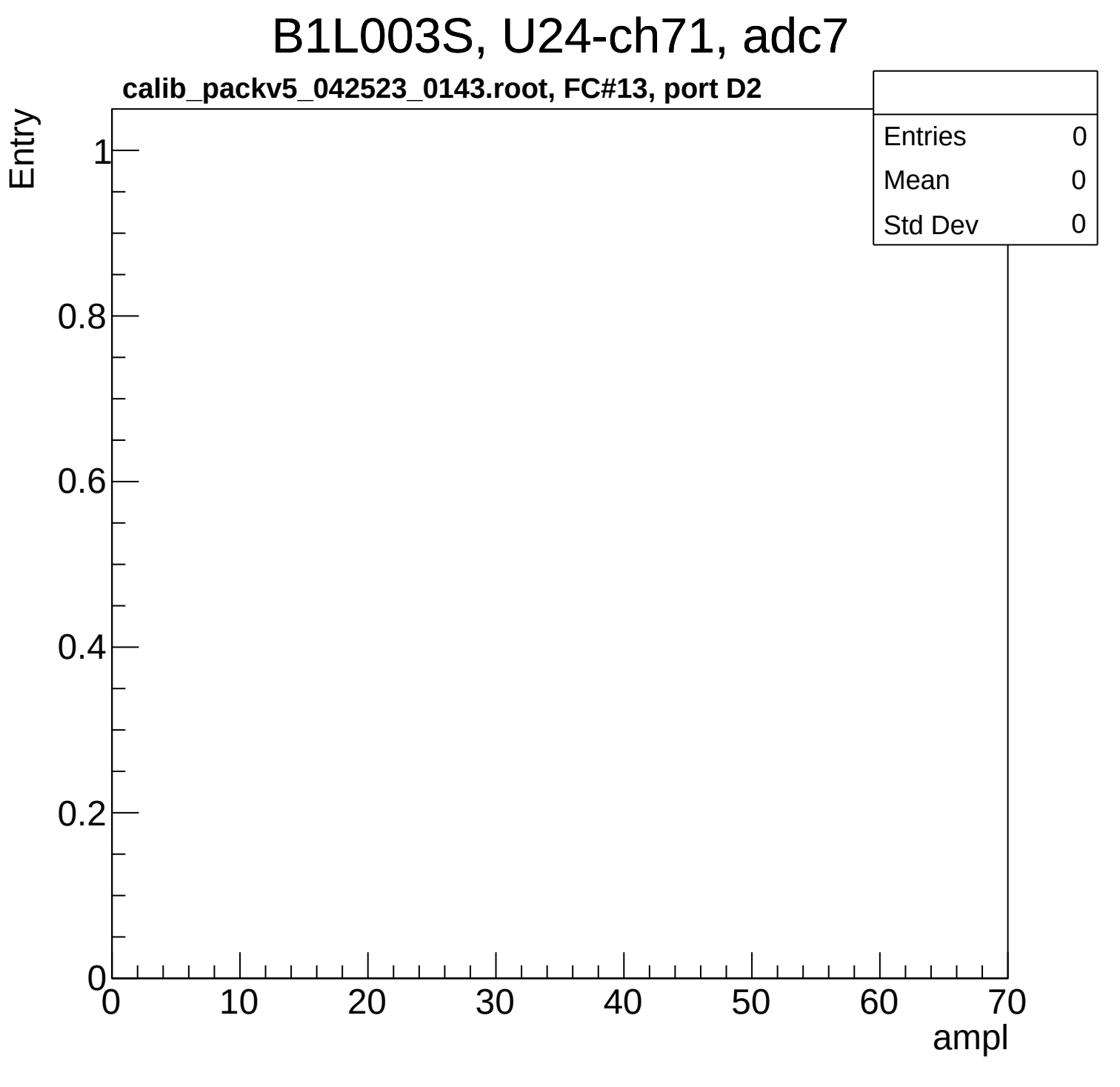
B1L003S, U24-ch71, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
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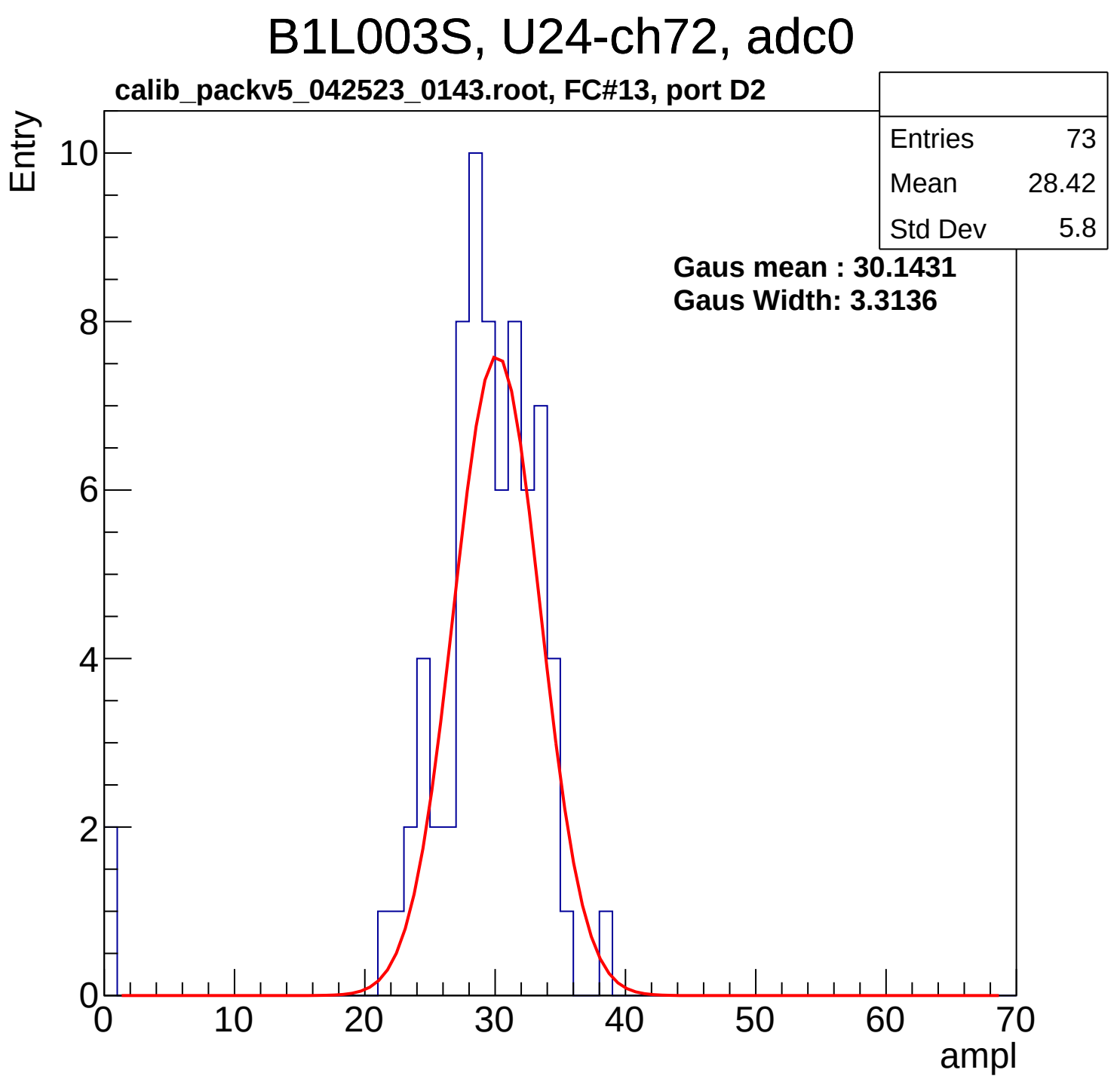
50
60
70
ampl


B1L003S, U24-ch72, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 28.42
Std Dev 5.8
Gaus mean : 30.1431
Gaus Width: 3.3136
Entry
ampl
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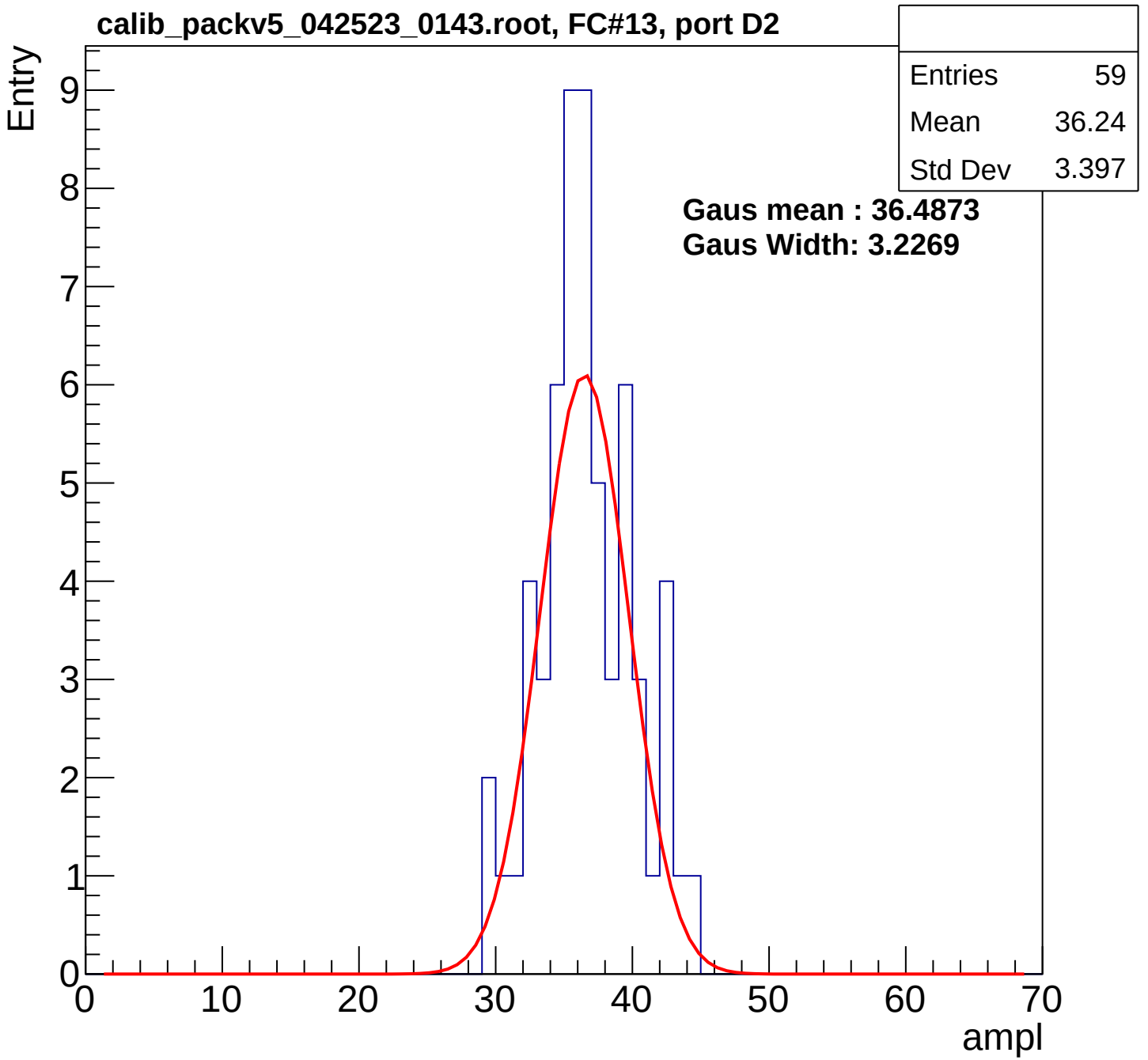
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch72, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.24
Std Dev 3.397
Gaus mean : 36.4873
Gaus Width: 3.2269
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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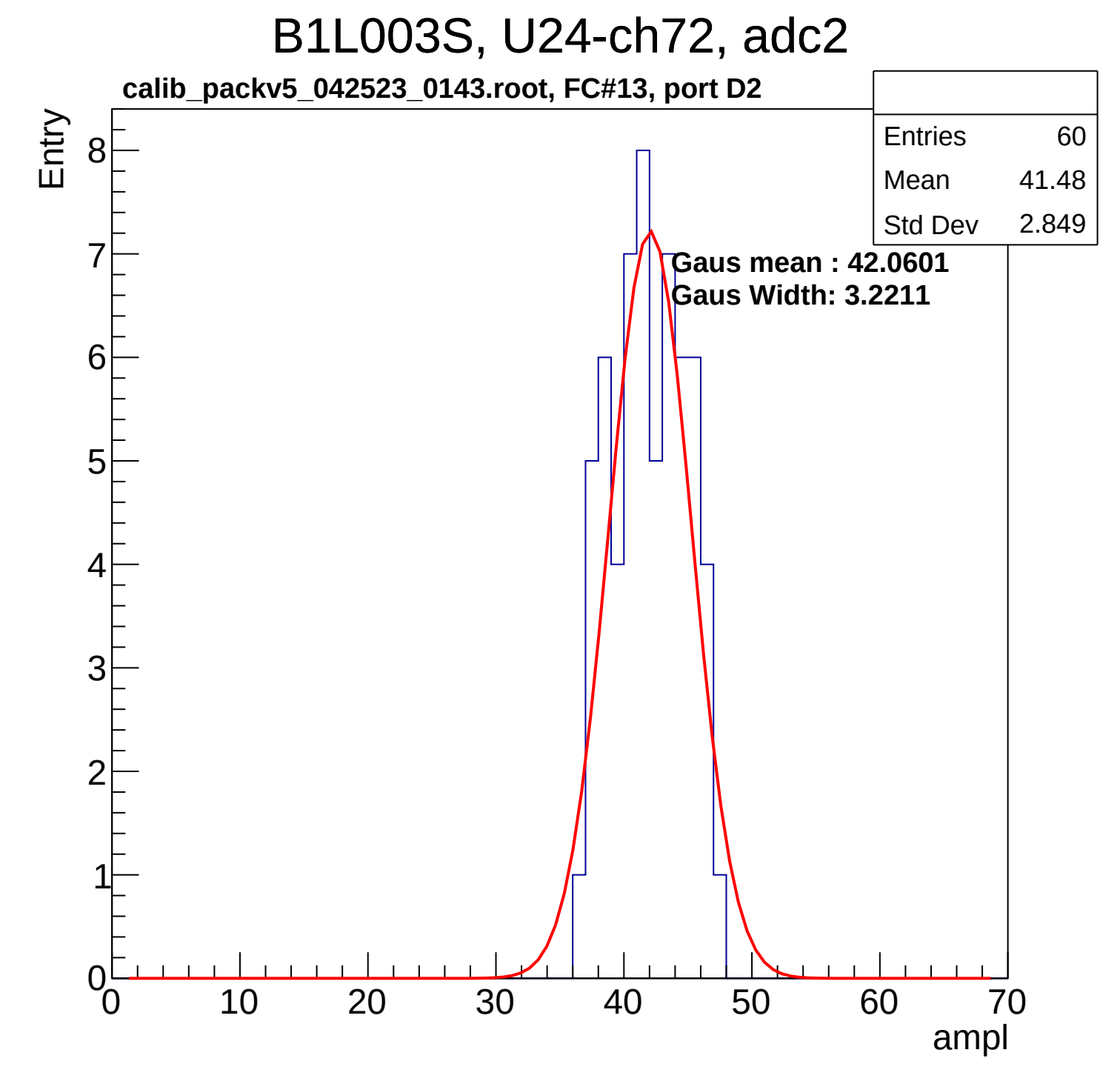
40
50
60
70


B1L003S, U24-ch72, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.48
Std Dev 2.849
Gaus mean : 42.0601
Gaus Width: 3.2211
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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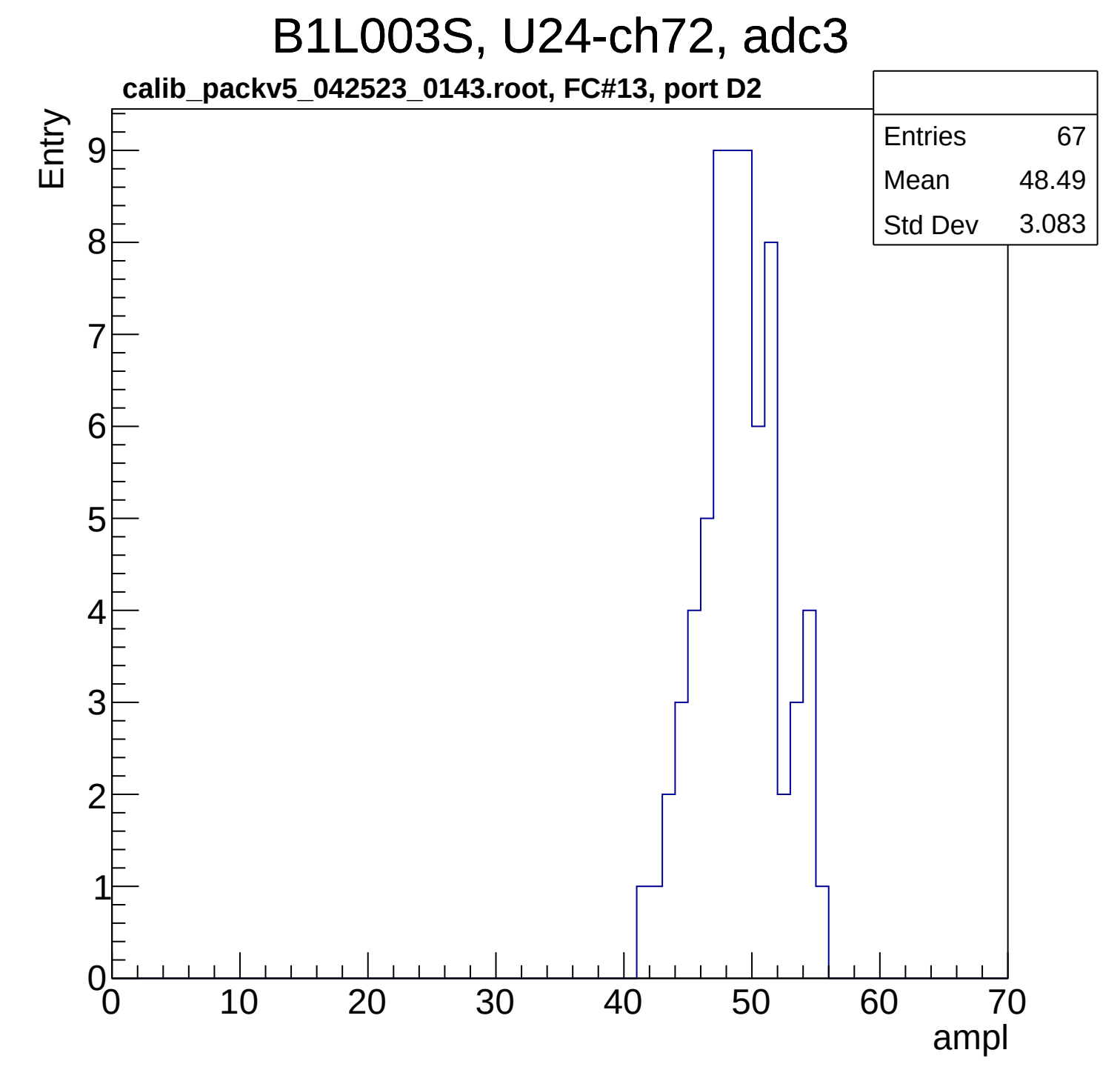
B1L003S, U24-ch72, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.49
Std Dev 3.083
Entry
ampl
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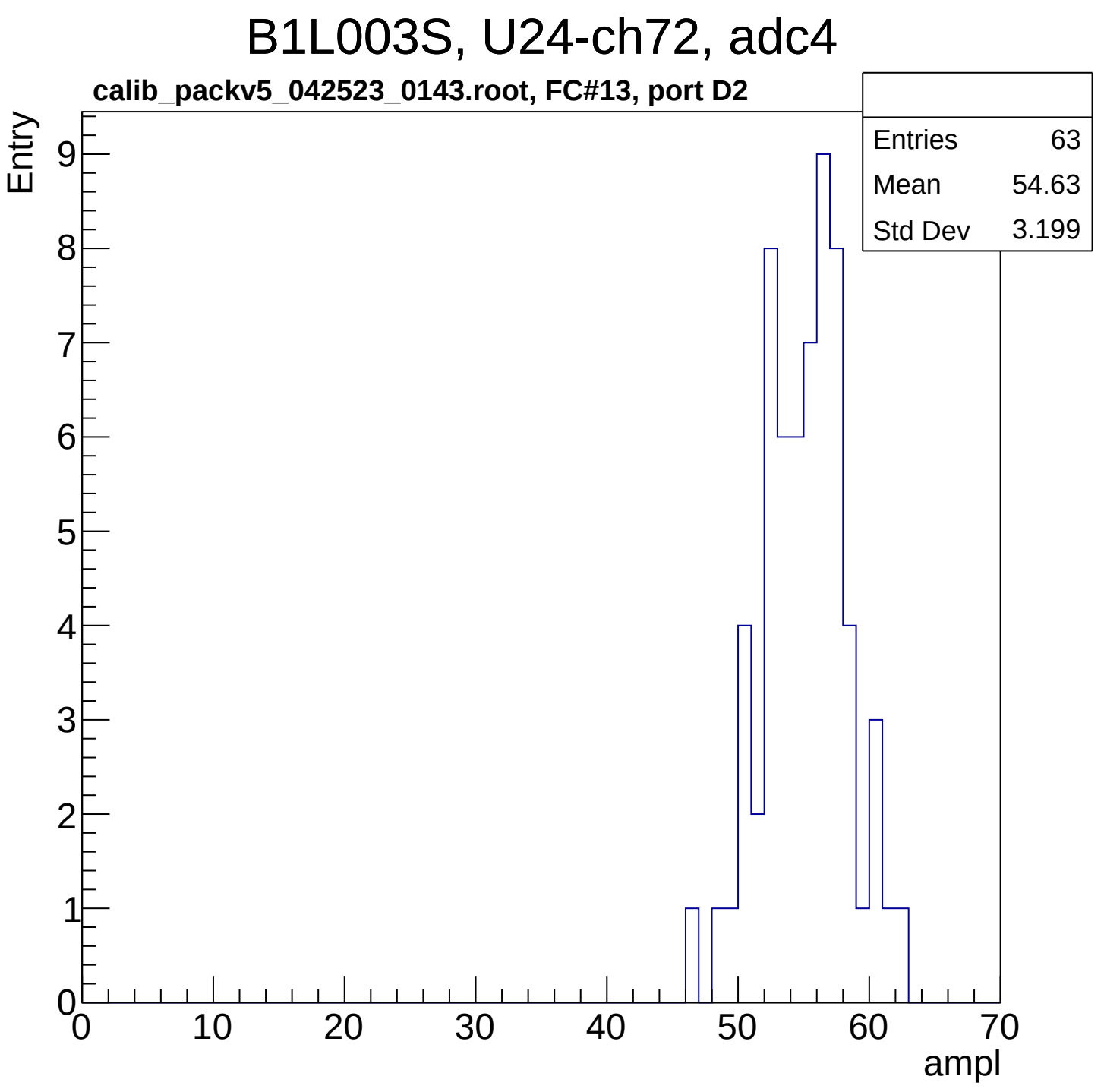
B1L003S, U24-ch72, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.63
Std Dev 3.199
Entry
ampl
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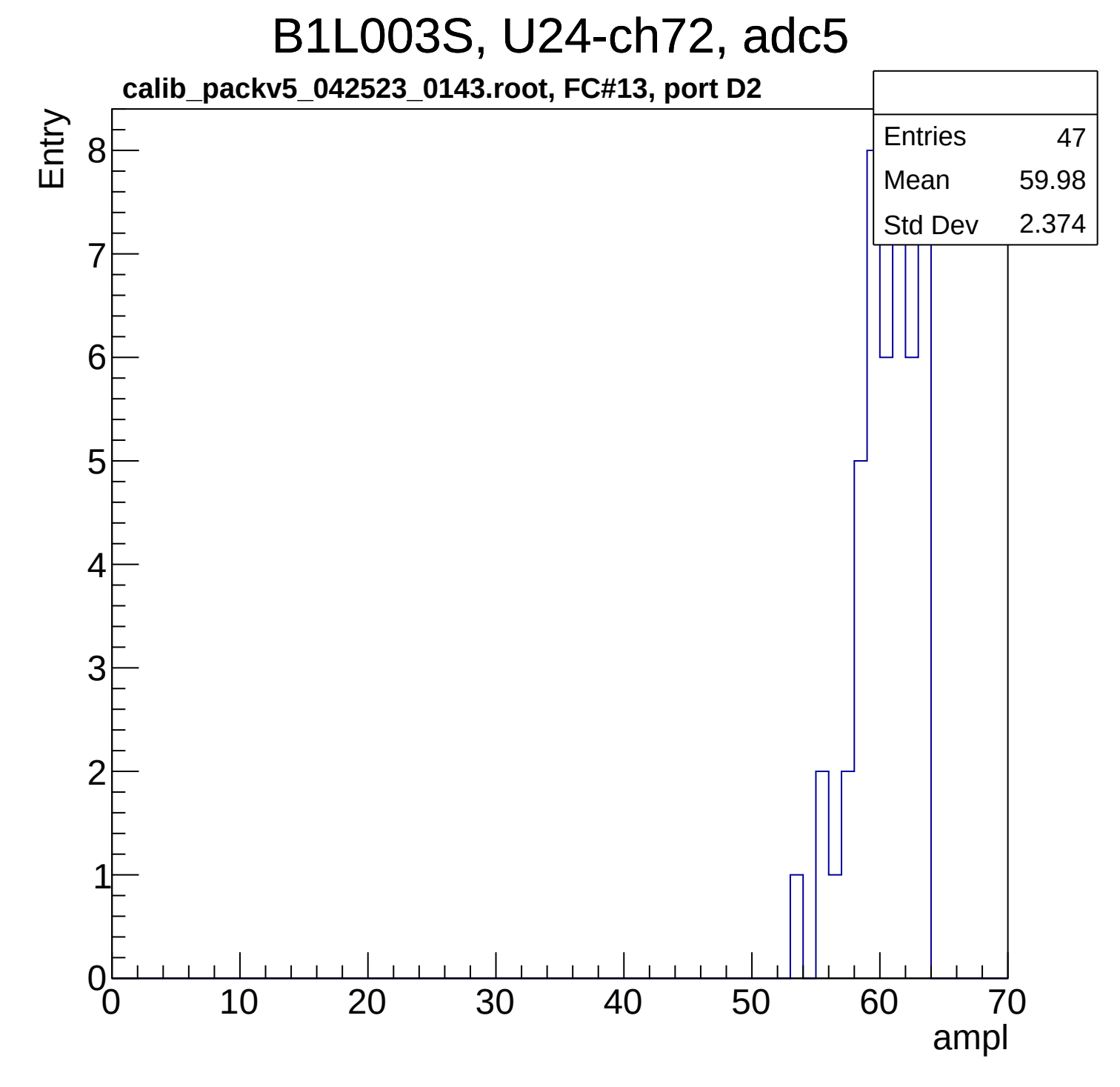
B1L003S, U24-ch72, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59.98
Std Dev 2.374
Entry
ampl
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2
3
4
5
6
7
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0
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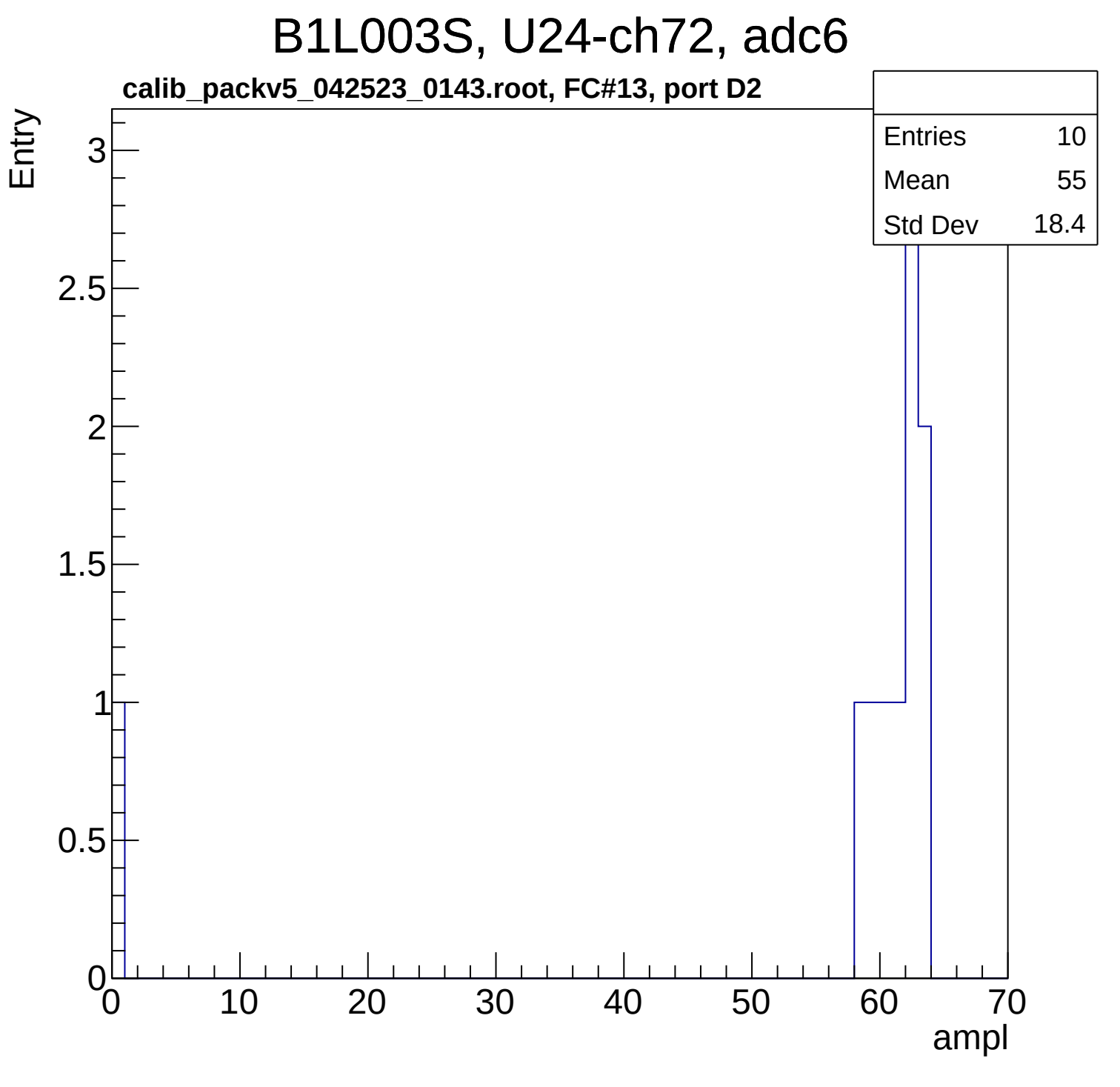
B1L003S, U24-ch72, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55
Std Dev 18.4
Entry
ampl
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1
1.5
2
2.5
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0
10
20
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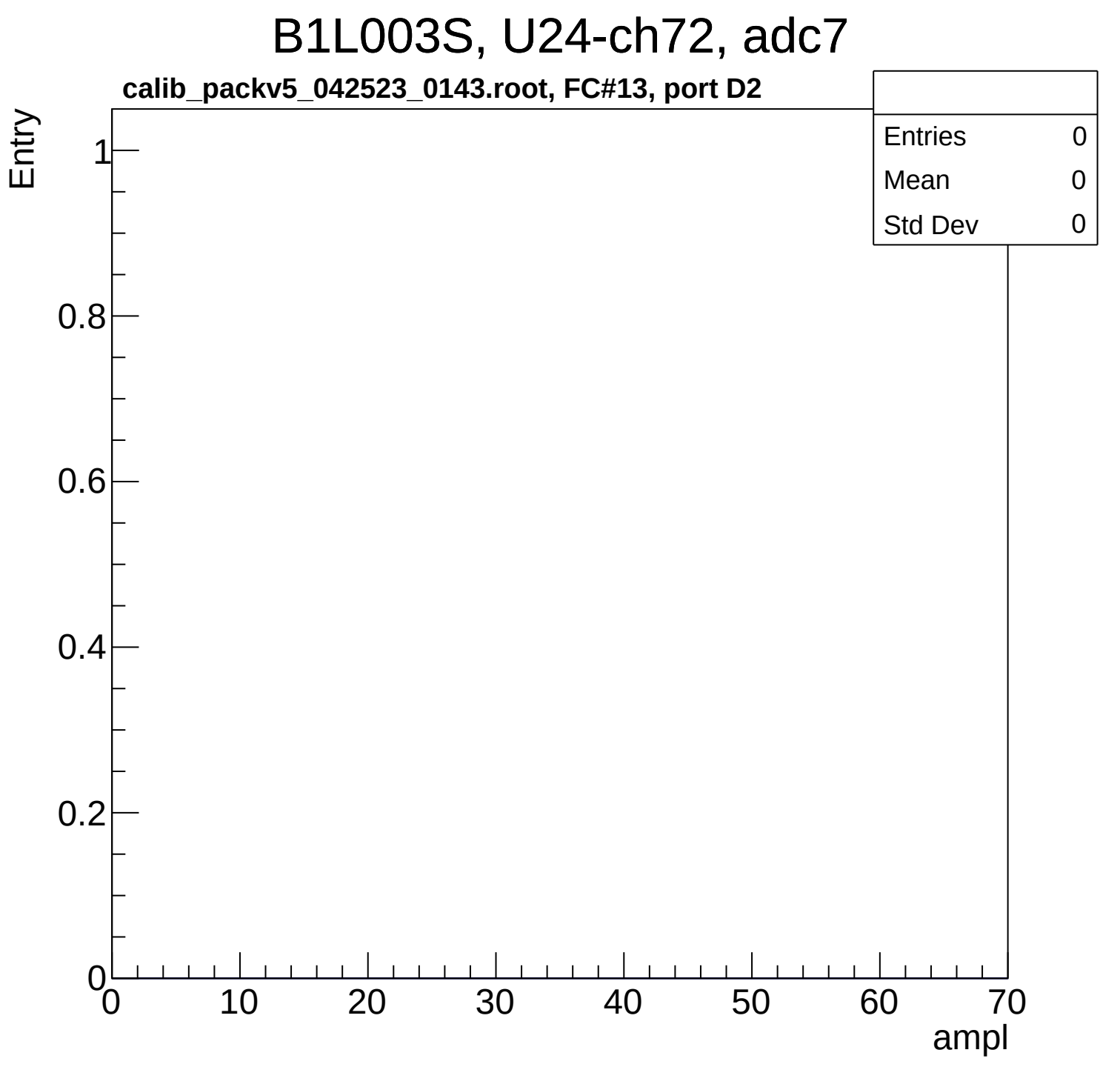
40
50
60
70


B1L003S, U24-ch72, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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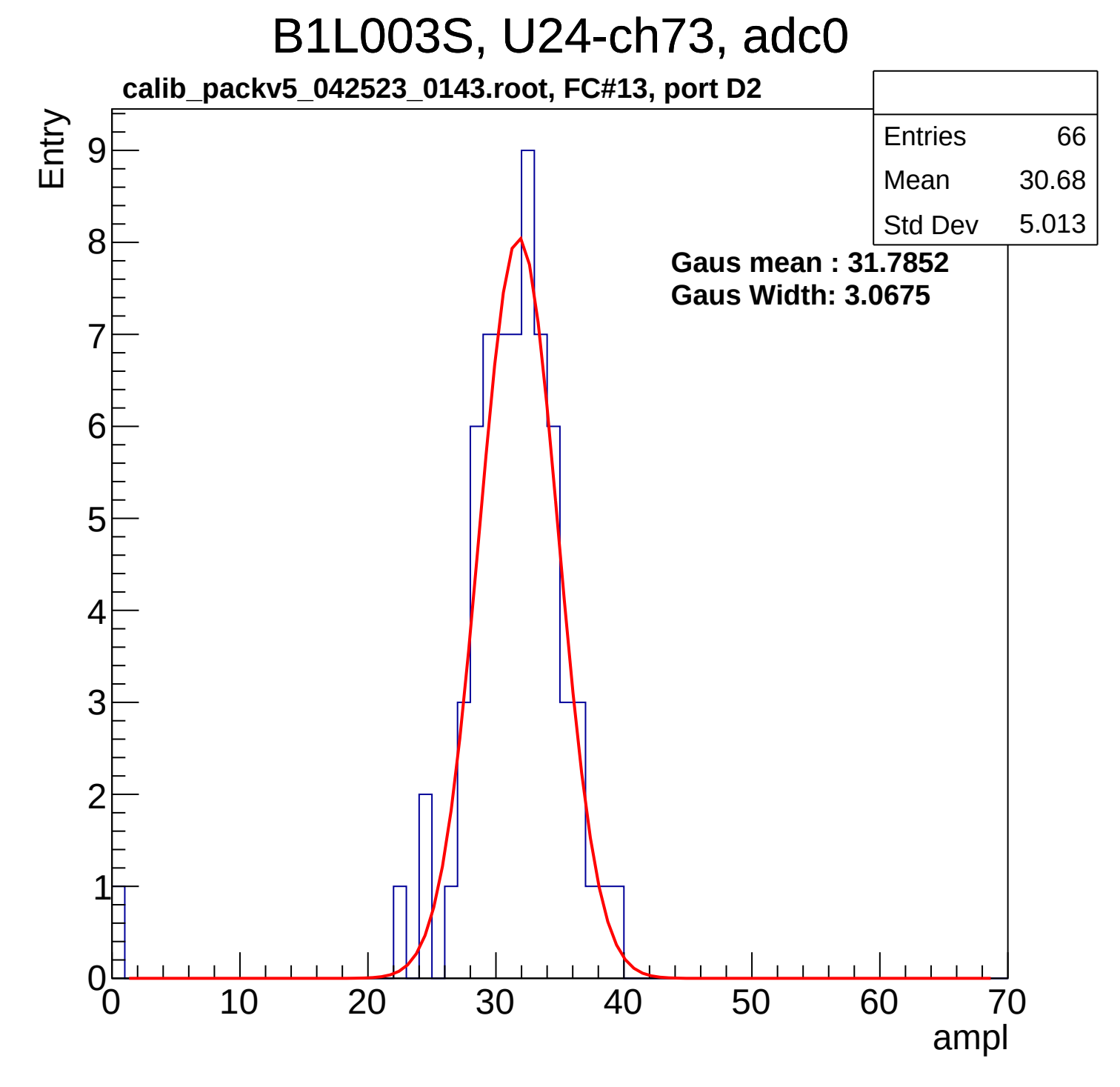
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch73, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.68
Std Dev 5.013
Gaus mean : 31.7852
Gaus Width: 3.0675
Entry
ampl
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3
4
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6
7
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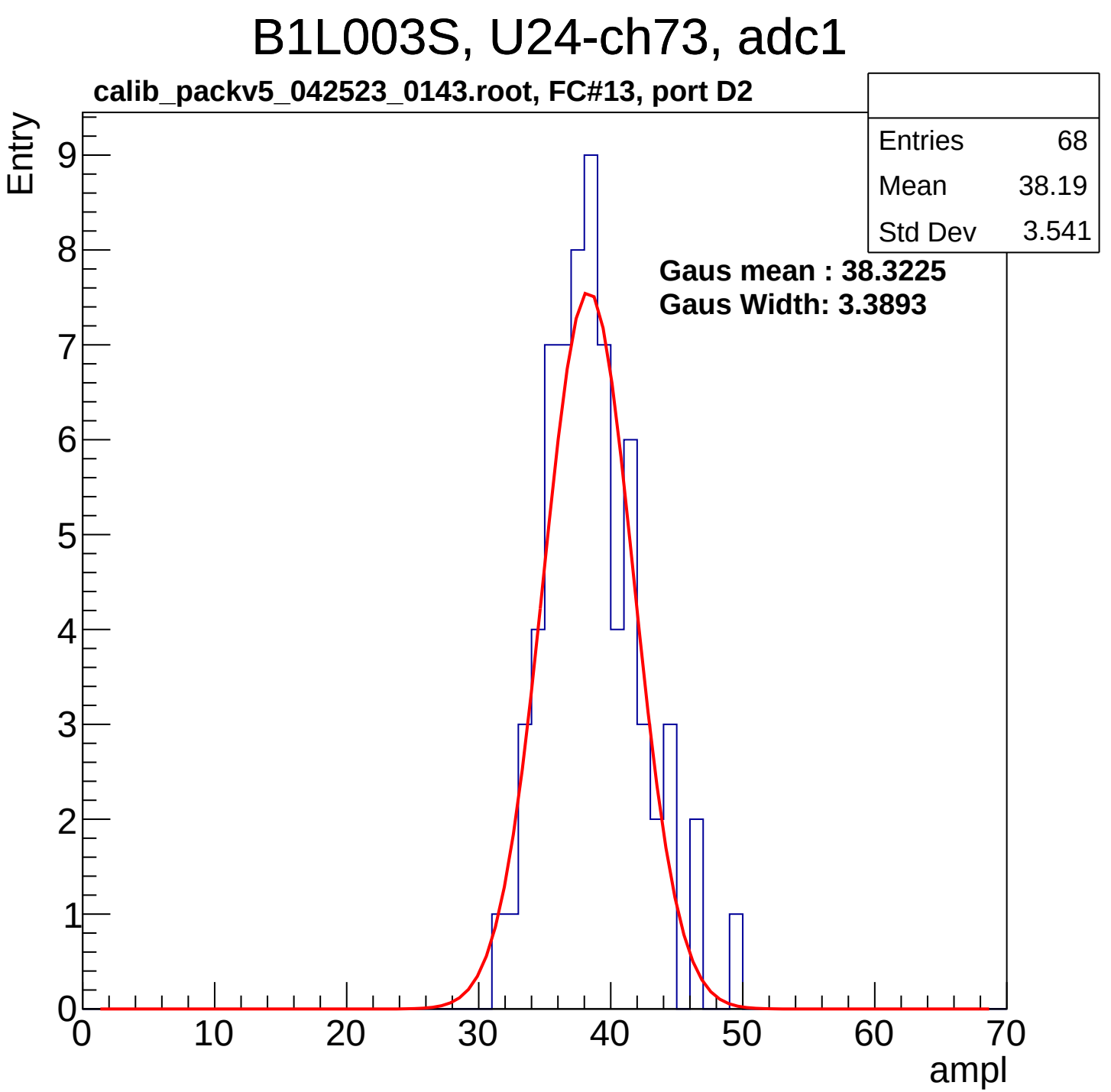
B1L003S, U24-ch73, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 38.19
Std Dev 3.541
Gaus mean : 38.3225
Gaus Width: 3.3893
Entry
ampl
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3
4
5
6
7
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9
0
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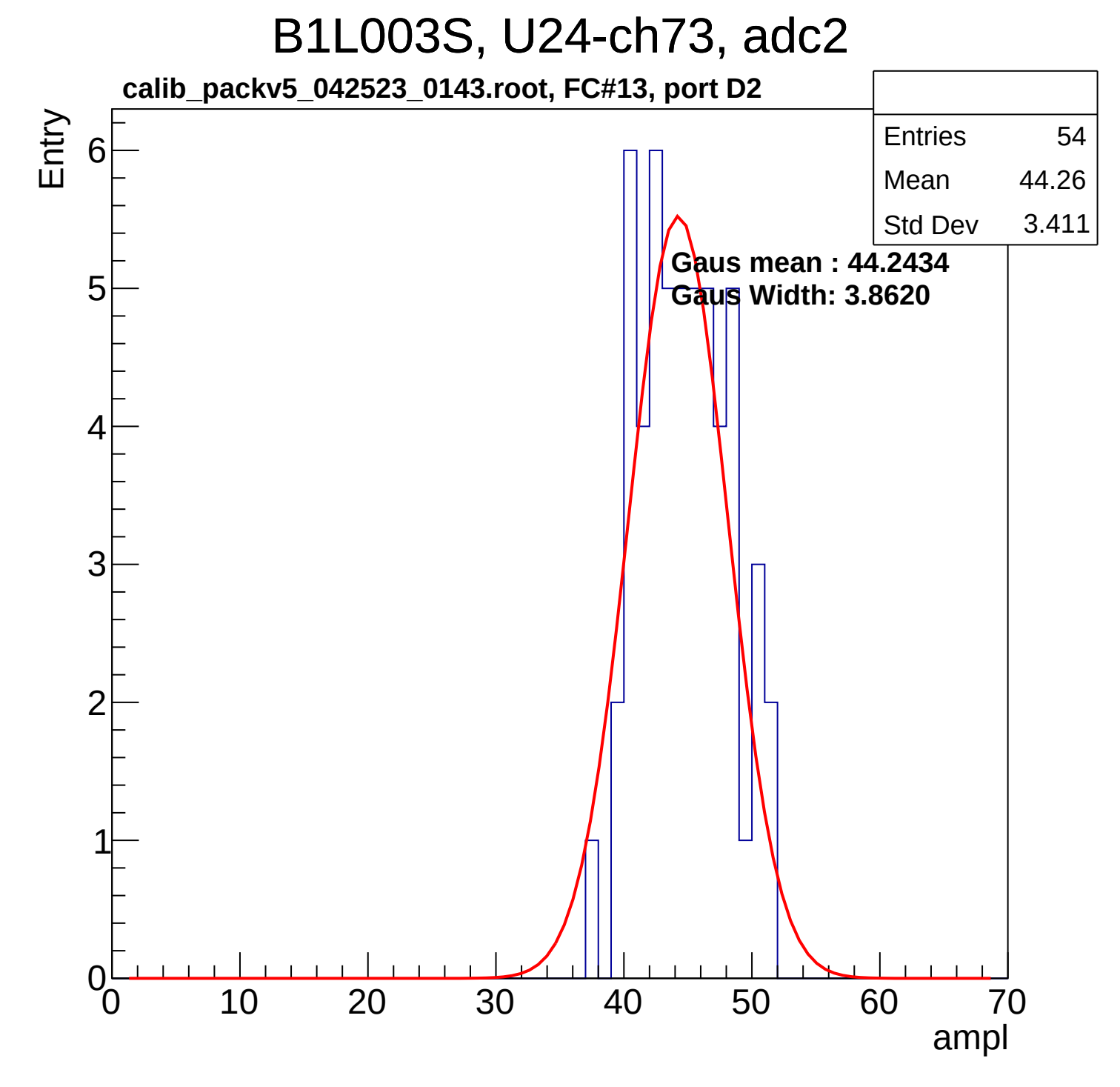
B1L003S, U24-ch73, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 44.26
Std Dev 3.411
Gaus mean : 44.2434
Gaus Width: 3.8620
Entry
ampl
0
1
2
3
4
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6
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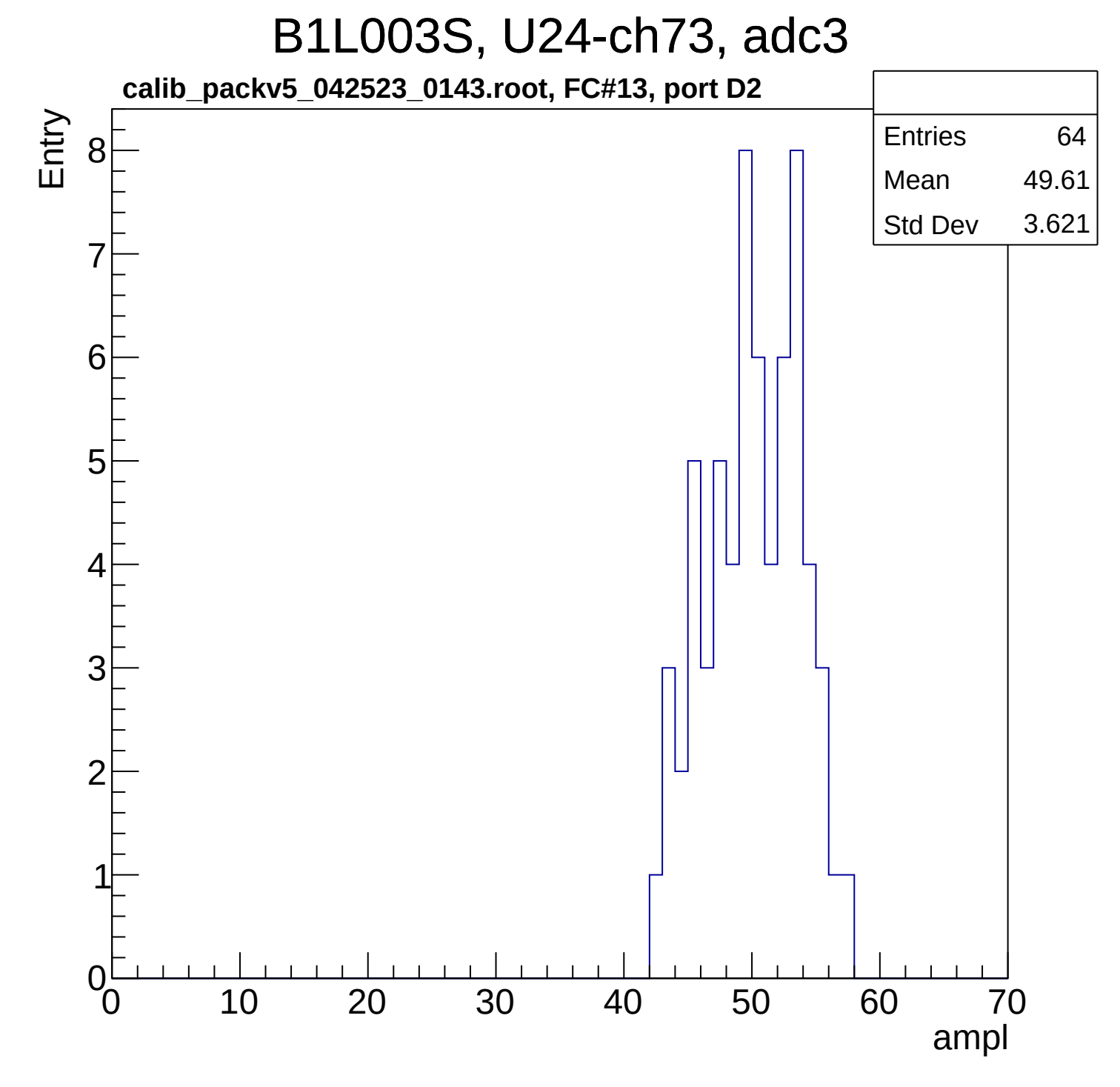
B1L003S, U24-ch73, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49.61
Std Dev 3.621
Entry
ampl
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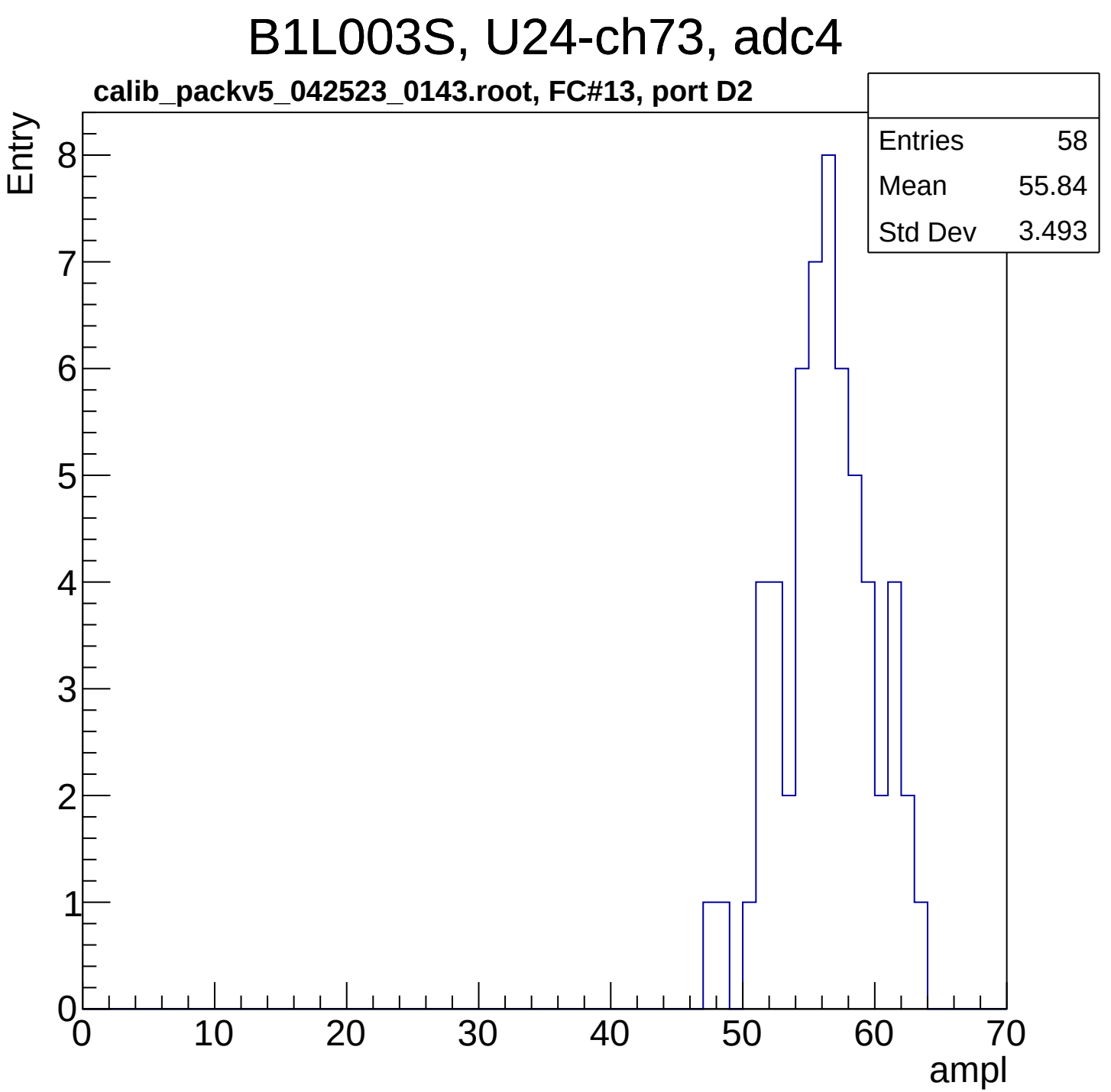
B1L003S, U24-ch73, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.84
Std Dev 3.493
Entry
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50
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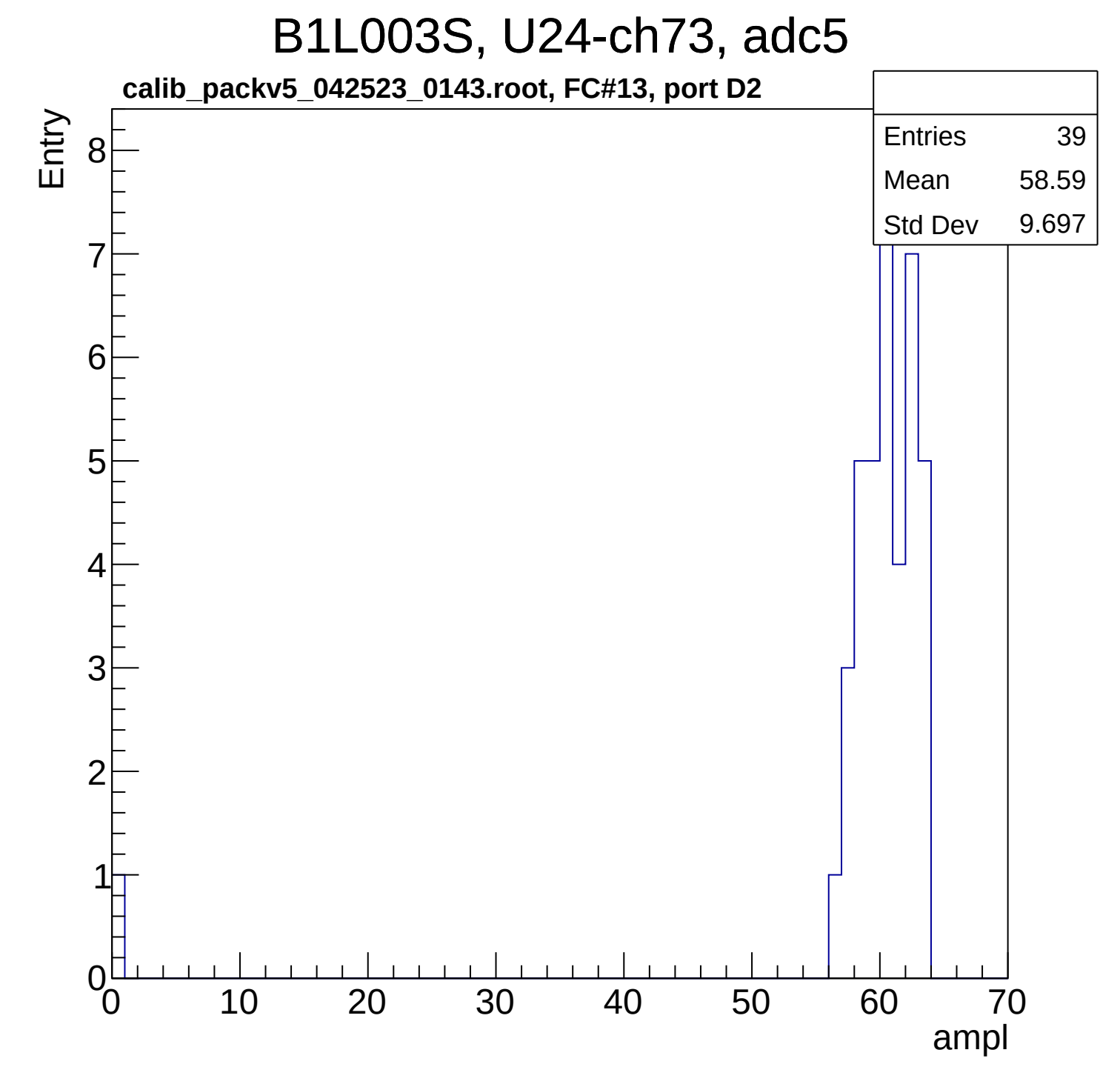
70
ampl


B1L003S, U24-ch73, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 58.59
Std Dev 9.697
Entry
ampl
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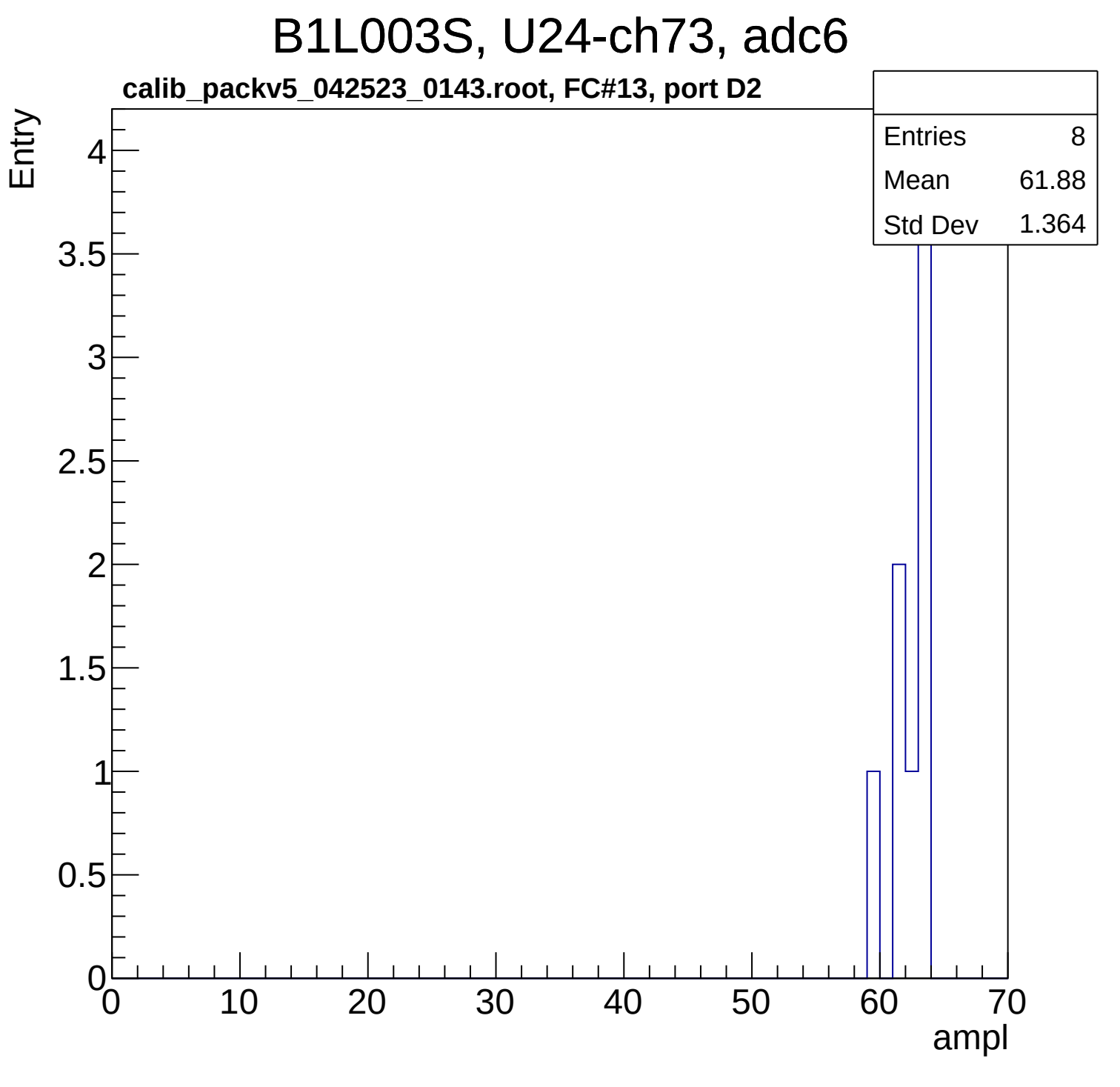
B1L003S, U24-ch73, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.88
Std Dev 1.364
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
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0
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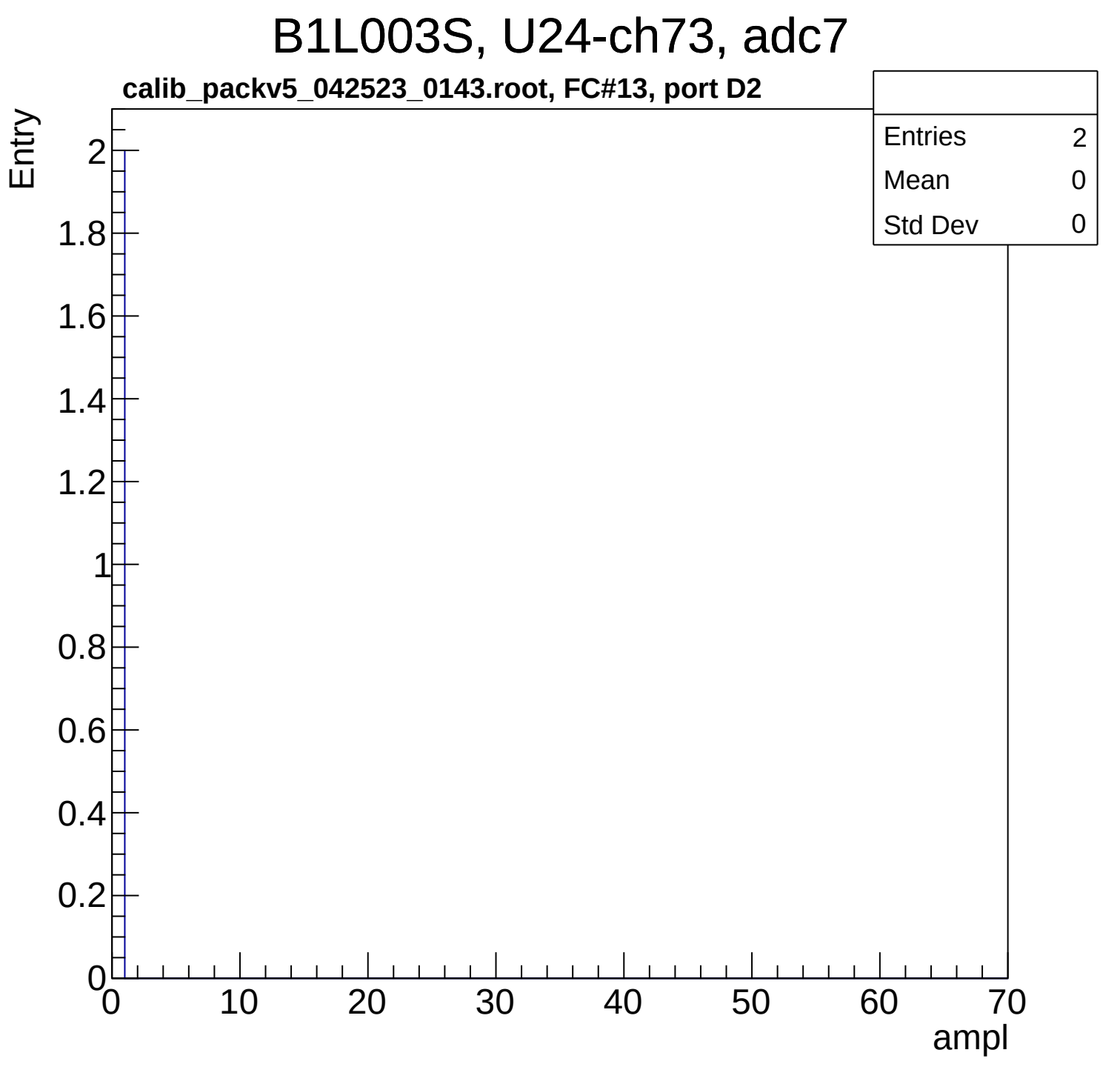
B1L003S, U24-ch73, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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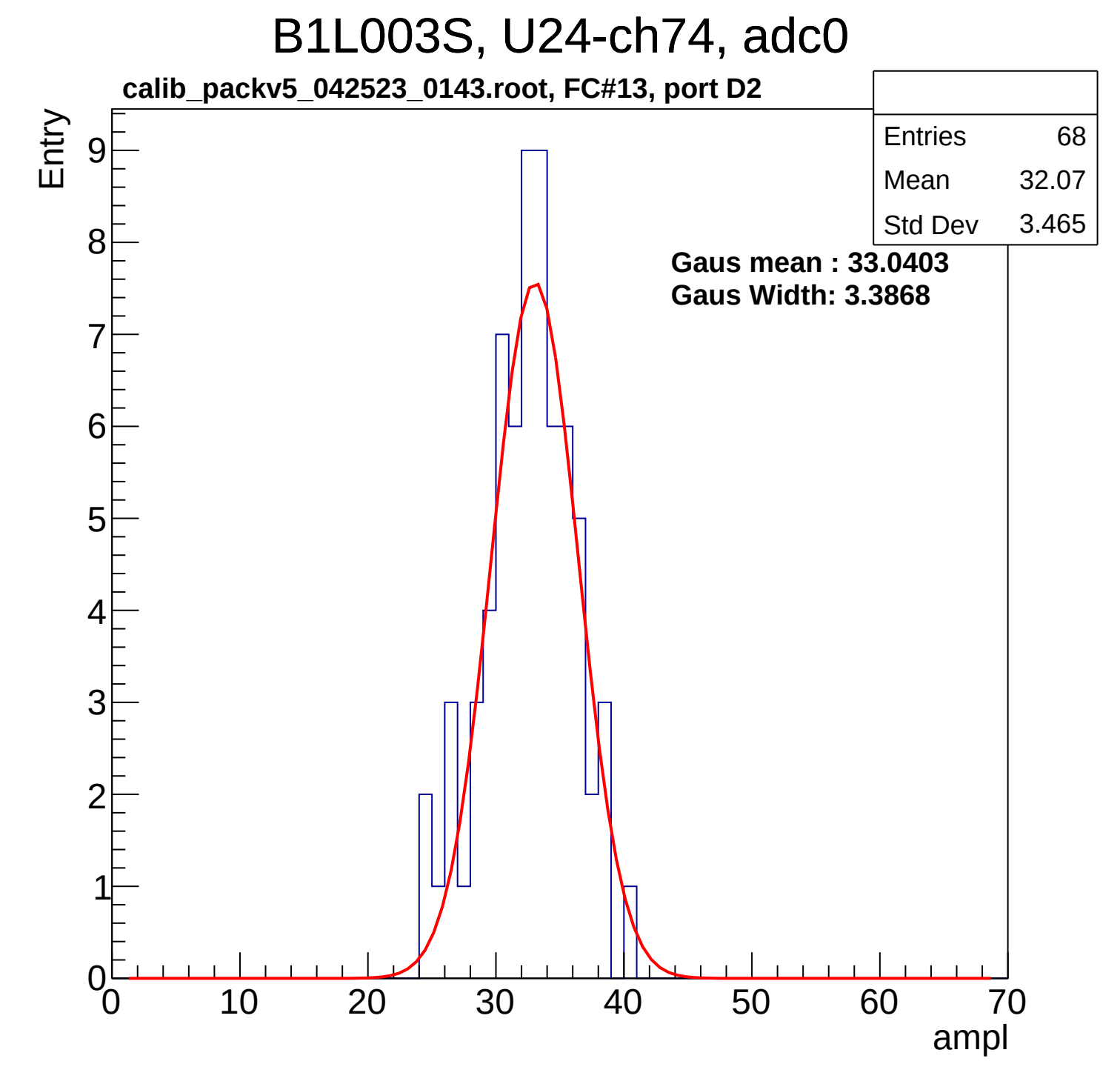
B1L003S, U24-ch74, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 32.07
Std Dev 3.465
Gaus mean : 33.0403
Gaus Width: 3.3868
Entry
ampl
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2
3
4
5
6
7
8
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0
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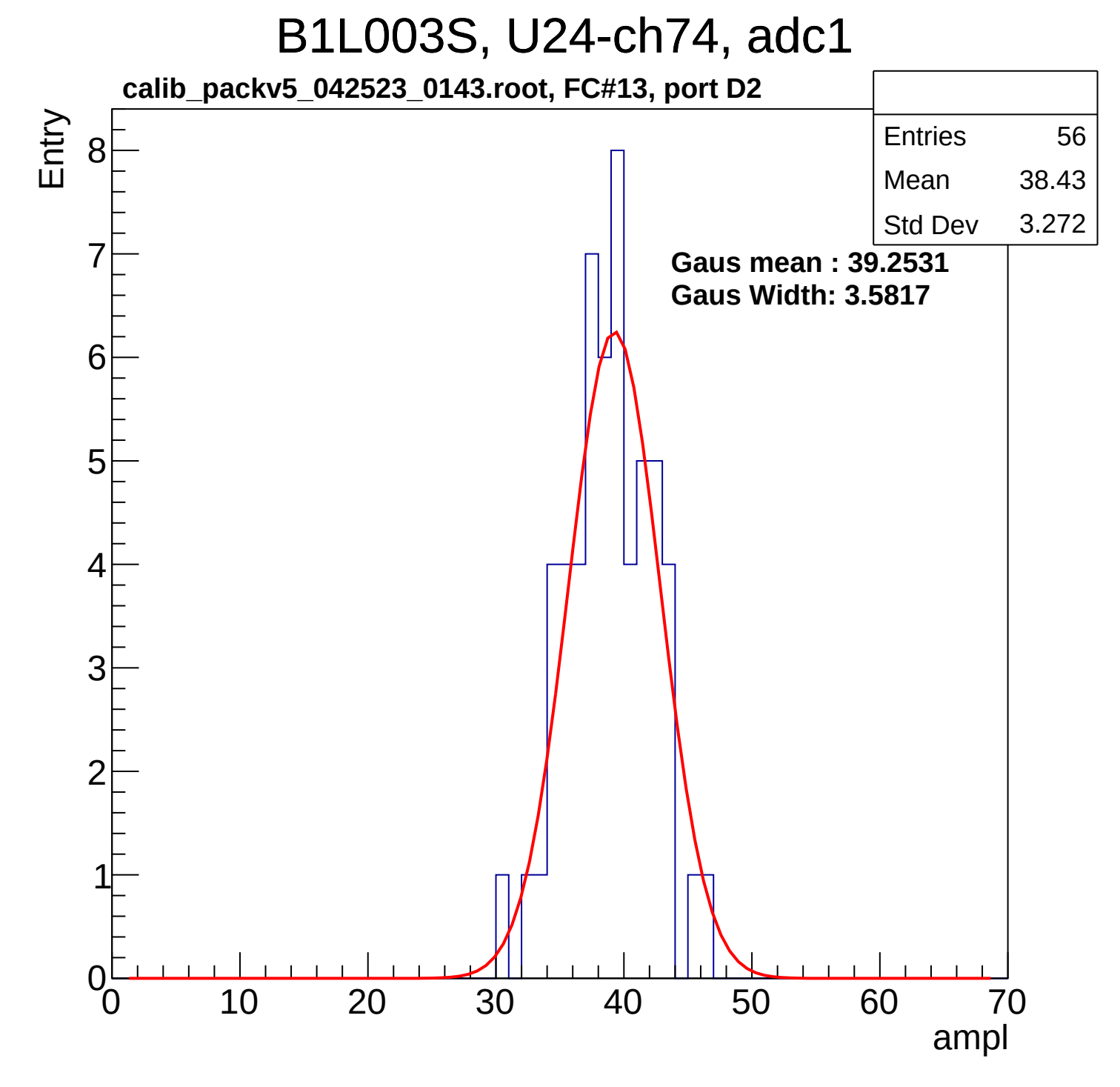
B1L003S, U24-ch74, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 38.43
Std Dev 3.272
Gaus mean : 39.2531
Gaus Width: 3.5817
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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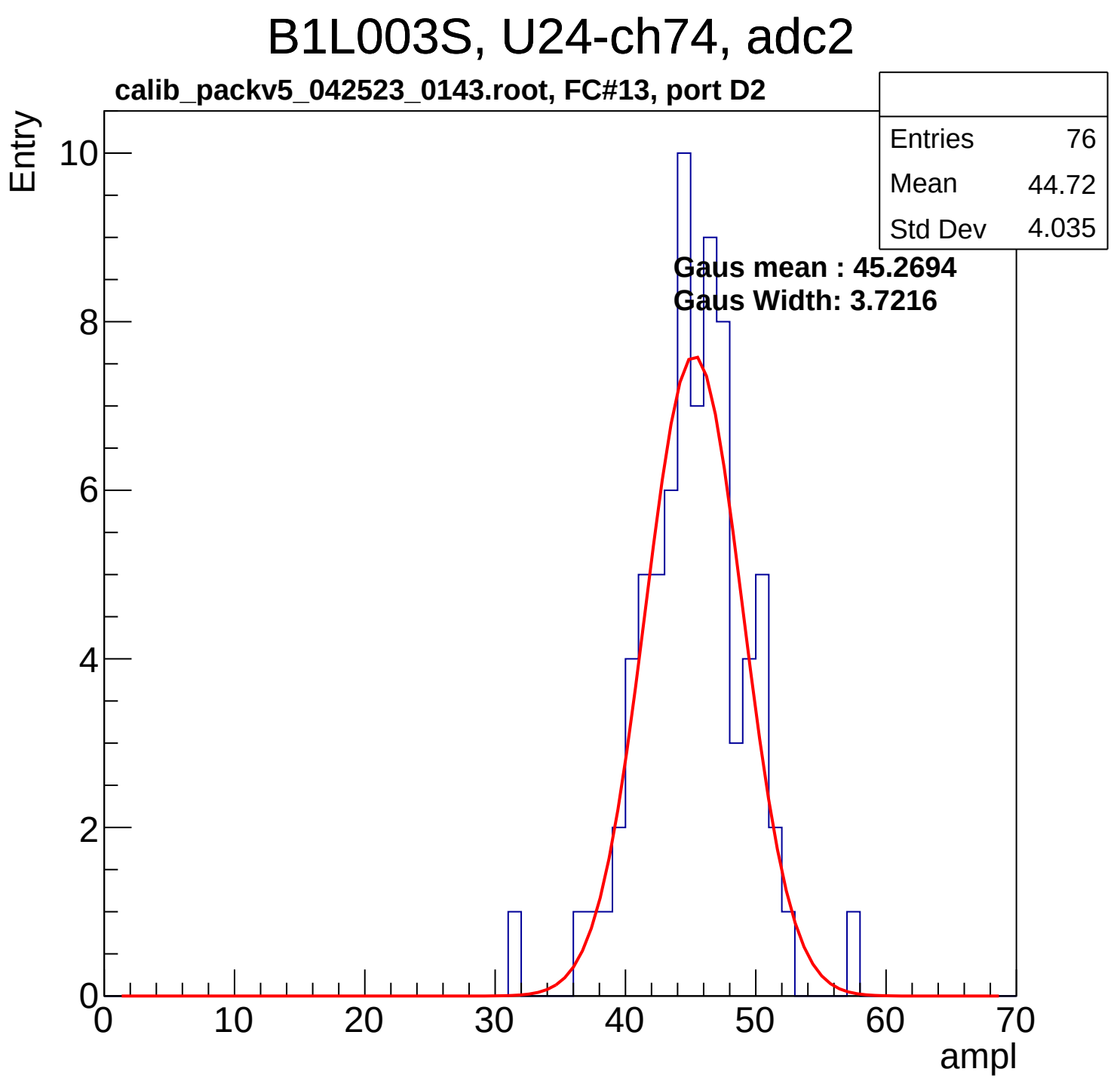
B1L003S, U24-ch74, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 44.72
Std Dev 4.035
Gaus mean : 45.2694
Gaus Width: 3.7216
Entry
ampl
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8
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0
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40
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60
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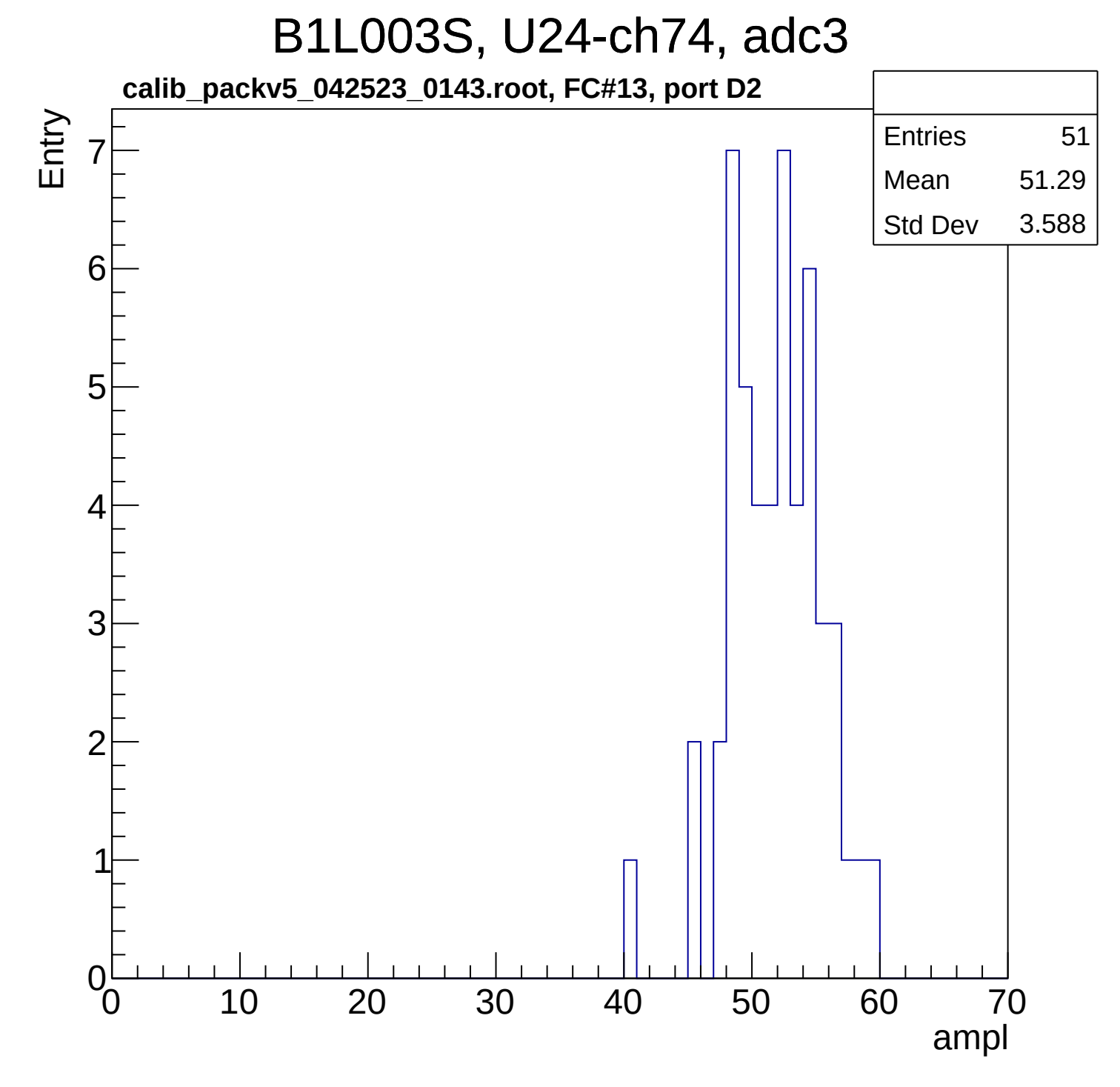
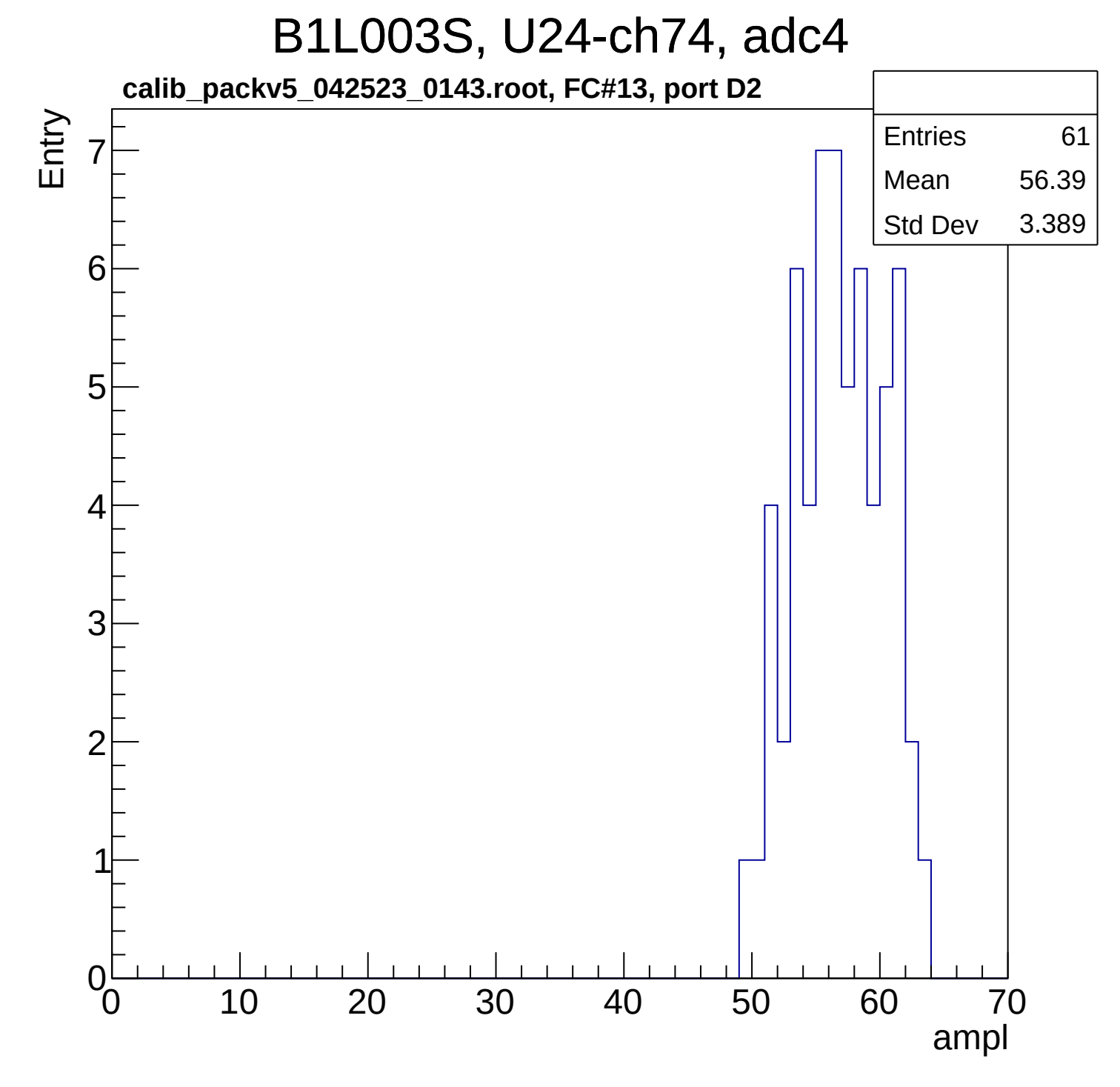

B1L003S, U24-ch74, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 51.29
Std Dev 3.588
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch74, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 56.39
Std Dev 3.389
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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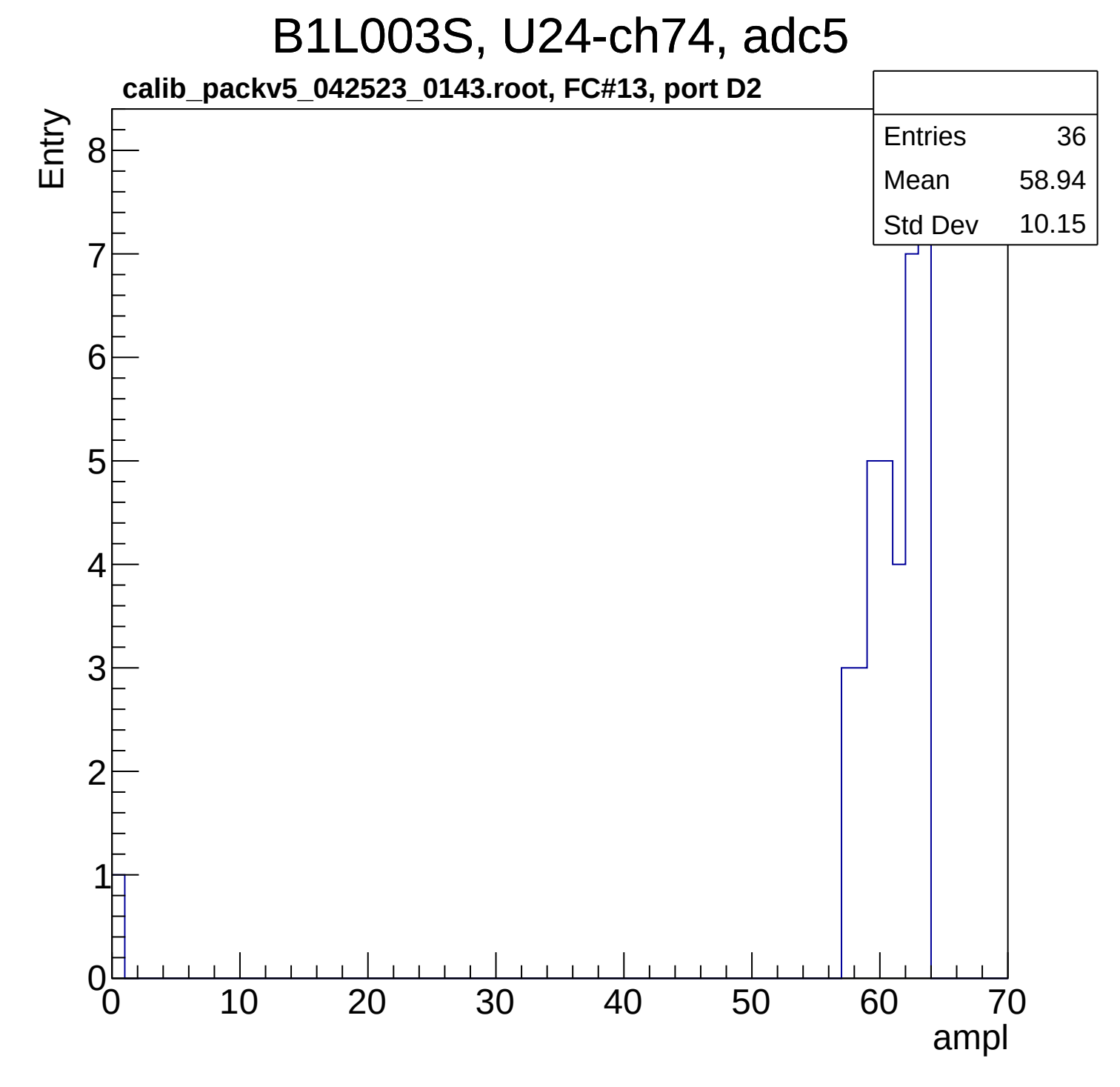
B1L003S, U24-ch74, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.94
Std Dev 10.15
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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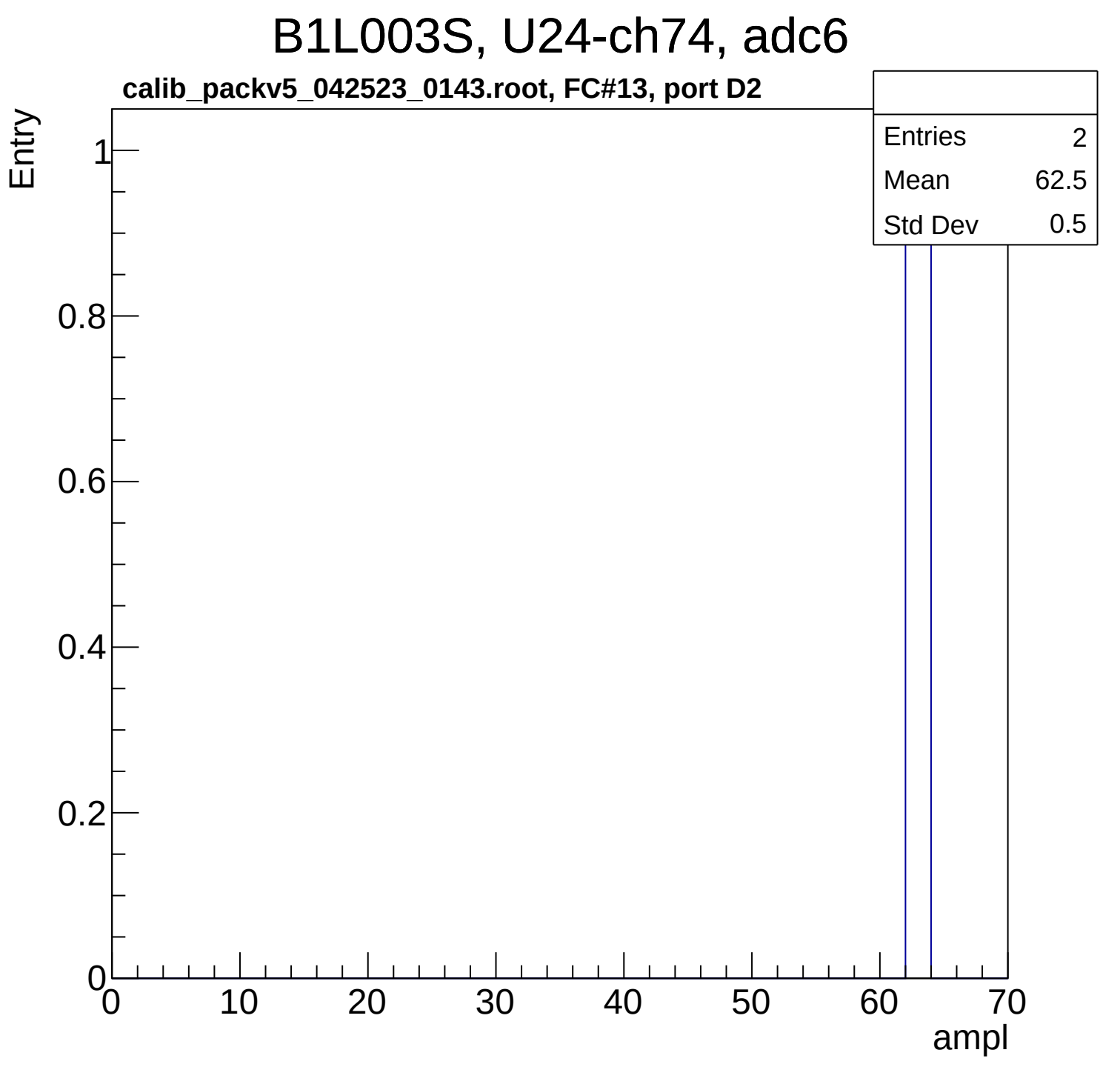
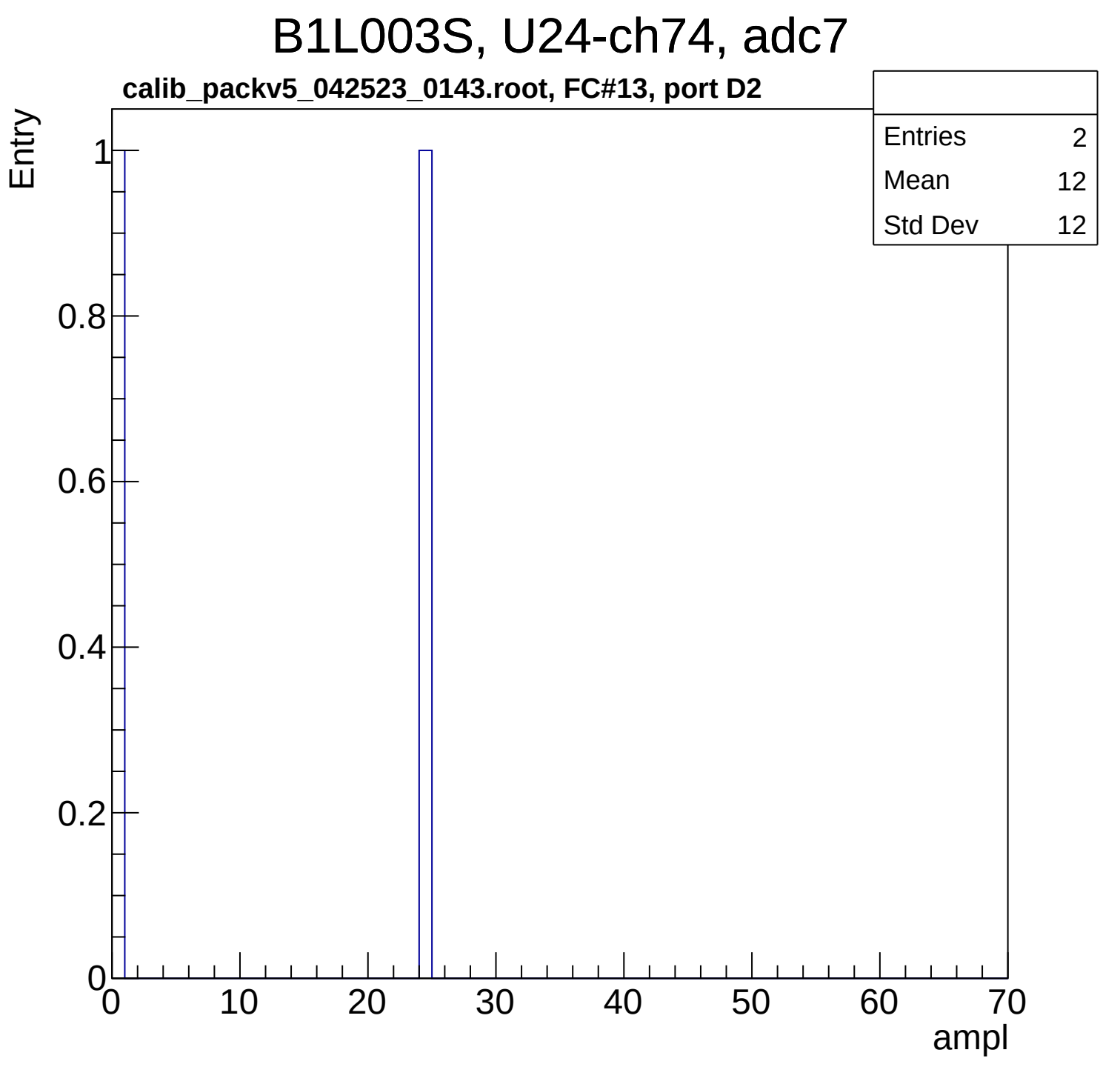
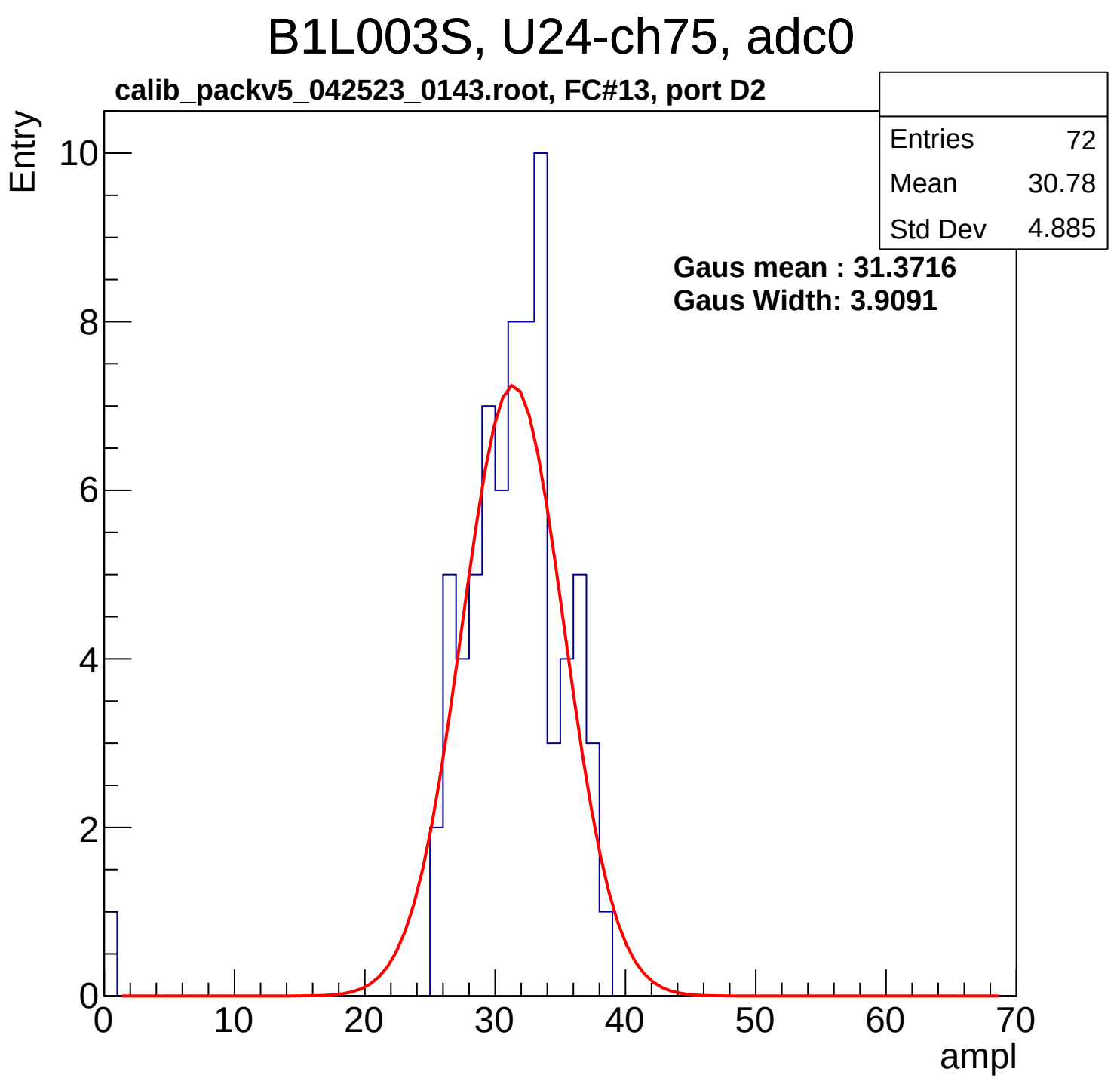

B1L003S, U24-ch74, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch74, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch75, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 30.78
Std Dev 4.885
Gaus mean : 31.3716
Gaus Width: 3.9091
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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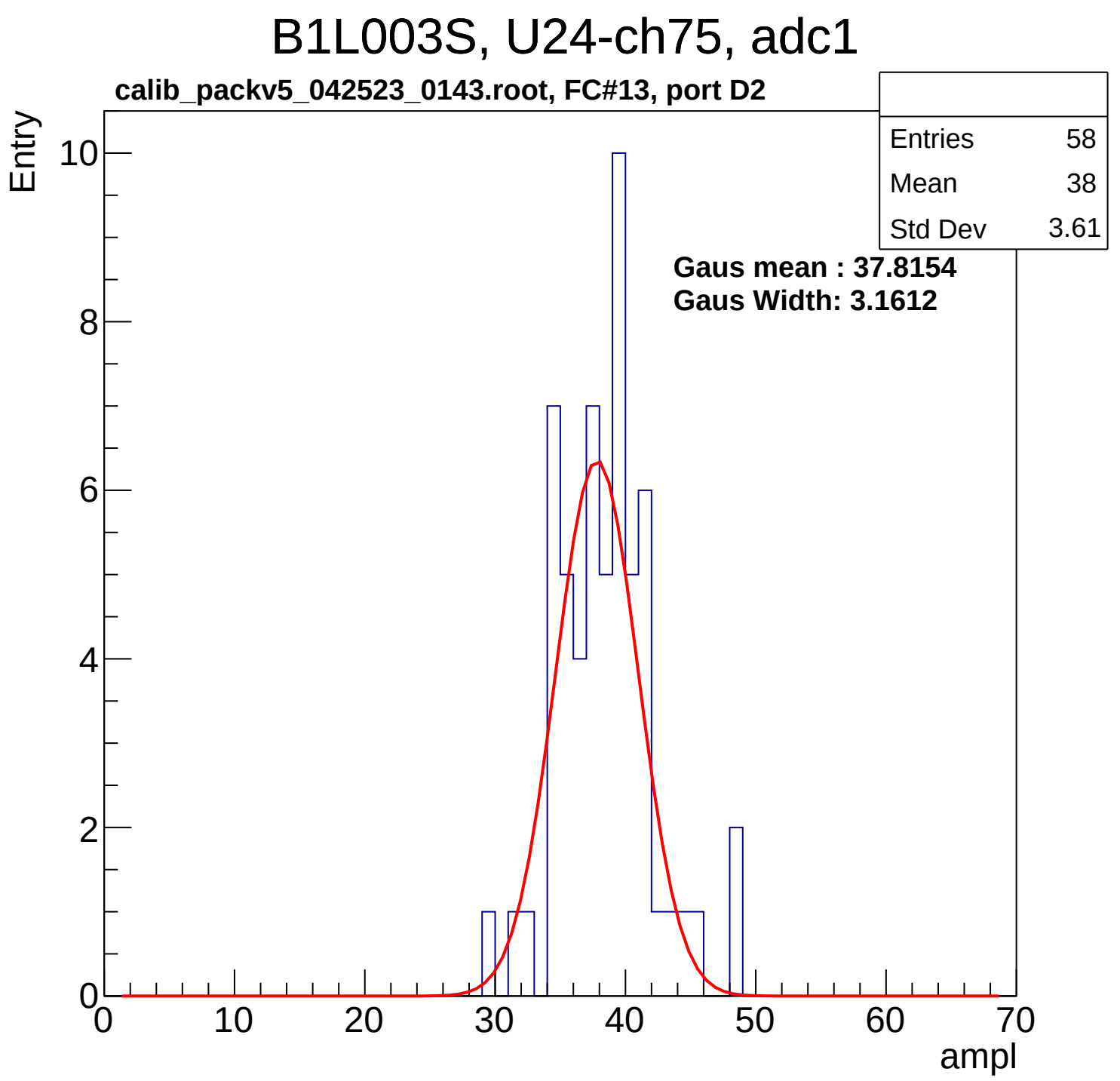
B1L003S, U24-ch75, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 38
Std Dev 3.61
Gaus mean : 37.8154
Gaus Width: 3.1612
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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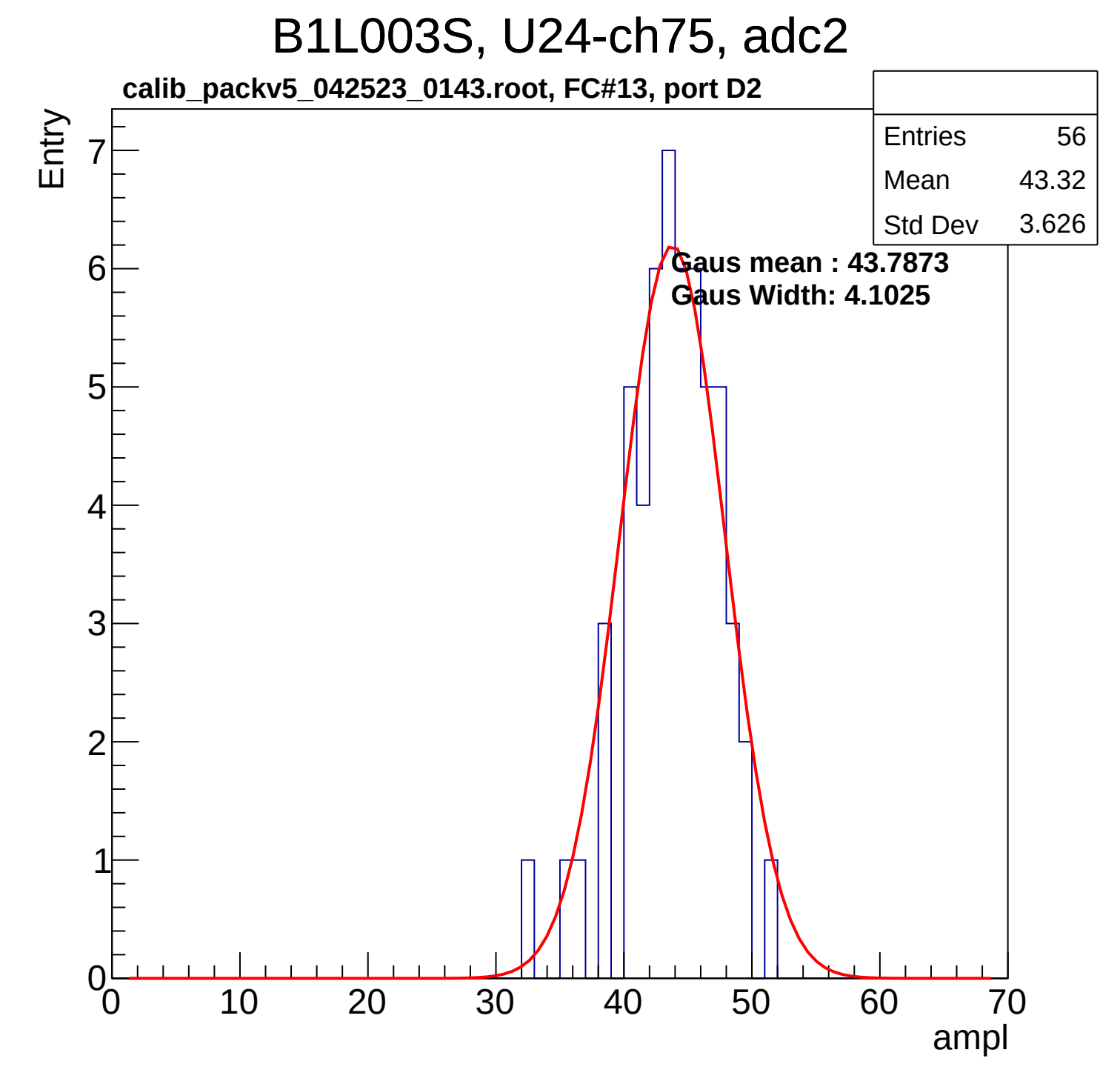
B1L003S, U24-ch75, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 43.32
Std Dev 3.626
Gaus mean : 43.7873
Gaus Width: 4.1025
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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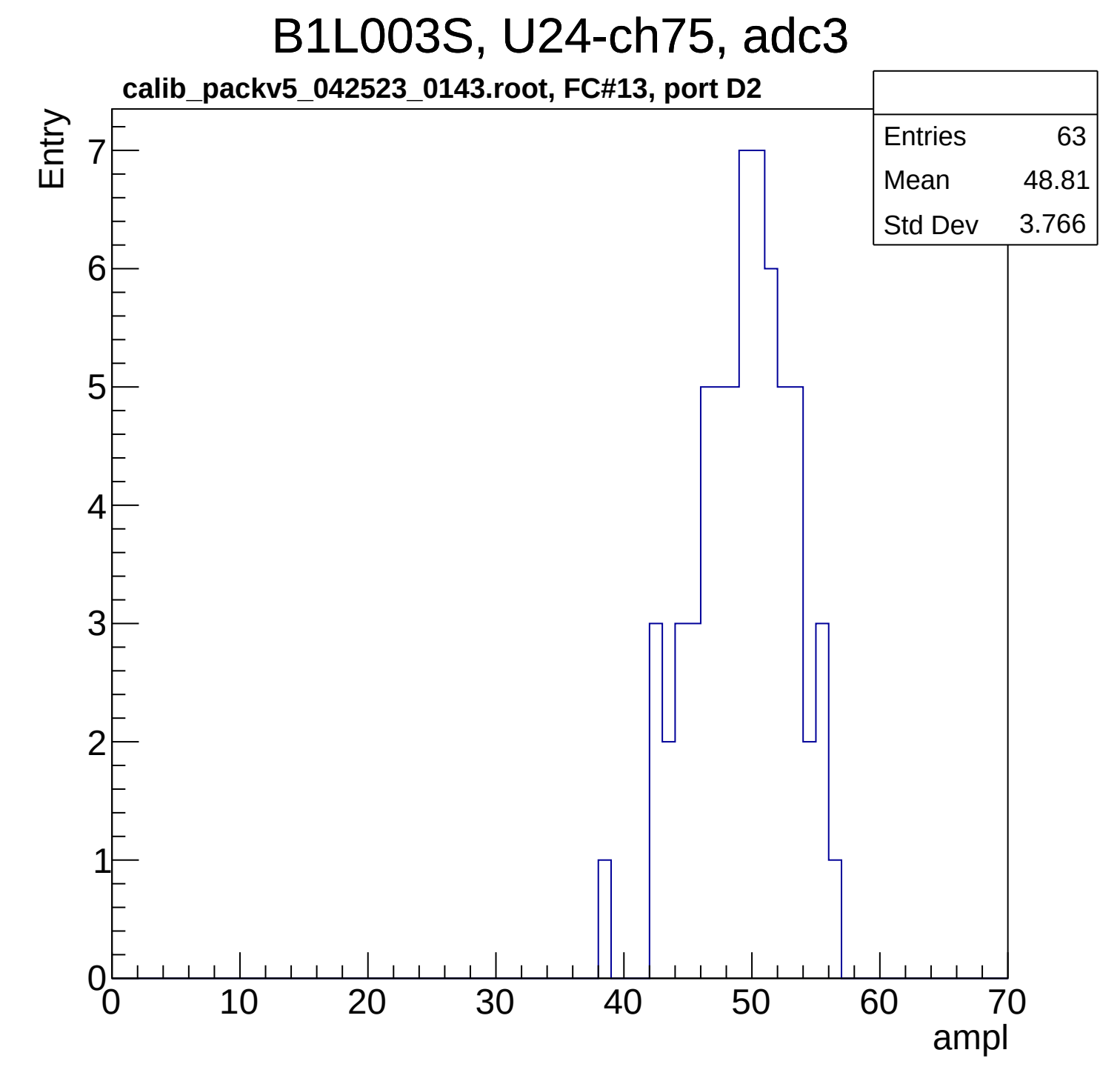
B1L003S, U24-ch75, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.81
Std Dev 3.766
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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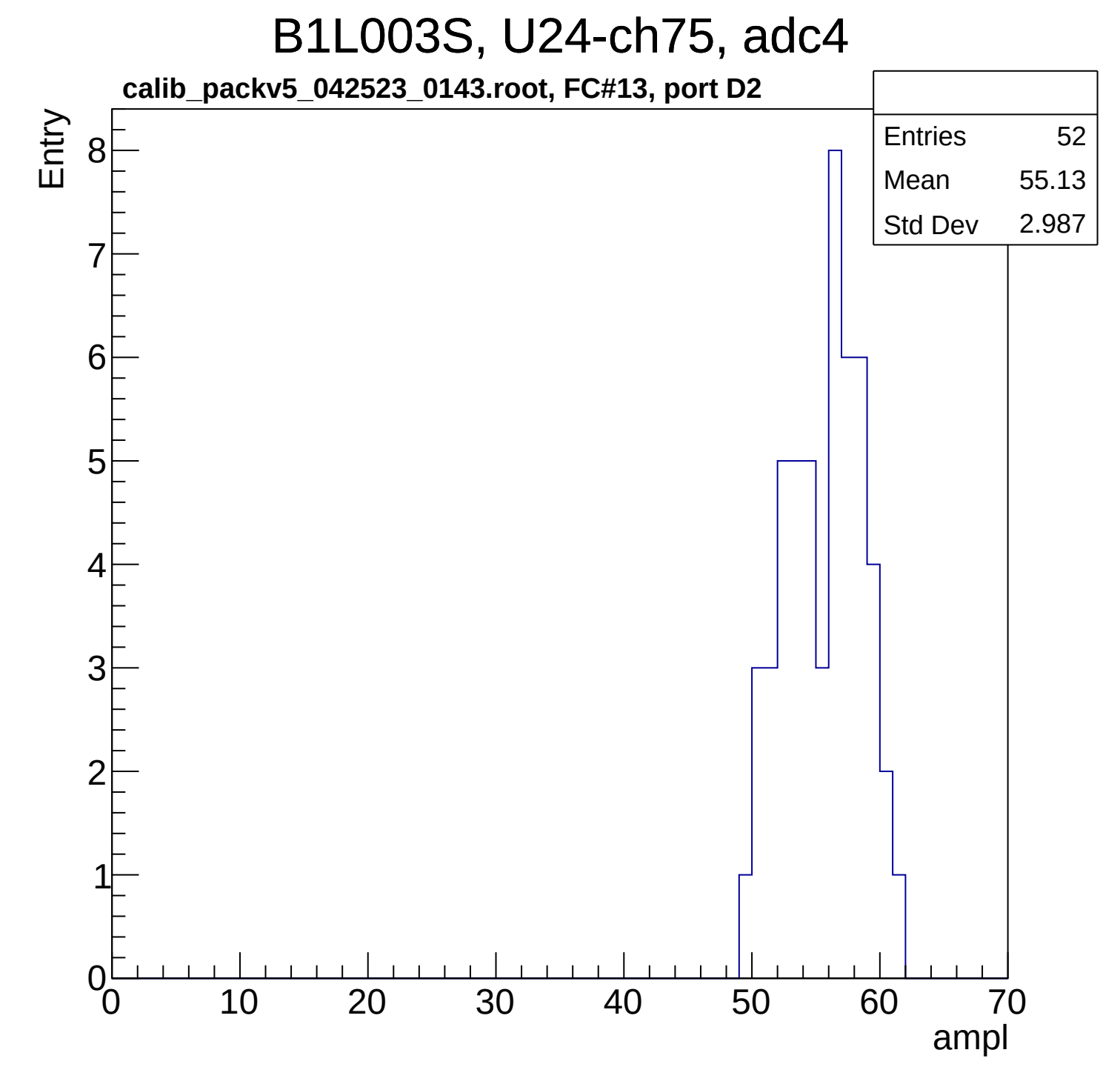
B1L003S, U24-ch75, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.13
Std Dev 2.987
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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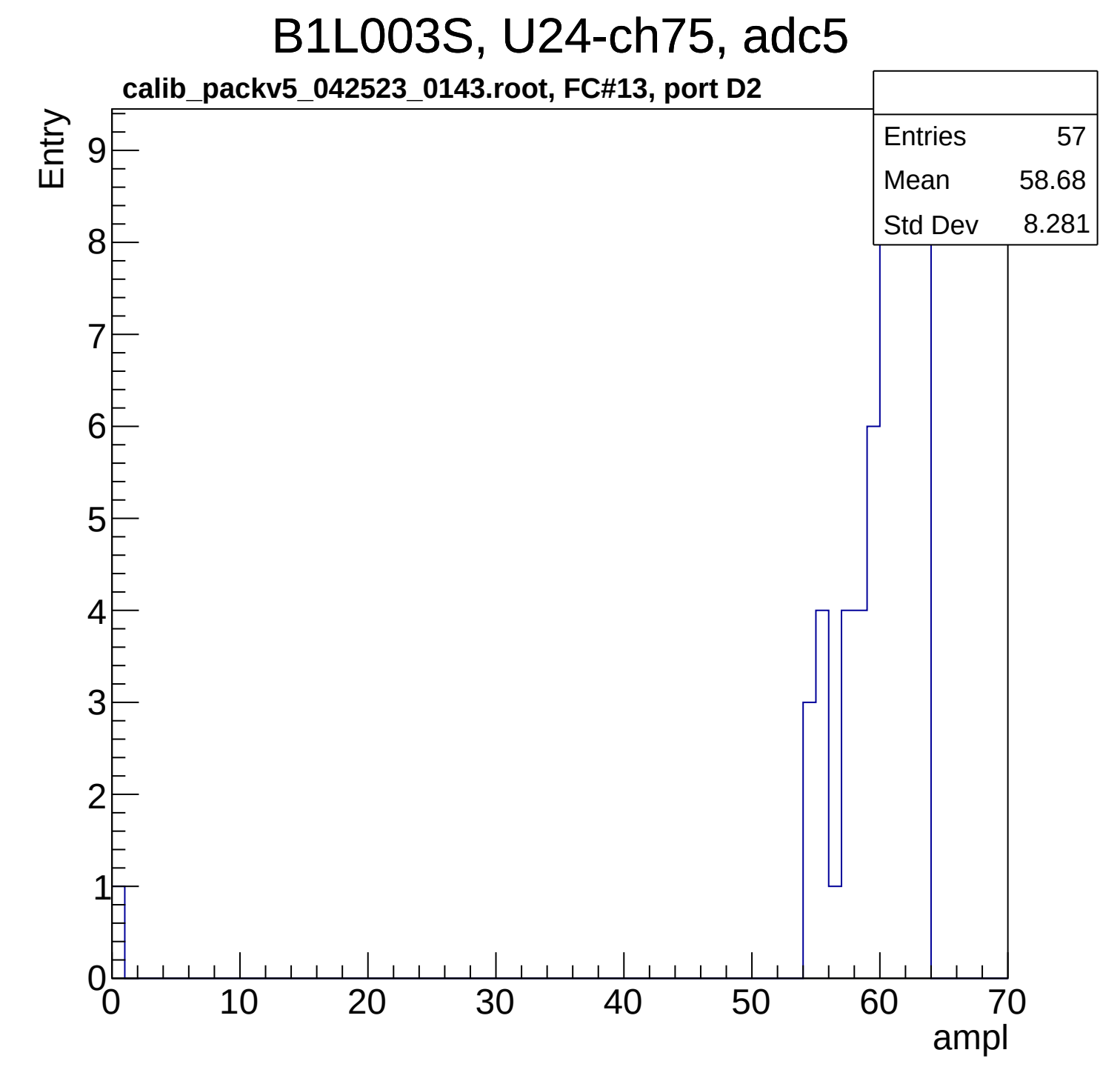
B1L003S, U24-ch75, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.68
Std Dev 8.281
Entry
ampl
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3
4
5
6
7
8
9
0
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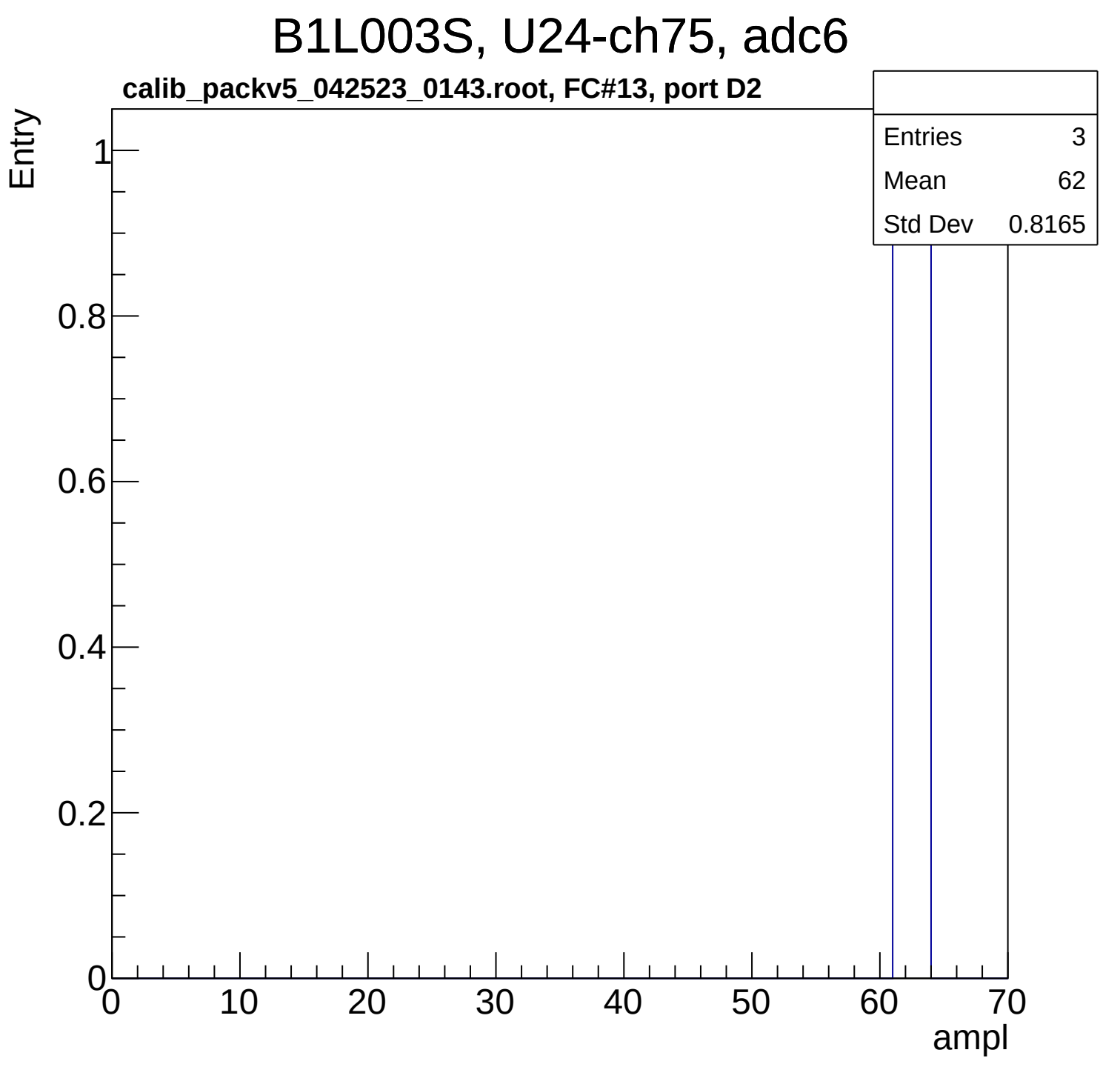
60
70


B1L003S, U24-ch75, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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0.6
0.4
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10
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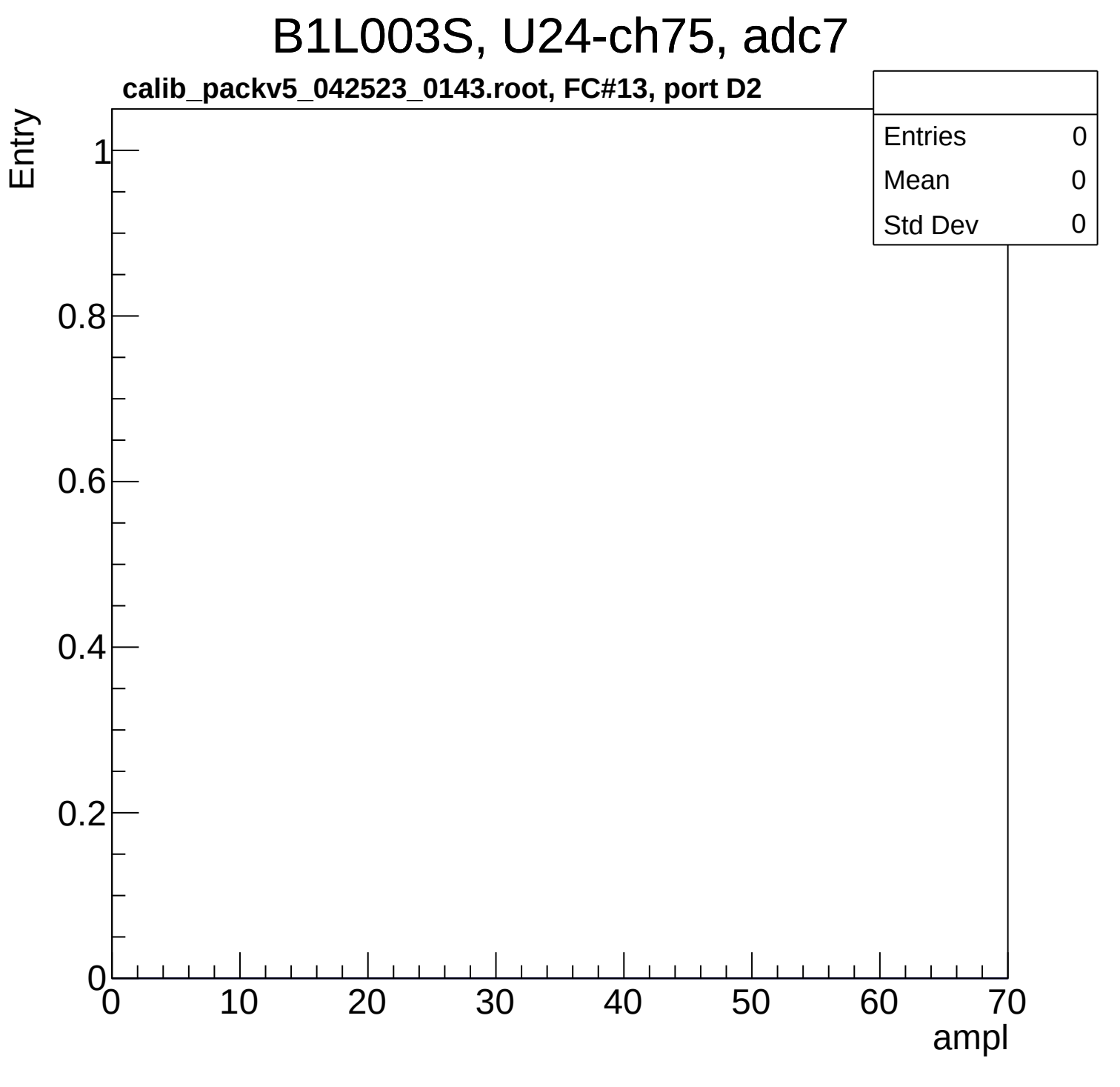
50
60
70
ampl


B1L003S, U24-ch75, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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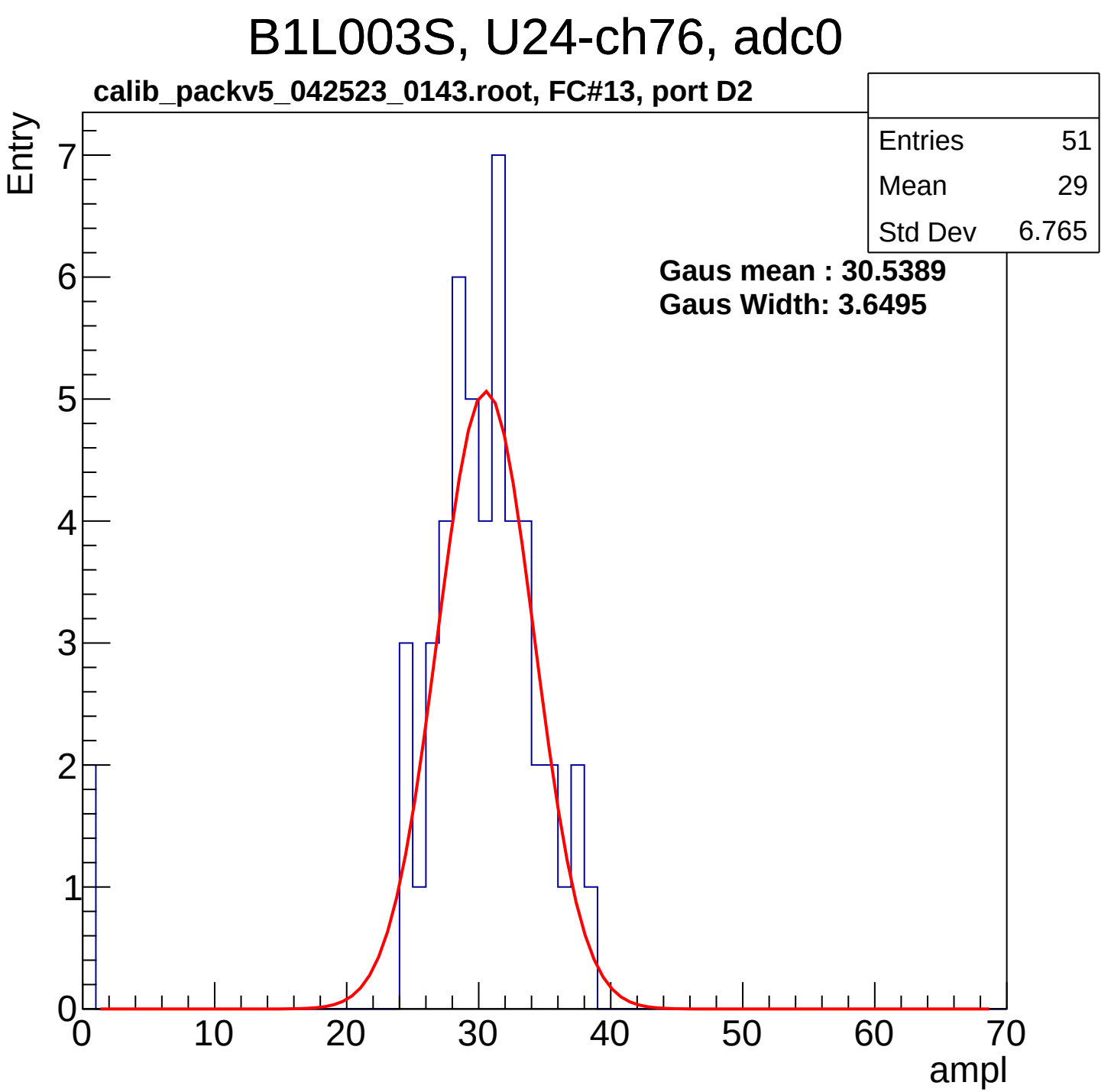
50
60
70
ampl


B1L003S, U24-ch76, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 29
Std Dev 6.765
Gaus mean : 30.5389
Gaus Width: 3.6495
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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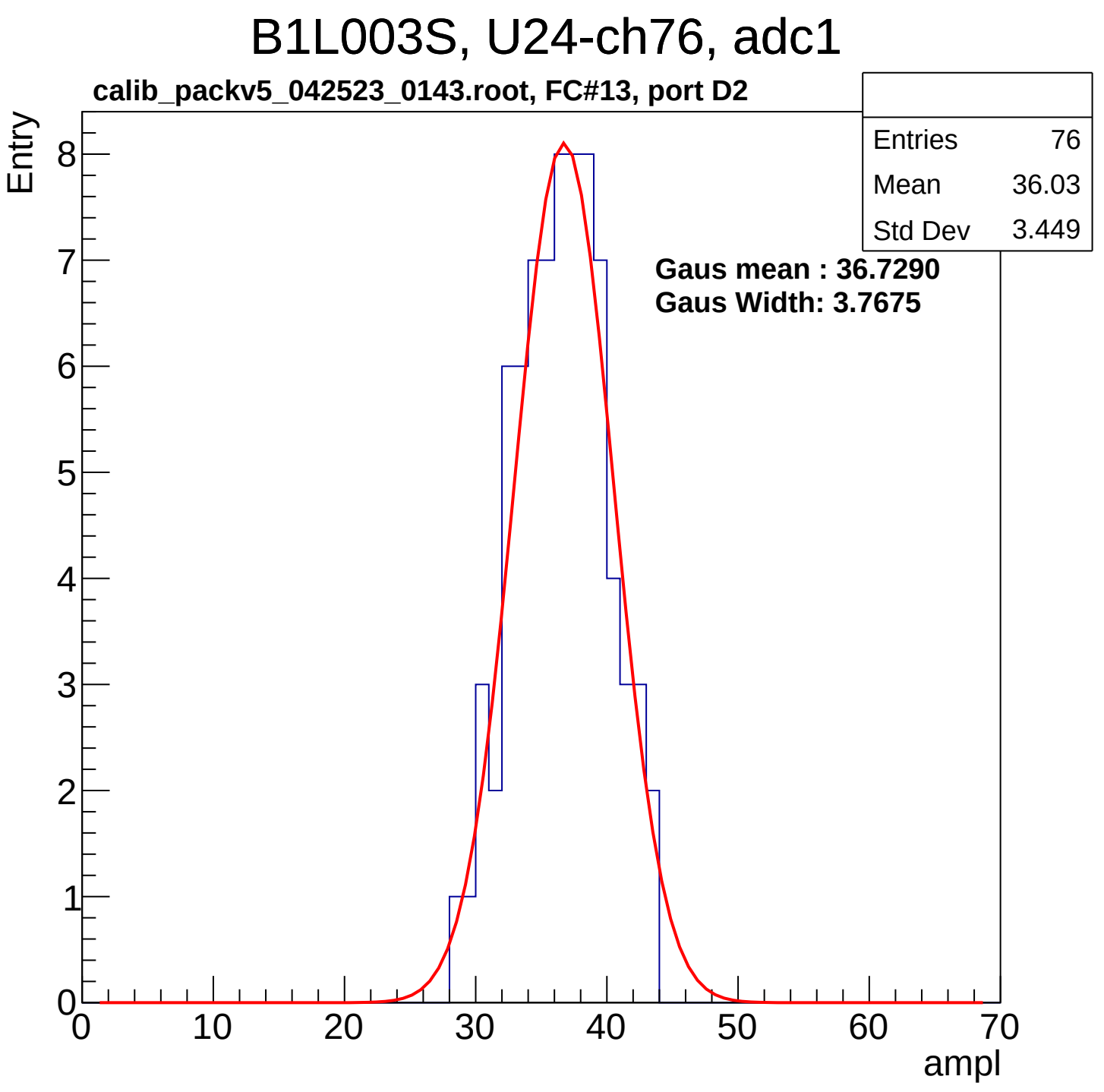
B1L003S, U24-ch76, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.03
Std Dev 3.449
Gaus mean : 36.7290
Gaus Width: 3.7675
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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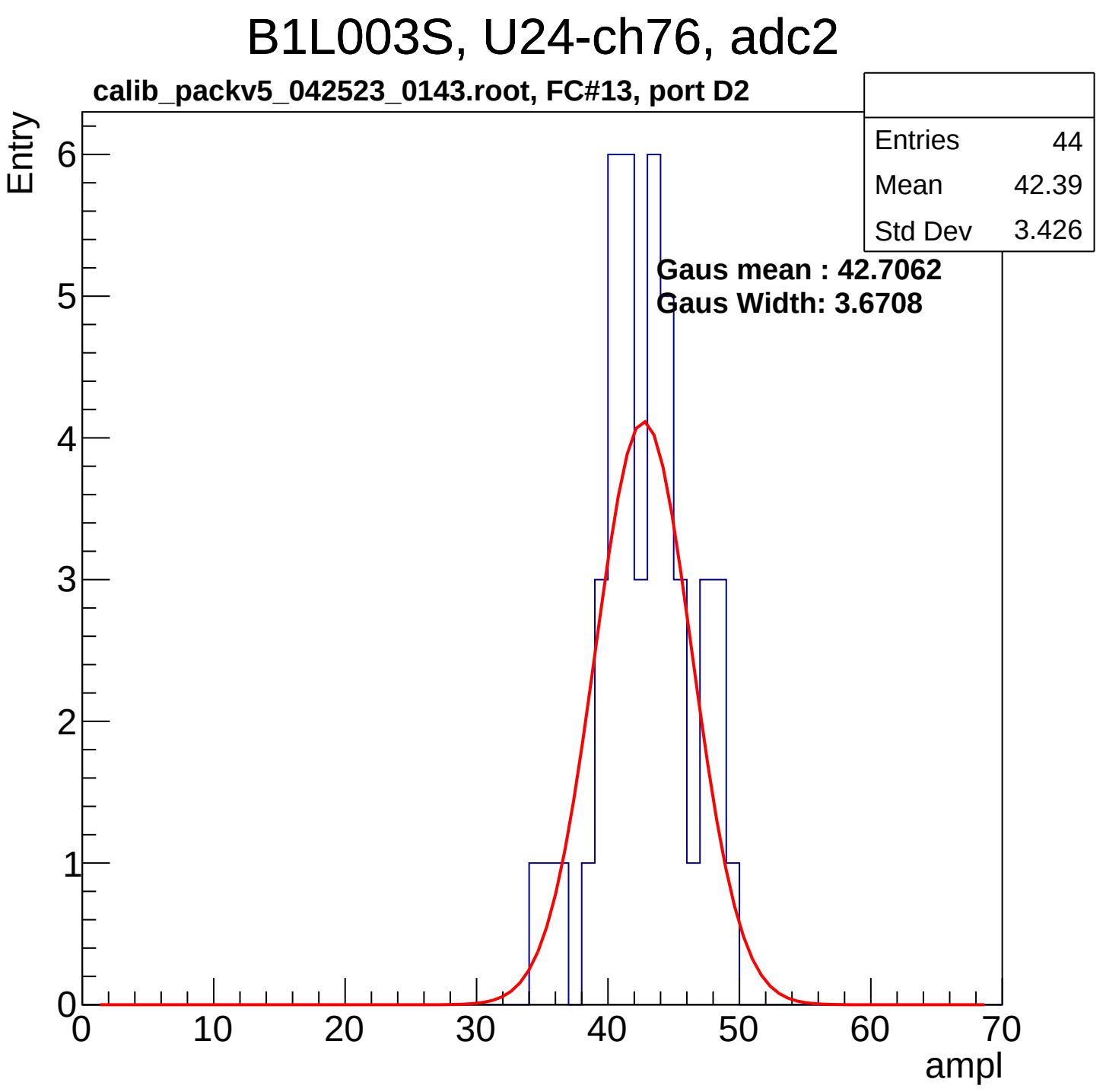
B1L003S, U24-ch76, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 42.39
Std Dev 3.426
Gaus mean : 42.7062
Gaus Width: 3.6708
Entry
ampl
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3
4
5
6
0
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20
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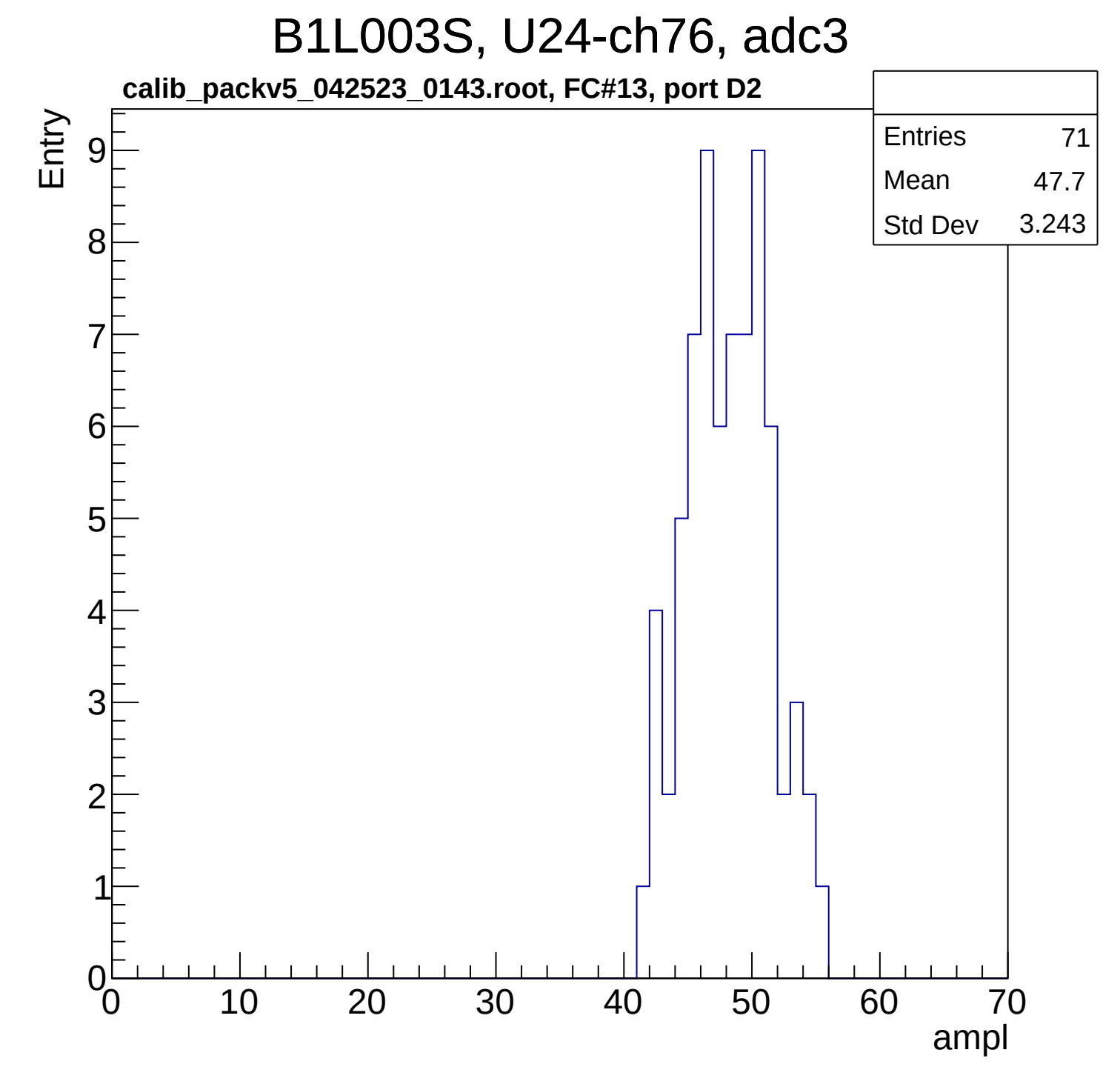
40
50
60
70


B1L003S, U24-ch76, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 47.7
Std Dev 3.243
Entry
ampl
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3
4
5
6
7
8
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0
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60
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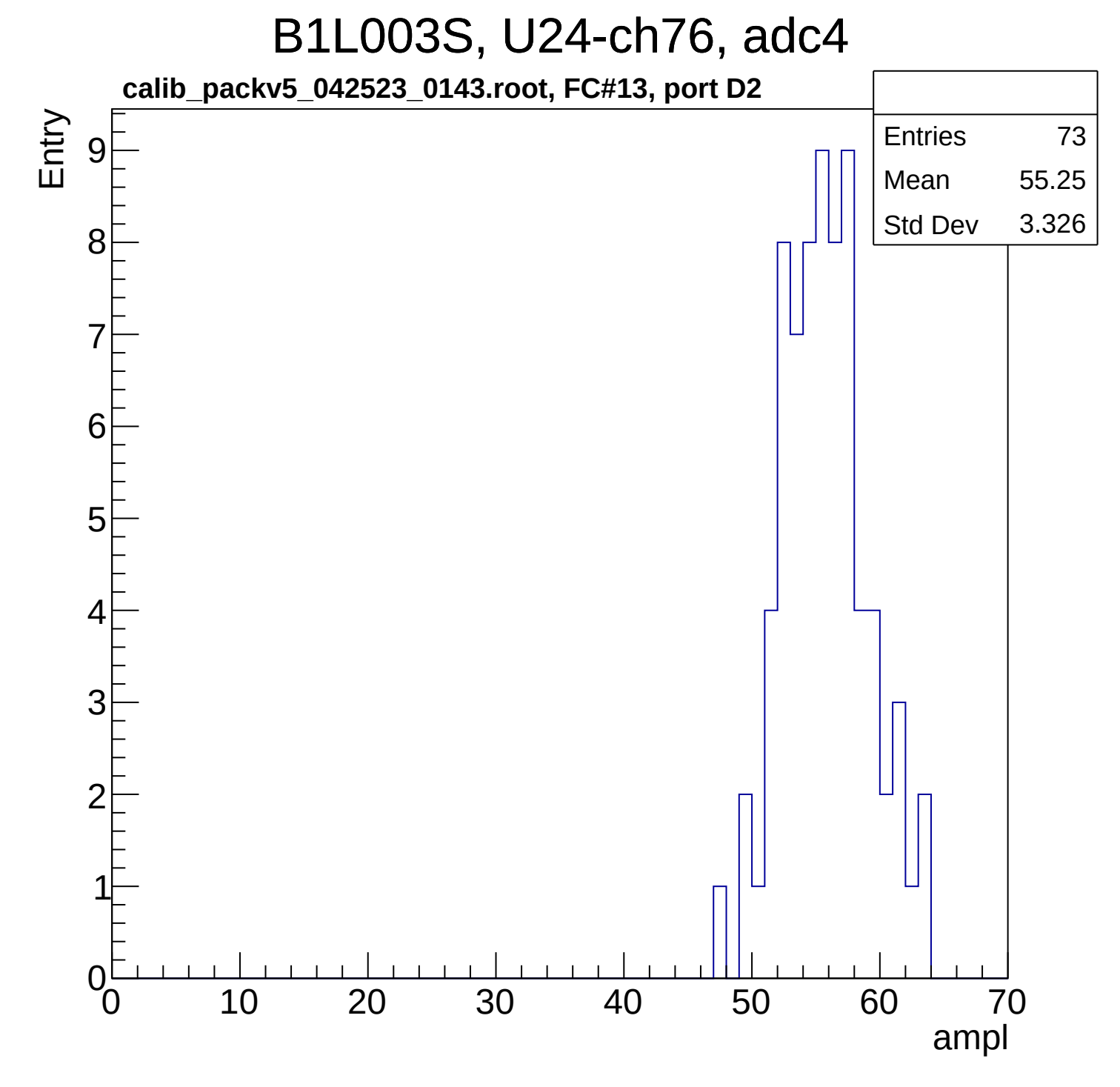
B1L003S, U24-ch76, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 55.25
Std Dev 3.326
Entry
ampl
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3
4
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6
7
8
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0
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60
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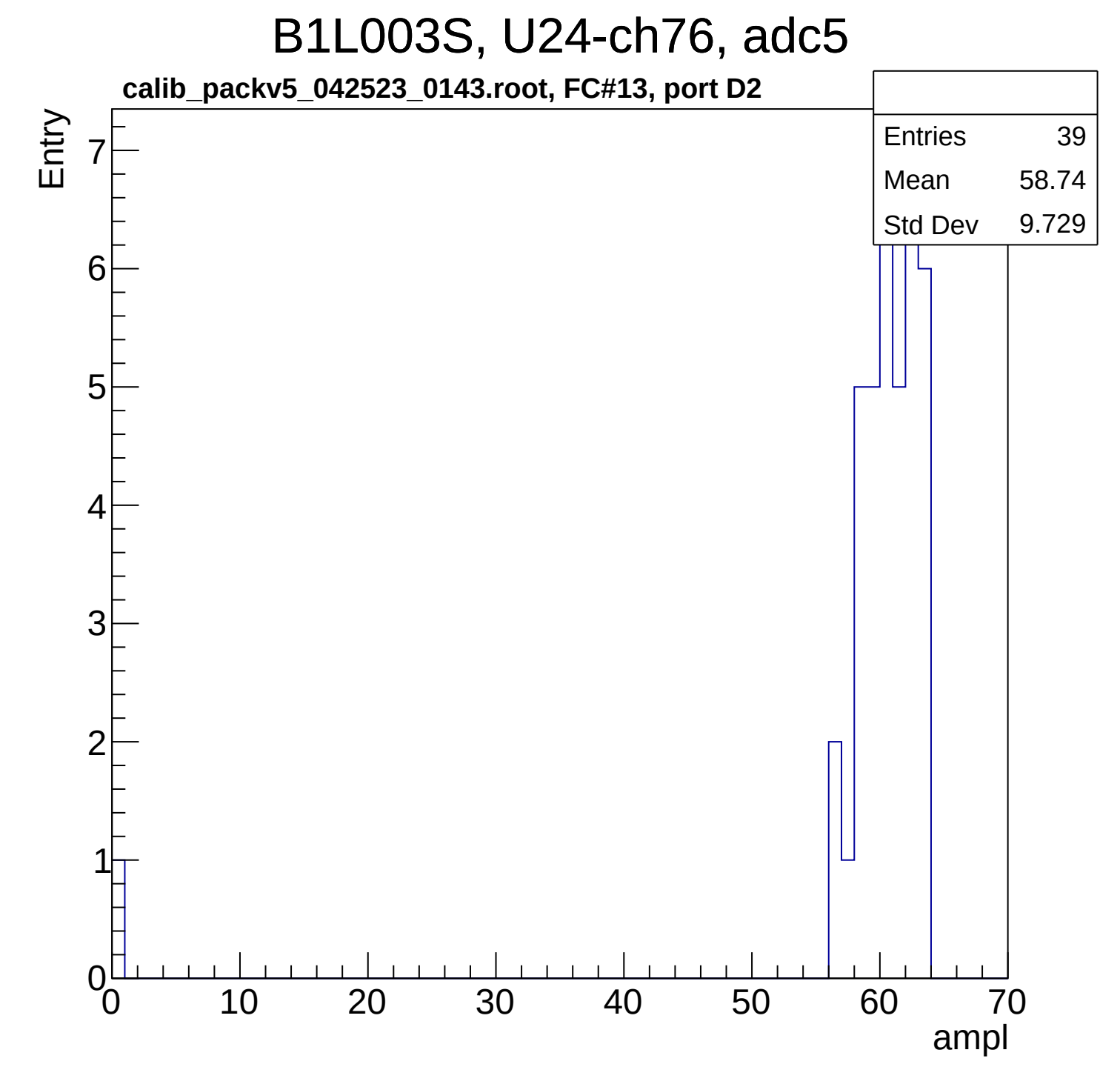
B1L003S, U24-ch76, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 58.74
Std Dev 9.729
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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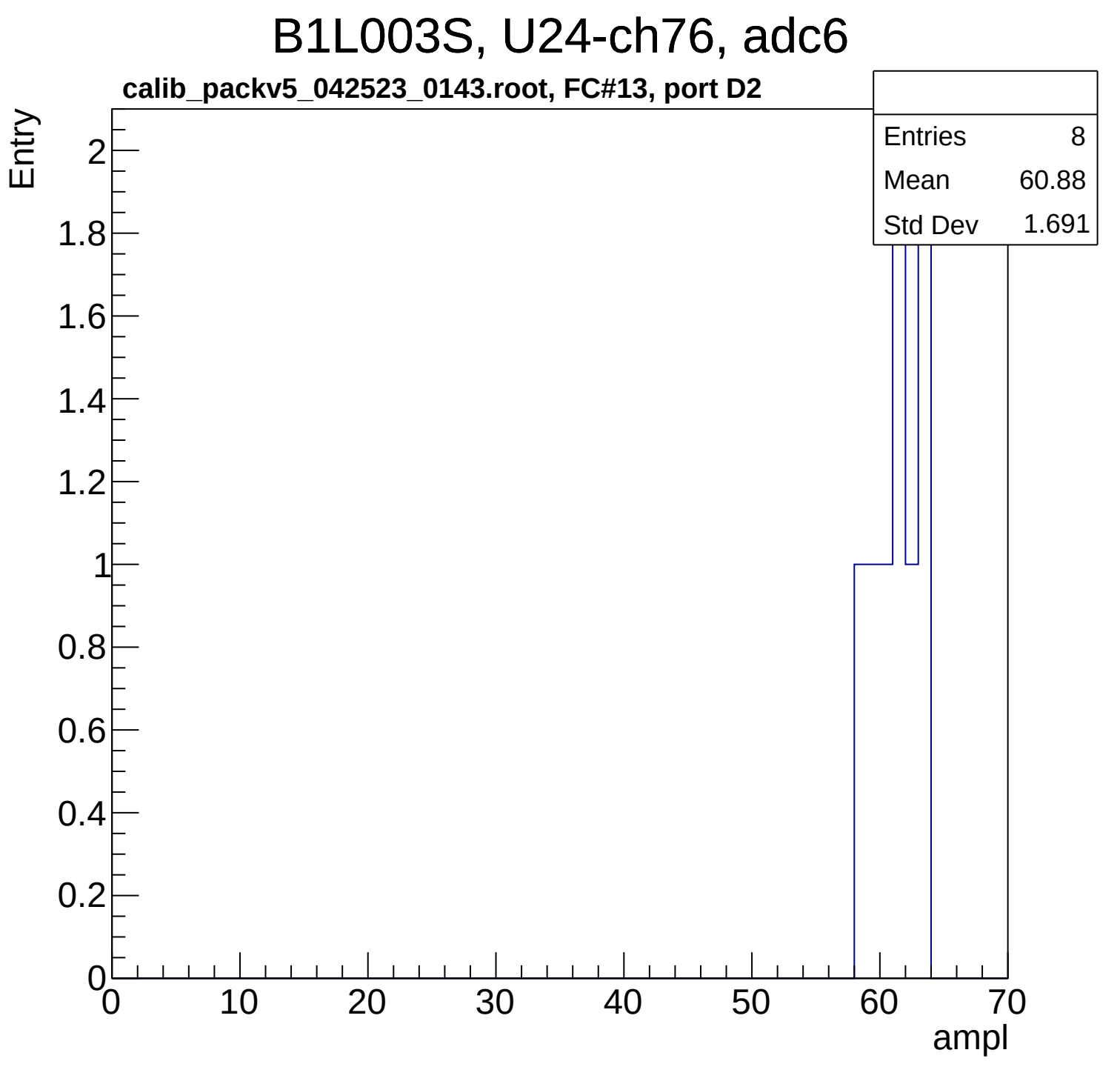
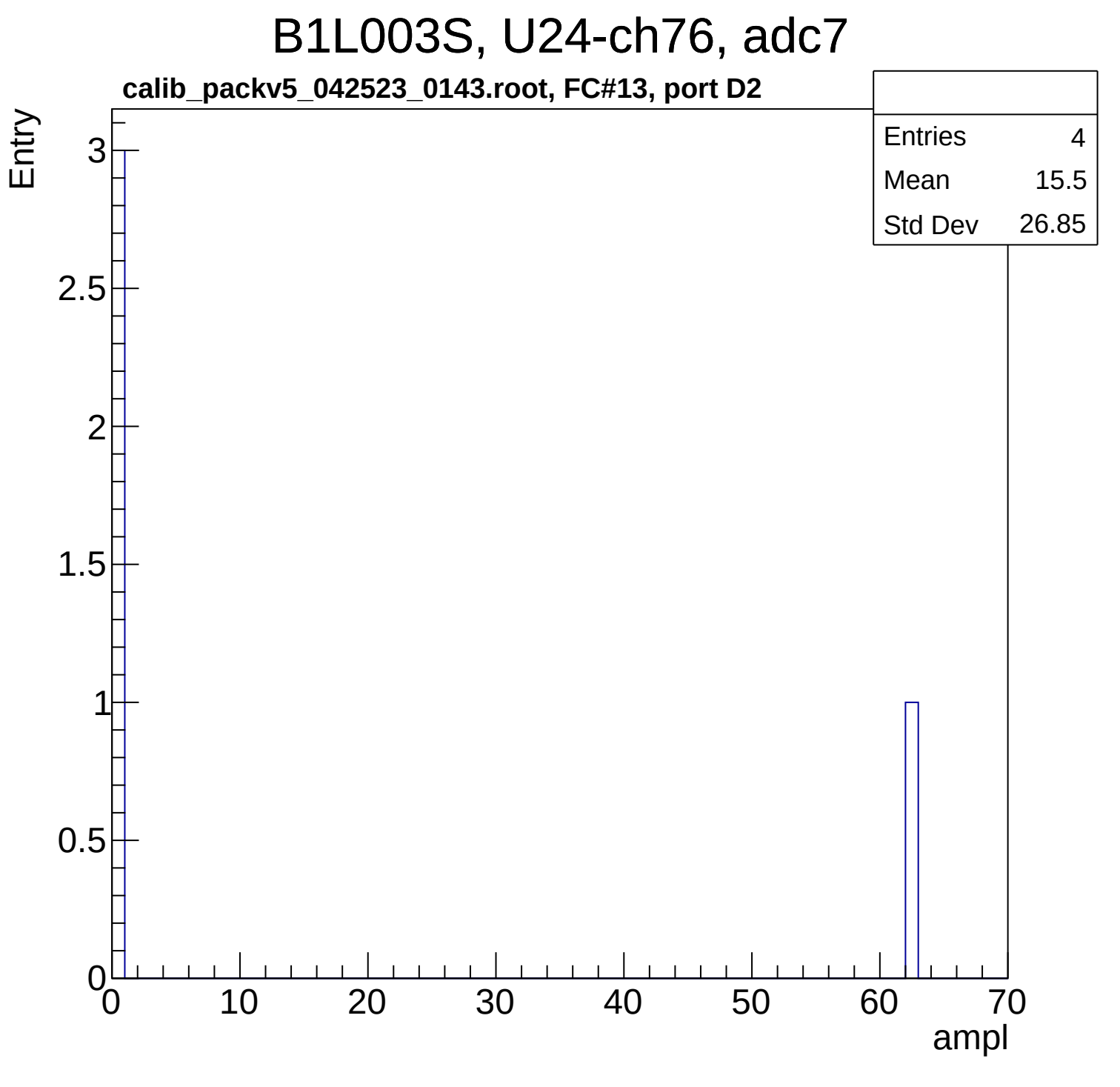

B1L003S, U24-ch76, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 60.88
Std Dev 1.691
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch76, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 15.5
Std Dev 26.85
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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20
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40
50
60
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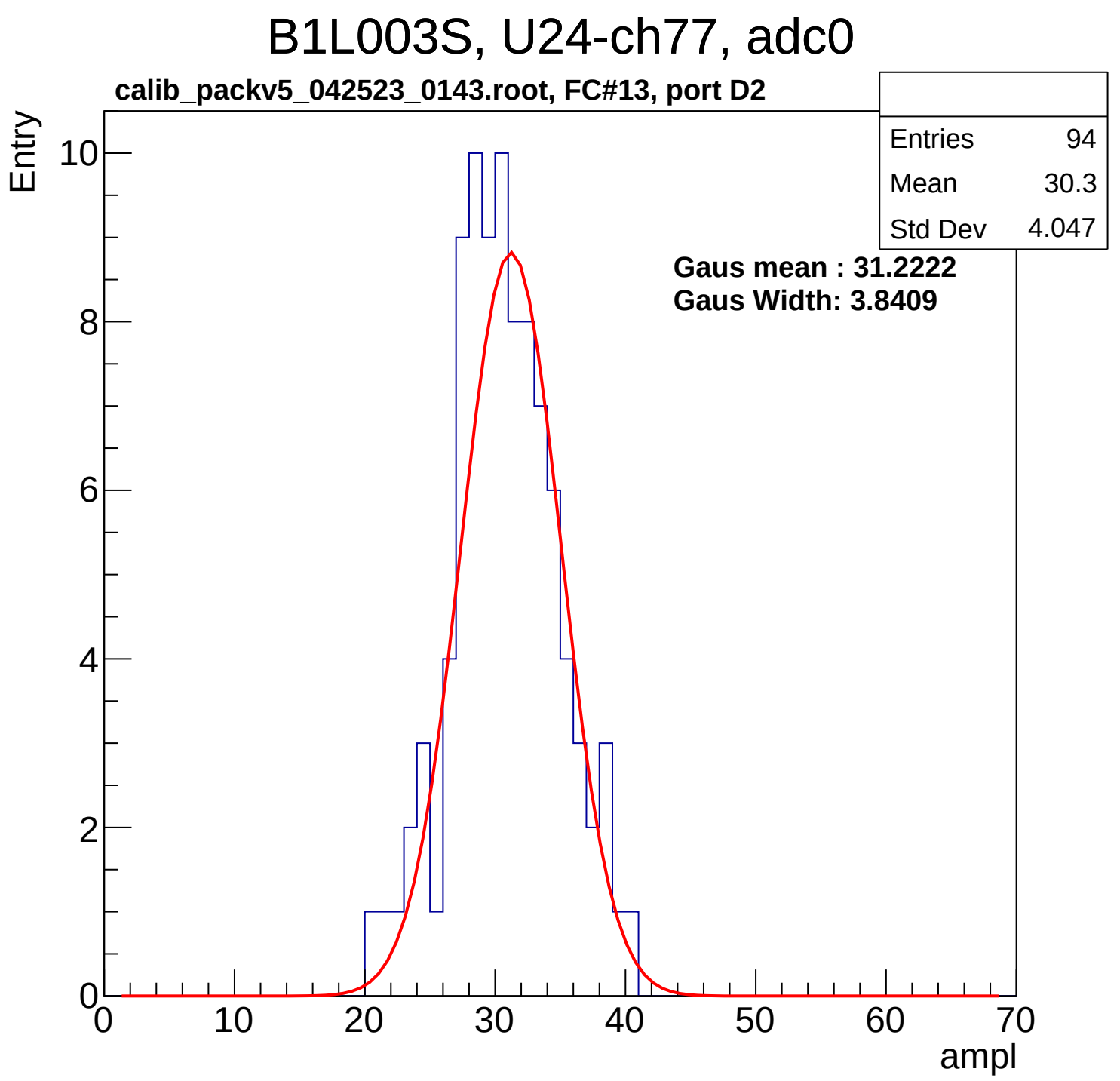
B1L003S, U24-ch77, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 94
Mean 30.3
Std Dev 4.047
Gaus mean : 31.2222
Gaus Width: 3.8409
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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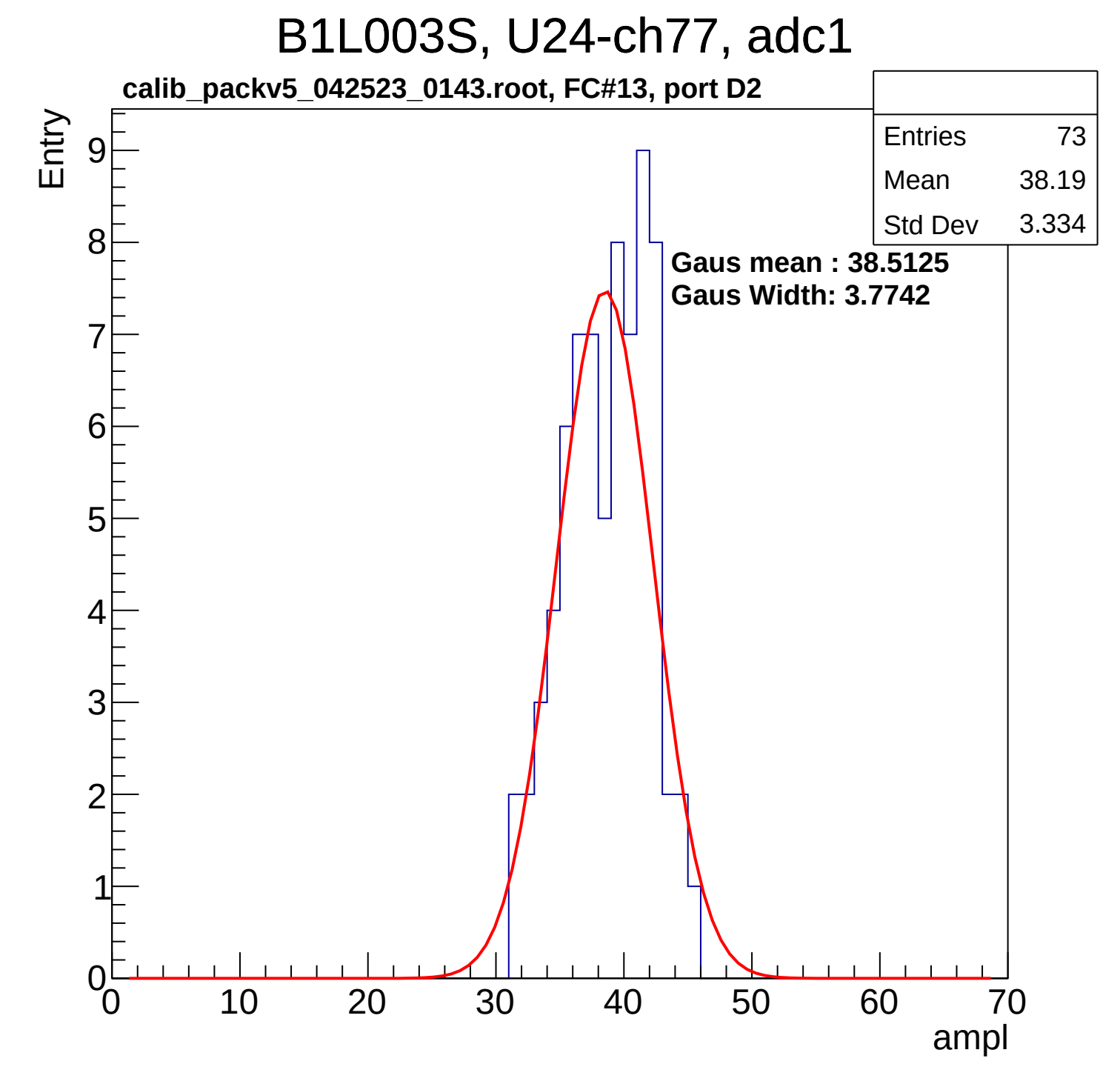
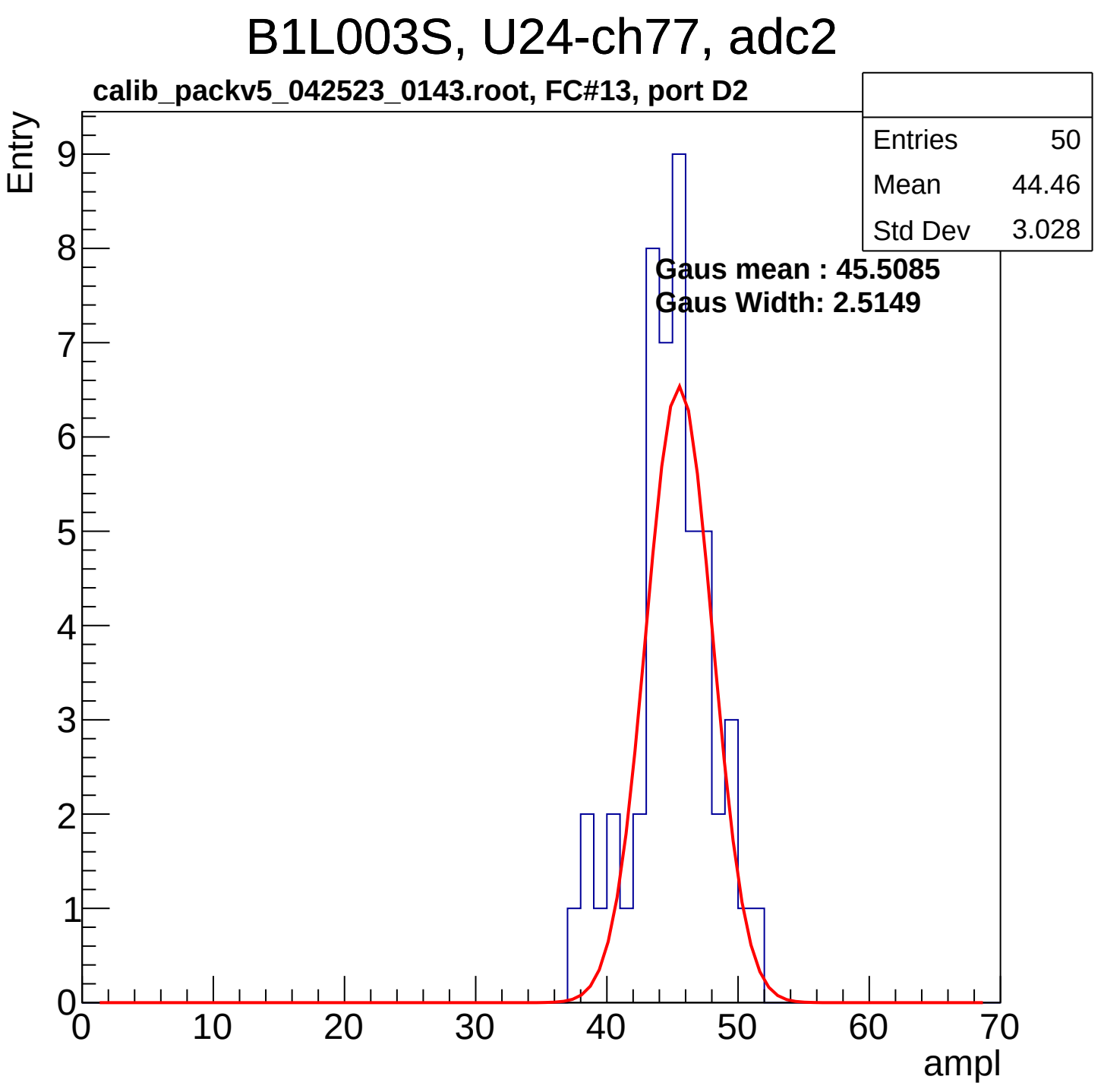

B1L003S, U24-ch77, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 38.19
Std Dev 3.334
Gaus mean : 38.5125
Gaus Width: 3.7742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch77, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 44.46
Std Dev 3.028
Gaus mean : 45.5085
Gaus Width: 2.5149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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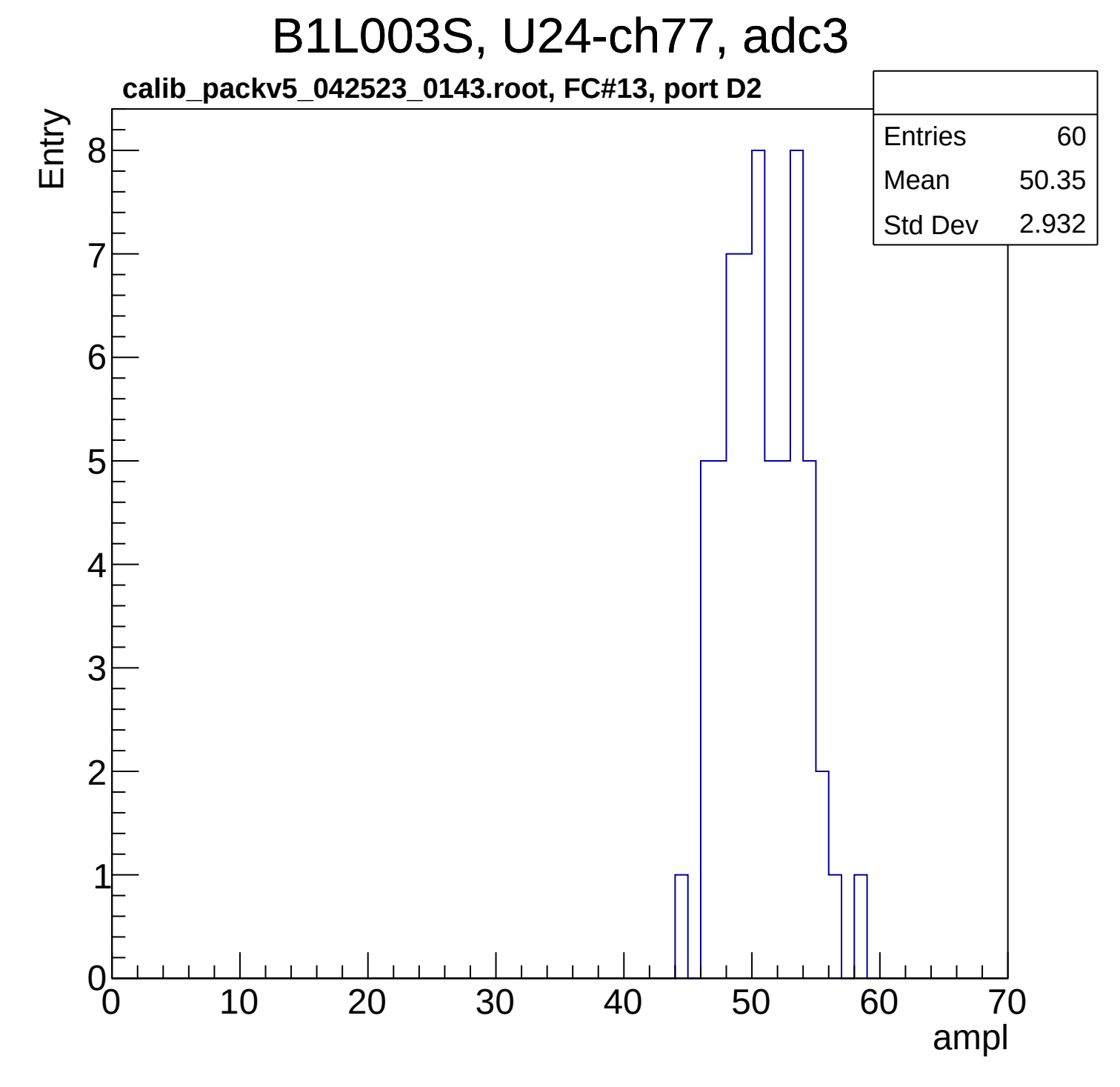
B1L003S, U24-ch77, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 50.35
Std Dev 2.932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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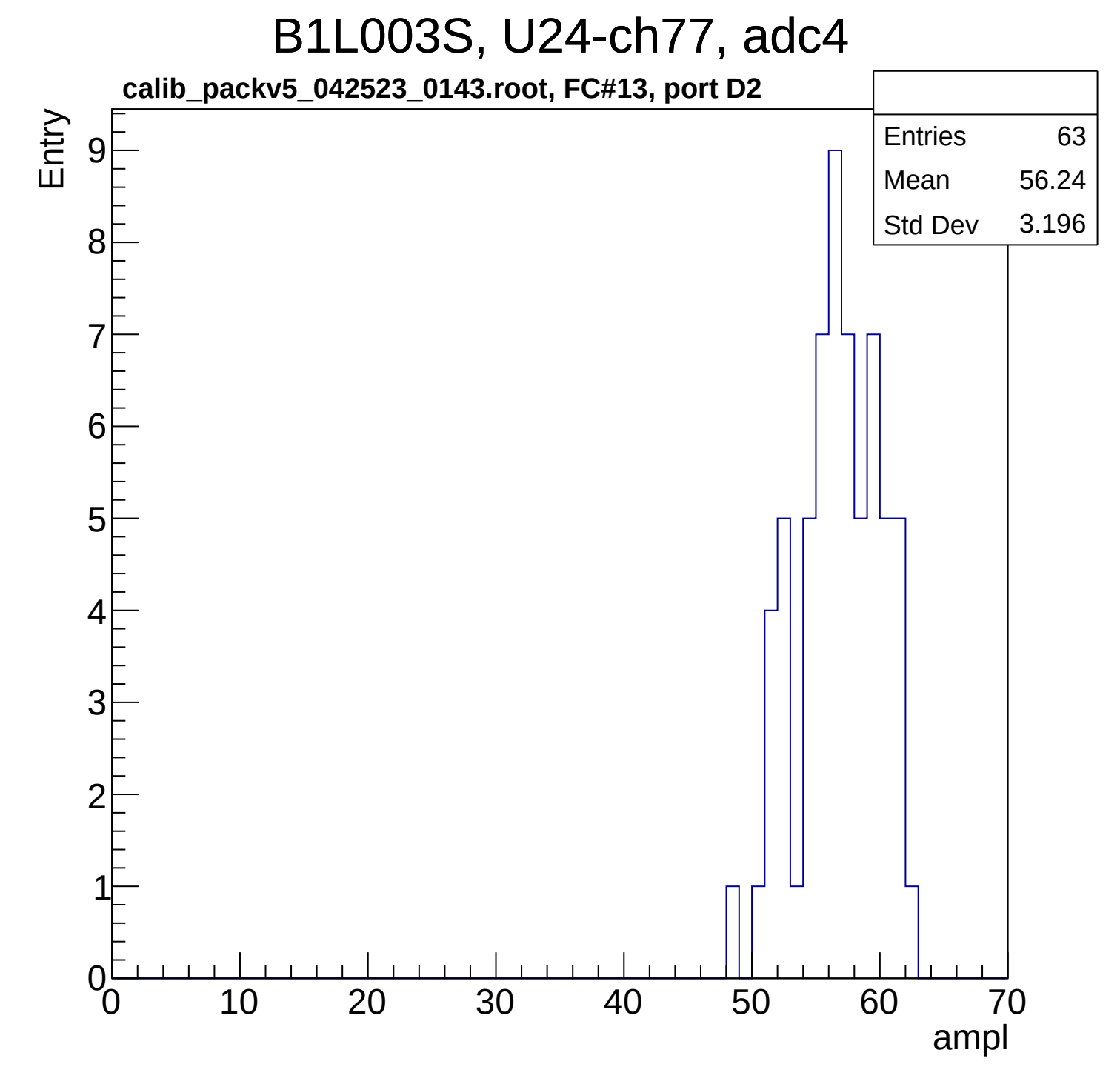
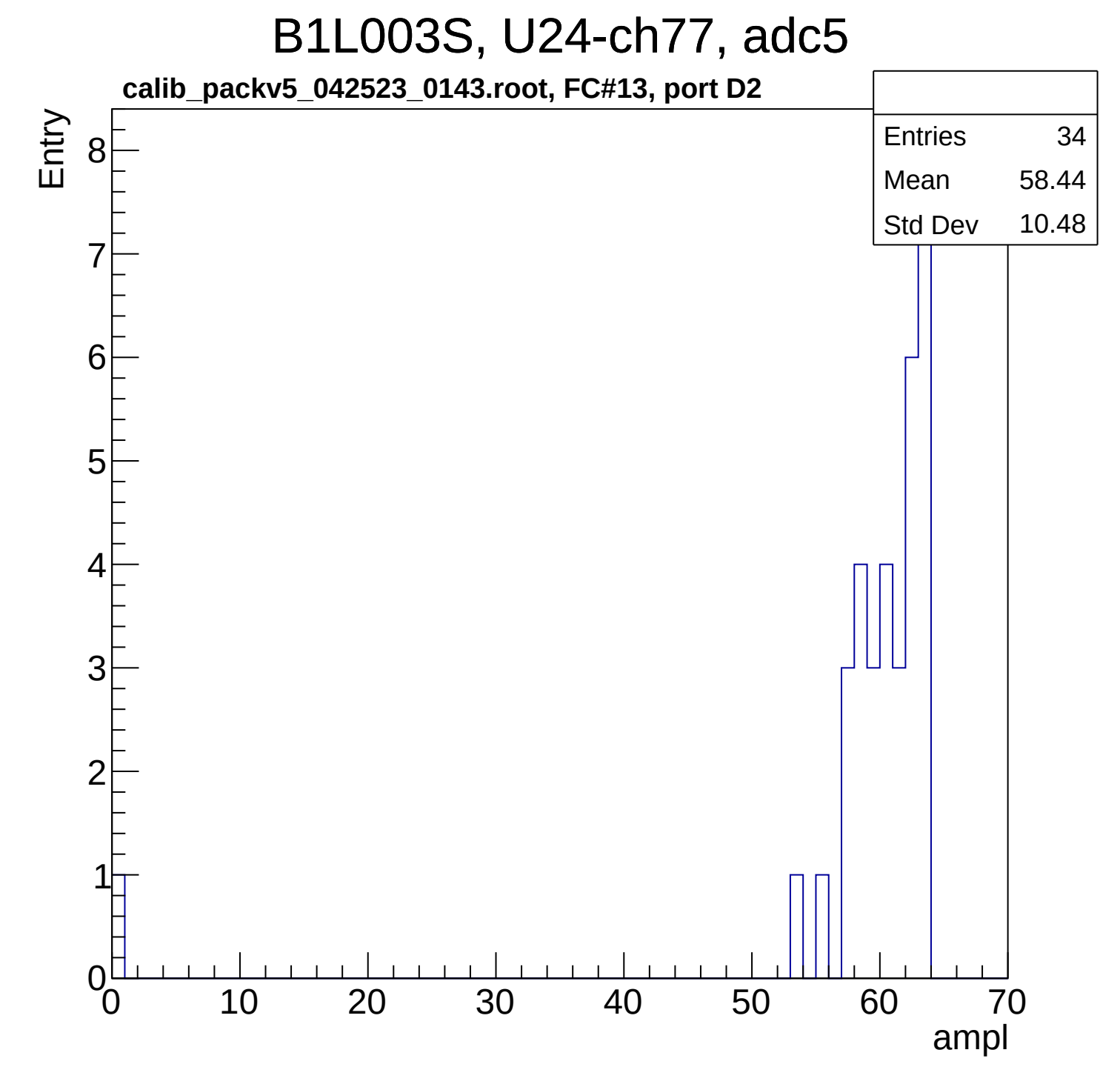

B1L003S, U24-ch77, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 56.24
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch77, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 58.44
Std Dev 10.48
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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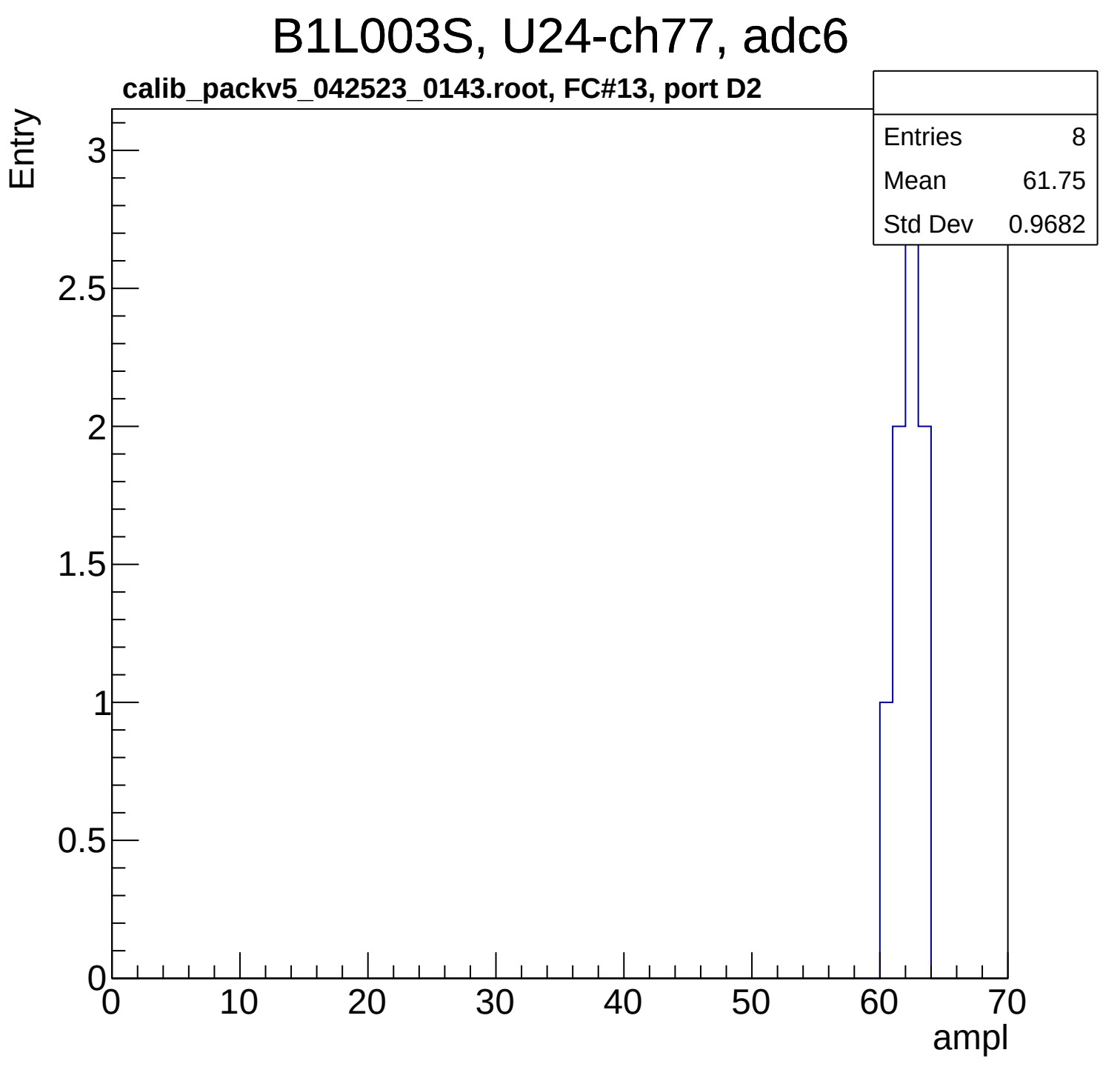
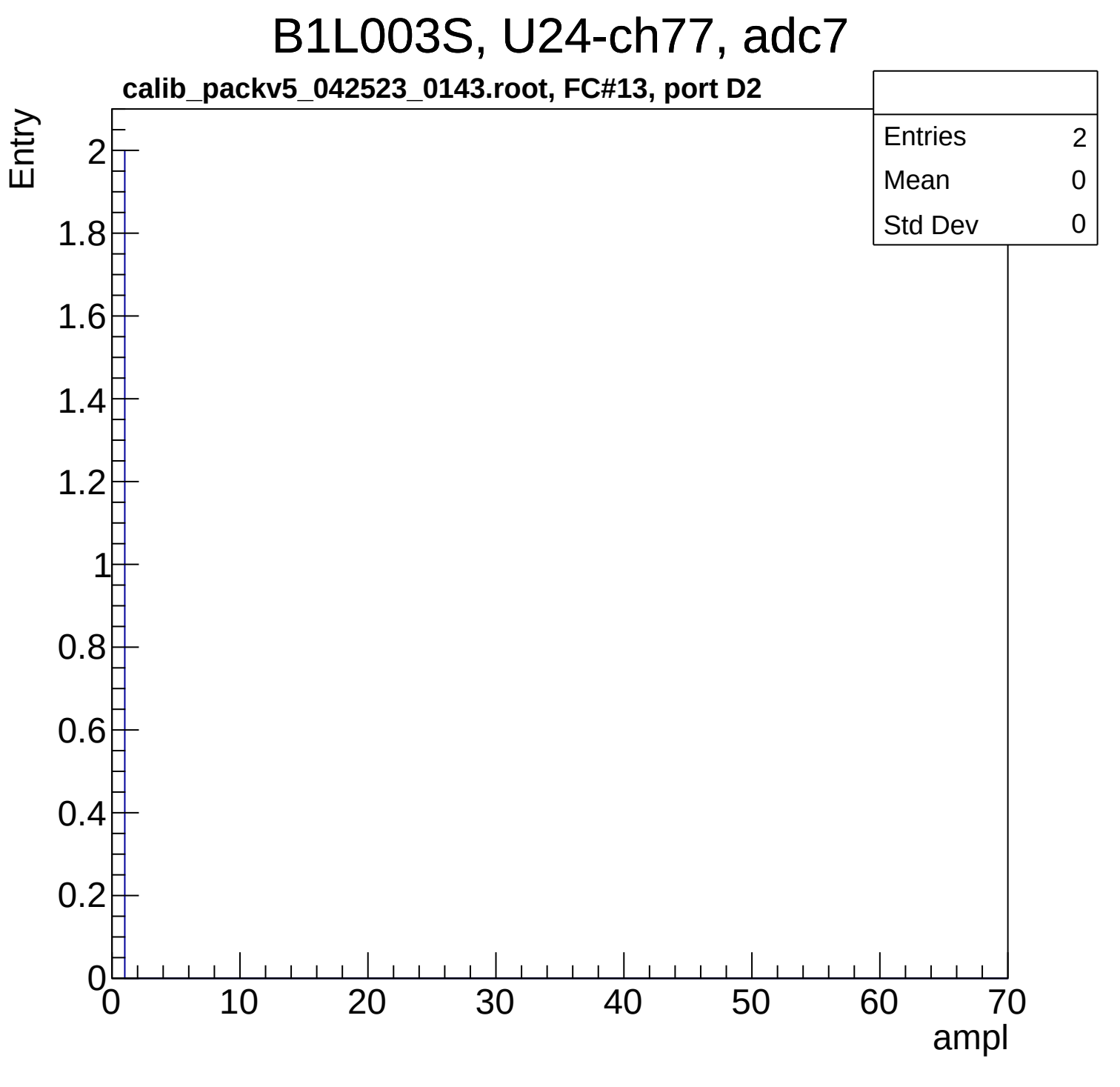

B1L003S, U24-ch77, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.75
Std Dev 0.9682
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch77, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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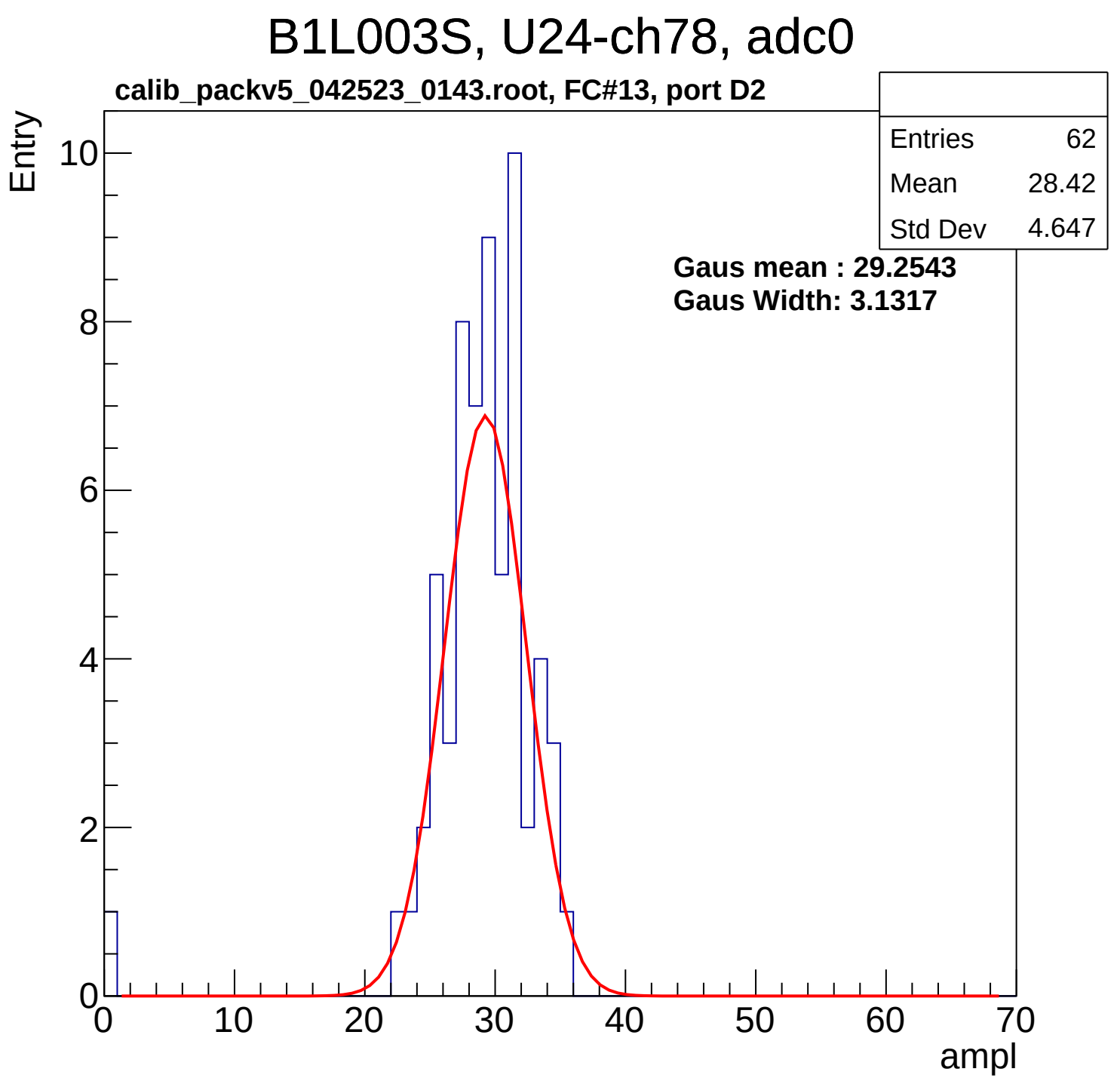
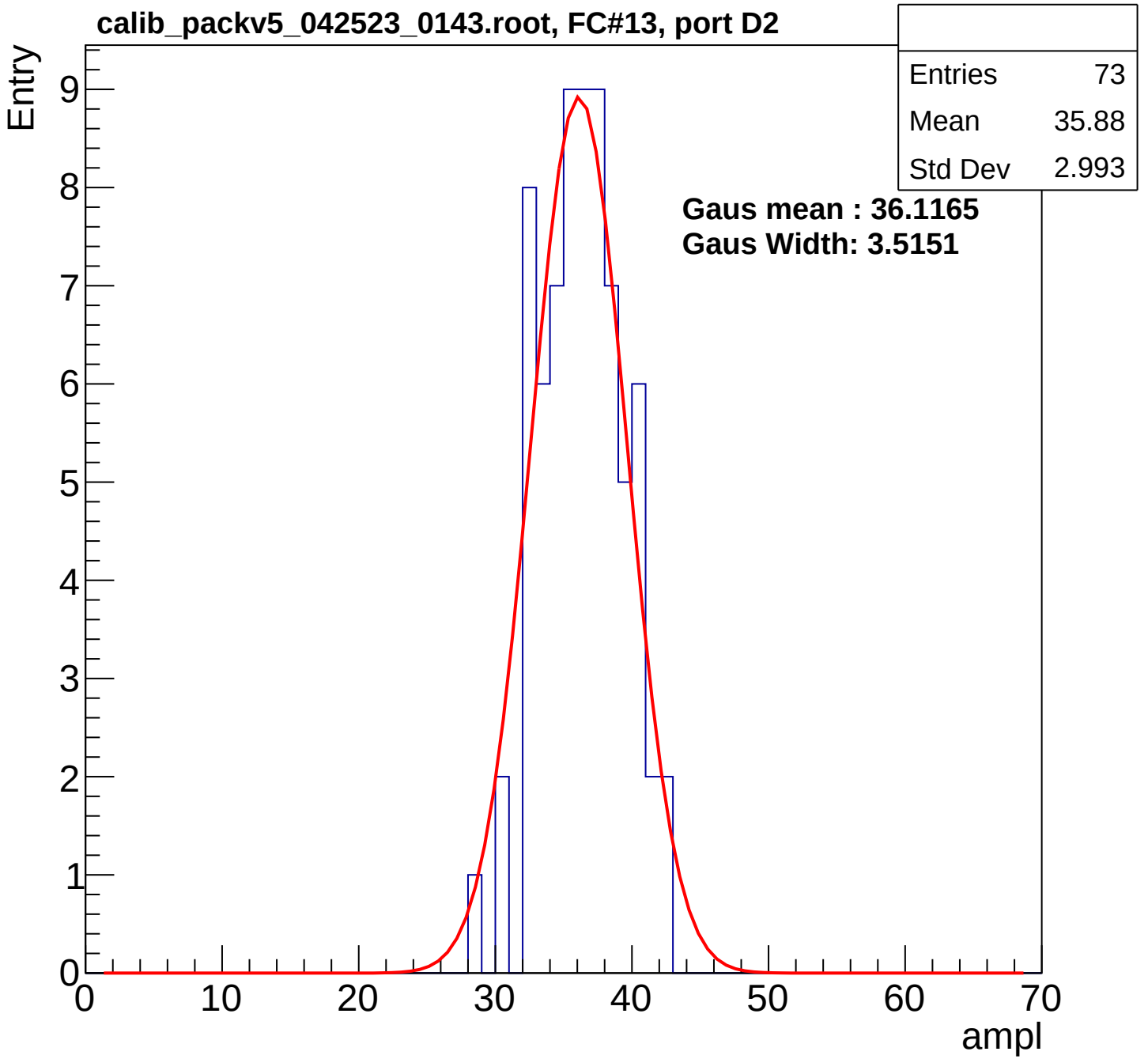
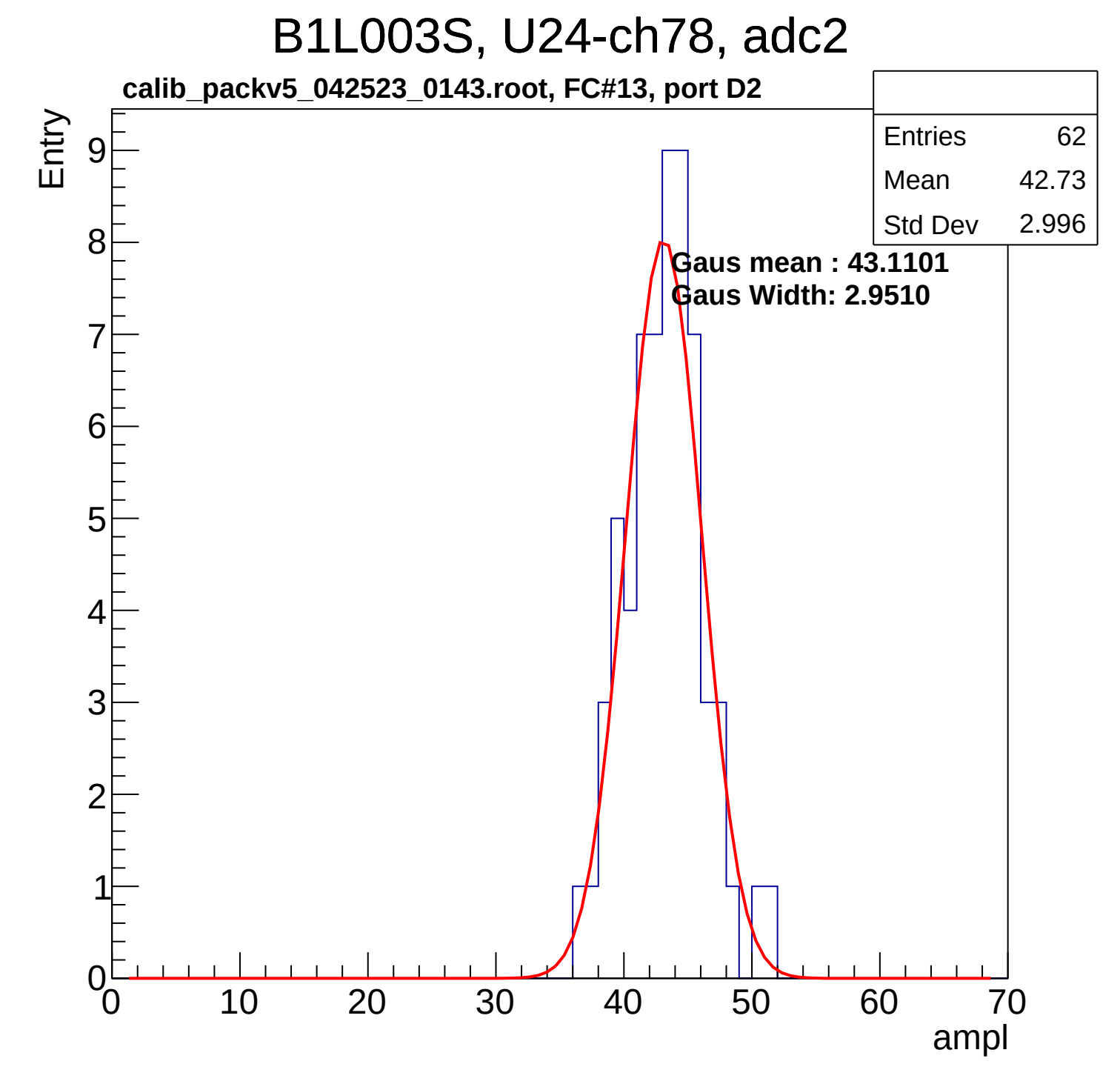
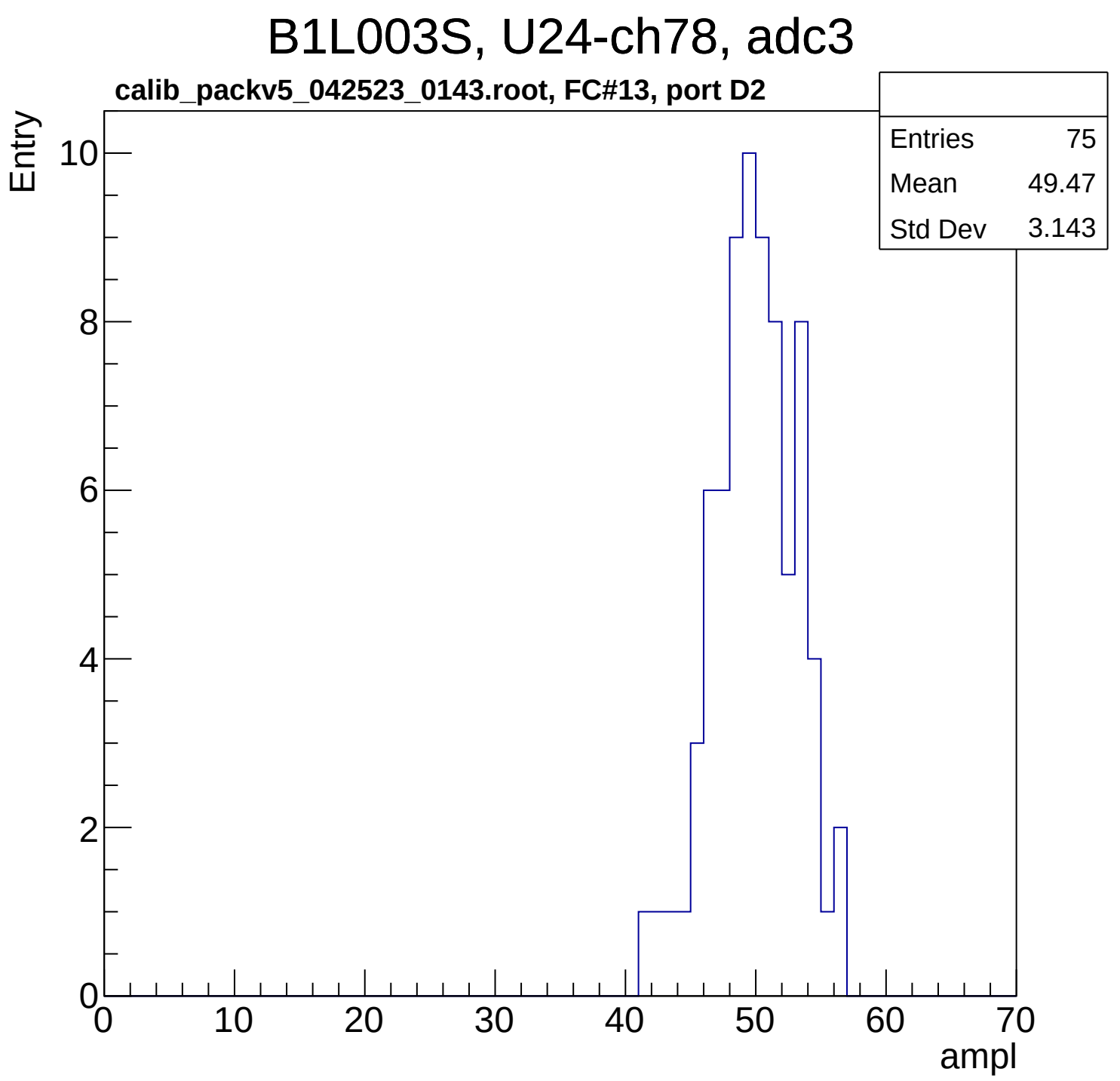
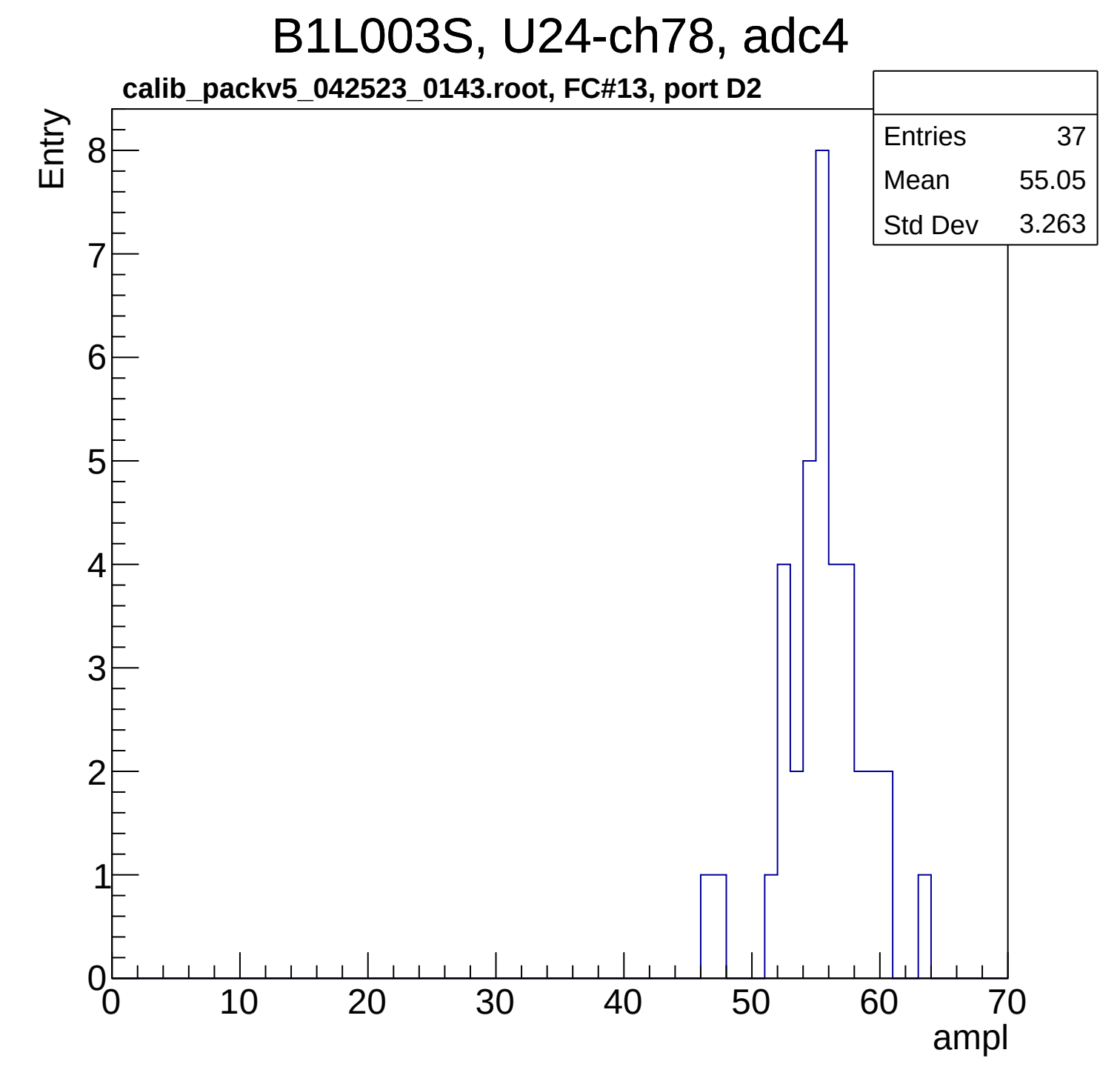
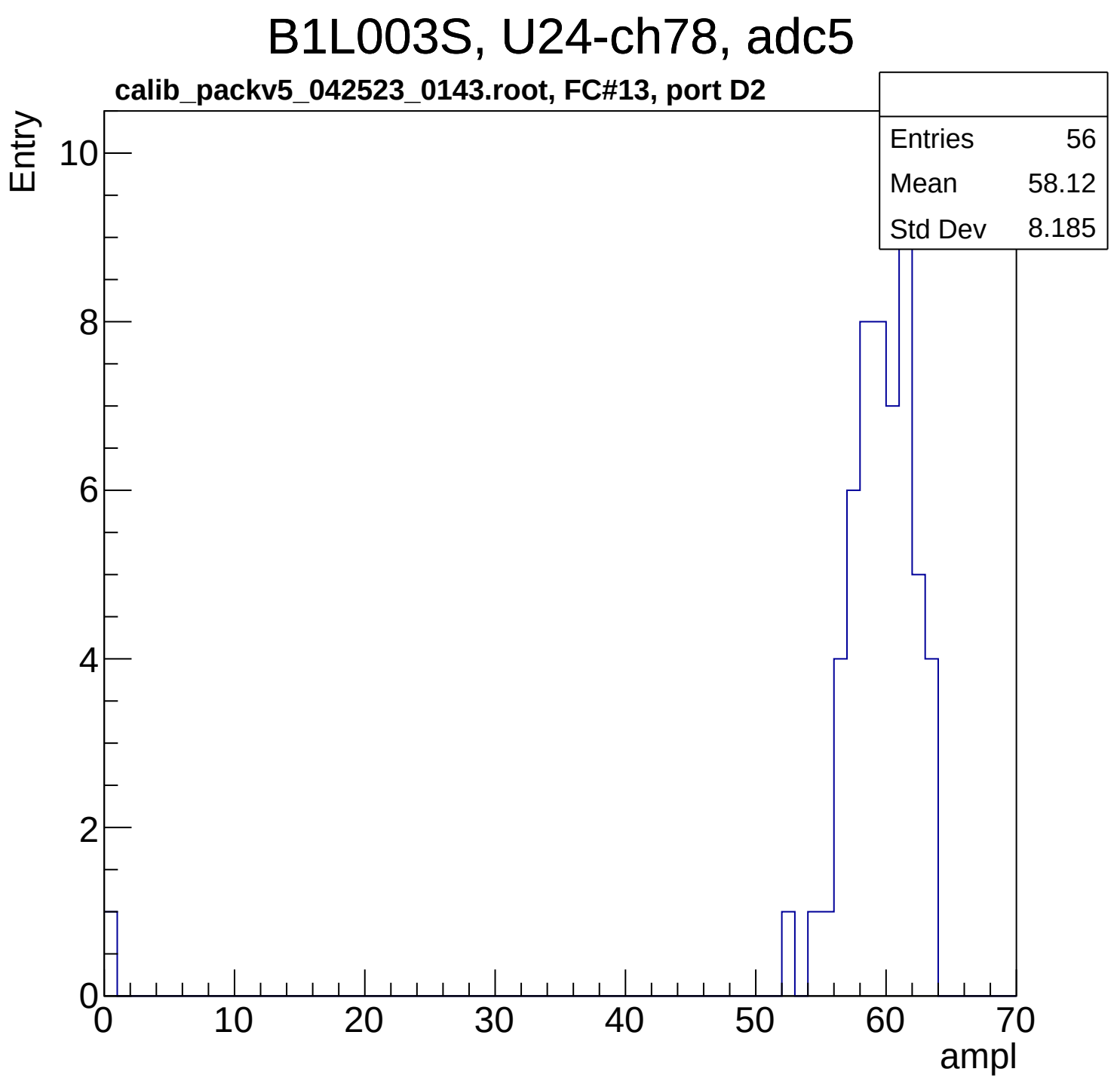

B1L003S, U24-ch78, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.42
Std Dev 4.647
Gaus mean : 29.2543
Gaus Width: 3.1317
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

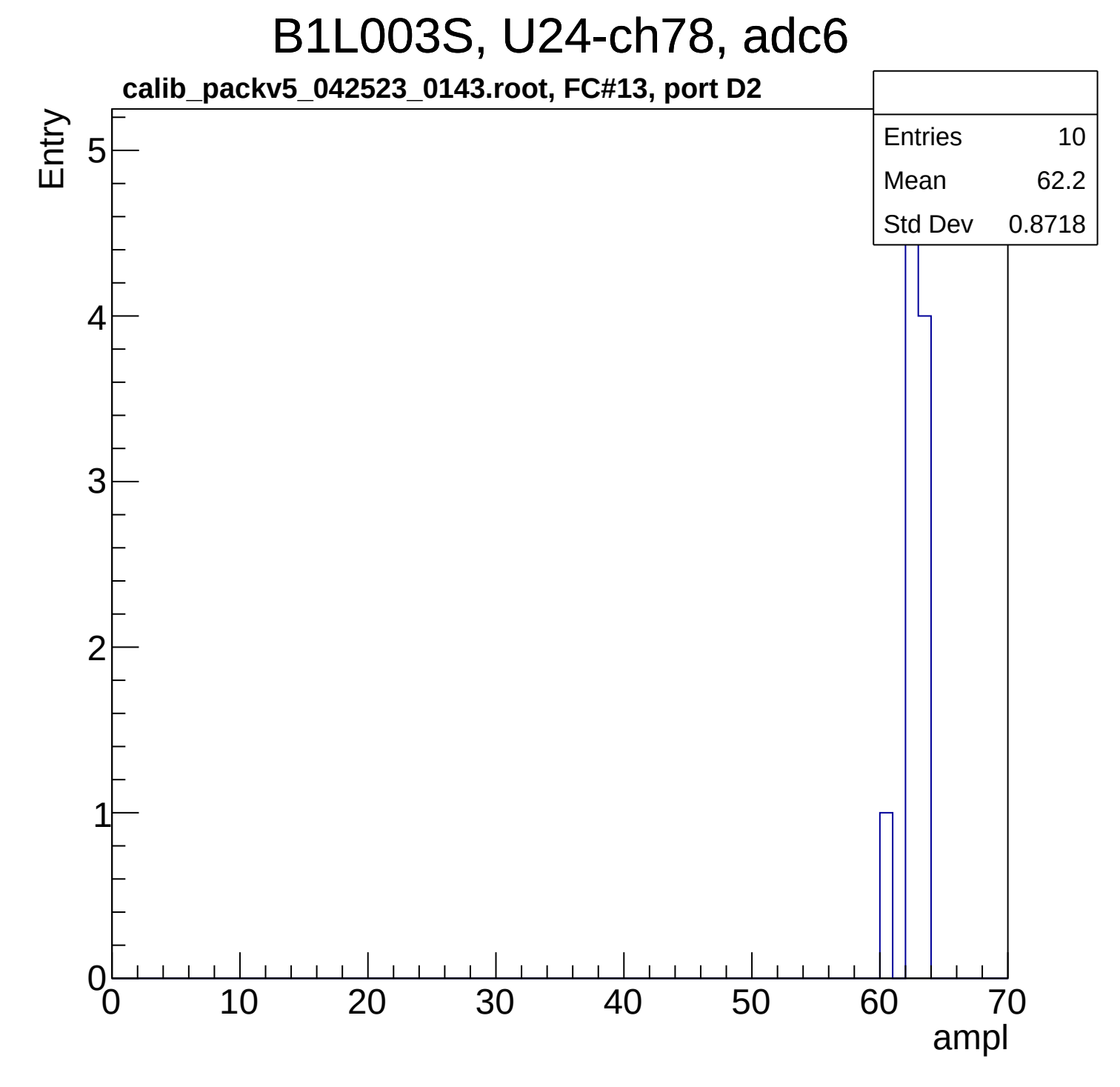

B1L003S, U24-ch78, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.88
Std Dev 2.993
Gaus mean : 36.1165
Gaus Width: 3.5151
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

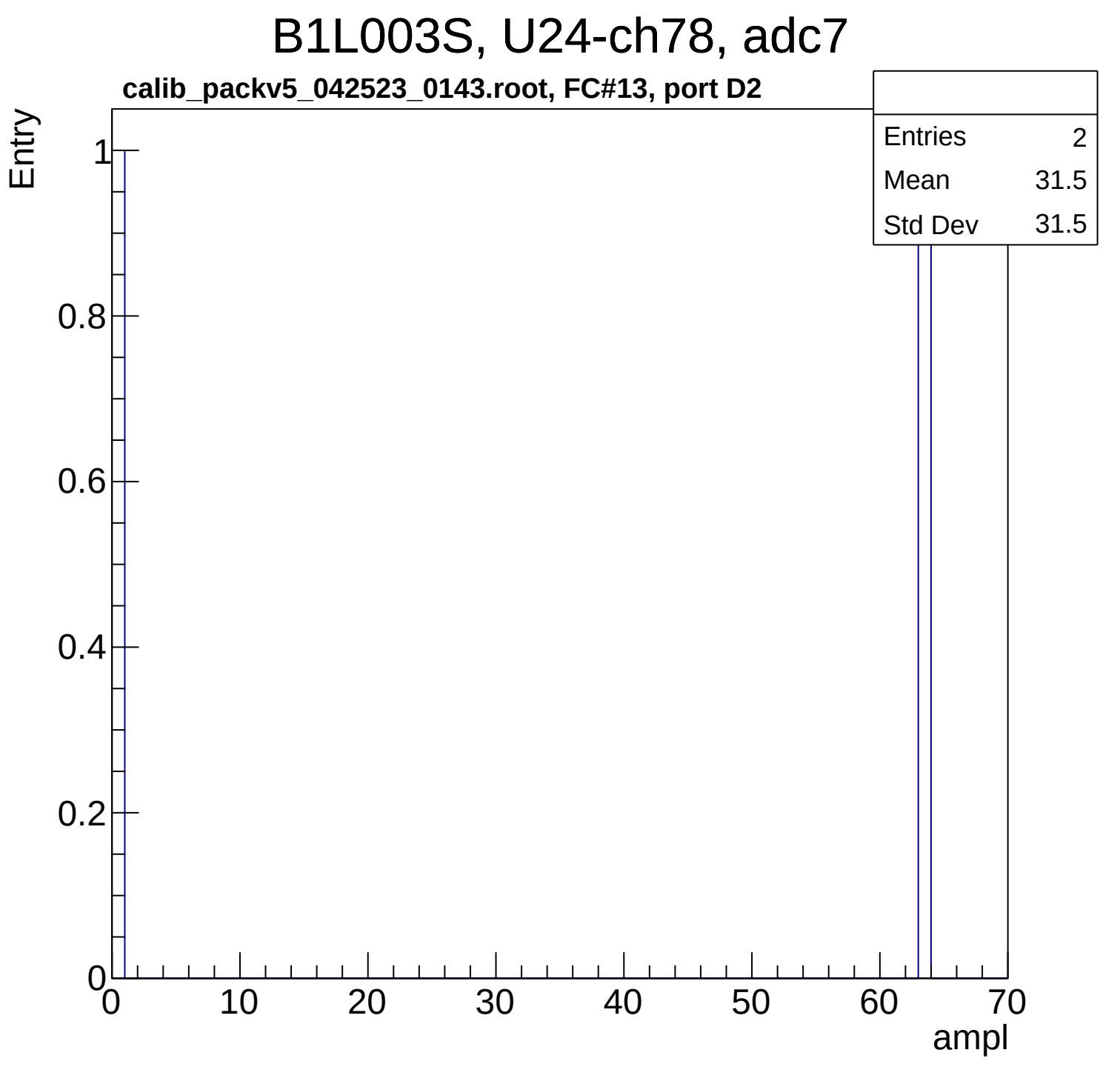

B1L003S, U24-ch78, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.73
Std Dev 2.996
Gaus mean : 43.1101
Gaus Width: 2.9510
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

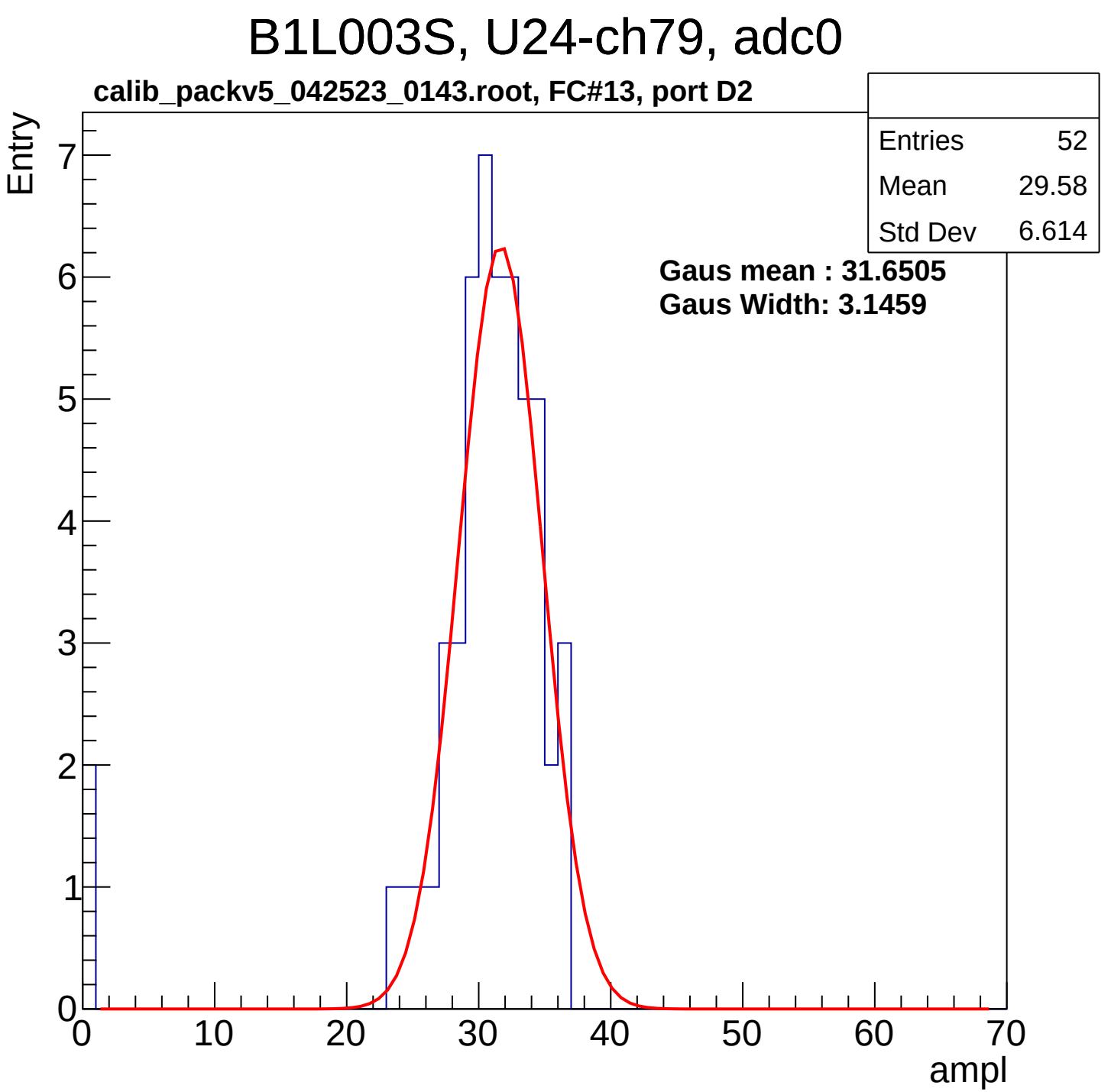

B1L003S, U24-ch78, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 49.47
Std Dev 3.143
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

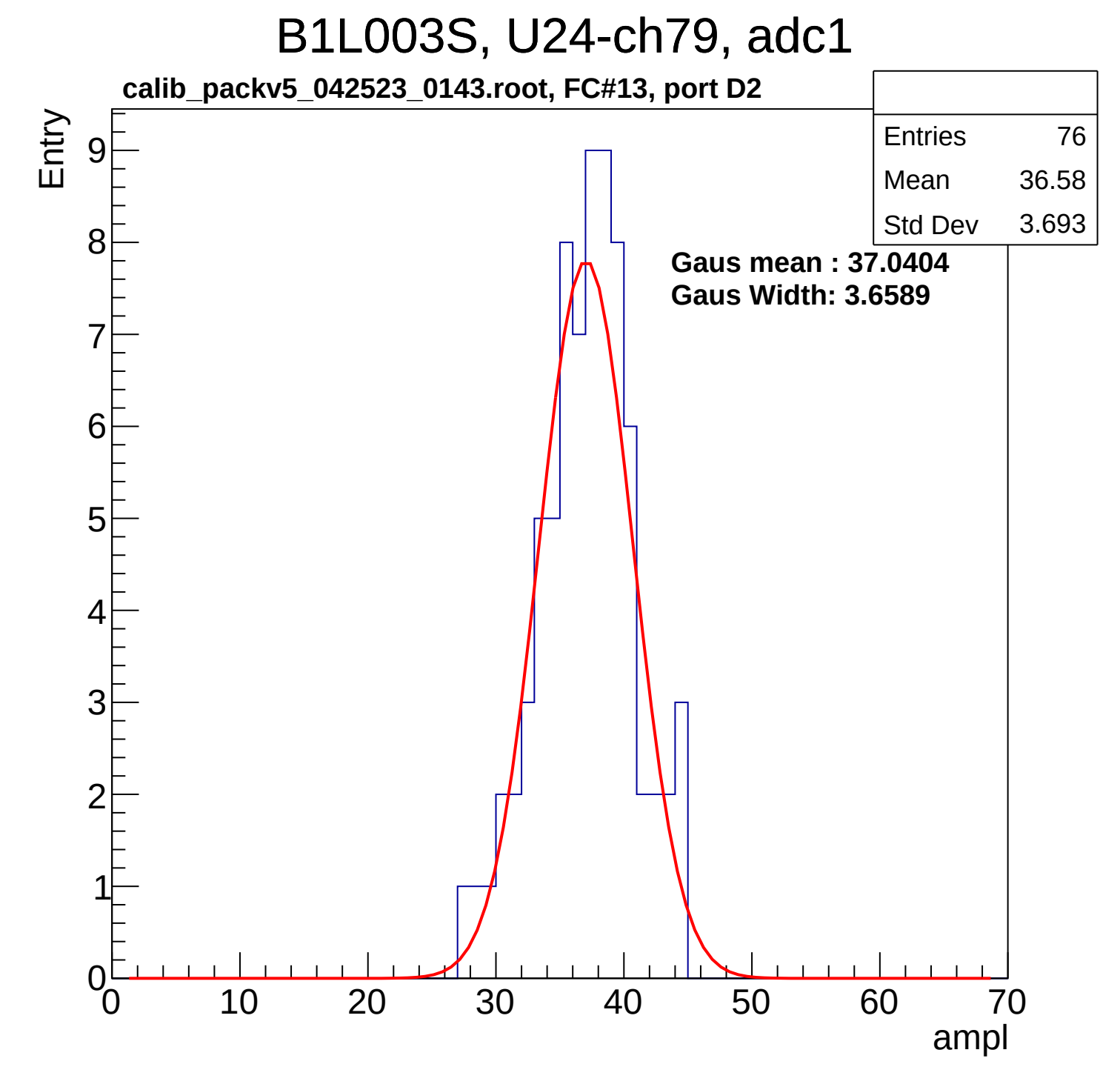

B1L003S, U24-ch78, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 55.05
Std Dev 3.263
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

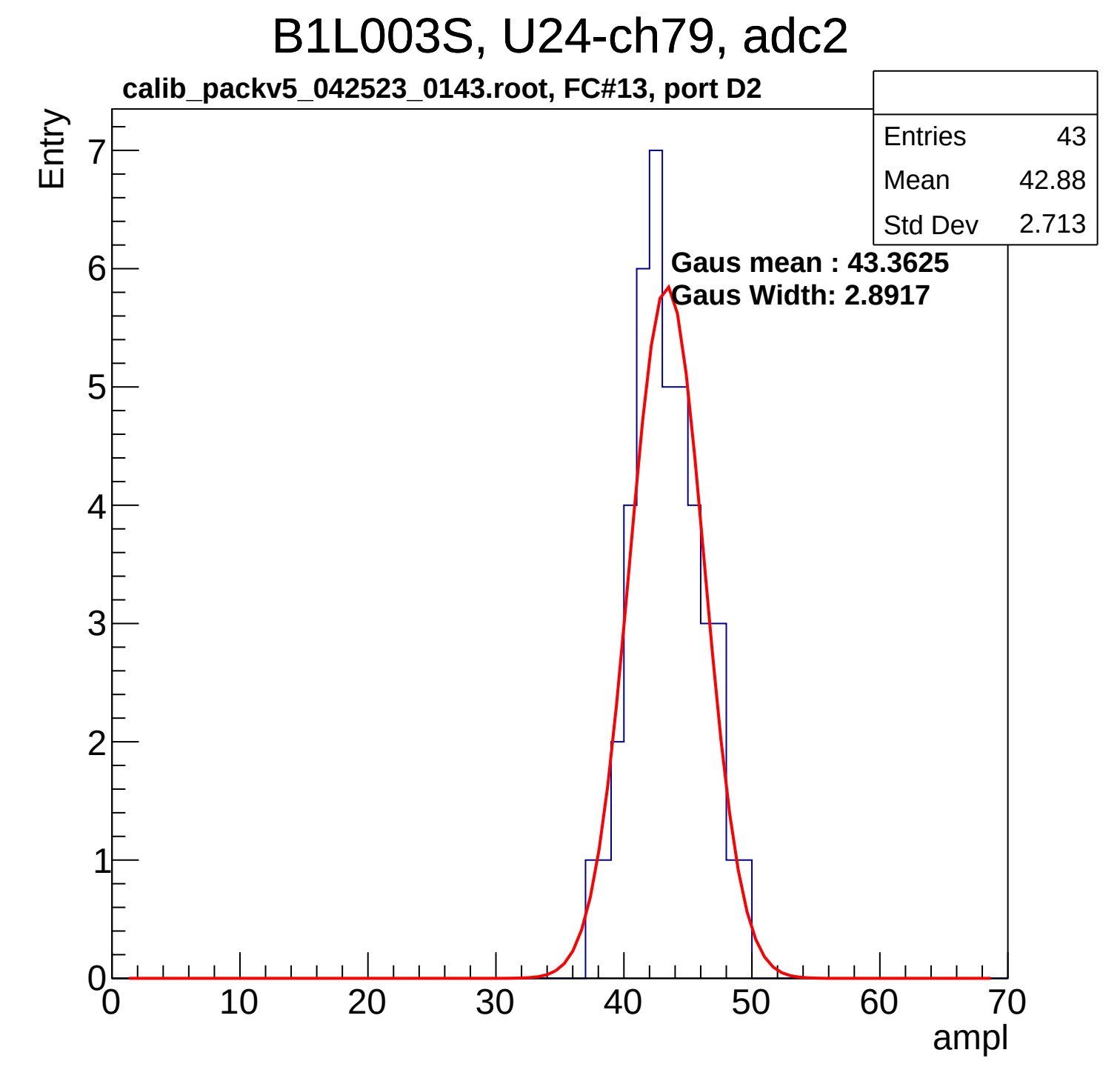

B1L003S, U24-ch78, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.12
Std Dev 8.185
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

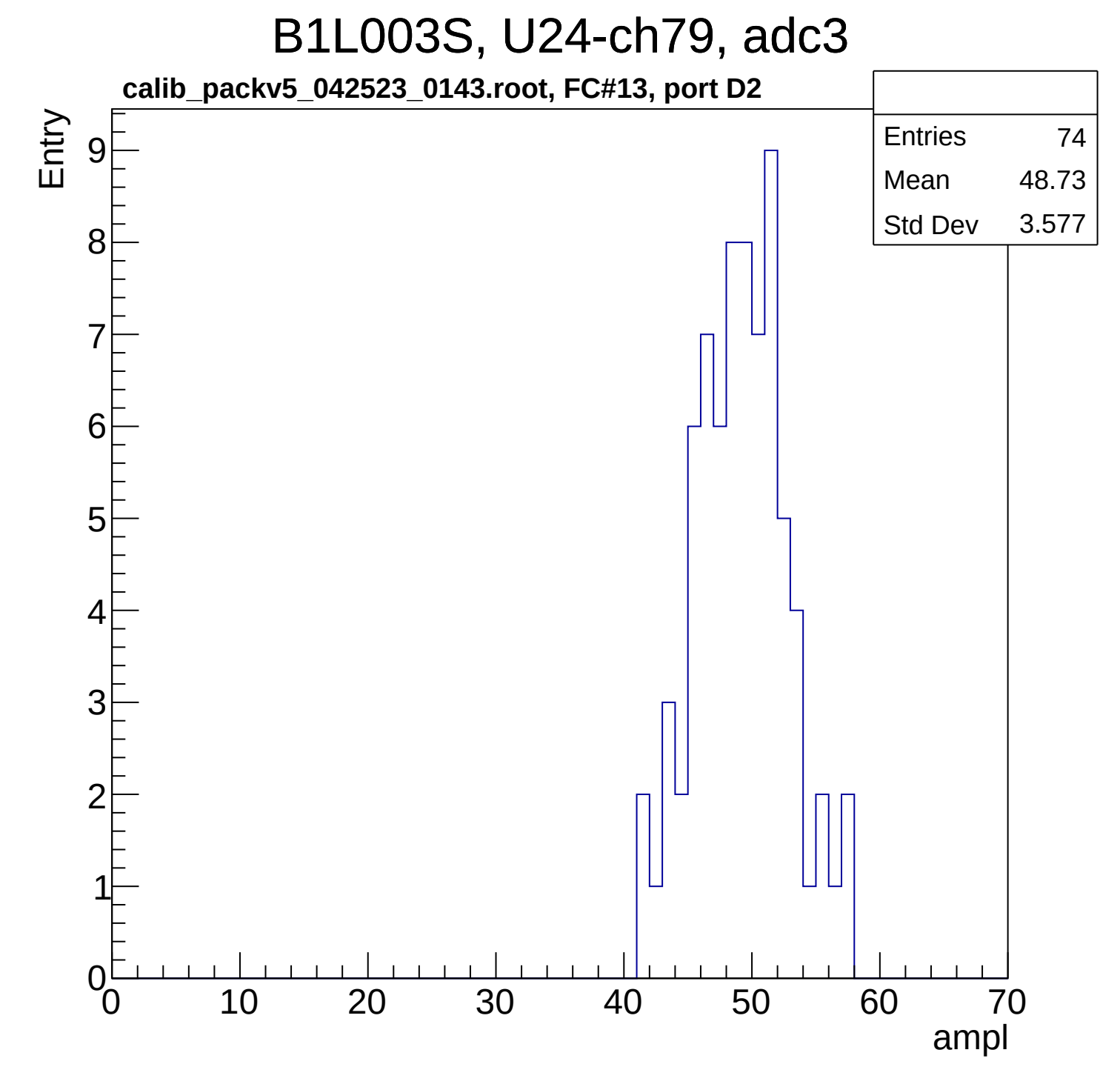

B1L003S, U24-ch78, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 62.2
Std Dev 0.8718
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

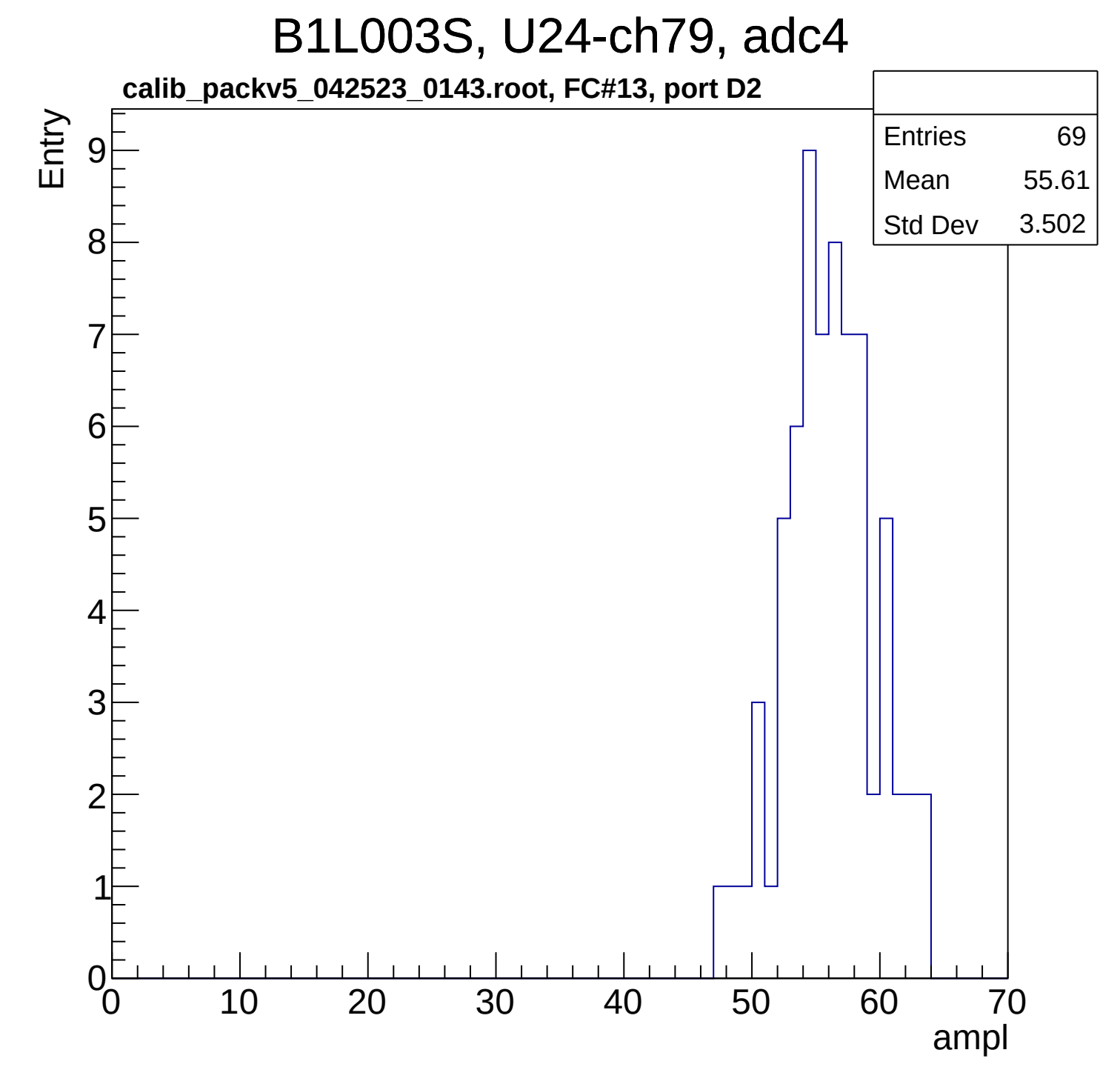

B1L003S, U24-ch78, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch79, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 29.58
Std Dev 6.614
Gaus mean : 31.6505
Gaus Width: 3.1459
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch79, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.58
Std Dev 3.693
Gaus mean : 37.0404
Gaus Width: 3.6589
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch79, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 42.88
Std Dev 2.713
Gaus mean : 43.3625
Gaus Width: 2.8917
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch79, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.73
Std Dev 3.577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch79, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 55.61
Std Dev 3.502
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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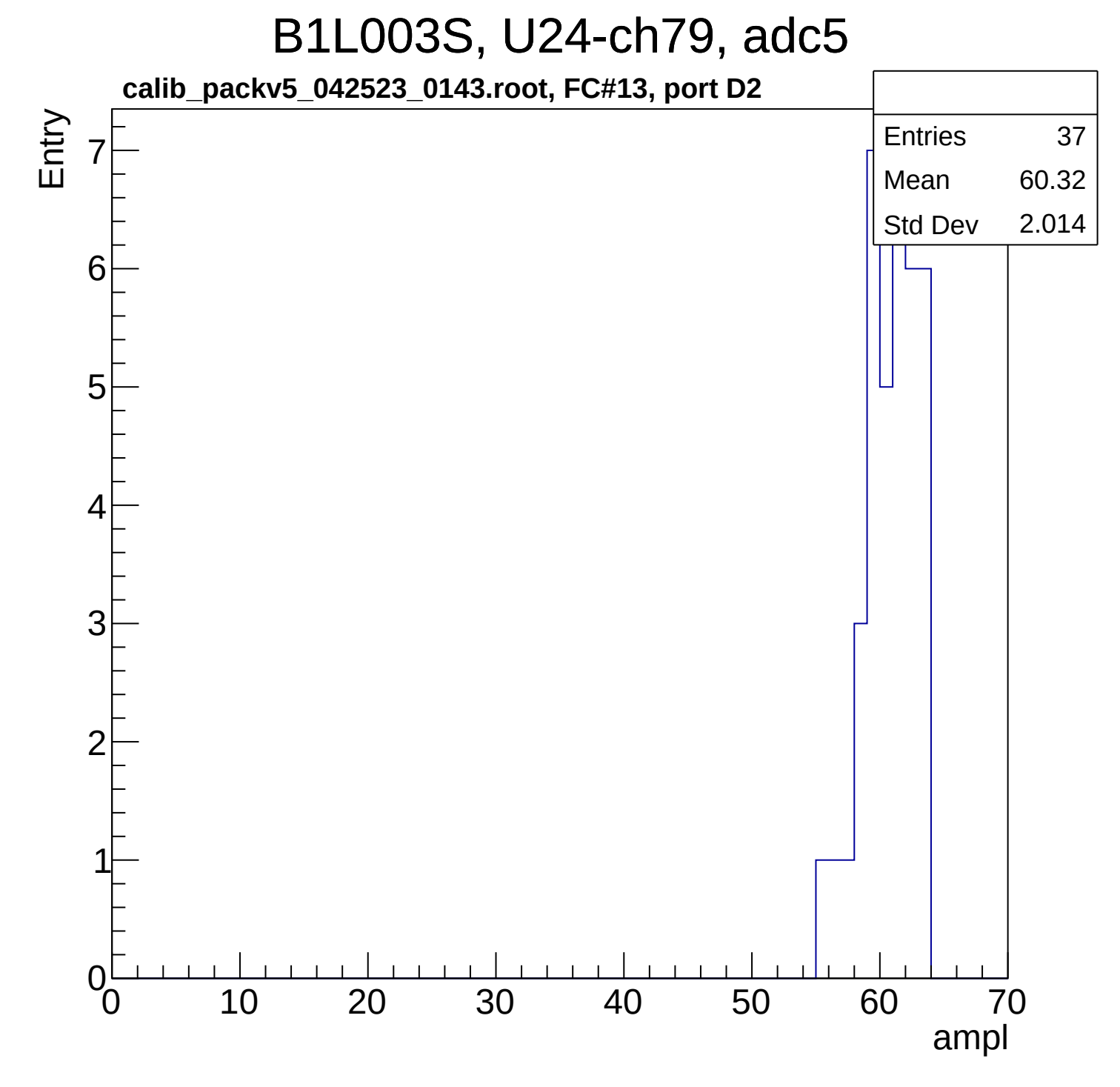
B1L003S, U24-ch79, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 60.32
Std Dev 2.014
Entry
ampl
0
1
2
3
4
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6
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0
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20
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40
50
60
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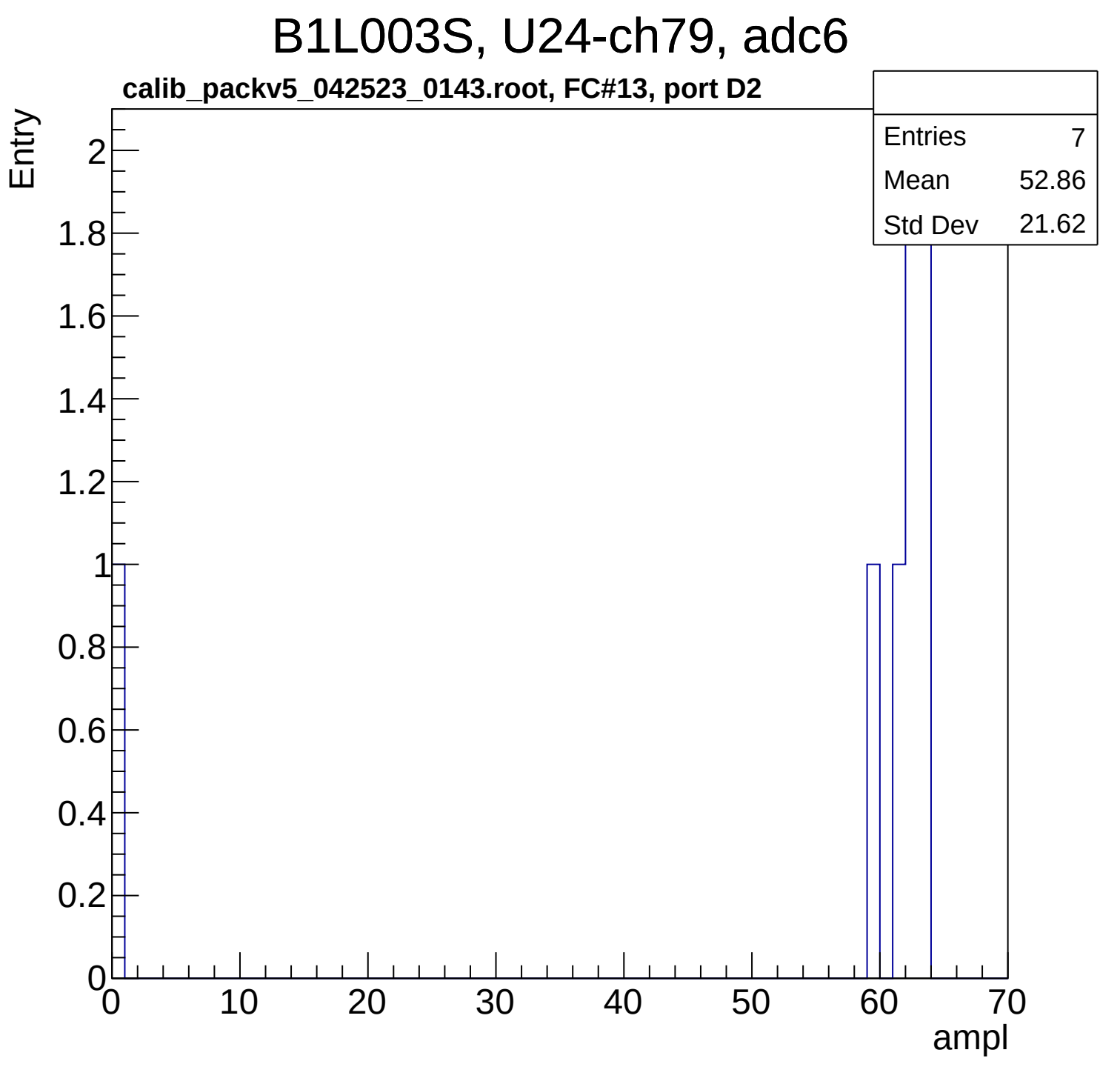
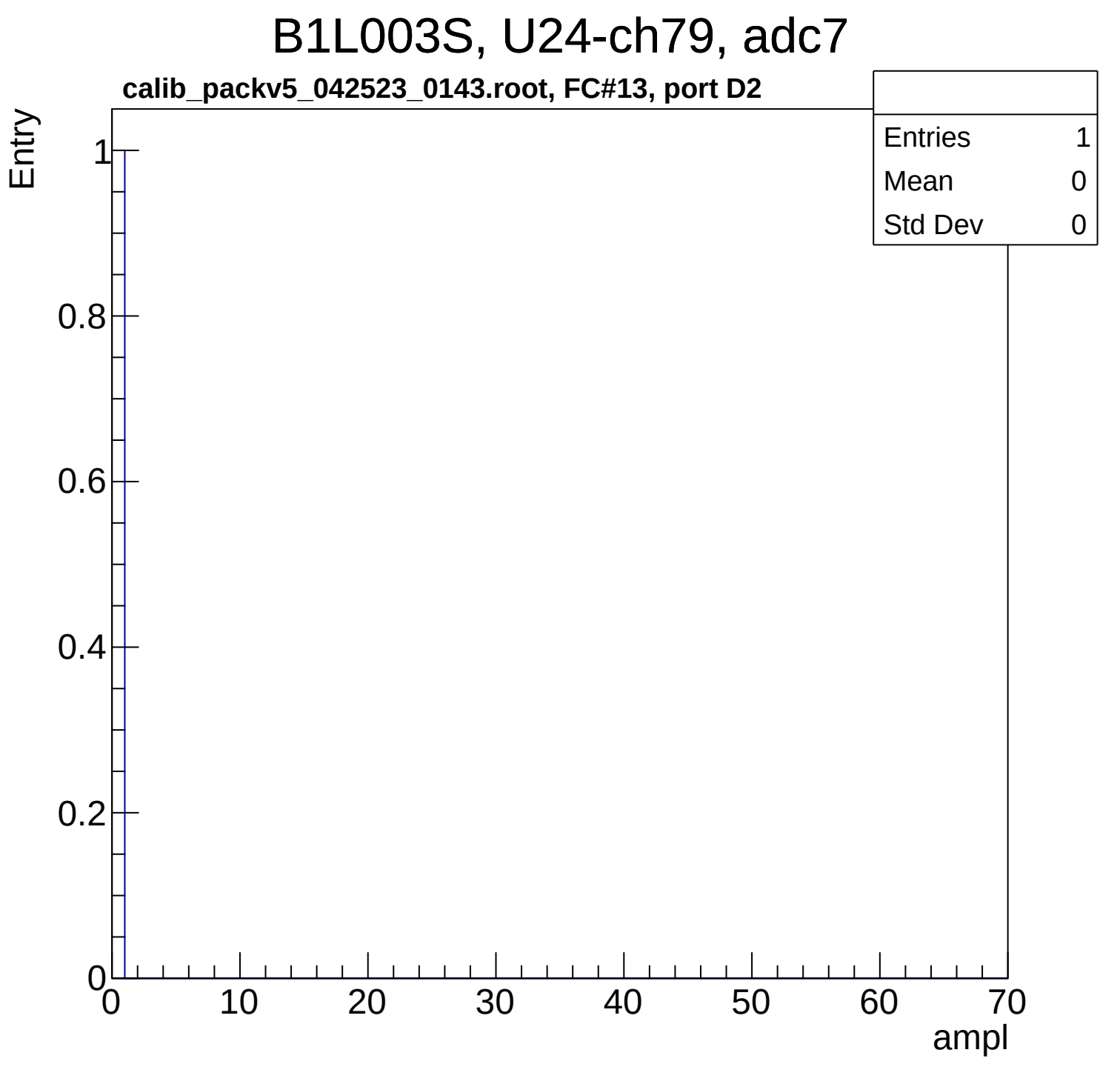
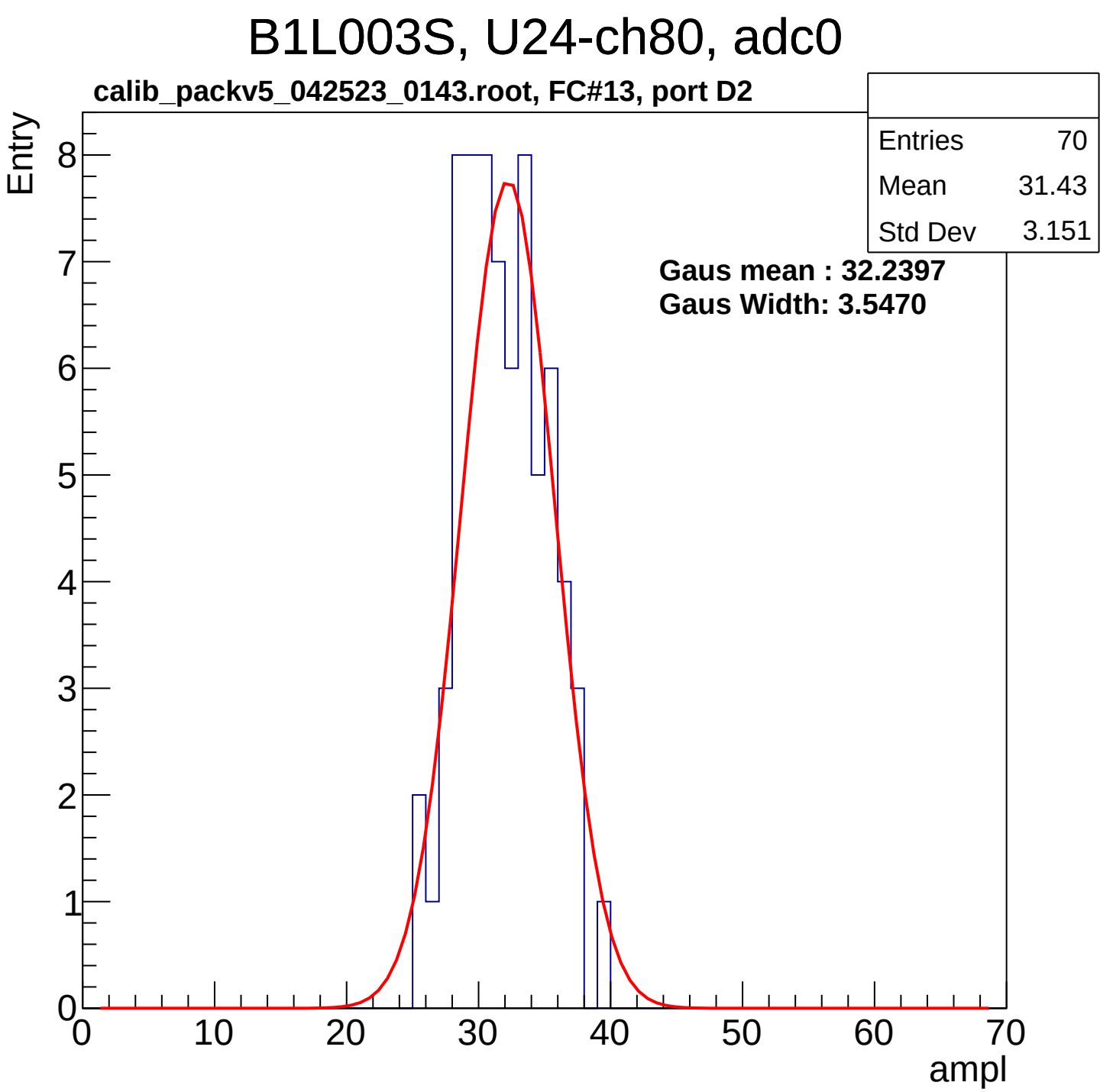
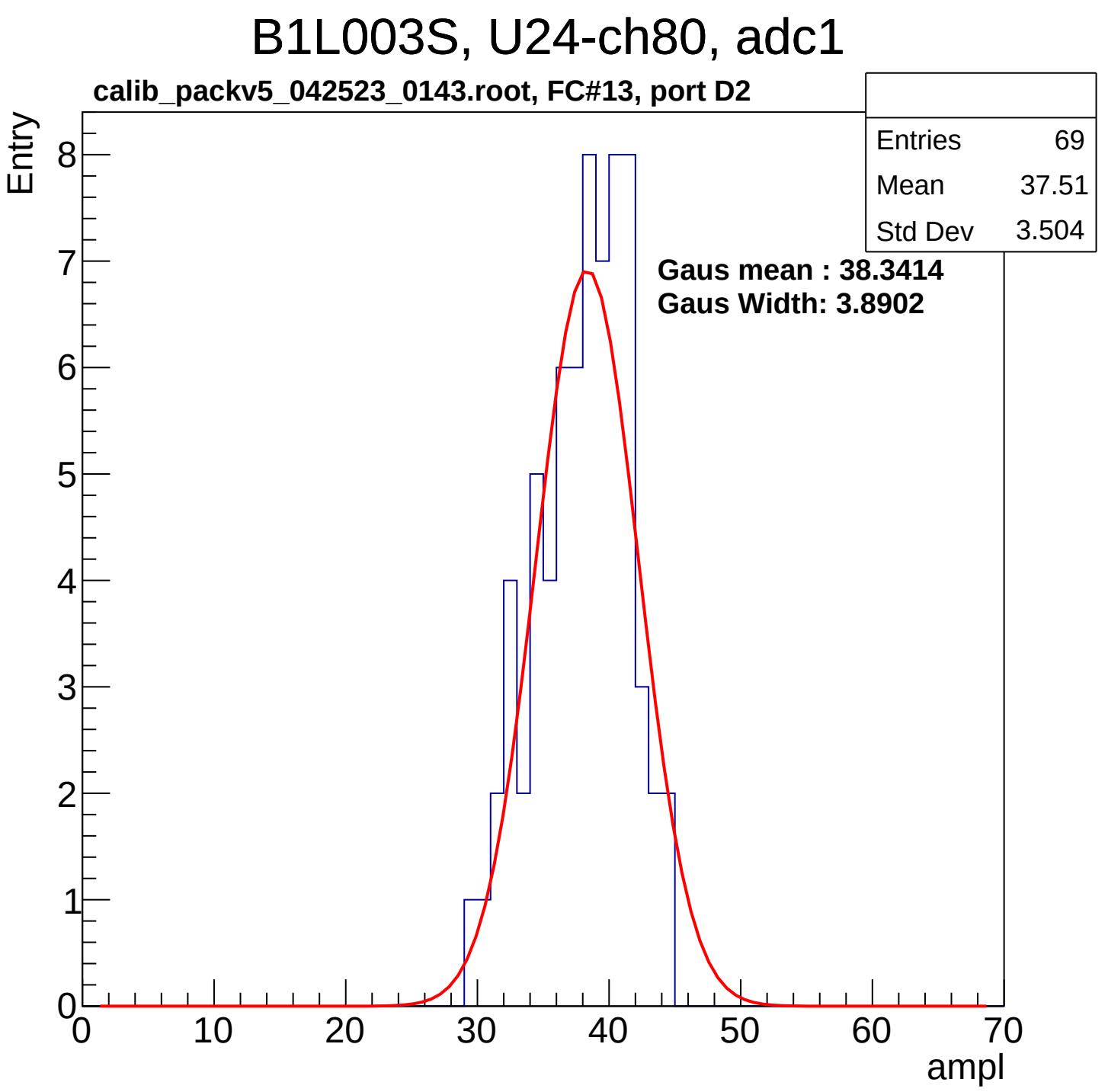

B1L003S, U24-ch79, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 52.86
Std Dev 21.62
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch79, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch80, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 31.43
Std Dev 3.151
Gaus mean : 32.2397
Gaus Width: 3.5470
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch80, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 37.51
Std Dev 3.504
Gaus mean : 38.3414
Gaus Width: 3.8902
Entry
ampl
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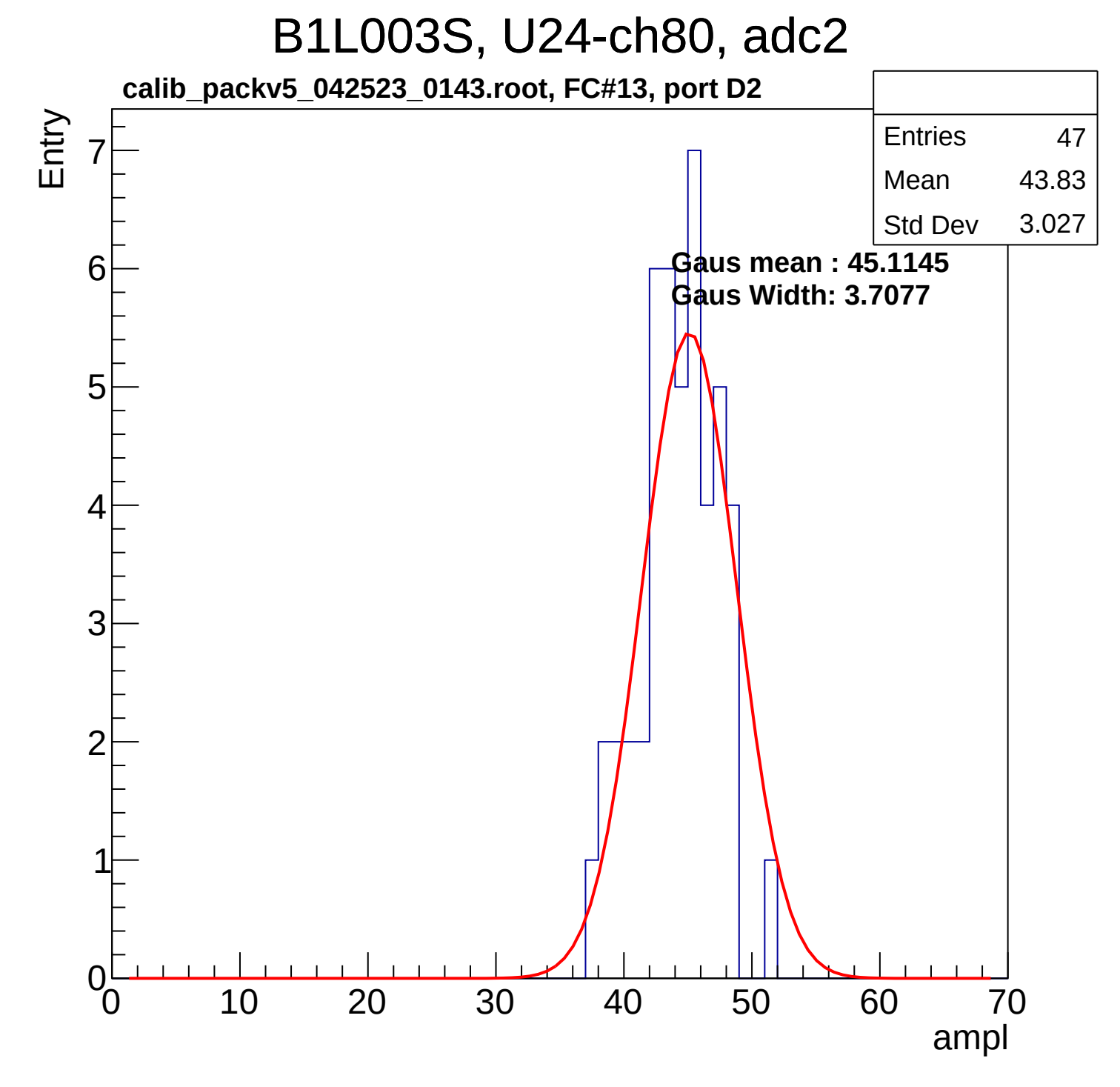
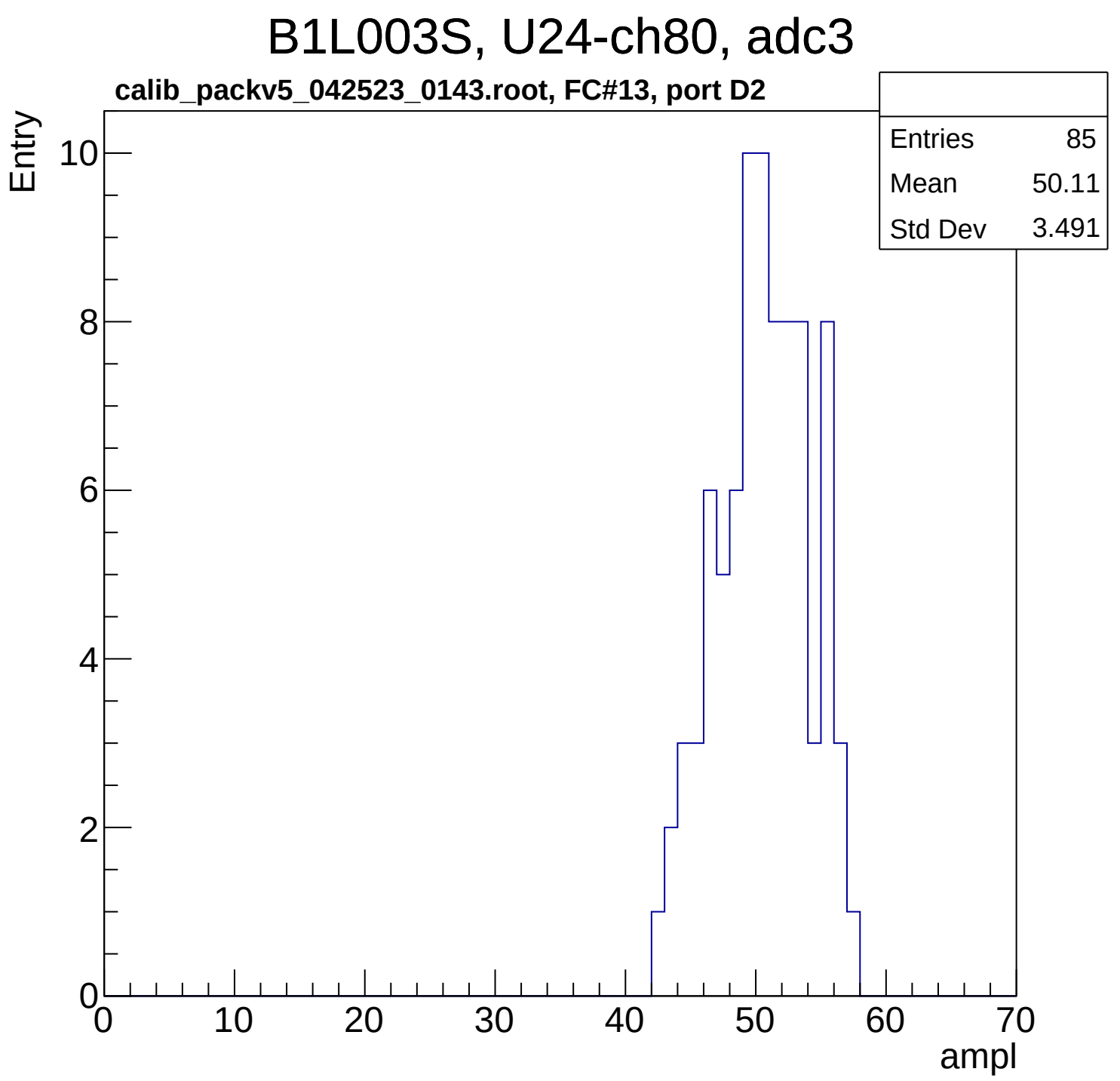
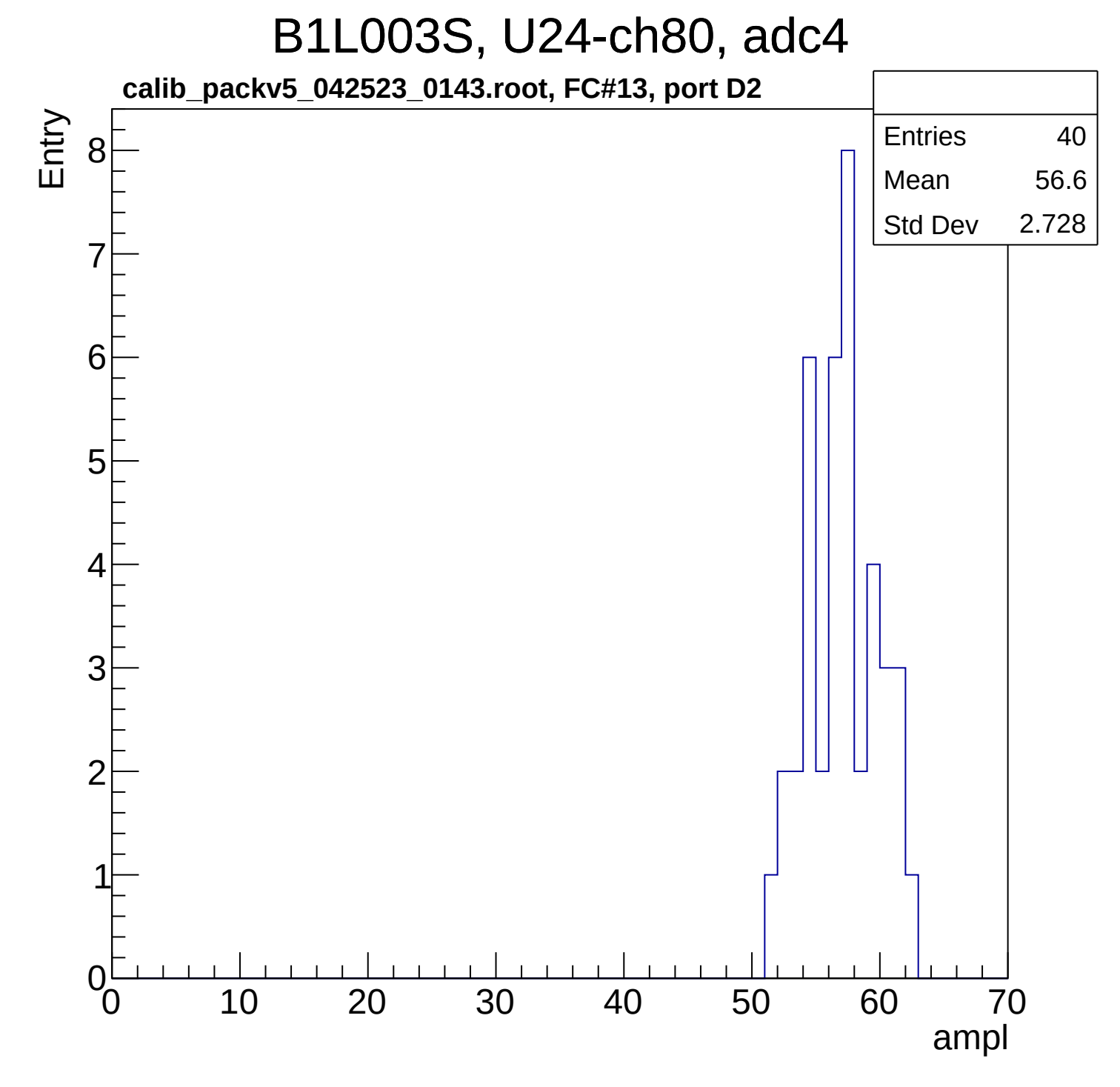
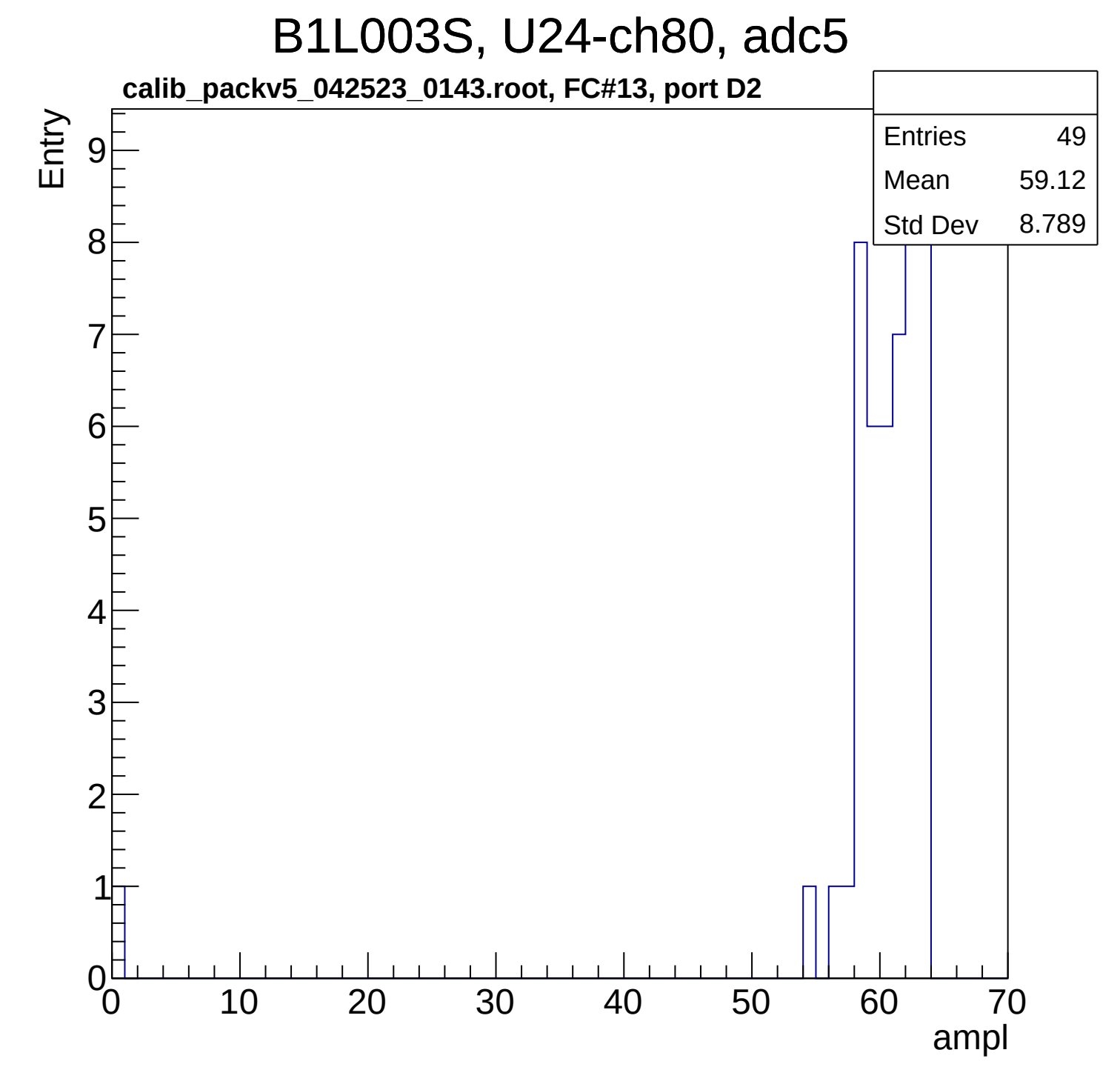
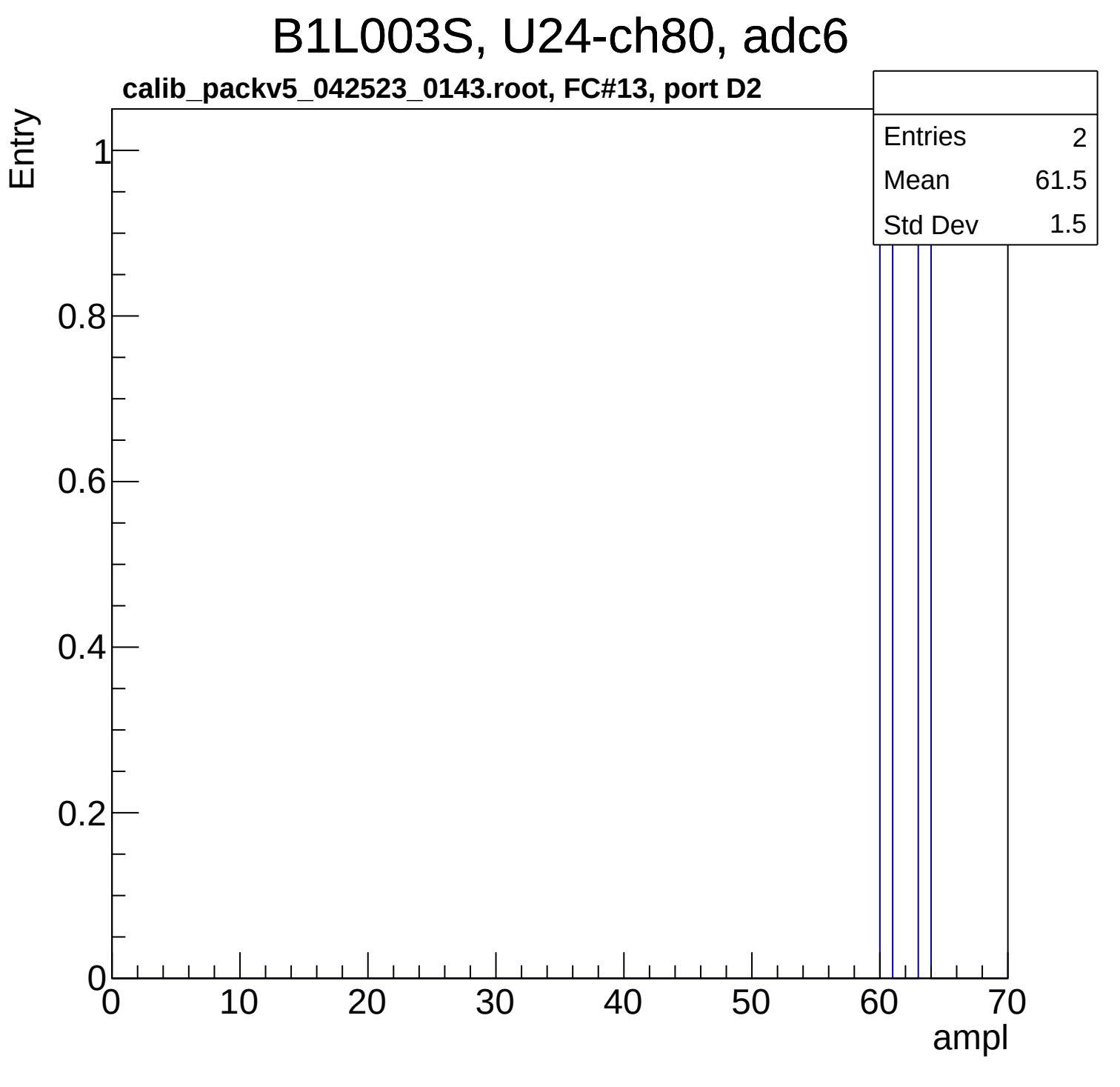
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

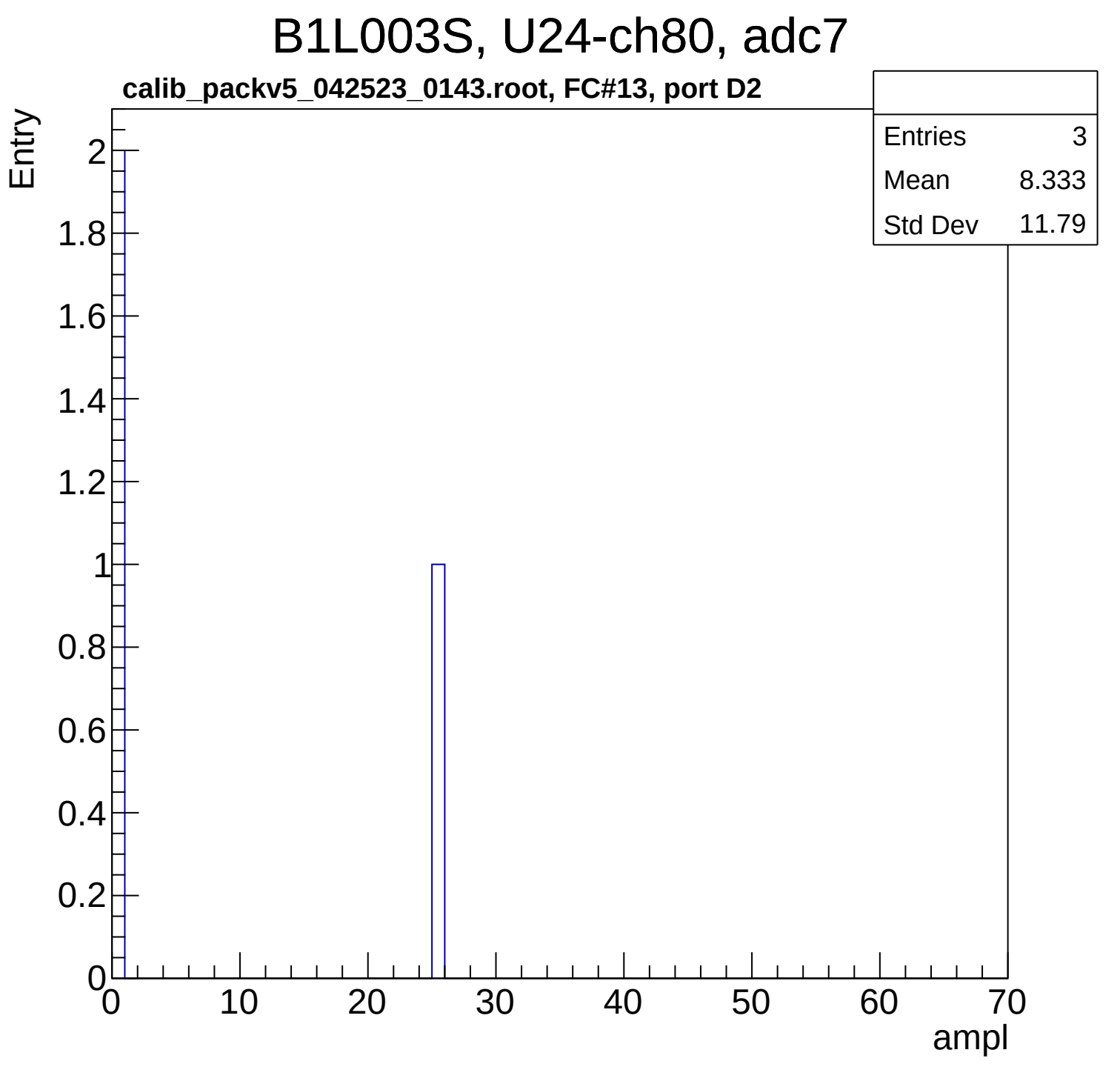

B1L003S, U24-ch80, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 43.83
Std Dev 3.027
Gaus mean : 45.1145
Gaus Width: 3.7077
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

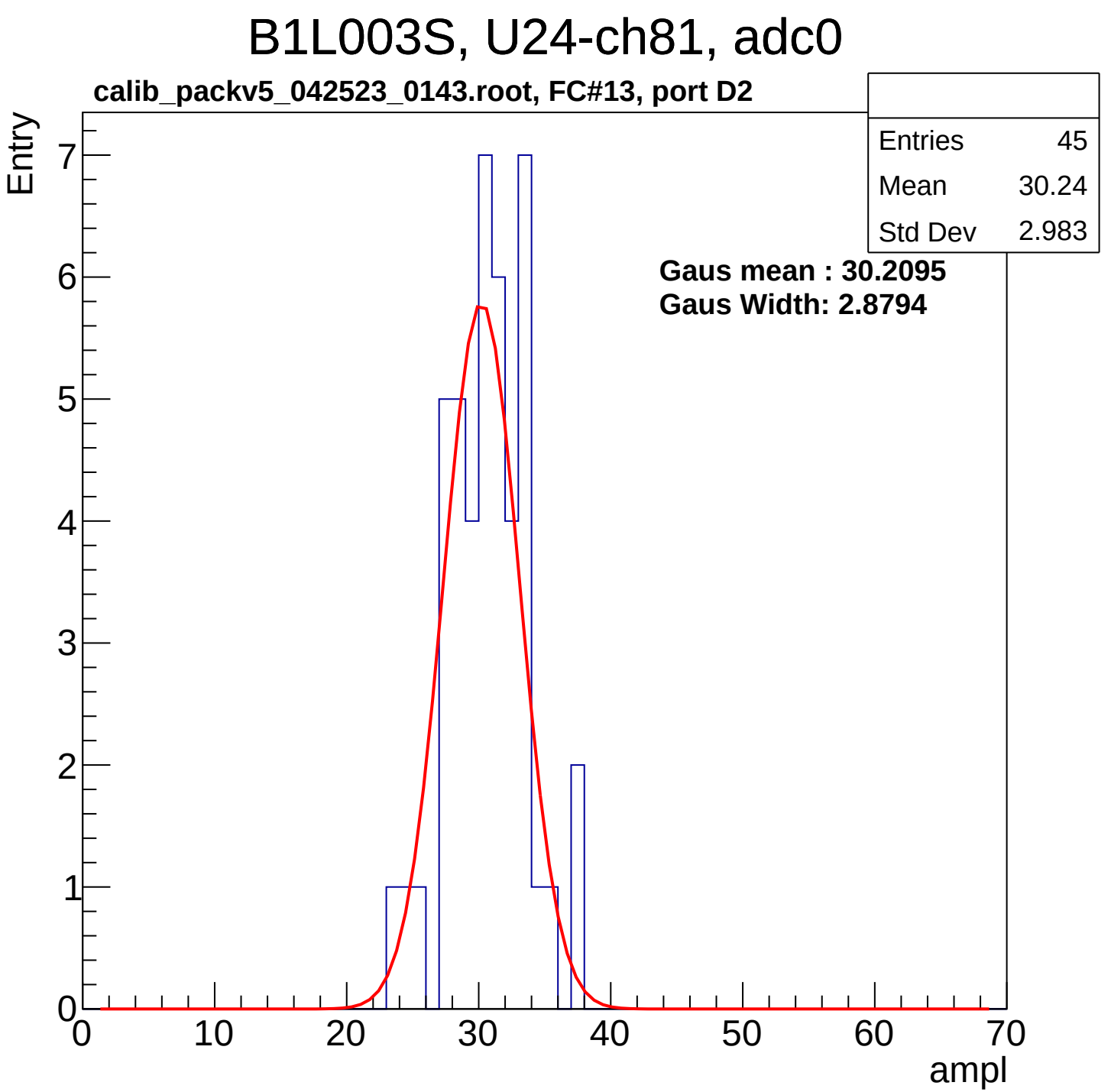

B1L003S, U24-ch80, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 50.11
Std Dev 3.491
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch80, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 56.6
Std Dev 2.728
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch80, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 59.12
Std Dev 8.789
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch80, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch80, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch81, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 30.24
Std Dev 2.983
Gaus mean : 30.2095
Gaus Width: 2.8794
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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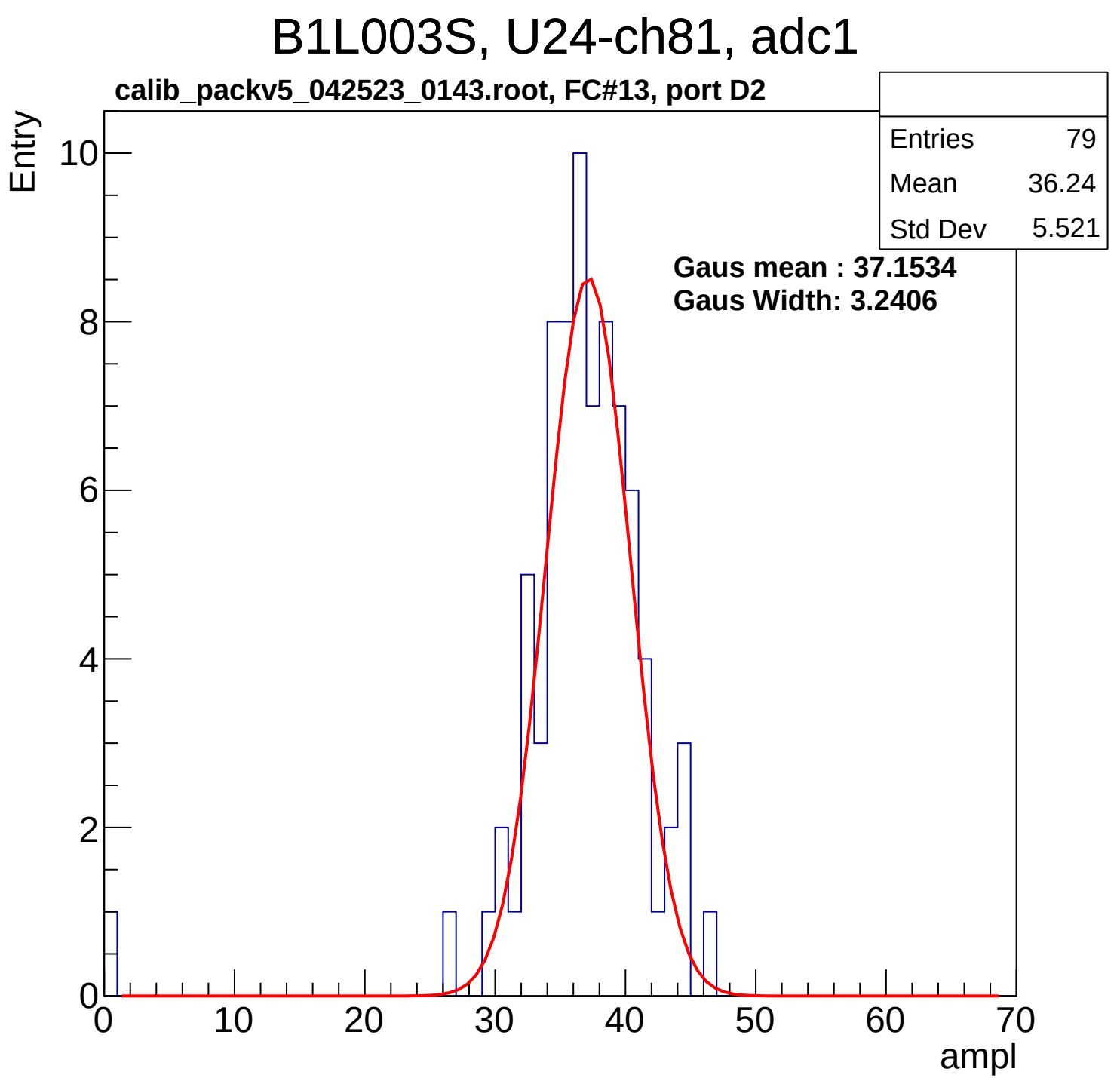
B1L003S, U24-ch81, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 36.24
Std Dev 5.521
Gaus mean : 37.1534
Gaus Width: 3.2406
Entry
ampl
0
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4
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8
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0
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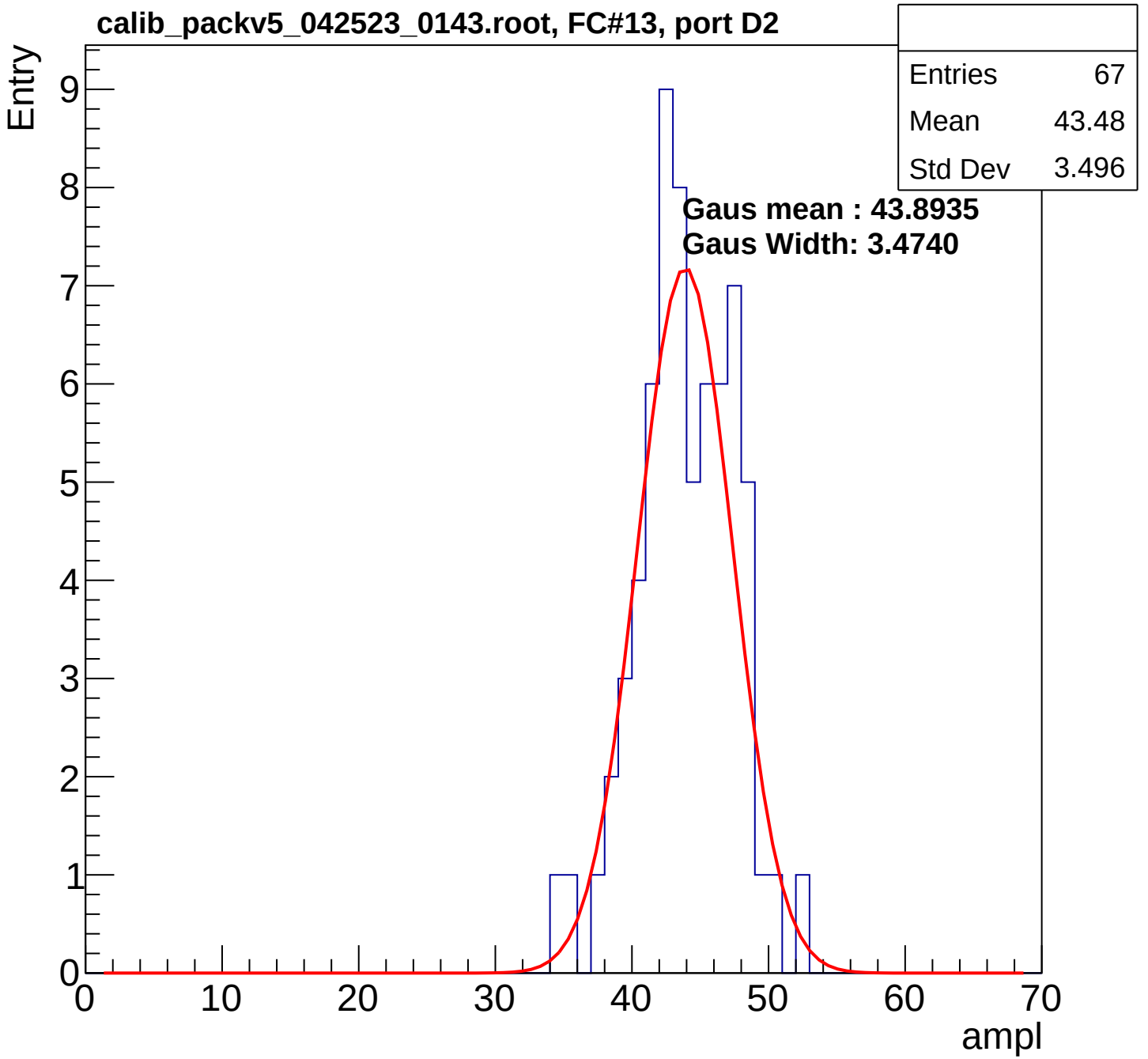
B1L003S, U24-ch81, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.48
Std Dev 3.496
Gaus mean : 43.8935
Gaus Width: 3.4740
Entry
ampl
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2
3
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0
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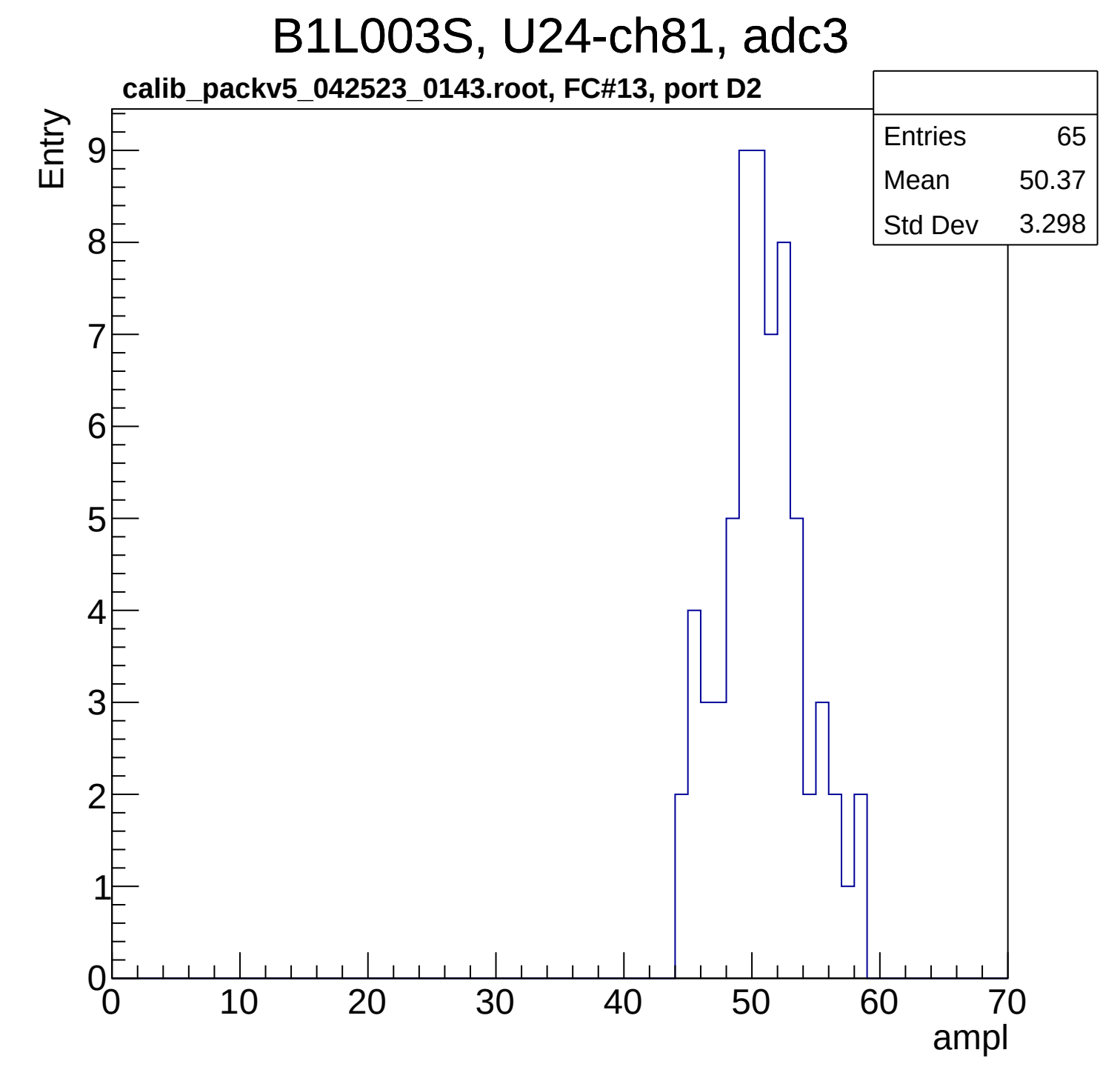
B1L003S, U24-ch81, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 50.37
Std Dev 3.298
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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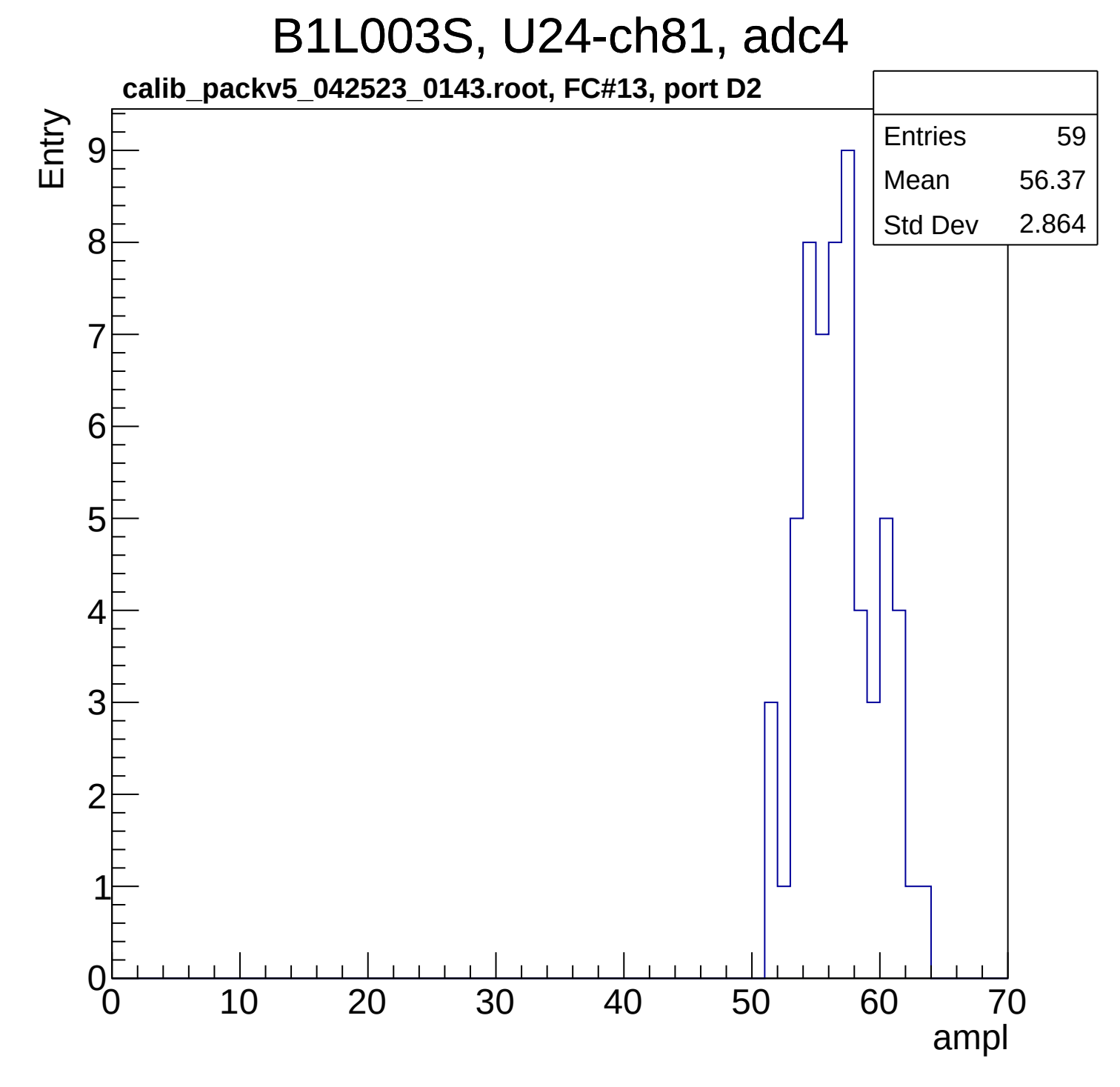
B1L003S, U24-ch81, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 56.37
Std Dev 2.864
Entry
ampl
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1
2
3
4
5
6
7
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0
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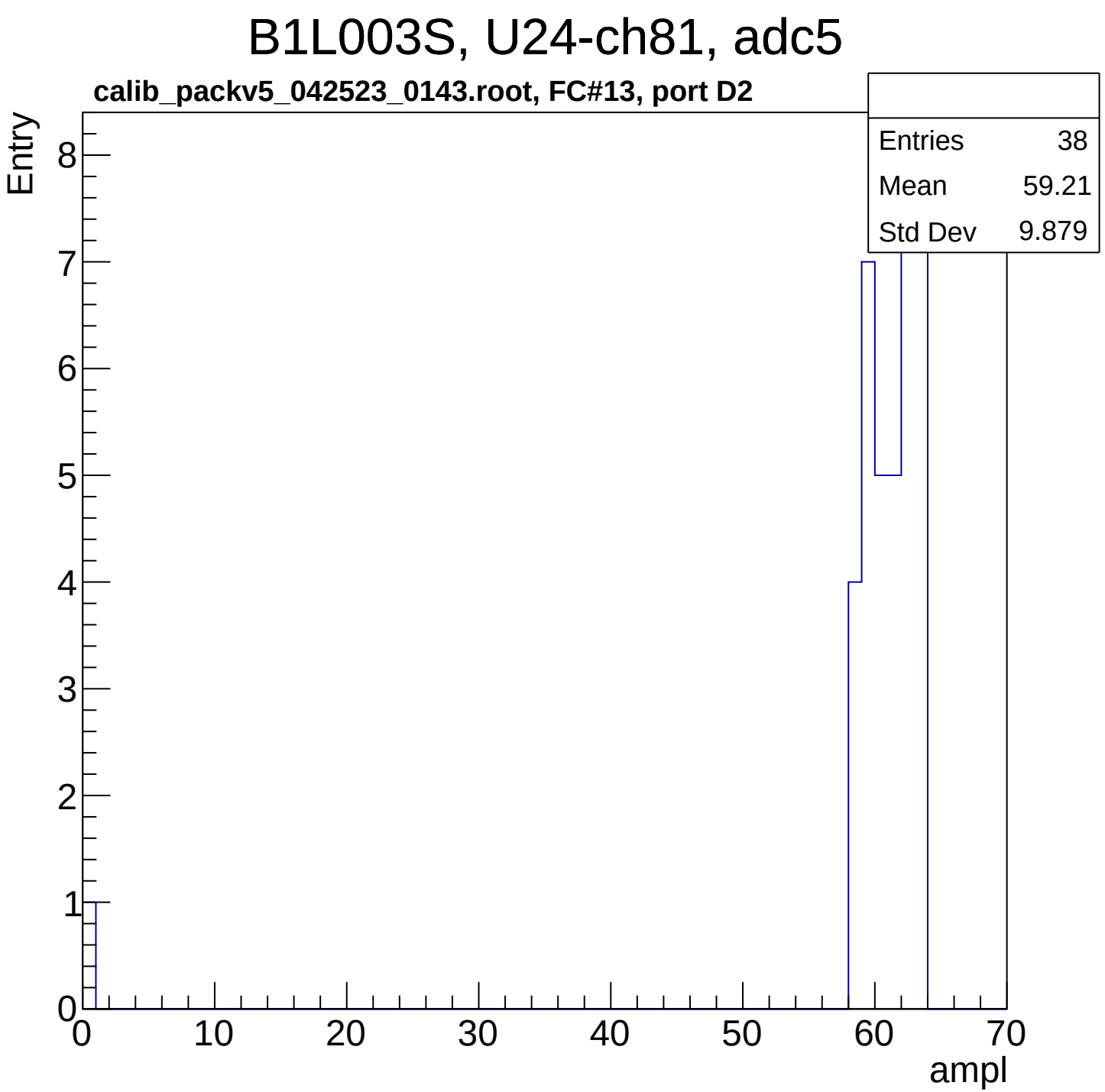
B1L003S, U24-ch81, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 59.21
Std Dev 9.879
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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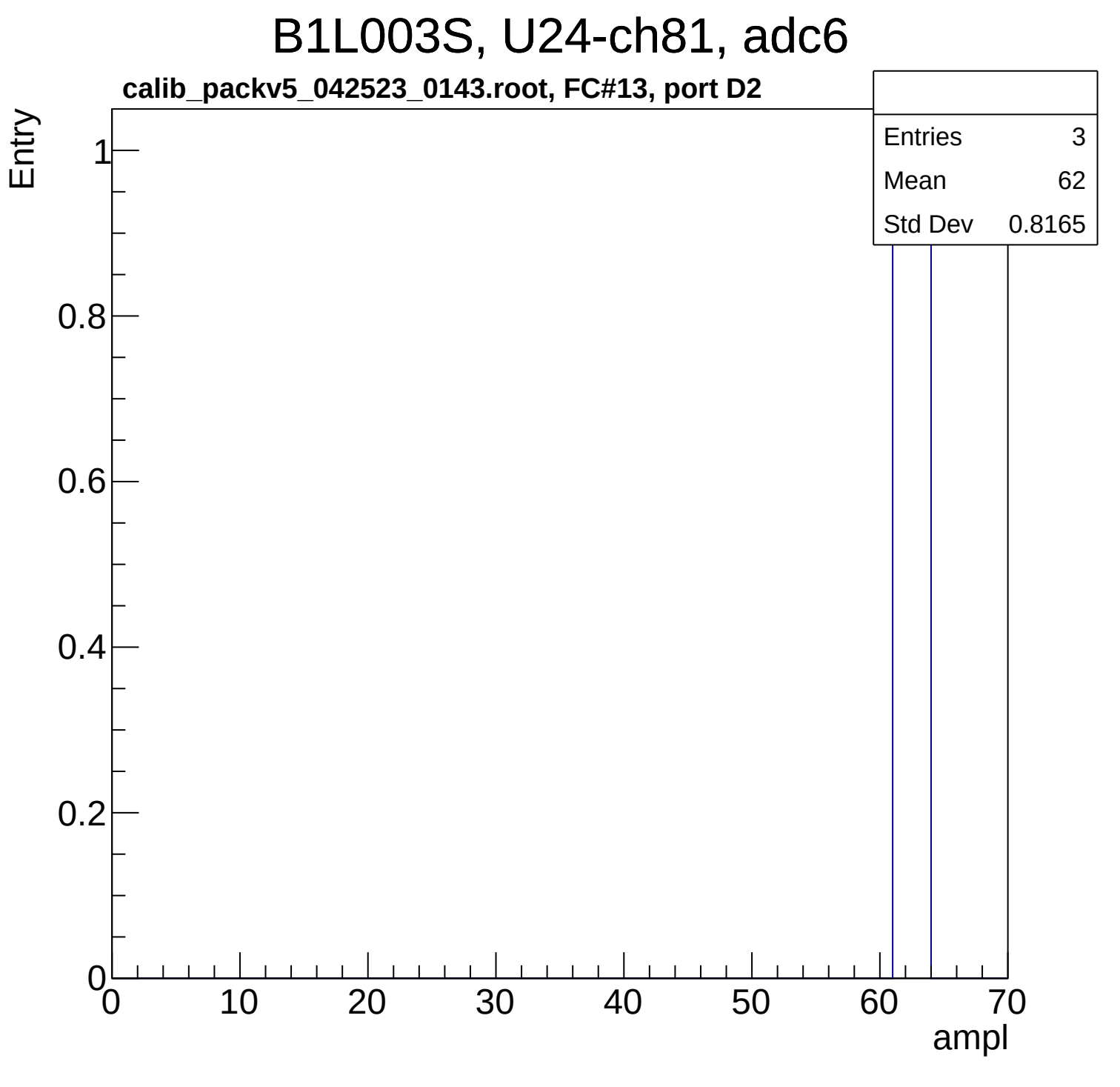
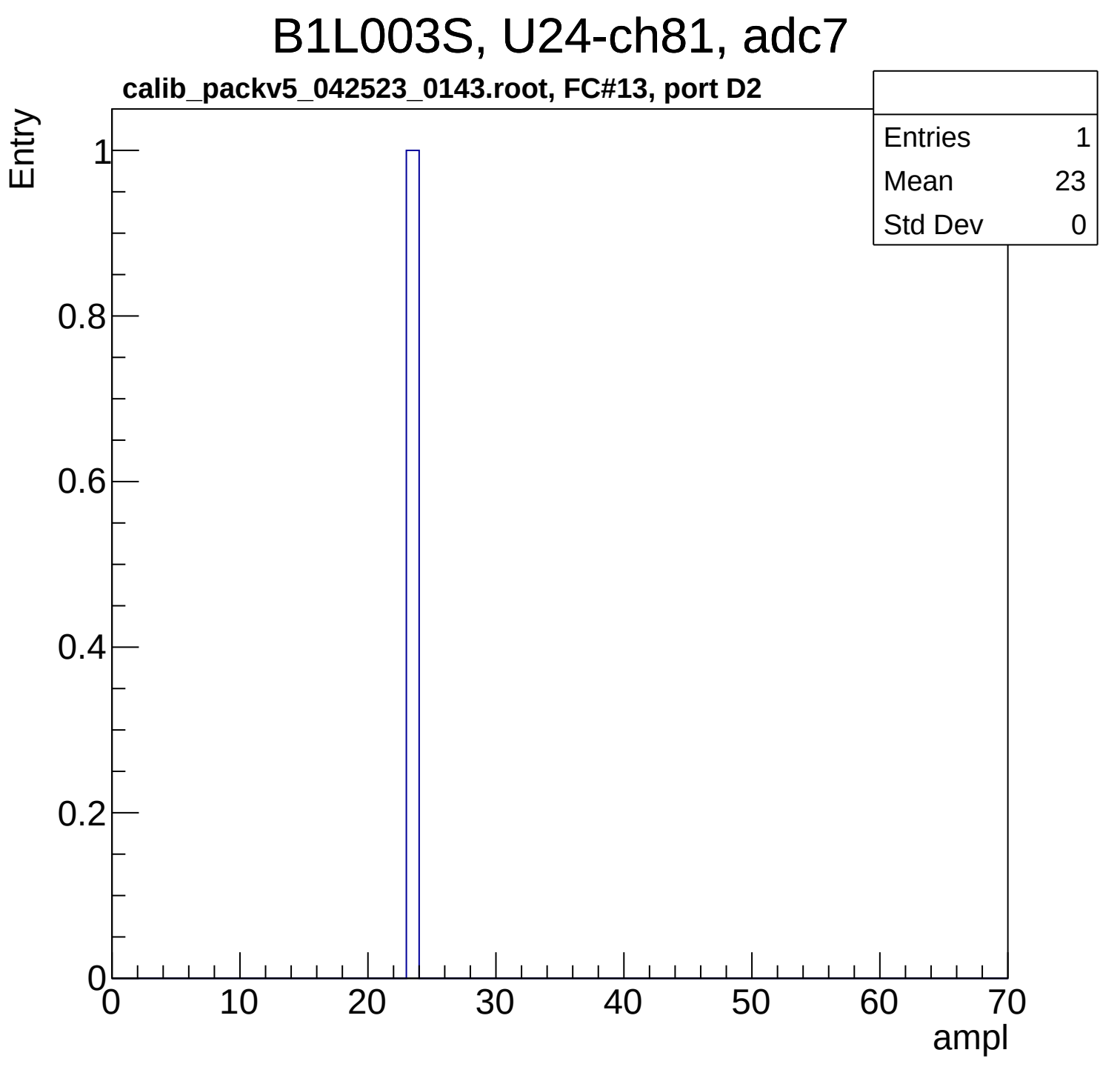

B1L003S, U24-ch81, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch81, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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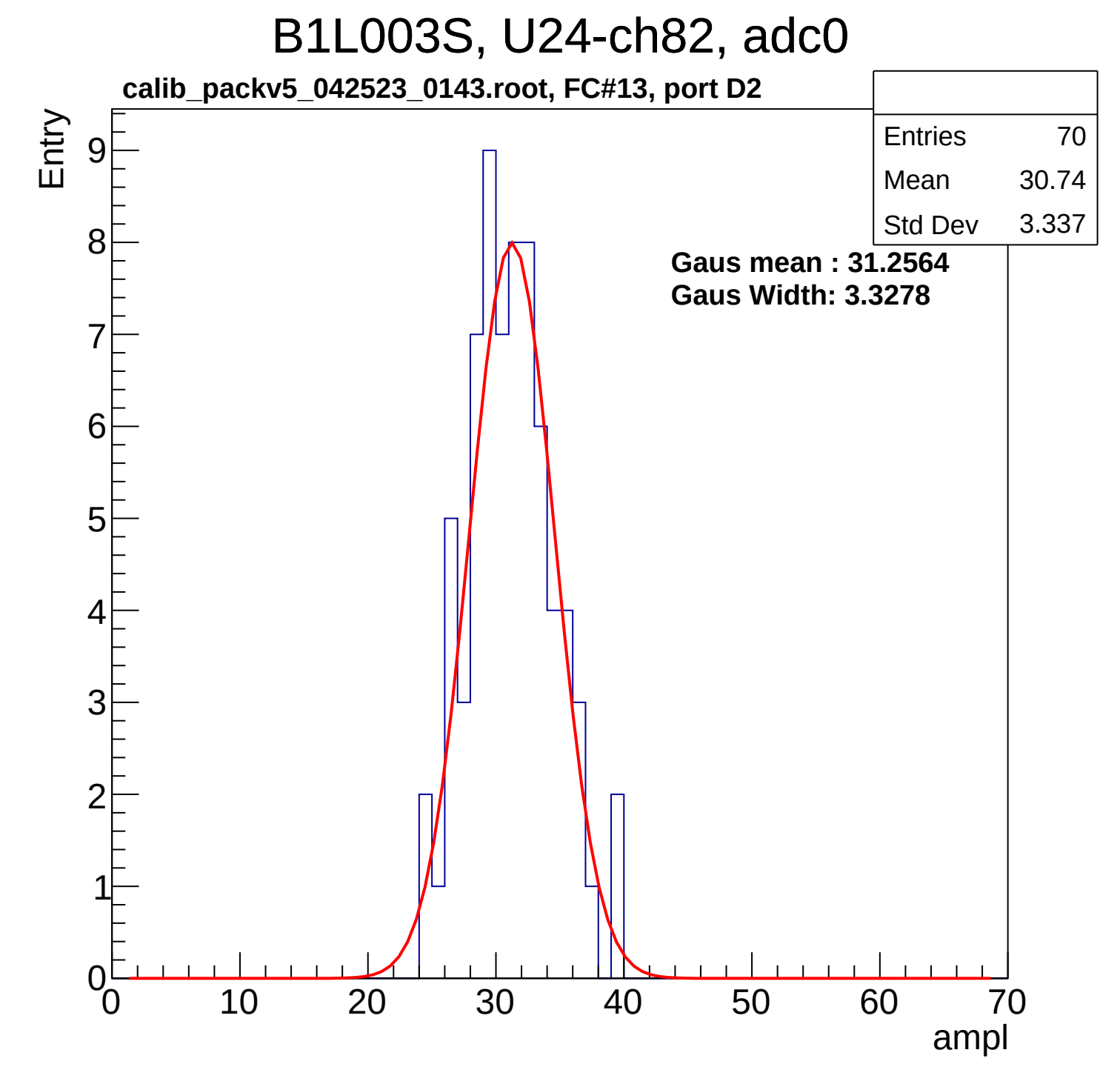
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch82, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 30.74
Std Dev 3.337
Gaus mean : 31.2564
Gaus Width: 3.3278
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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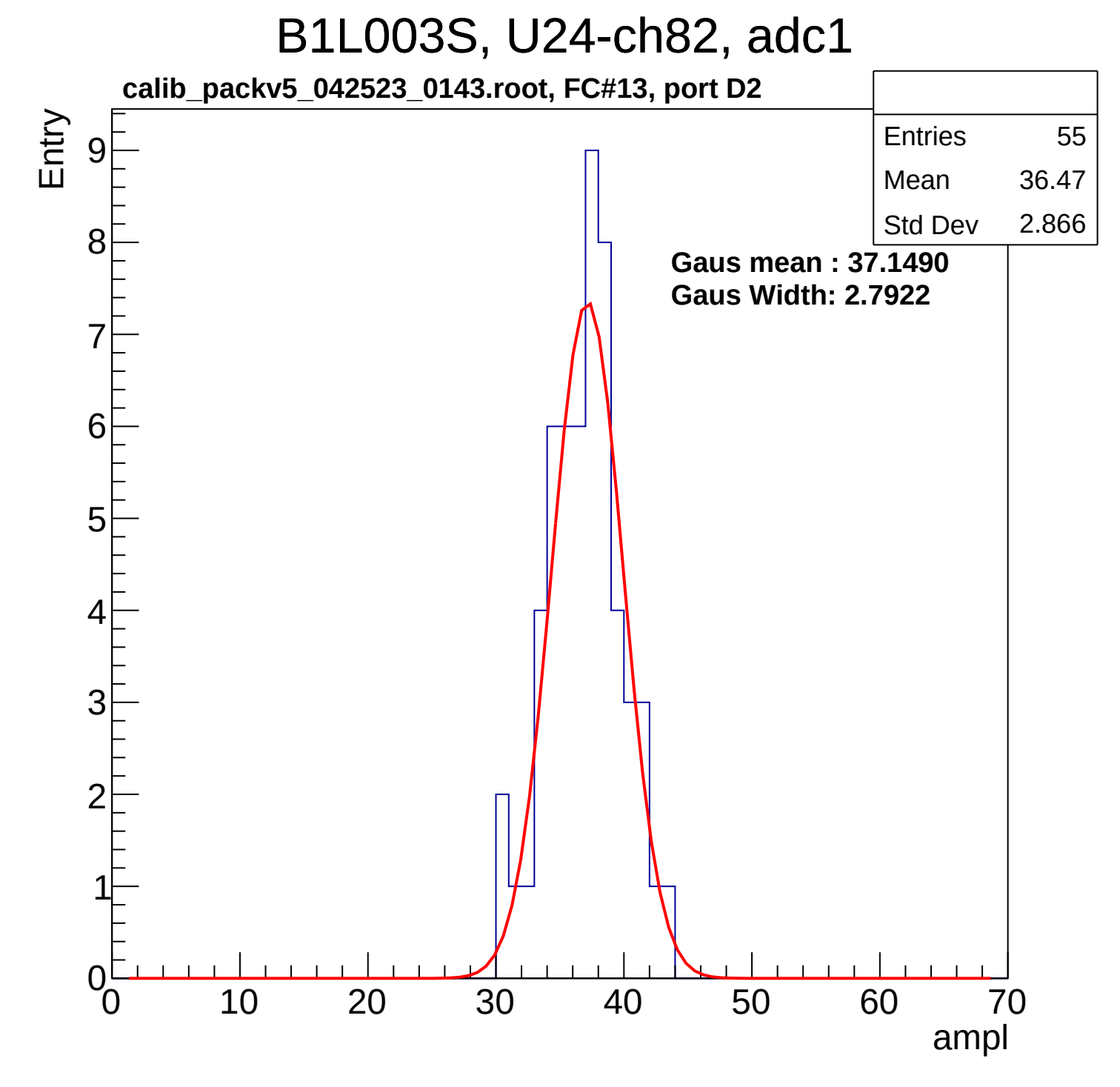
B1L003S, U24-ch82, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 36.47
Std Dev 2.866
Gaus mean : 37.1490
Gaus Width: 2.7922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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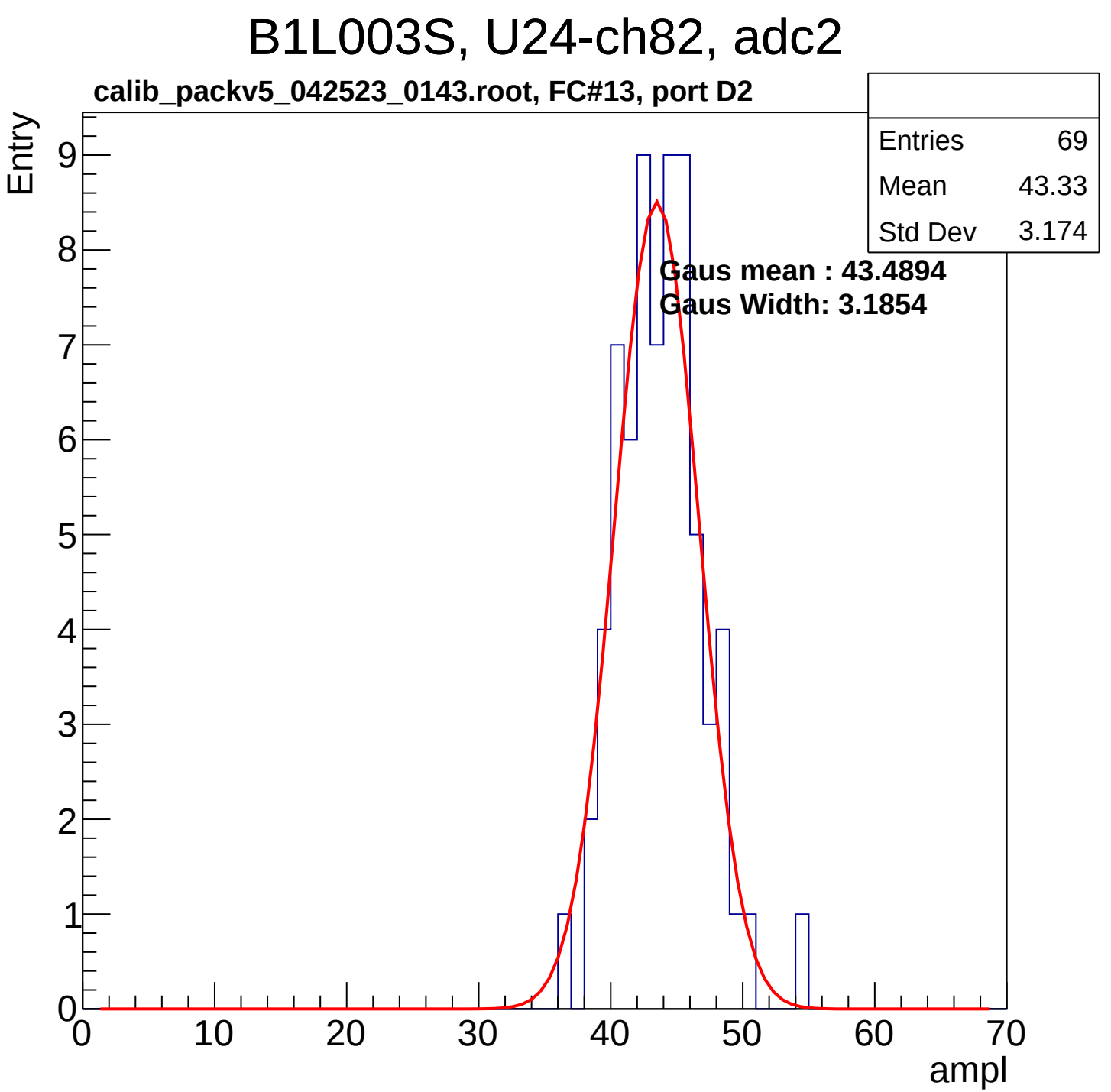
B1L003S, U24-ch82, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 43.33
Std Dev 3.174
Gaus mean : 43.4894
Gaus Width: 3.1854
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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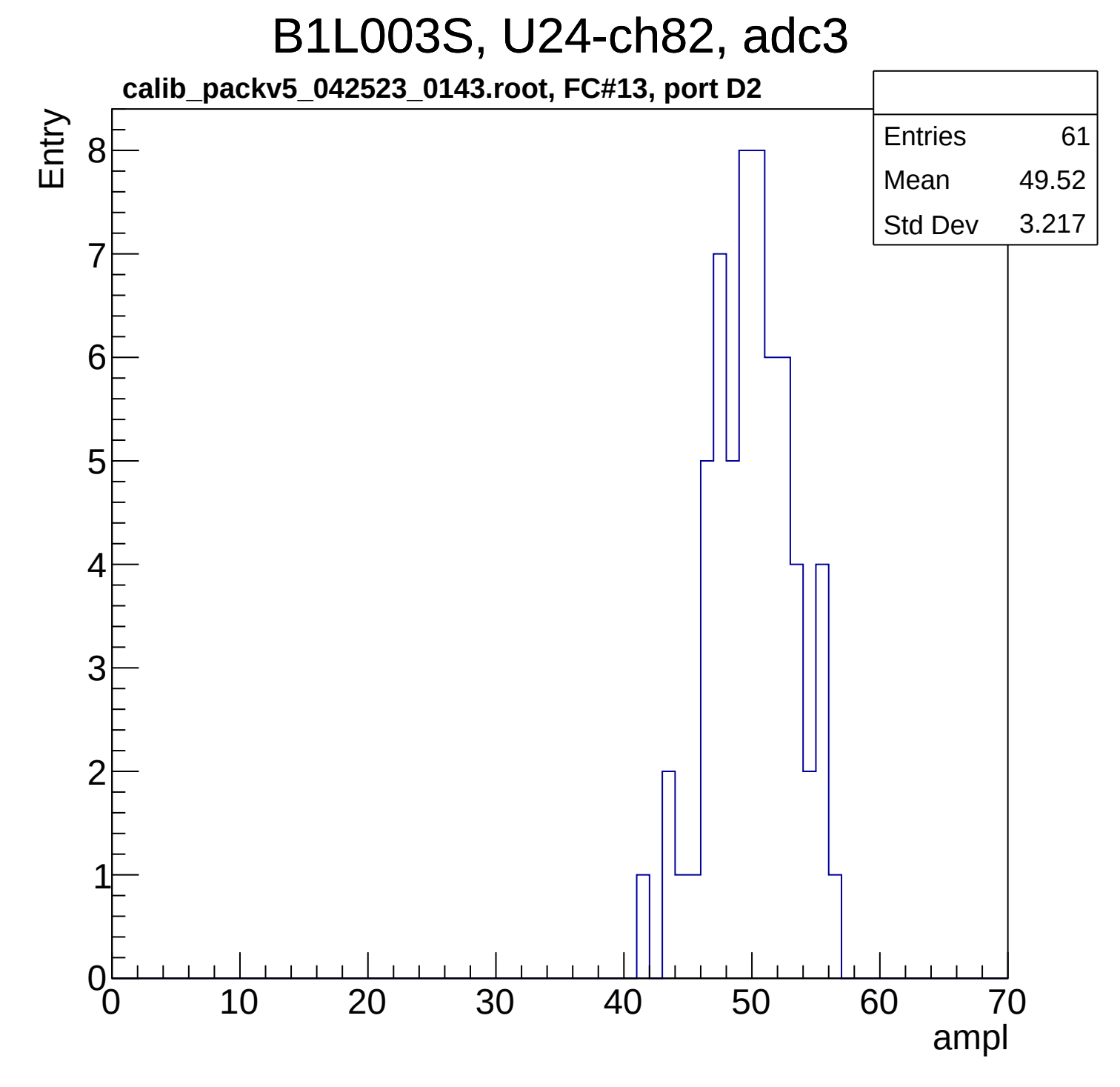
B1L003S, U24-ch82, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.52
Std Dev 3.217
Entry
ampl
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2
3
4
5
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7
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0
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20
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40
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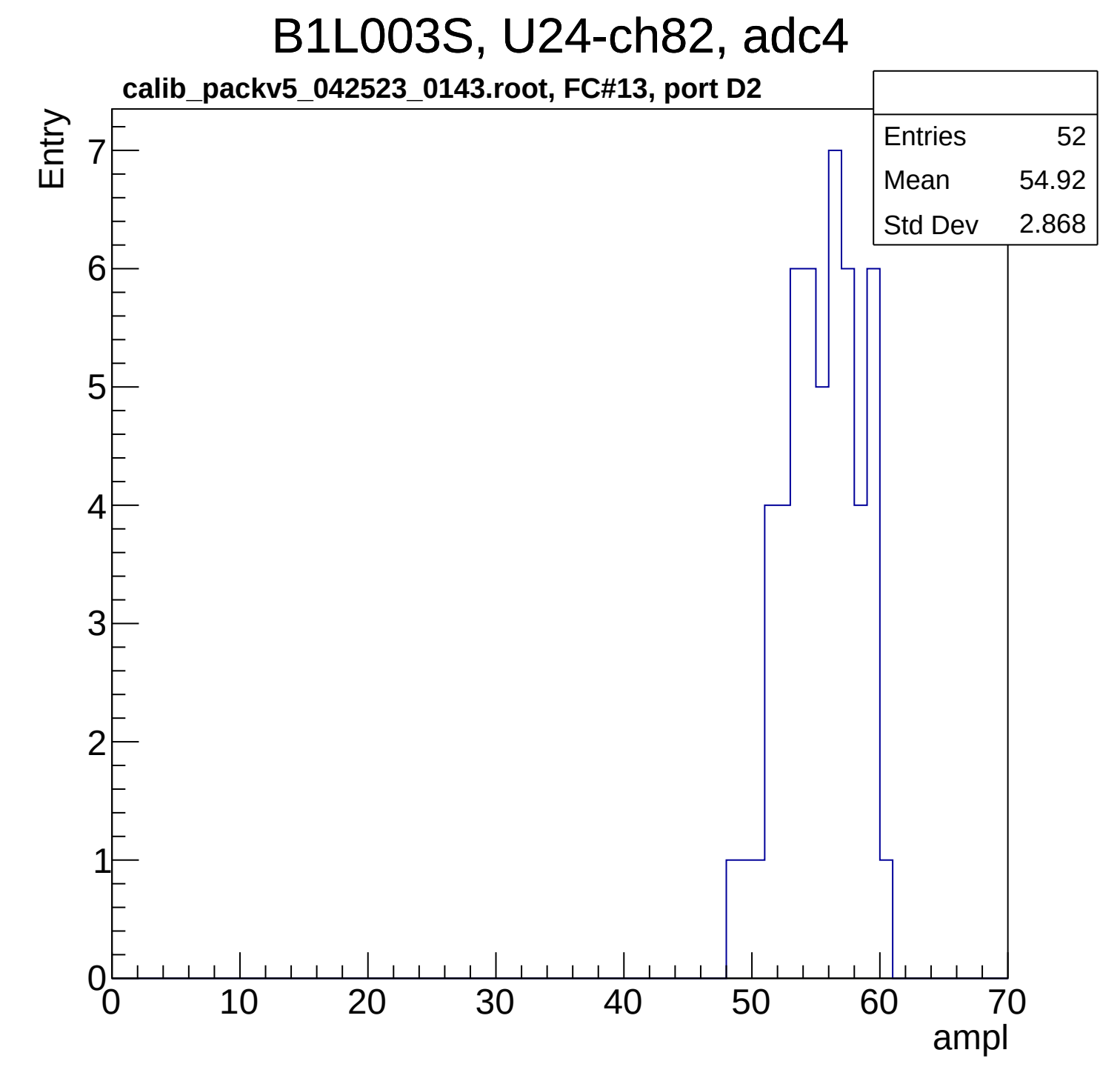
B1L003S, U24-ch82, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.92
Std Dev 2.868
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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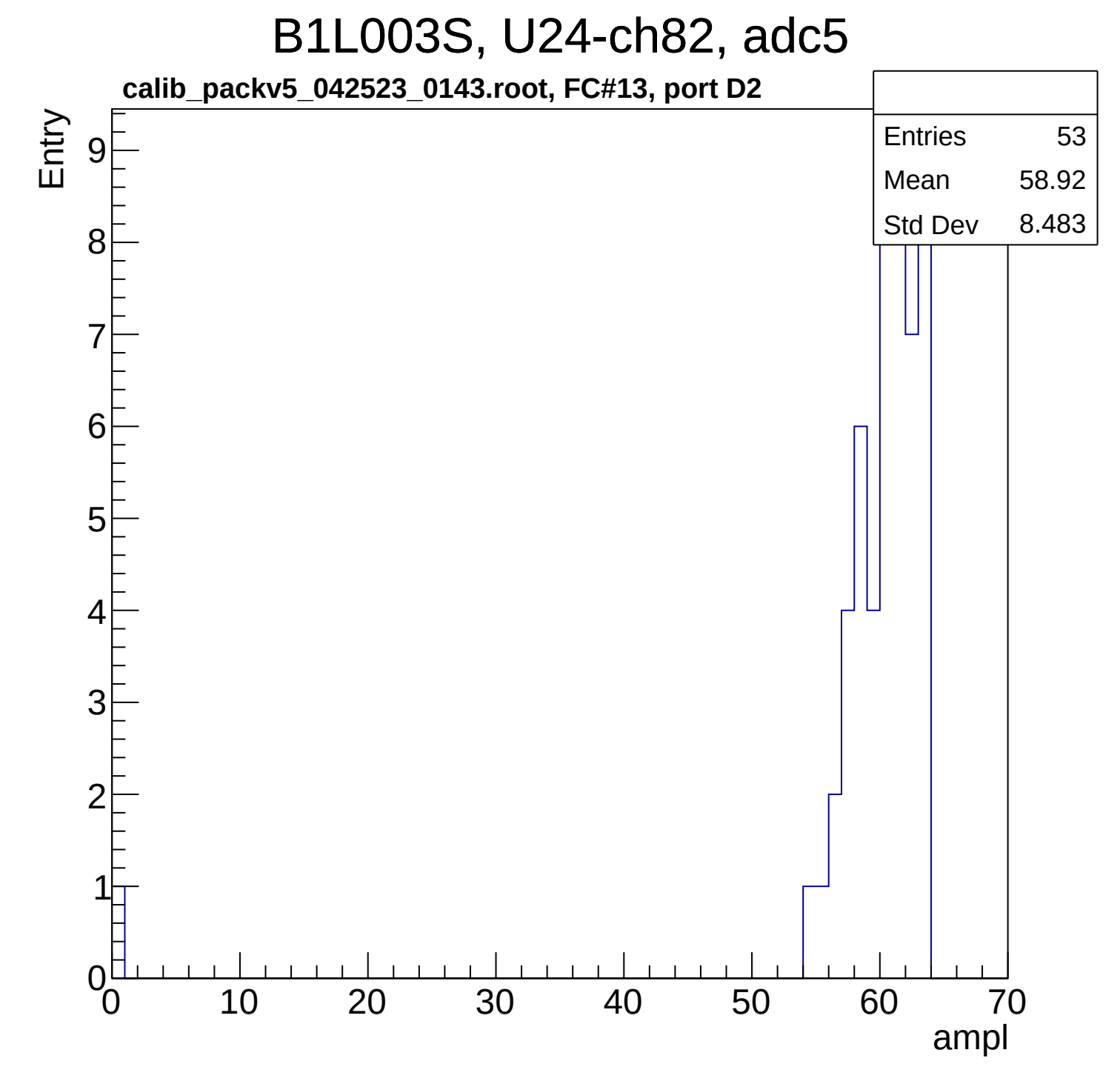
B1L003S, U24-ch82, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.92
Std Dev 8.483
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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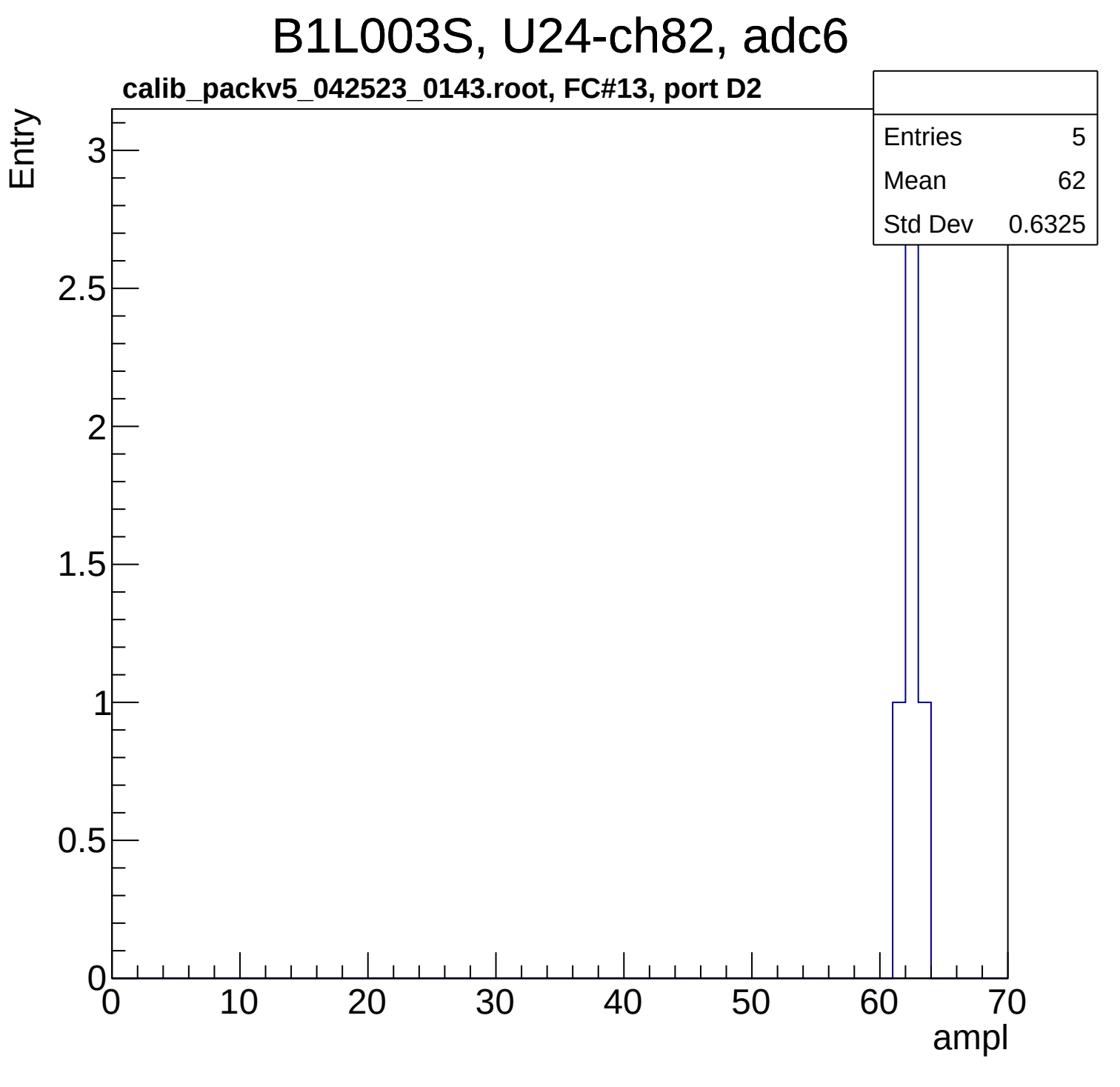
B1L003S, U24-ch82, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 0.6325
Entry
ampl
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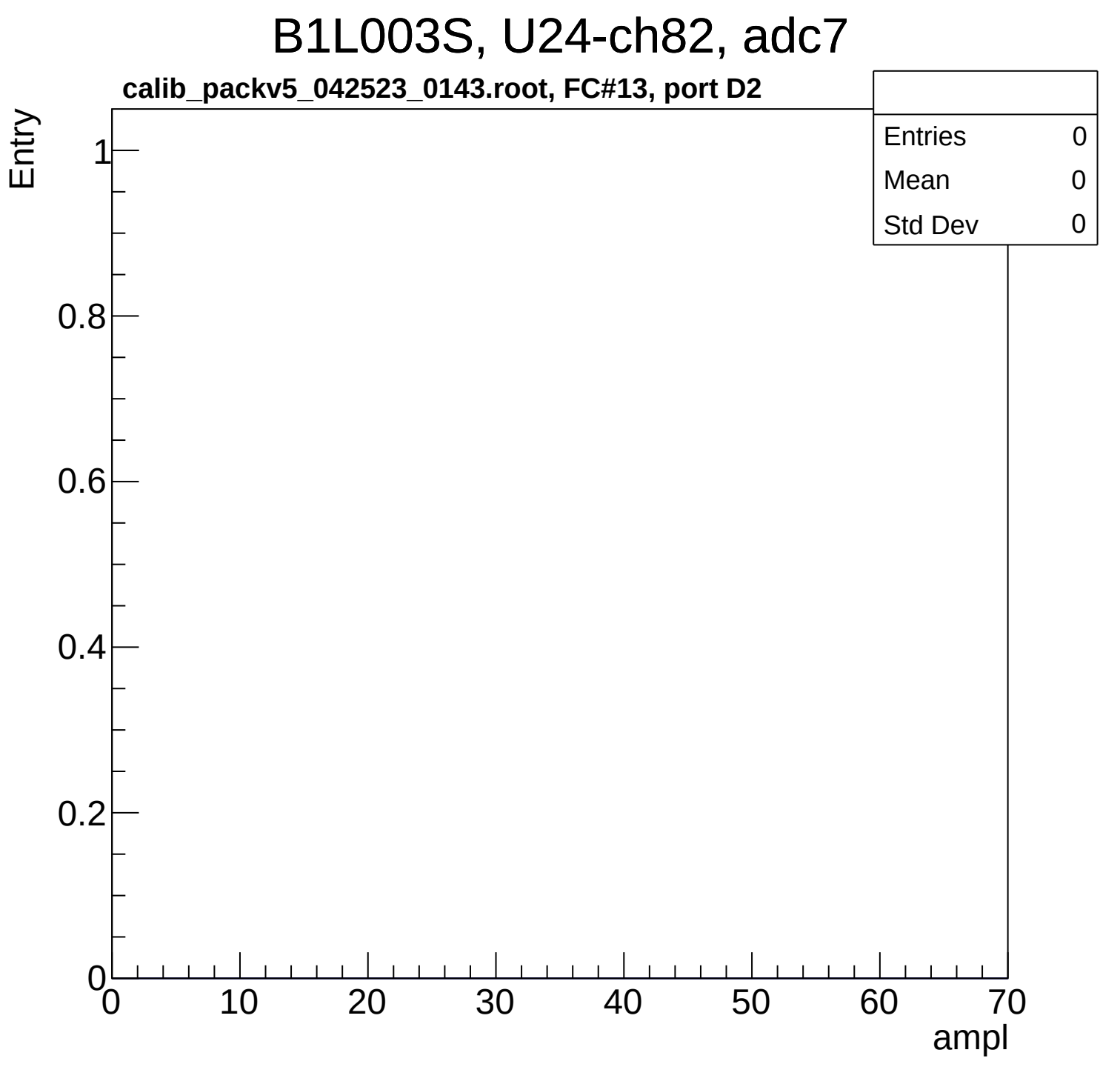
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch82, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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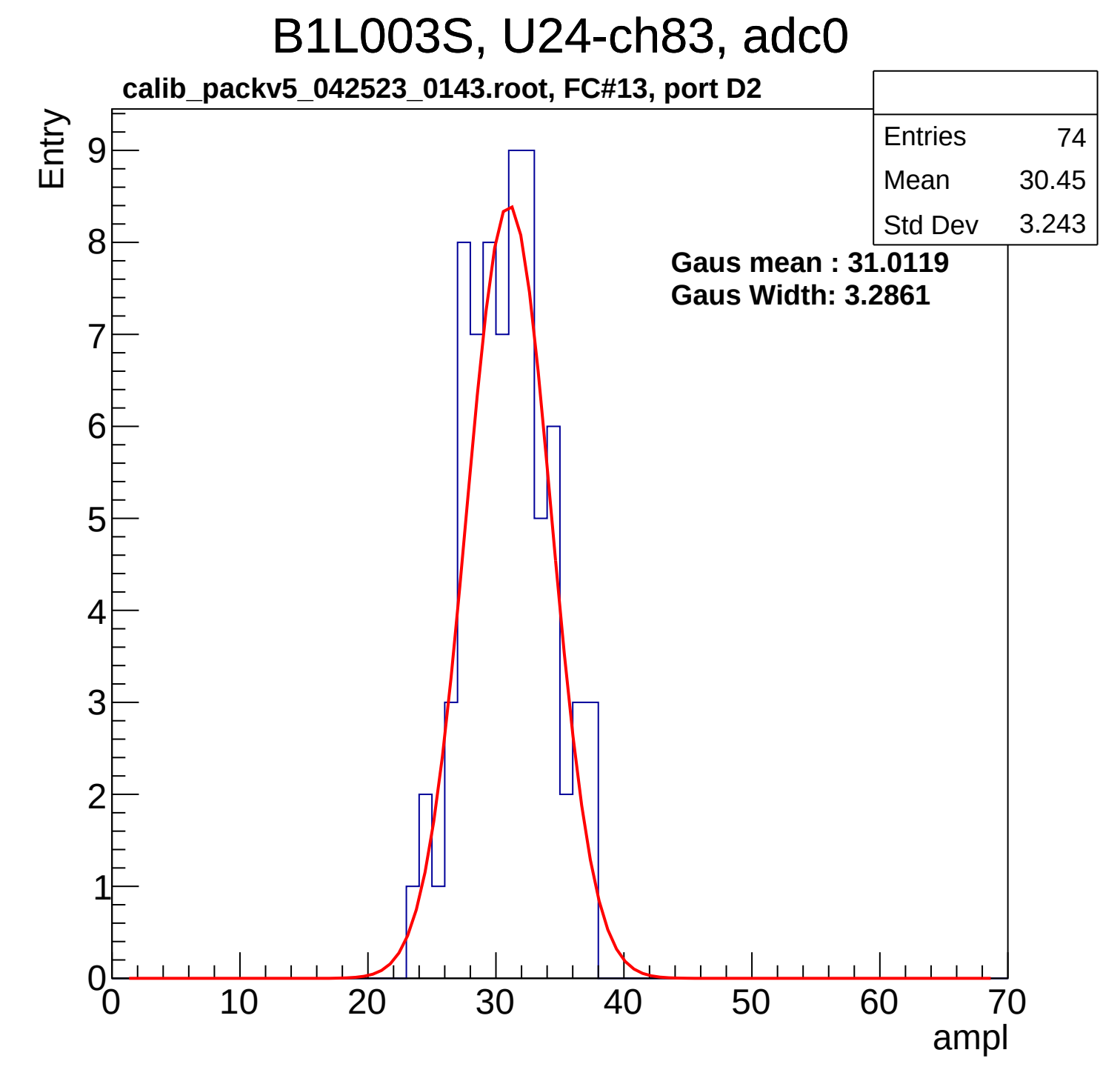
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch83, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 30.45
Std Dev 3.243
Gaus mean : 31.0119
Gaus Width: 3.2861
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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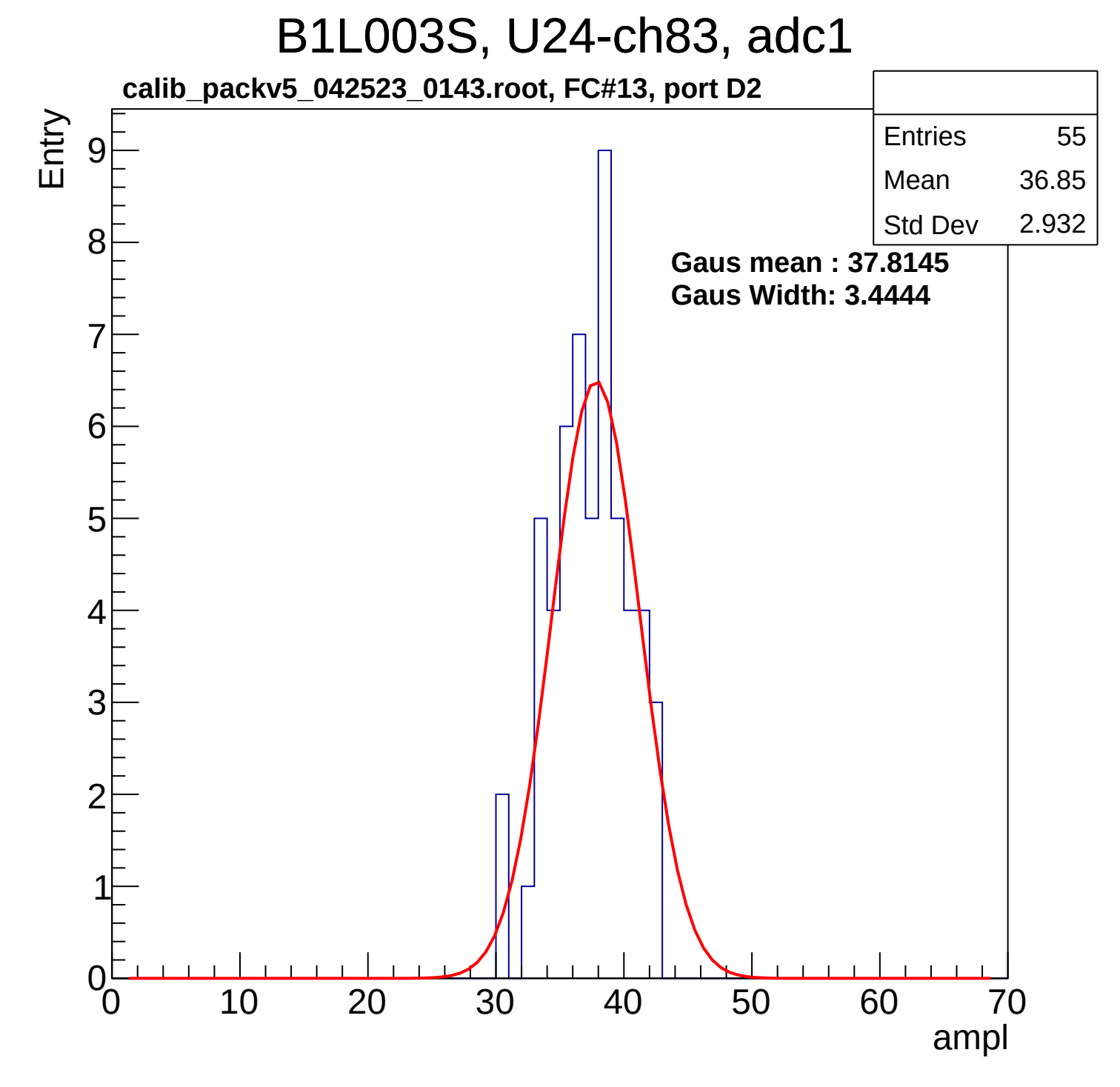
B1L003S, U24-ch83, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 36.85
Std Dev 2.932
Gaus mean : 37.8145
Gaus Width: 3.4444
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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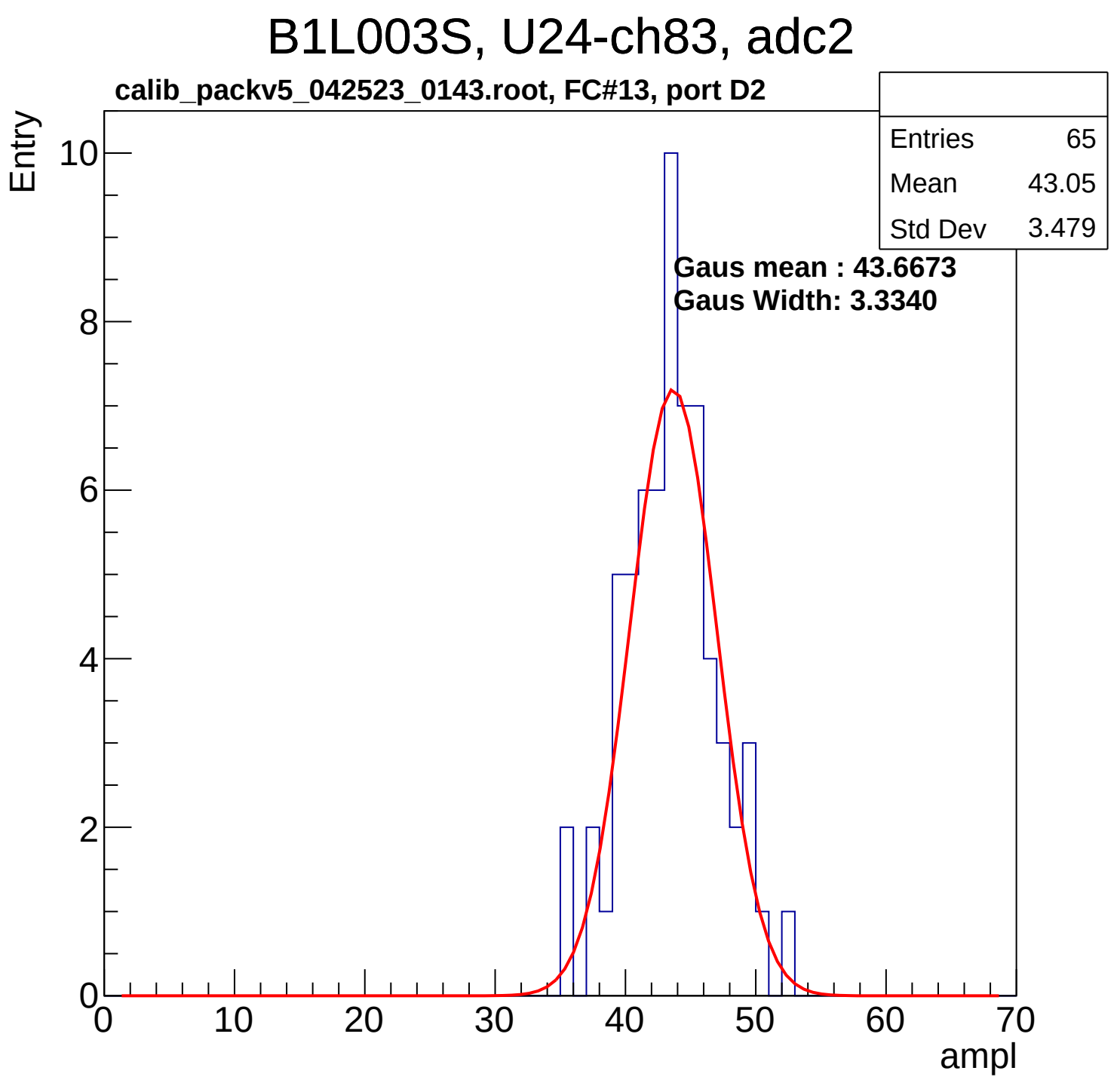
B1L003S, U24-ch83, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.05
Std Dev 3.479
Gaus mean : 43.6673
Gaus Width: 3.3340
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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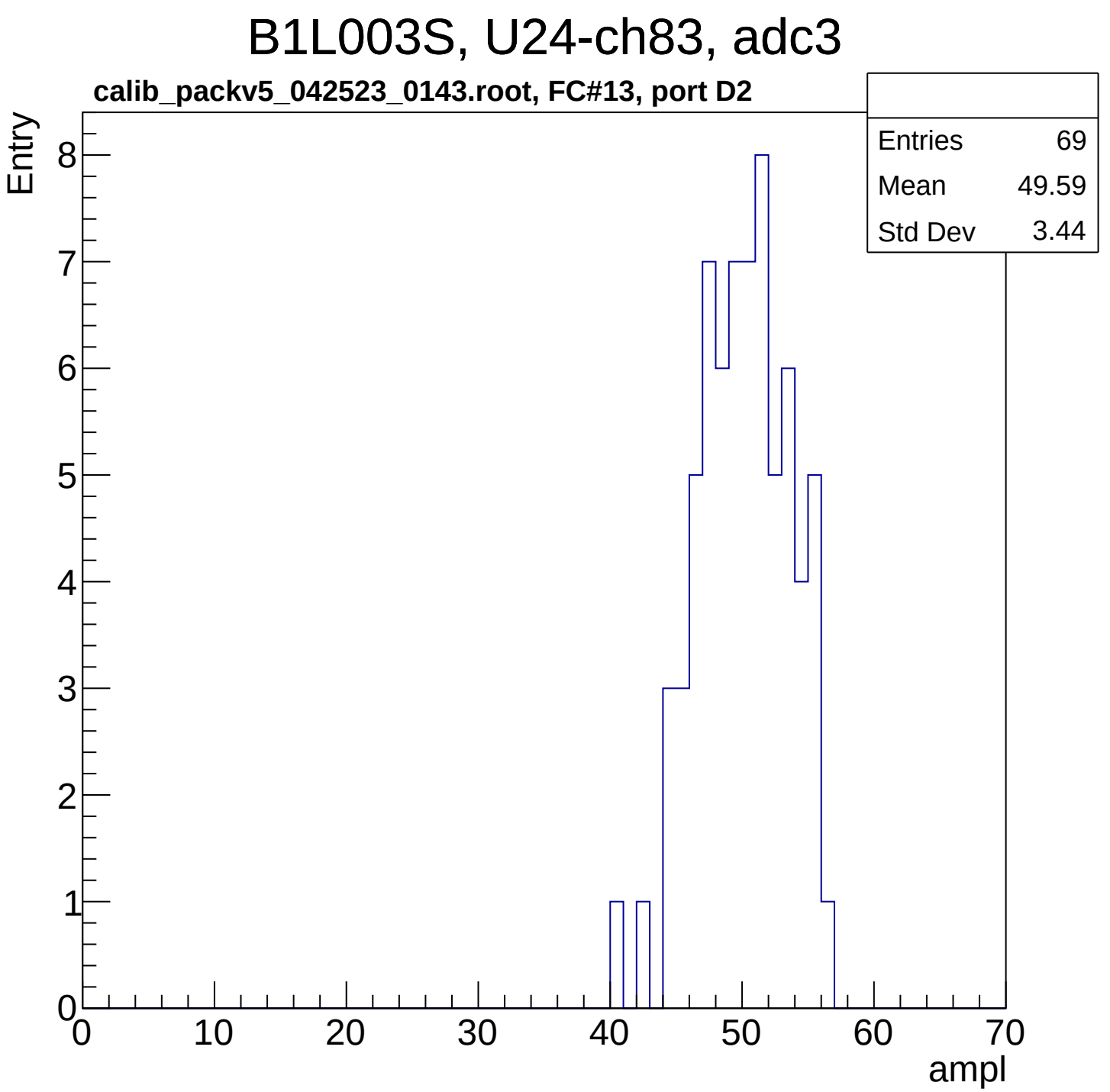
B1L003S, U24-ch83, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 49.59
Std Dev 3.44
Entry
ampl
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2
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0
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60
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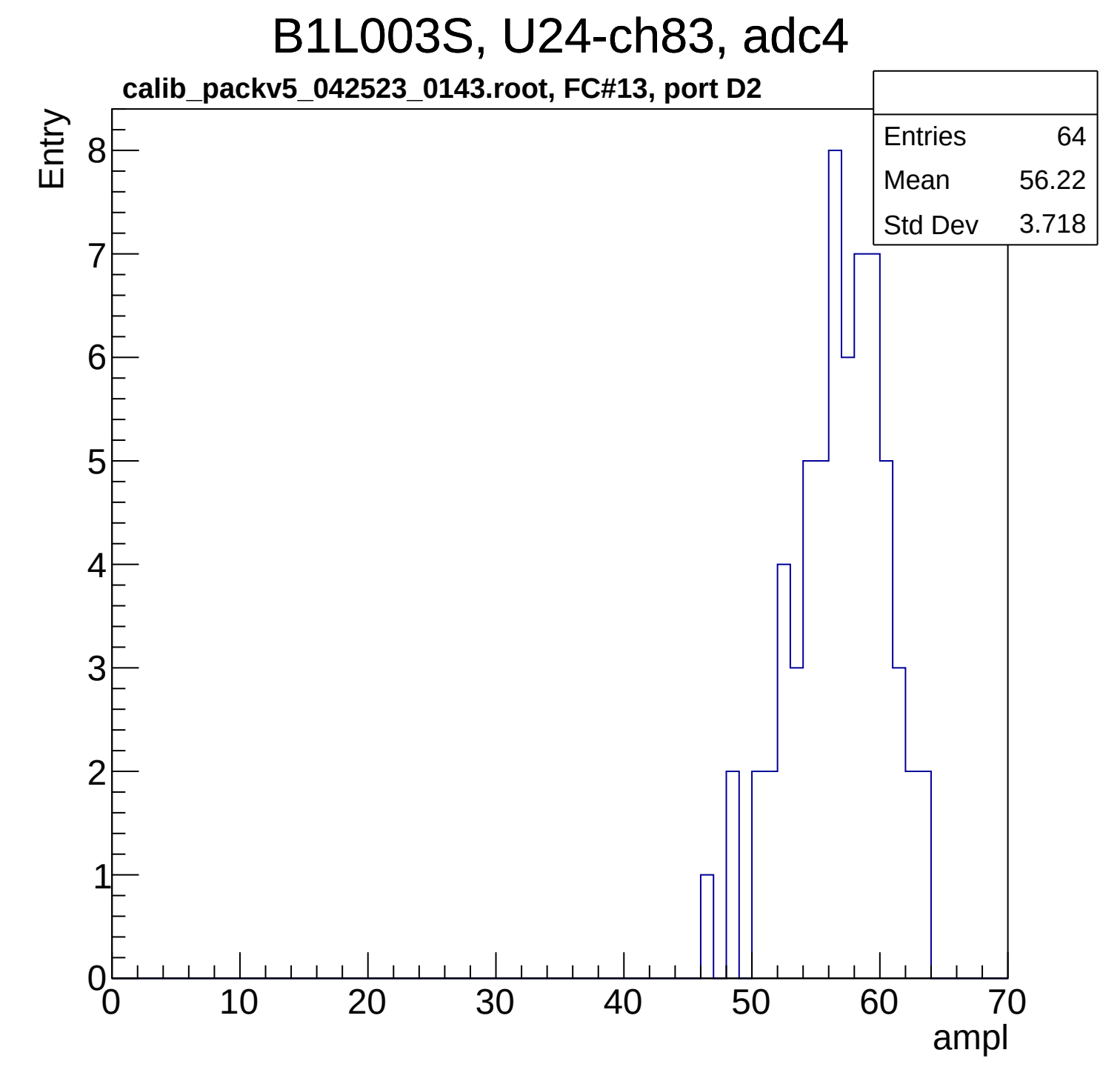
B1L003S, U24-ch83, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 56.22
Std Dev 3.718
Entry
ampl
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0
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60
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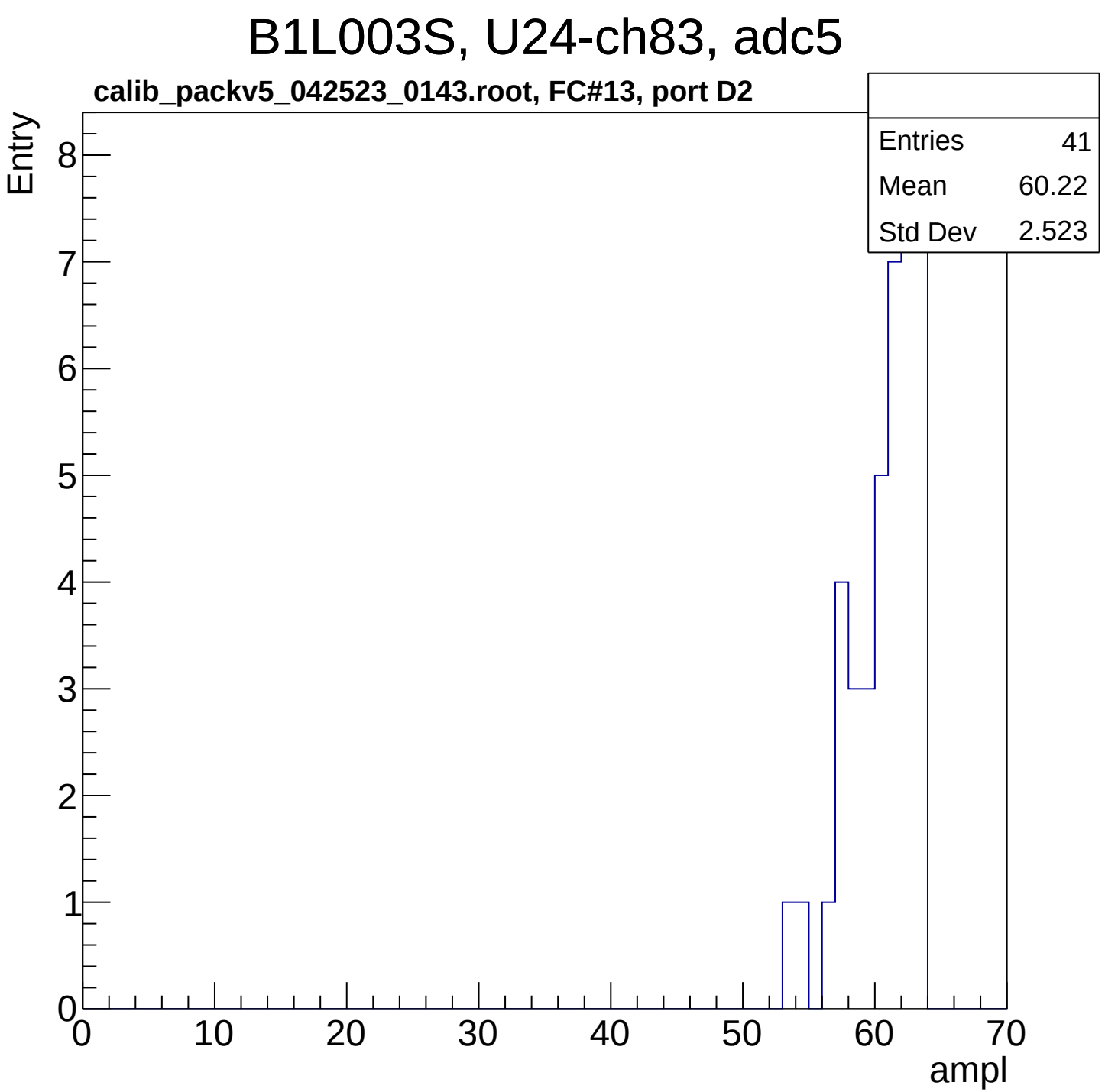
B1L003S, U24-ch83, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 60.22
Std Dev 2.523
Entry
ampl
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2
3
4
5
6
7
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0
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60
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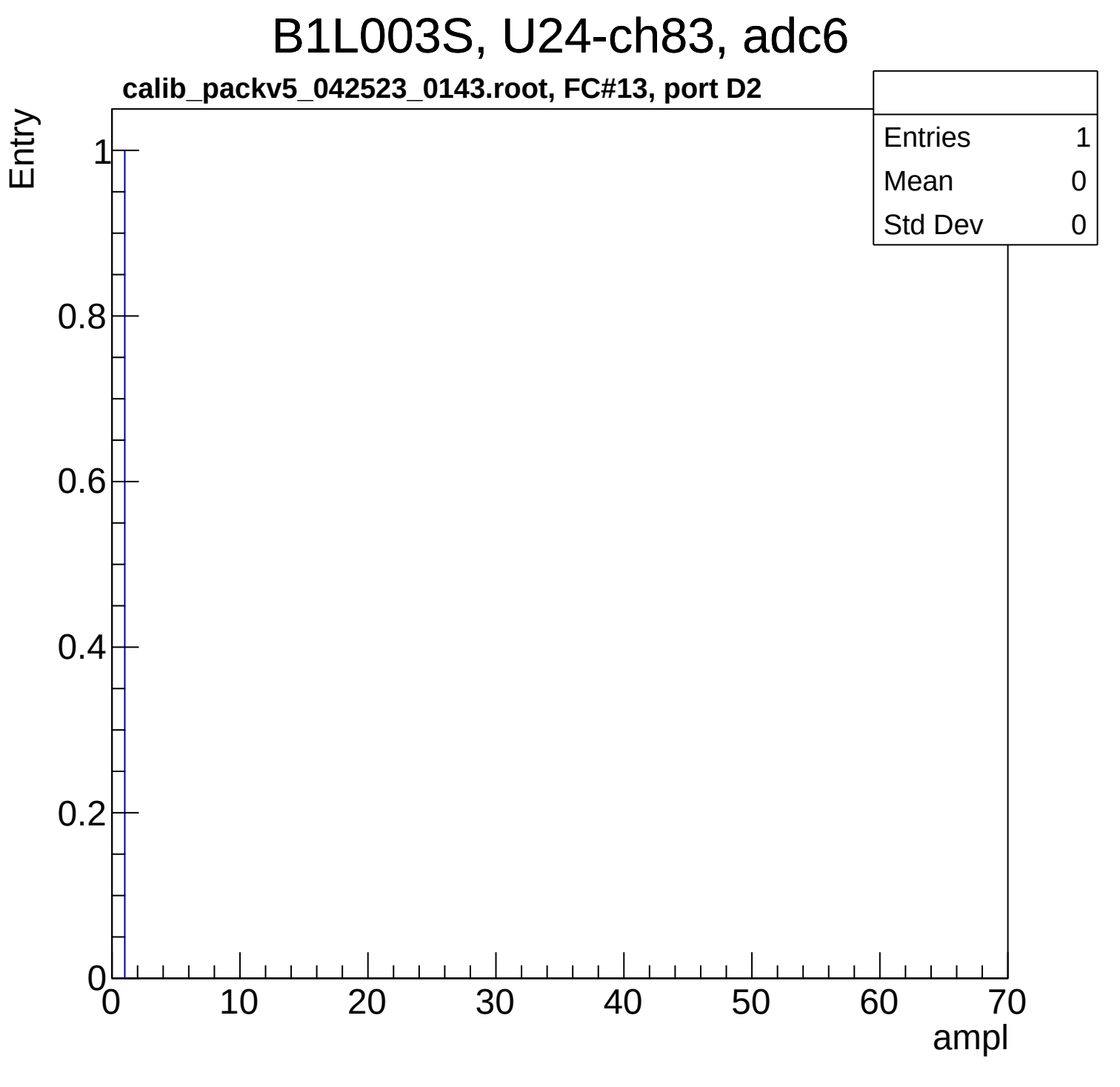
B1L003S, U24-ch83, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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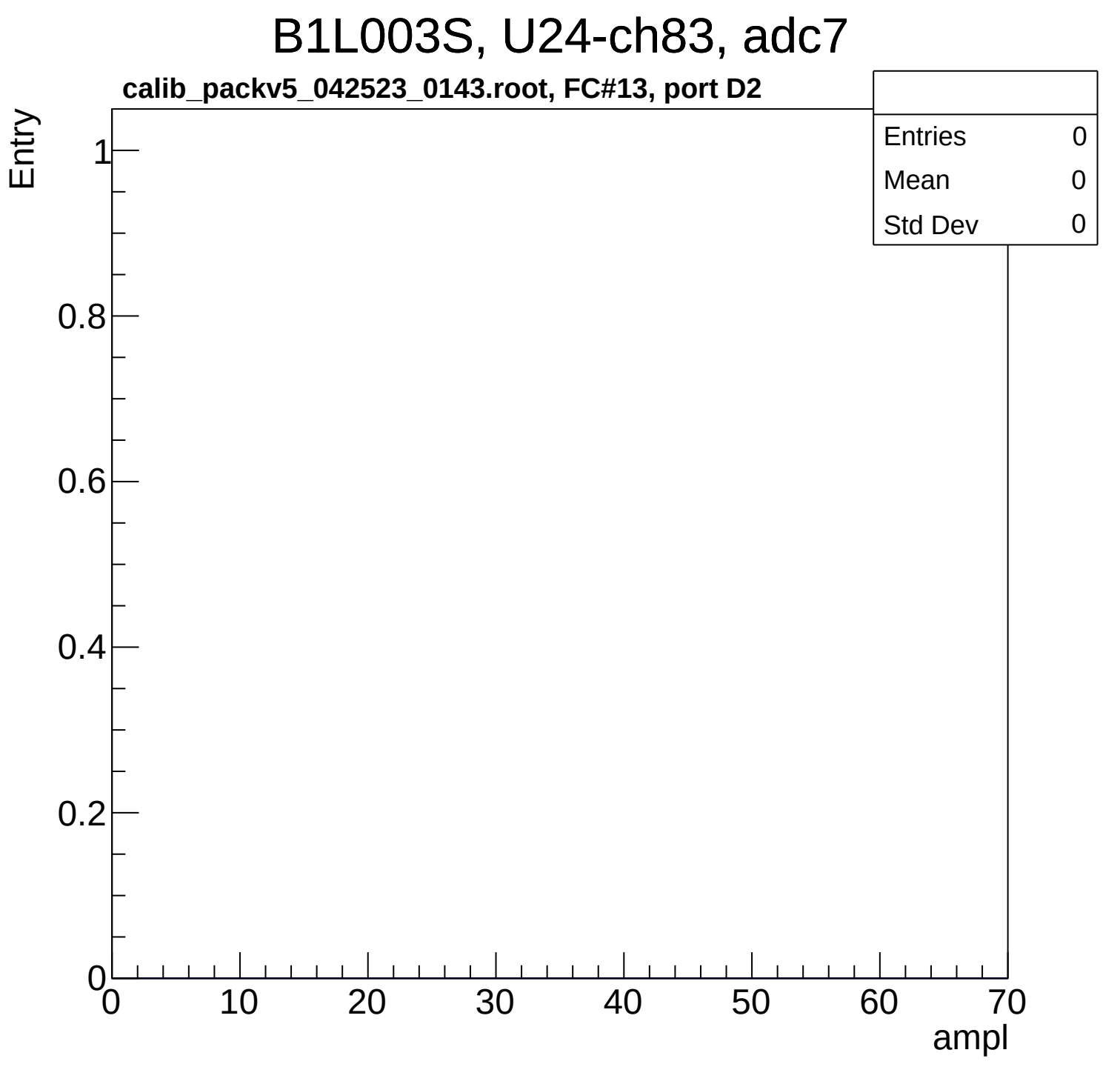
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch83, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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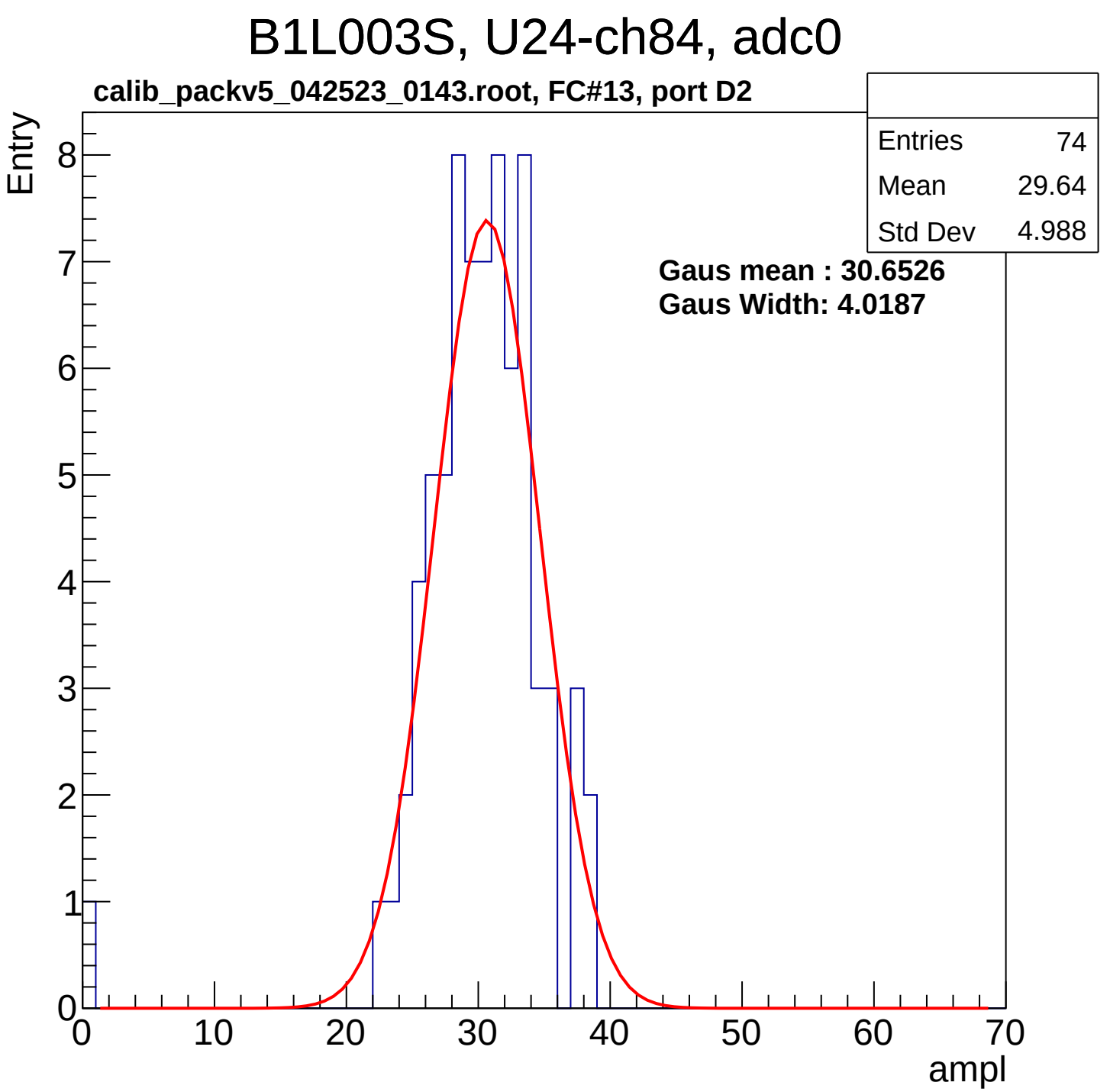
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch84, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.64
Std Dev 4.988
Gaus mean : 30.6526
Gaus Width: 4.0187
Entry
ampl
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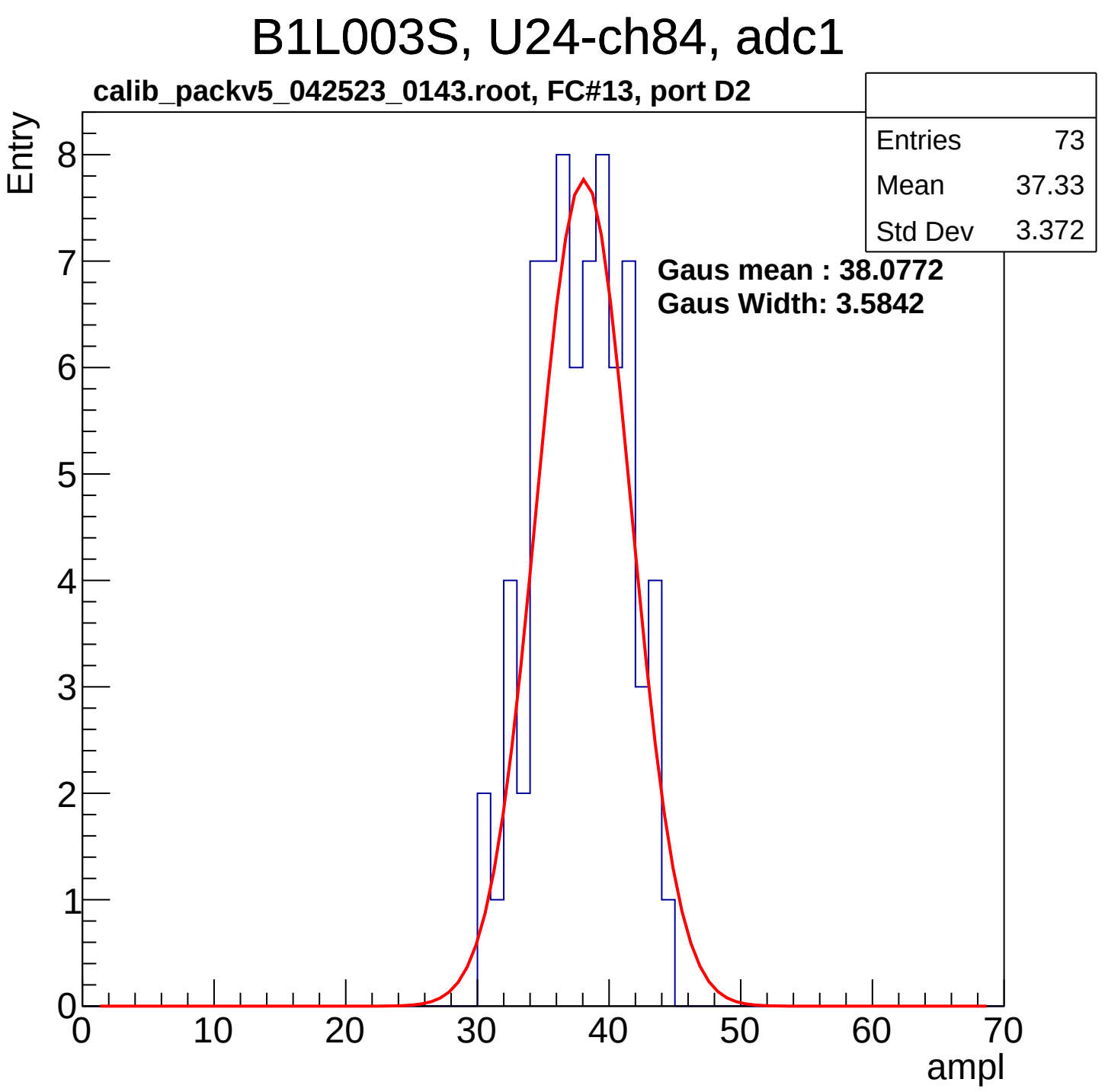
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch84, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 37.33
Std Dev 3.372
Gaus mean : 38.0772
Gaus Width: 3.5842
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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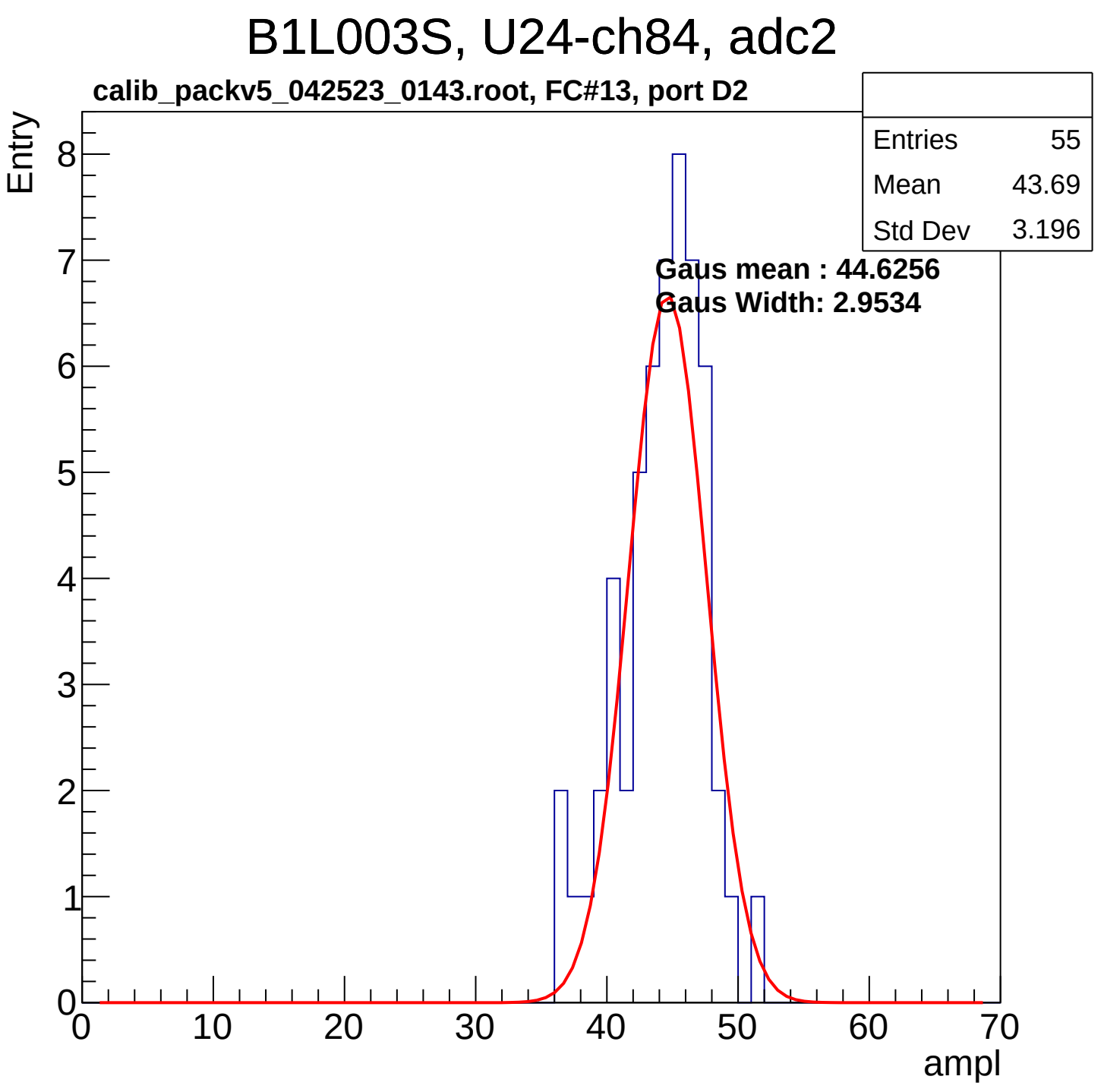
B1L003S, U24-ch84, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43.69
Std Dev 3.196
Gaus mean : 44.6256
Gaus Width: 2.9534
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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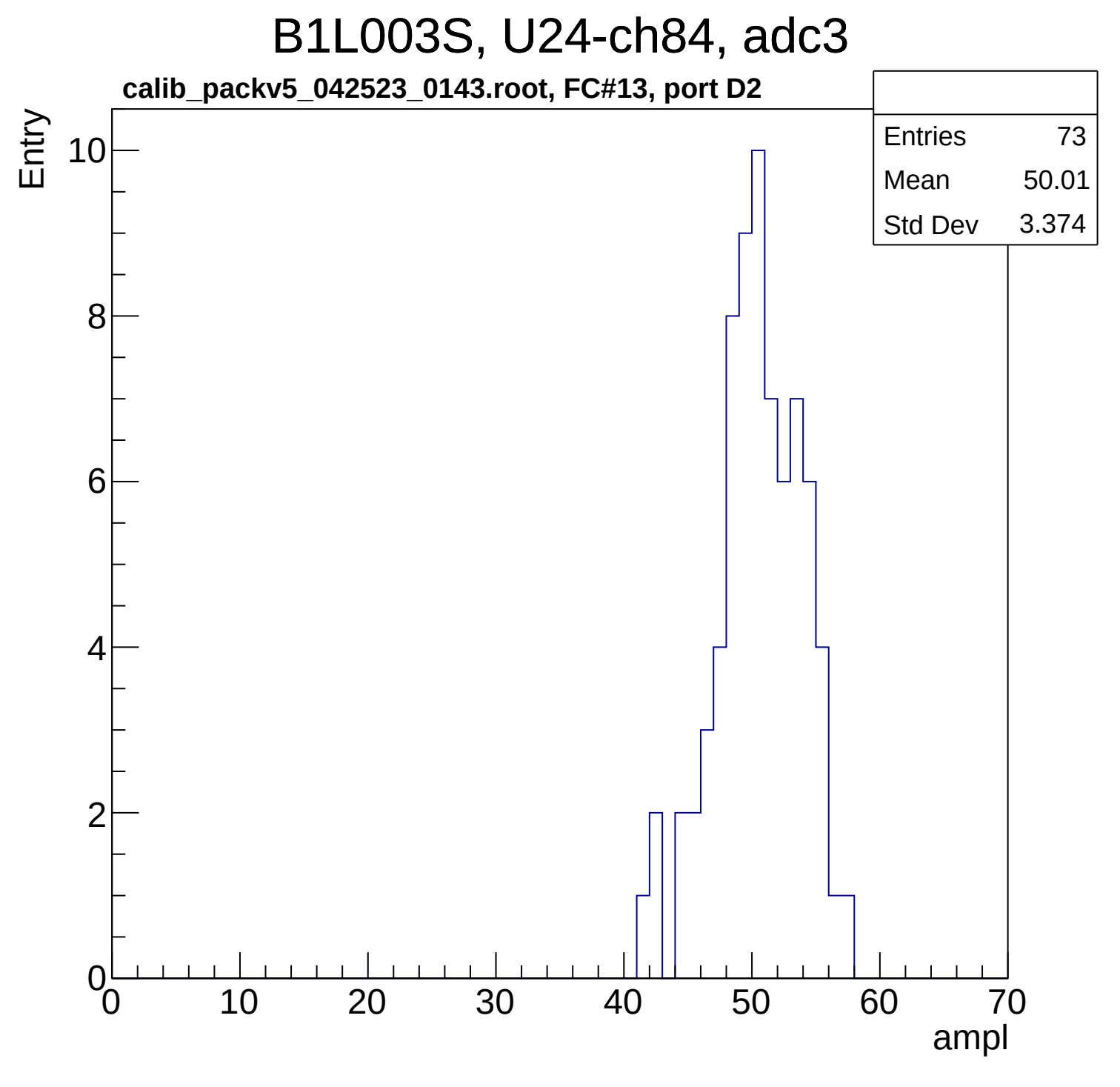
B1L003S, U24-ch84, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 50.01
Std Dev 3.374
Entry
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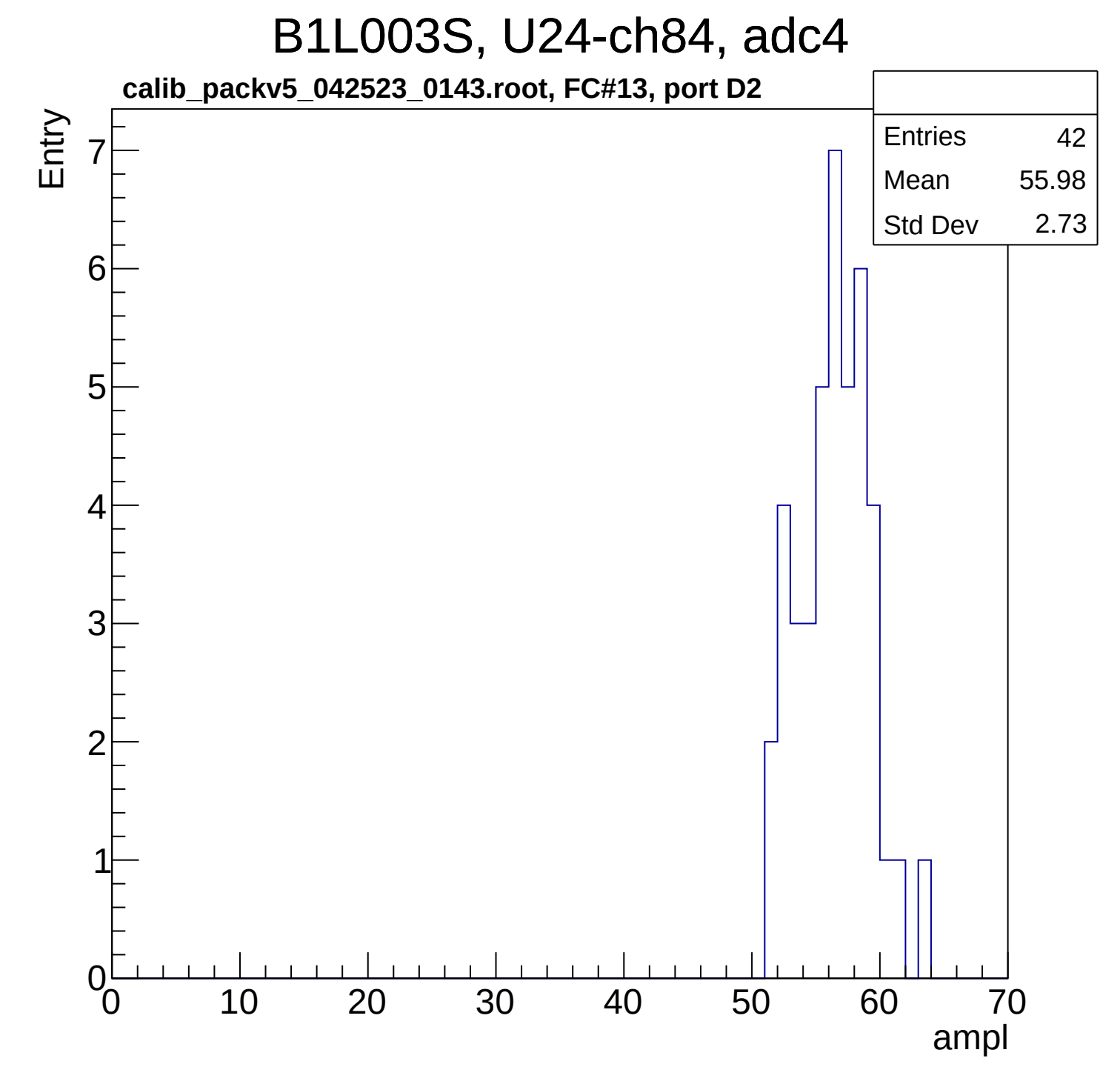
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch84, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 55.98
Std Dev 2.73
Entry
ampl
0
1
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3
4
5
6
7
0
10
20
30
40
50
60
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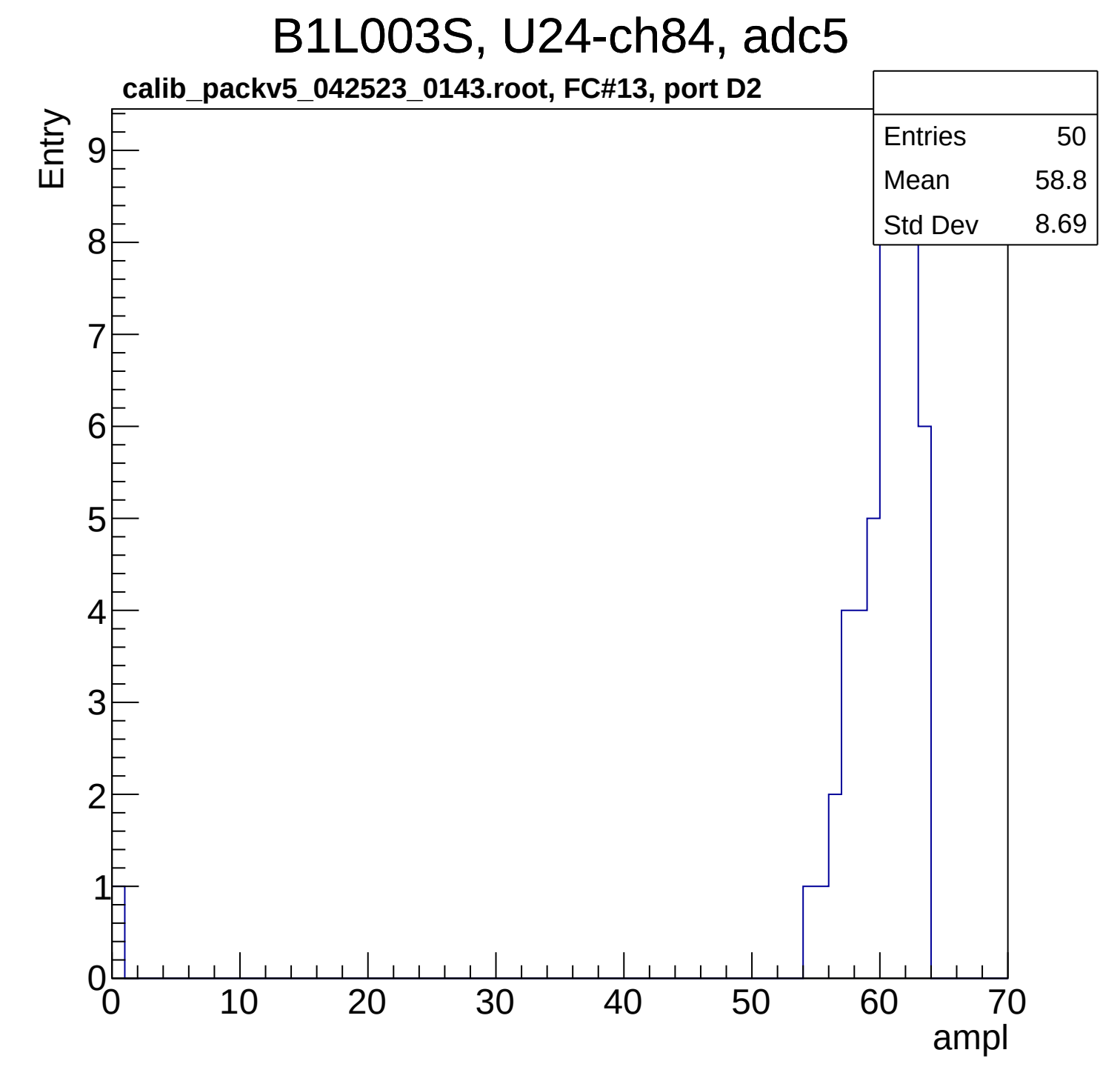
B1L003S, U24-ch84, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.8
Std Dev 8.69
Entry
ampl
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3
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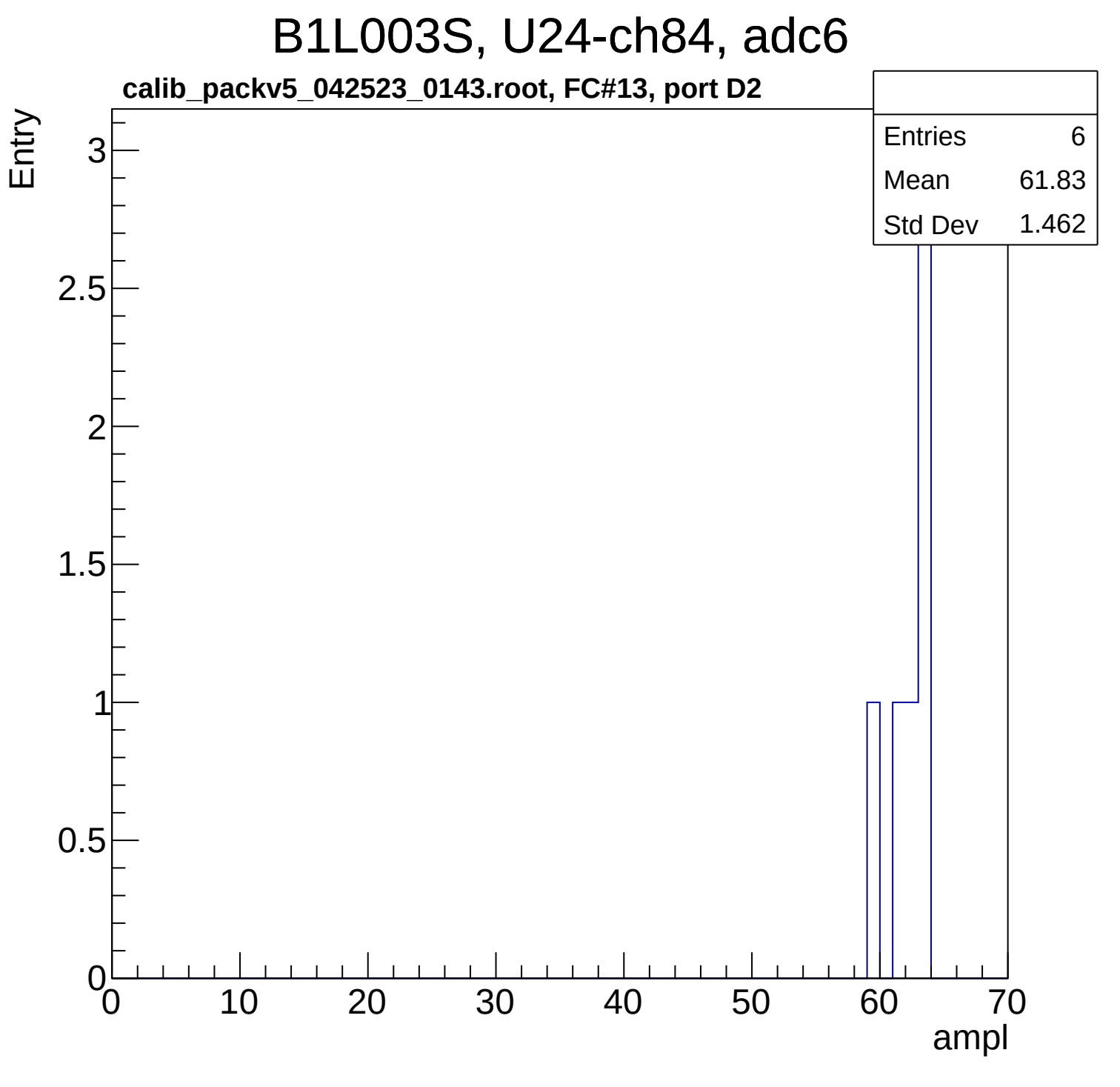
B1L003S, U24-ch84, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.83
Std Dev 1.462
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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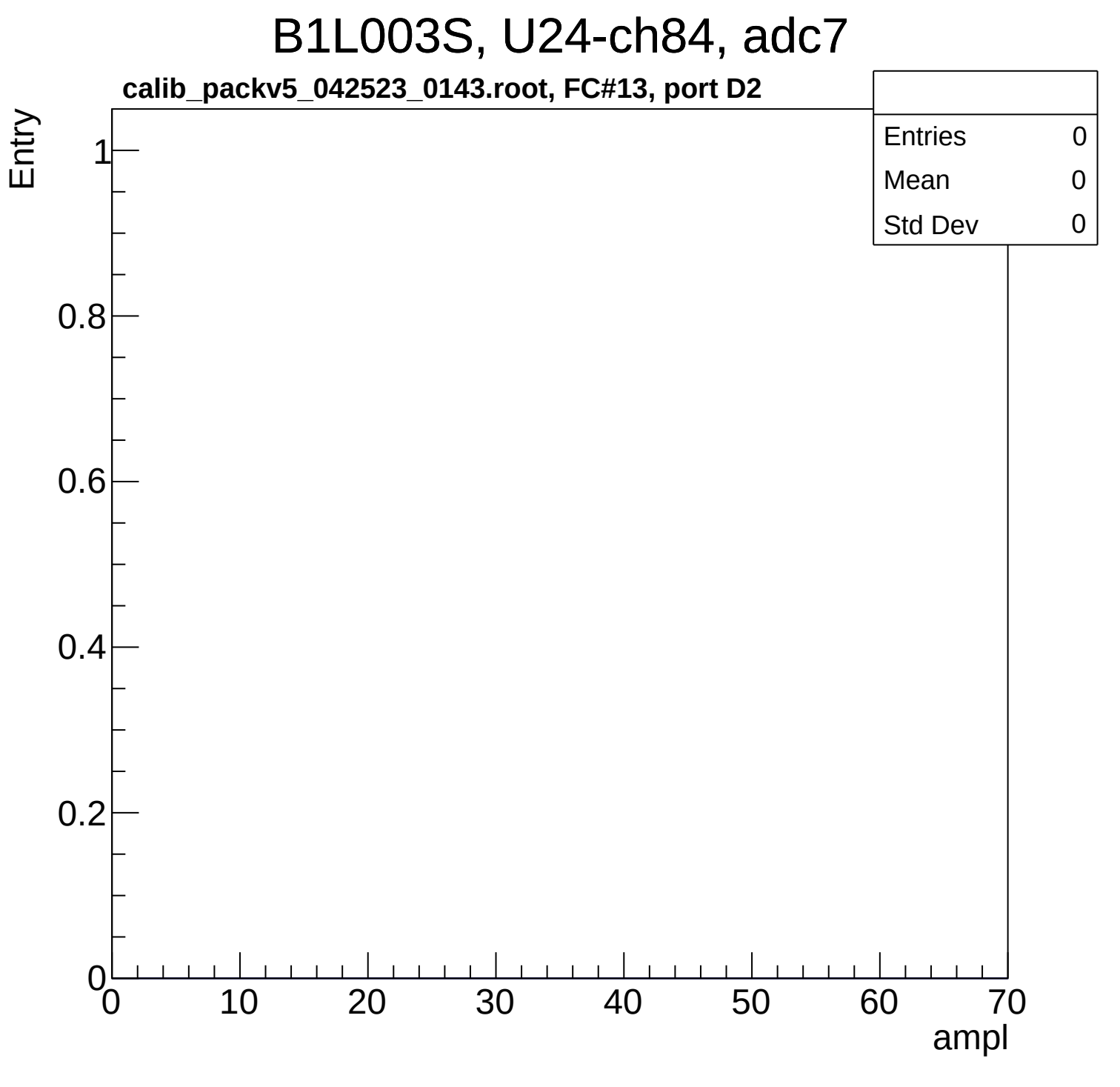
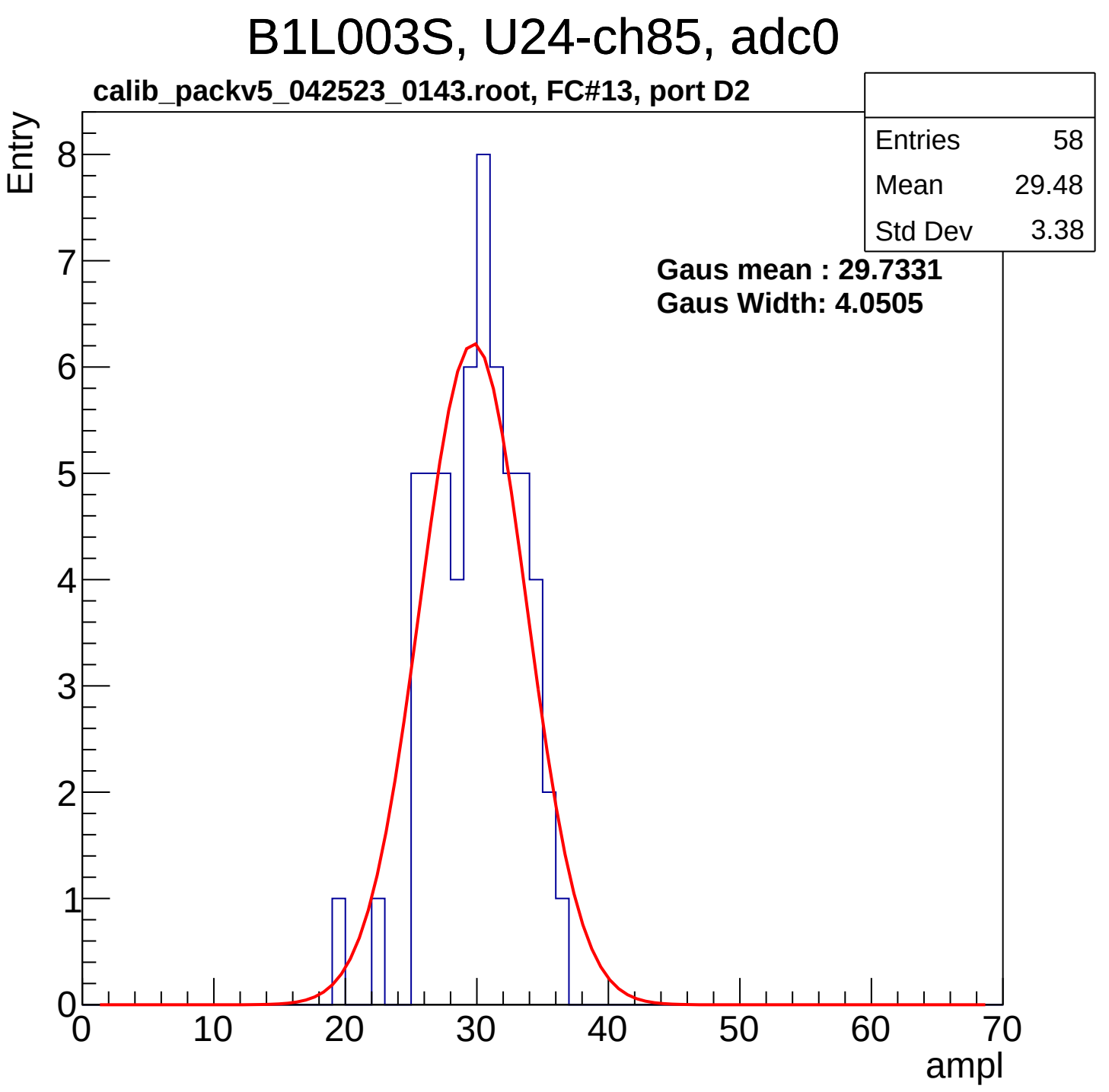
40
50
60
70


B1L003S, U24-ch84, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch85, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 29.48
Std Dev 3.38
Gaus mean : 29.7331
Gaus Width: 4.0505
Entry
ampl
0
1
2
3
4
5
6
7
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0
10
20
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40
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60
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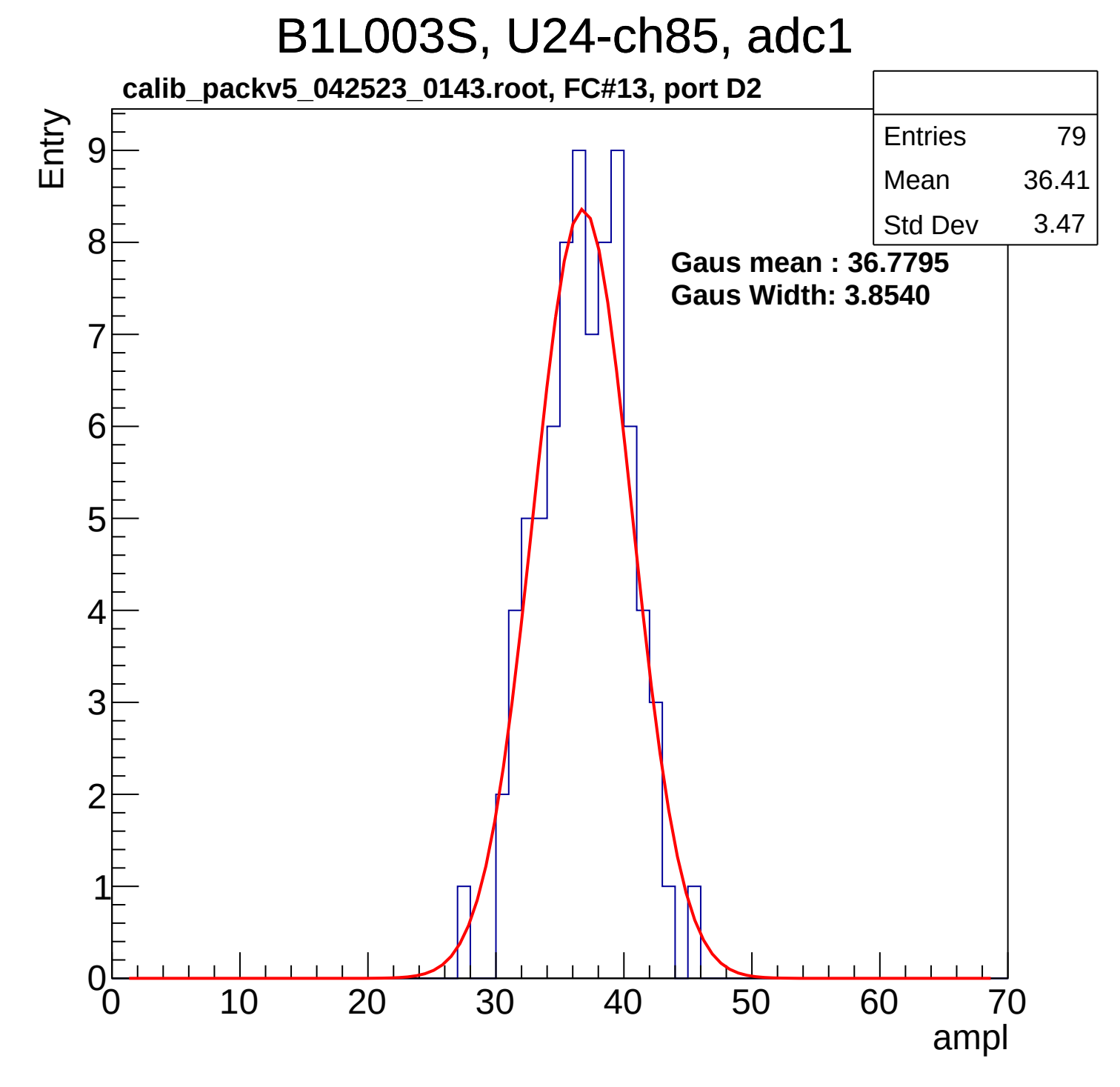
B1L003S, U24-ch85, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 36.41
Std Dev 3.47
Gaus mean : 36.7795
Gaus Width: 3.8540
Entry
ampl
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2
3
4
5
6
7
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9
0
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40
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60
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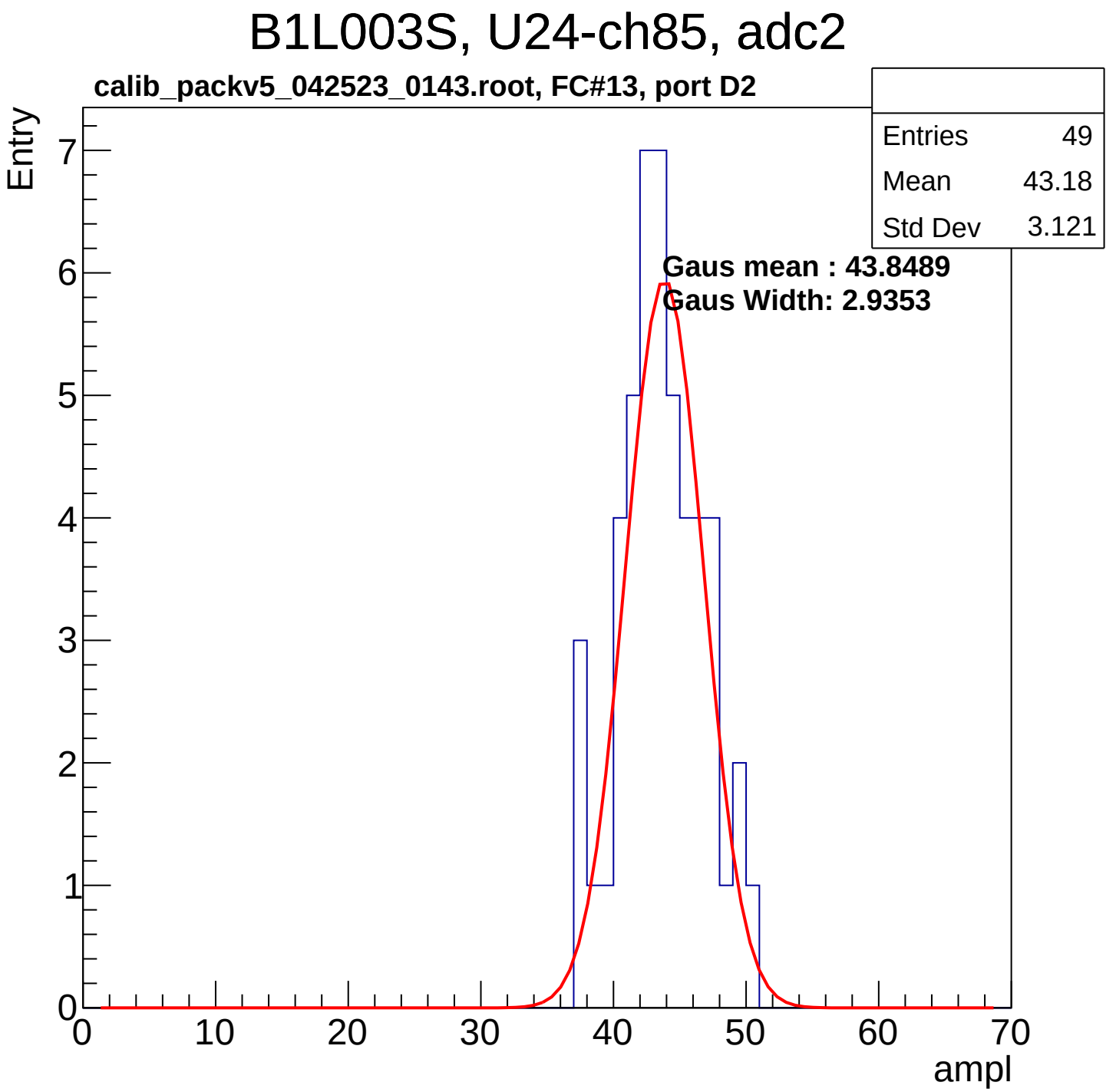
B1L003S, U24-ch85, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 43.18
Std Dev 3.121
Gaus mean : 43.8489
Gaus Width: 2.9353
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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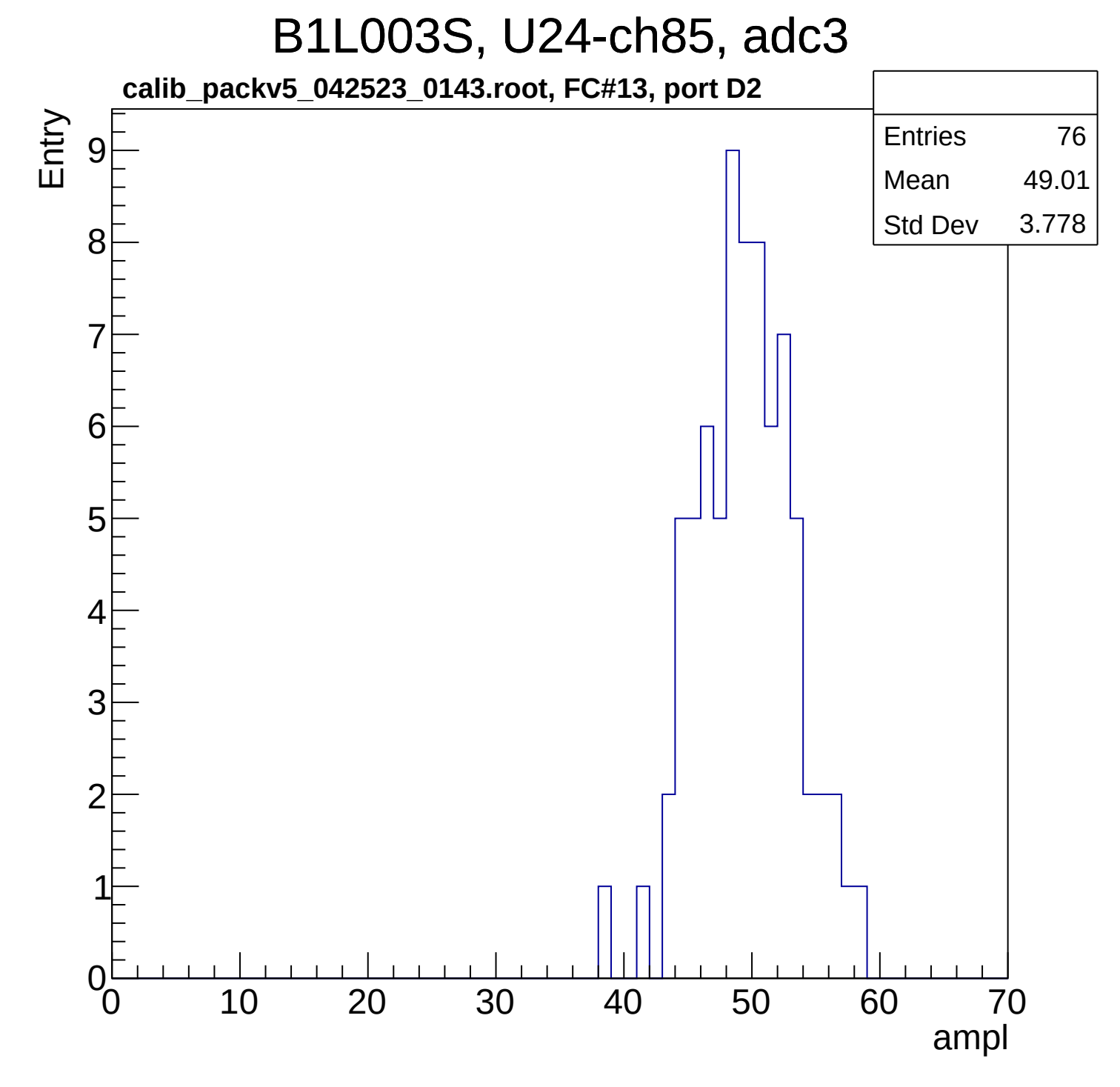
B1L003S, U24-ch85, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 49.01
Std Dev 3.778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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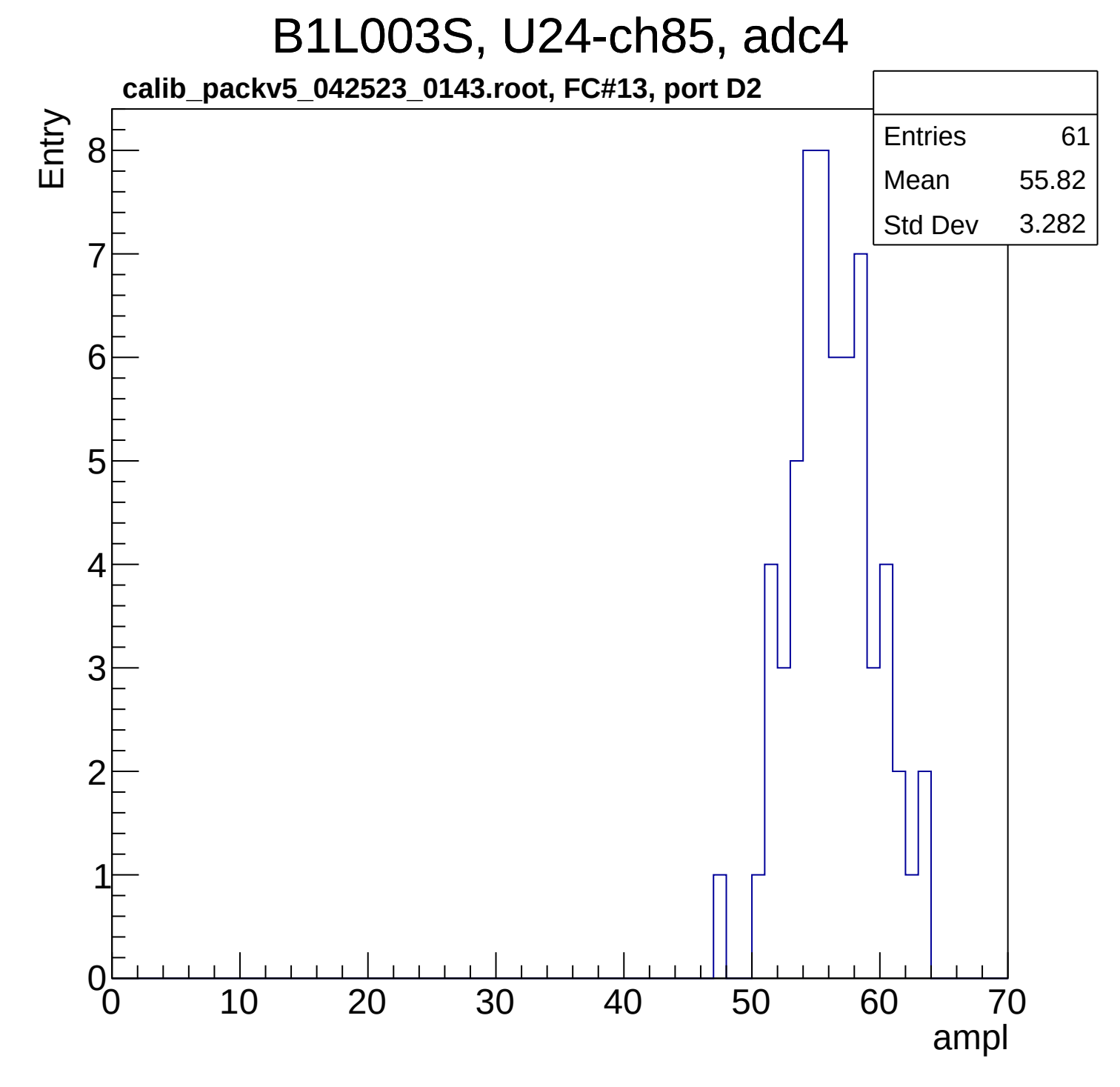
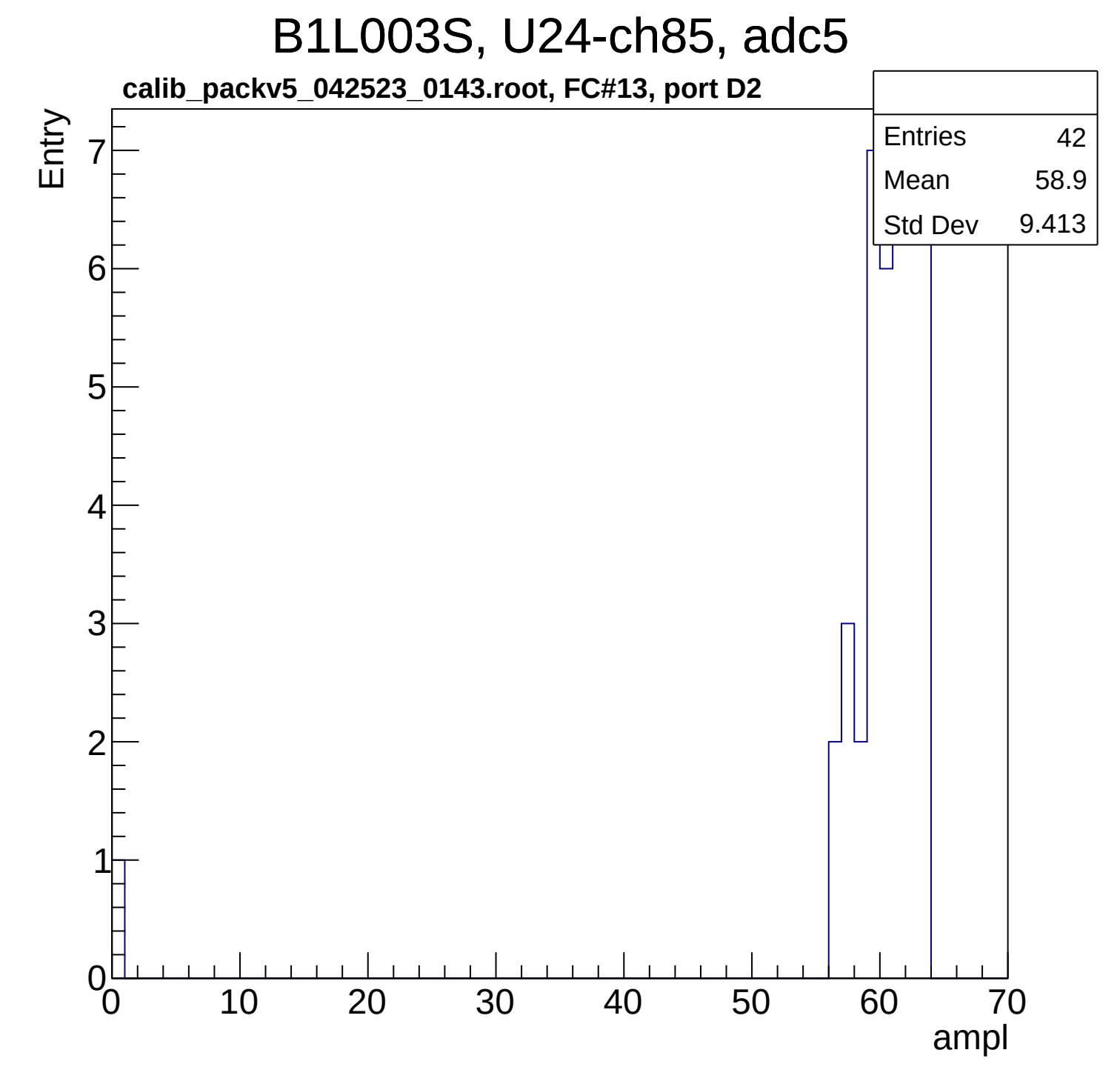

B1L003S, U24-ch85, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 55.82
Std Dev 3.282
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch85, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 58.9
Std Dev 9.413
Entry
ampl
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2
3
4
5
6
7
0
10
20
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40
50
60
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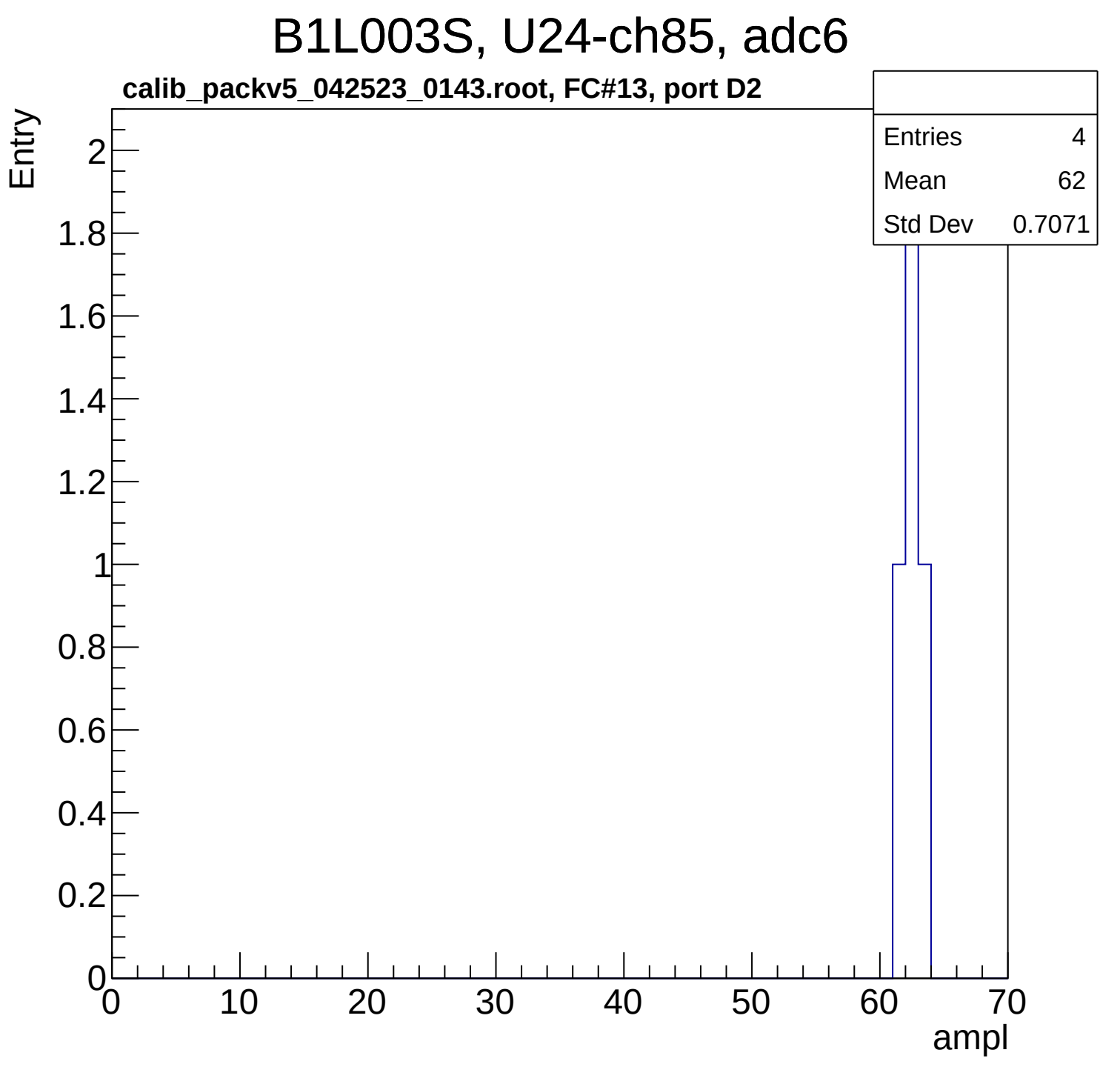
B1L003S, U24-ch85, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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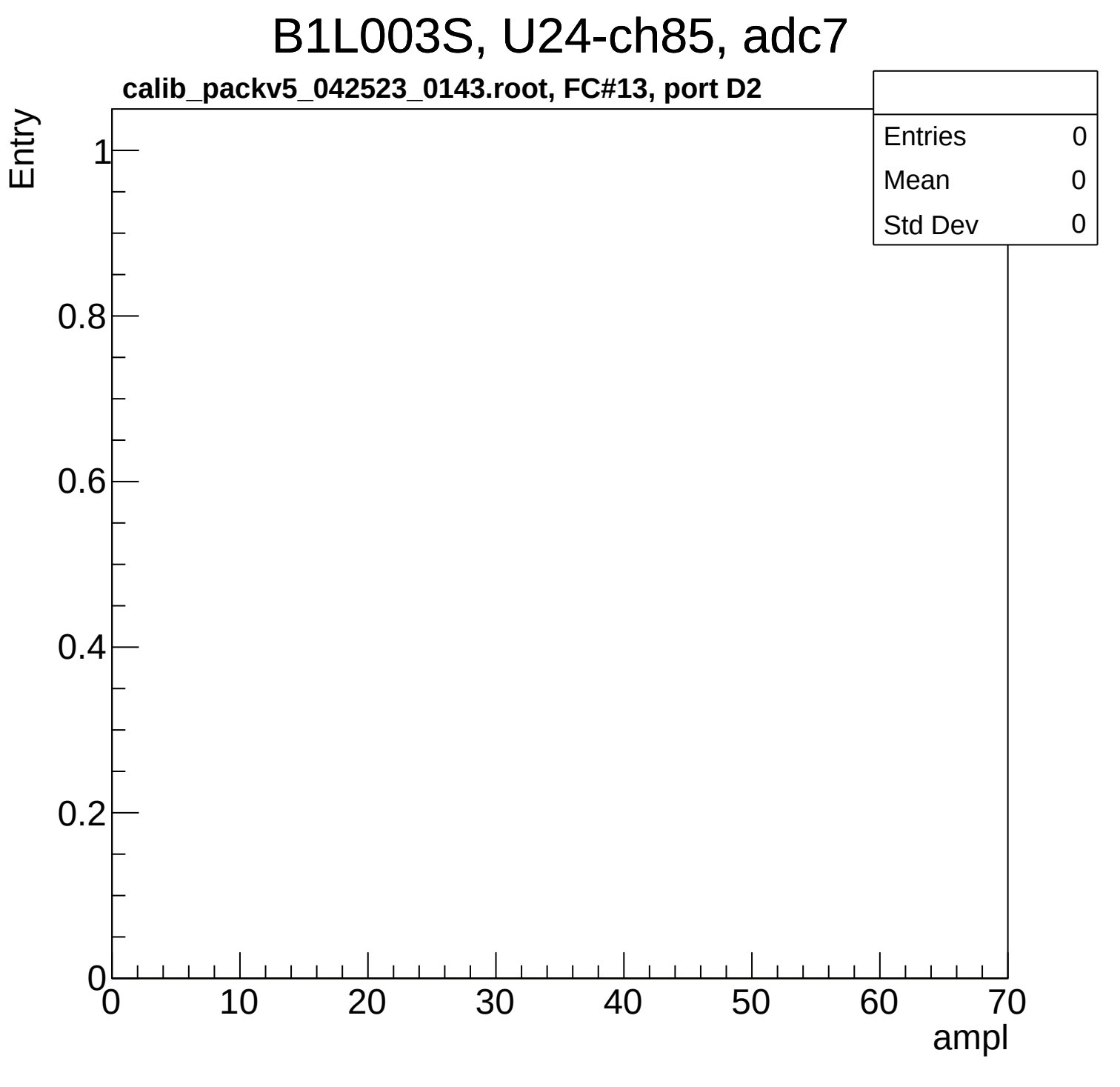
B1L003S, U24-ch85, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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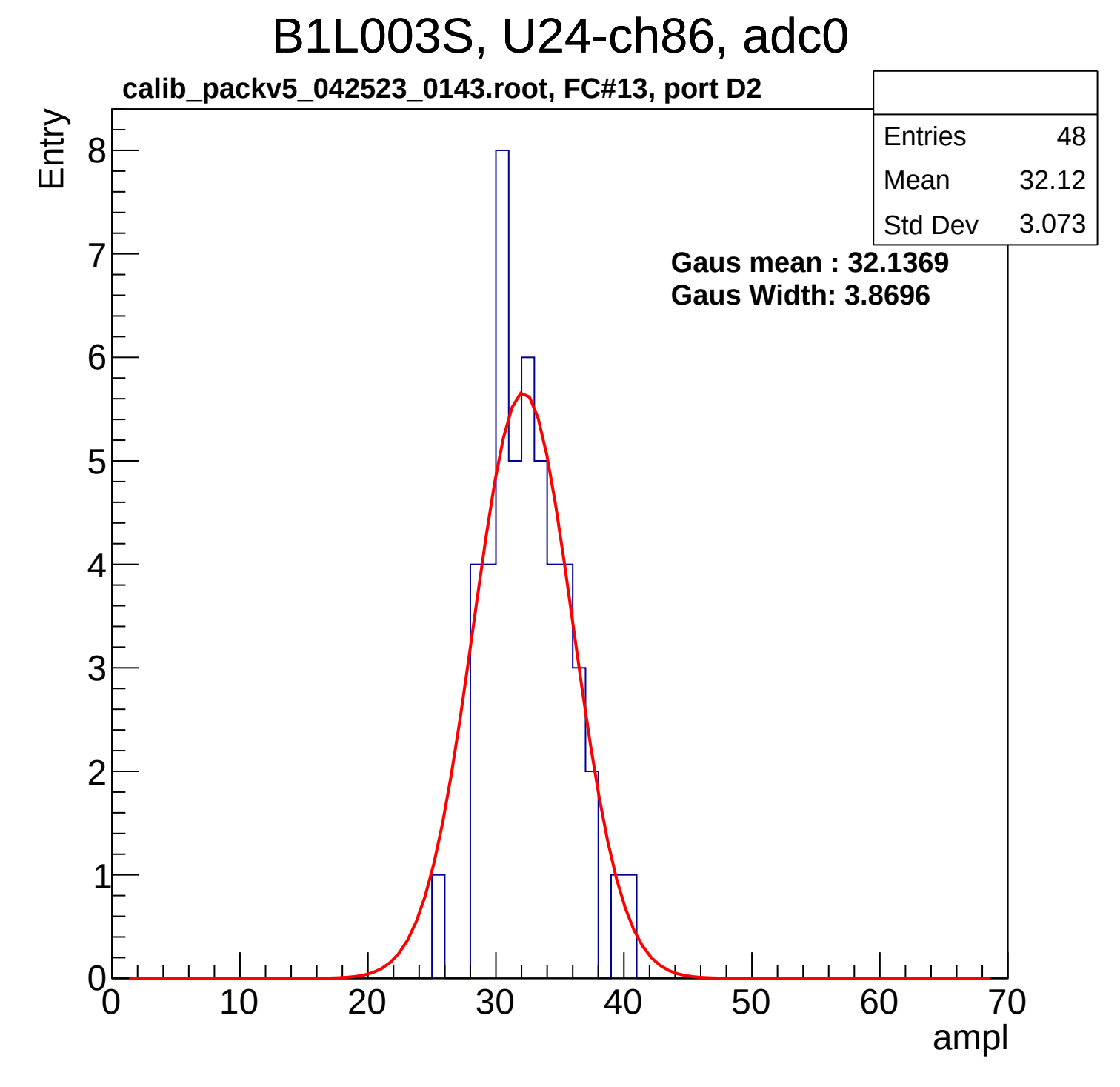
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch86, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 32.12
Std Dev 3.073
Gaus mean : 32.1369
Gaus Width: 3.8696
Entry
ampl
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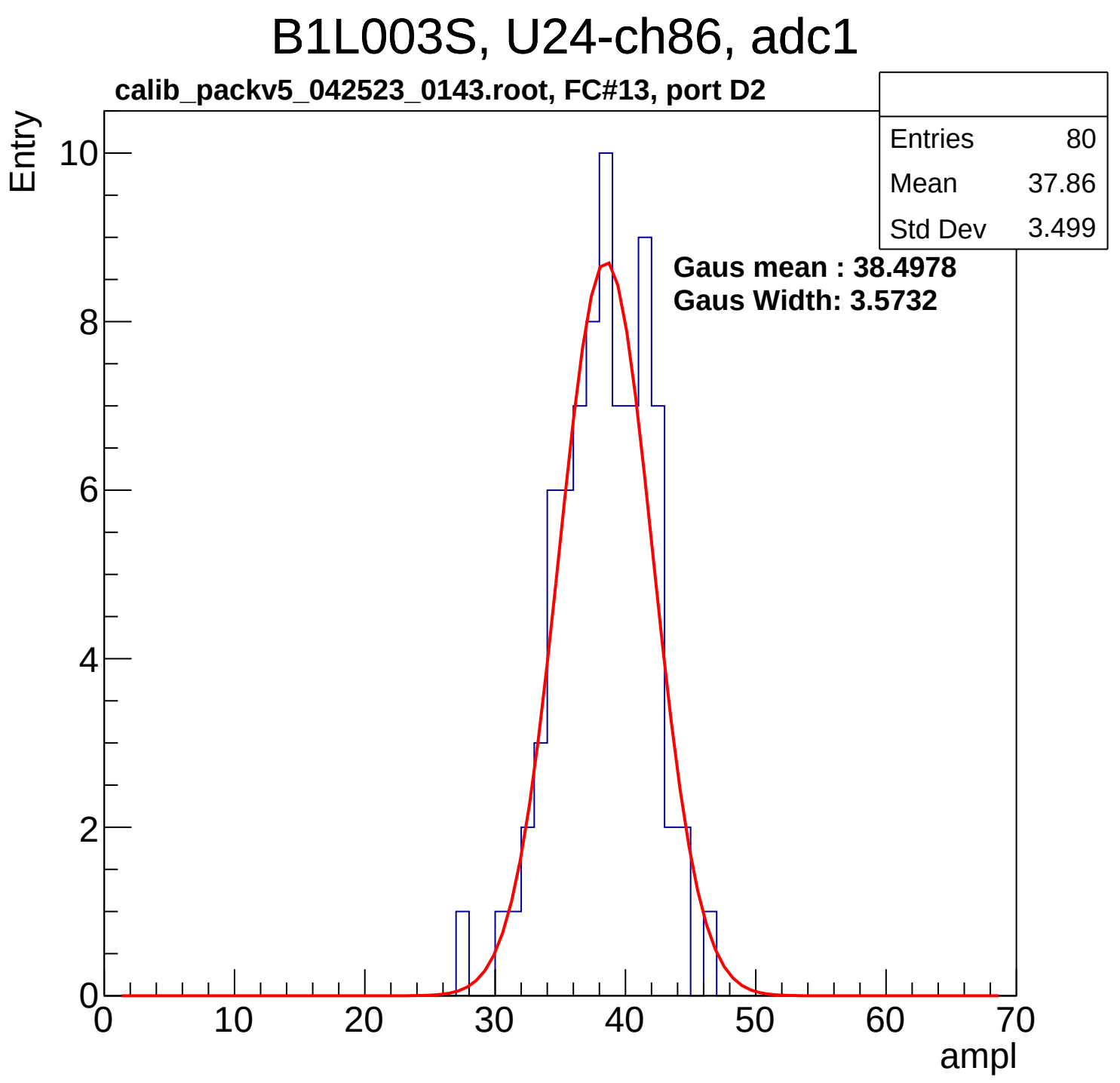
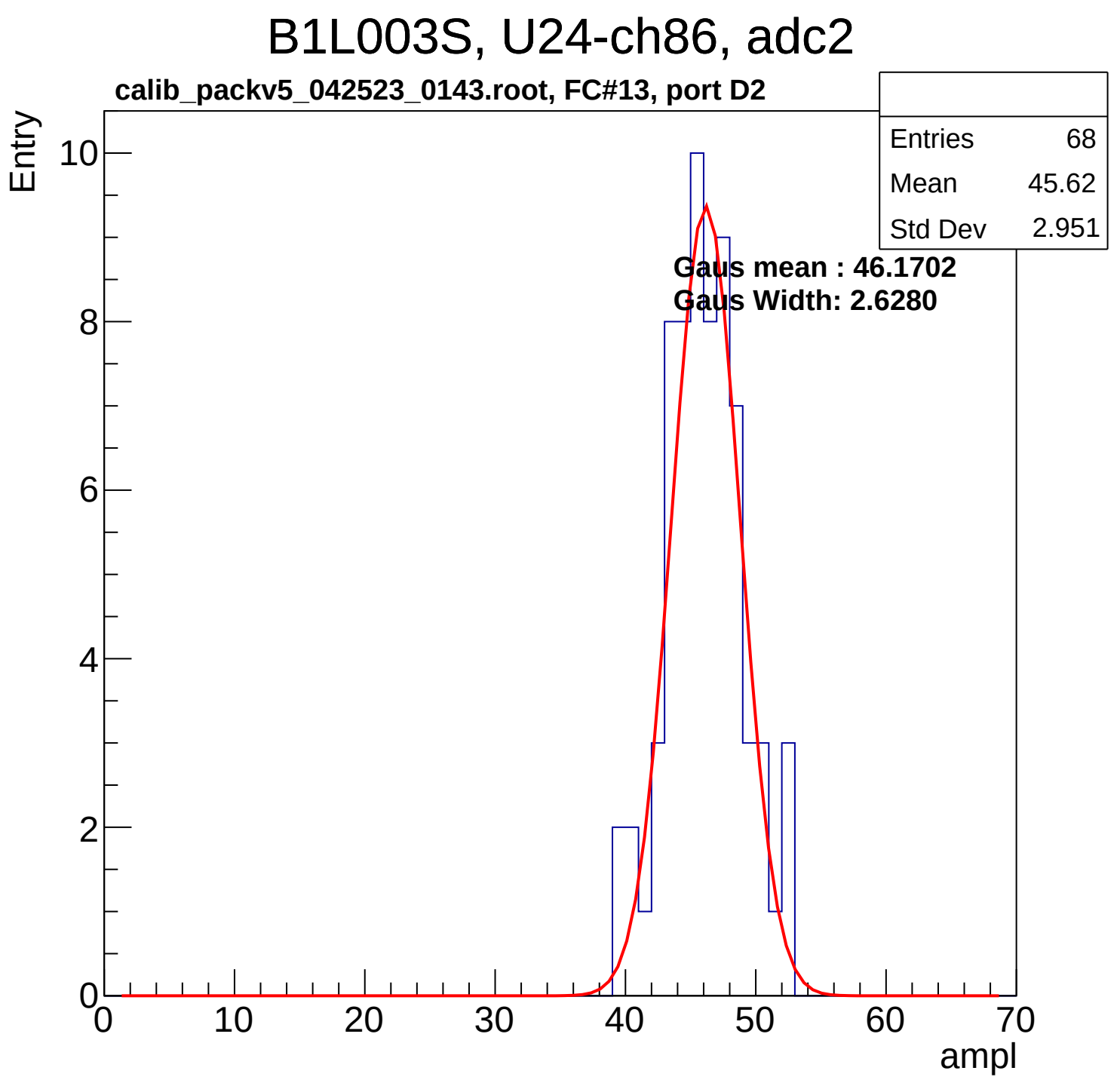
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch86, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 37.86
Std Dev 3.499
Gaus mean : 38.4978
Gaus Width: 3.5732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch86, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 45.62
Std Dev 2.951
Gaus mean : 46.1702
Gaus Width: 2.6280
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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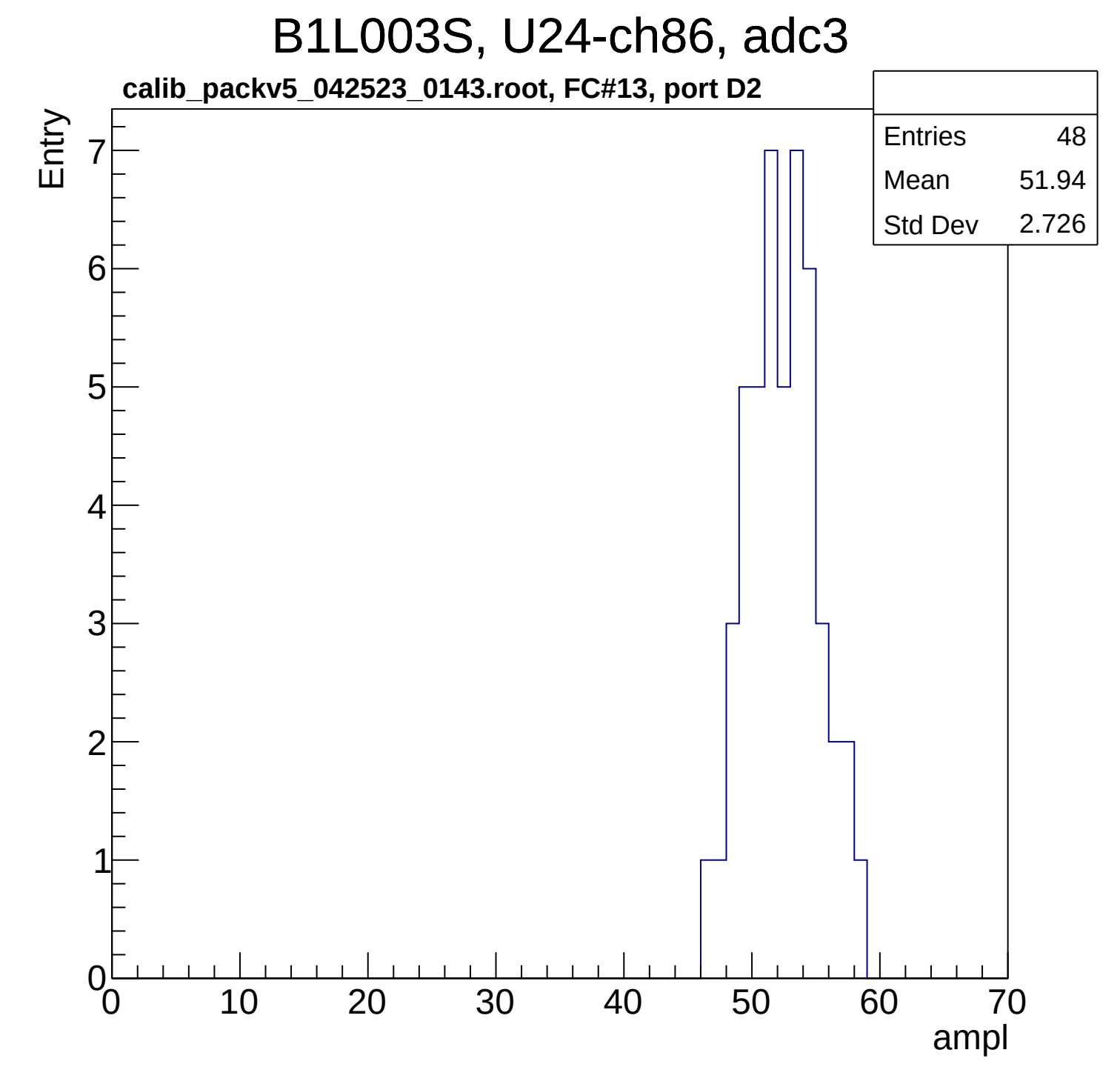
B1L003S, U24-ch86, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 51.94
Std Dev 2.726
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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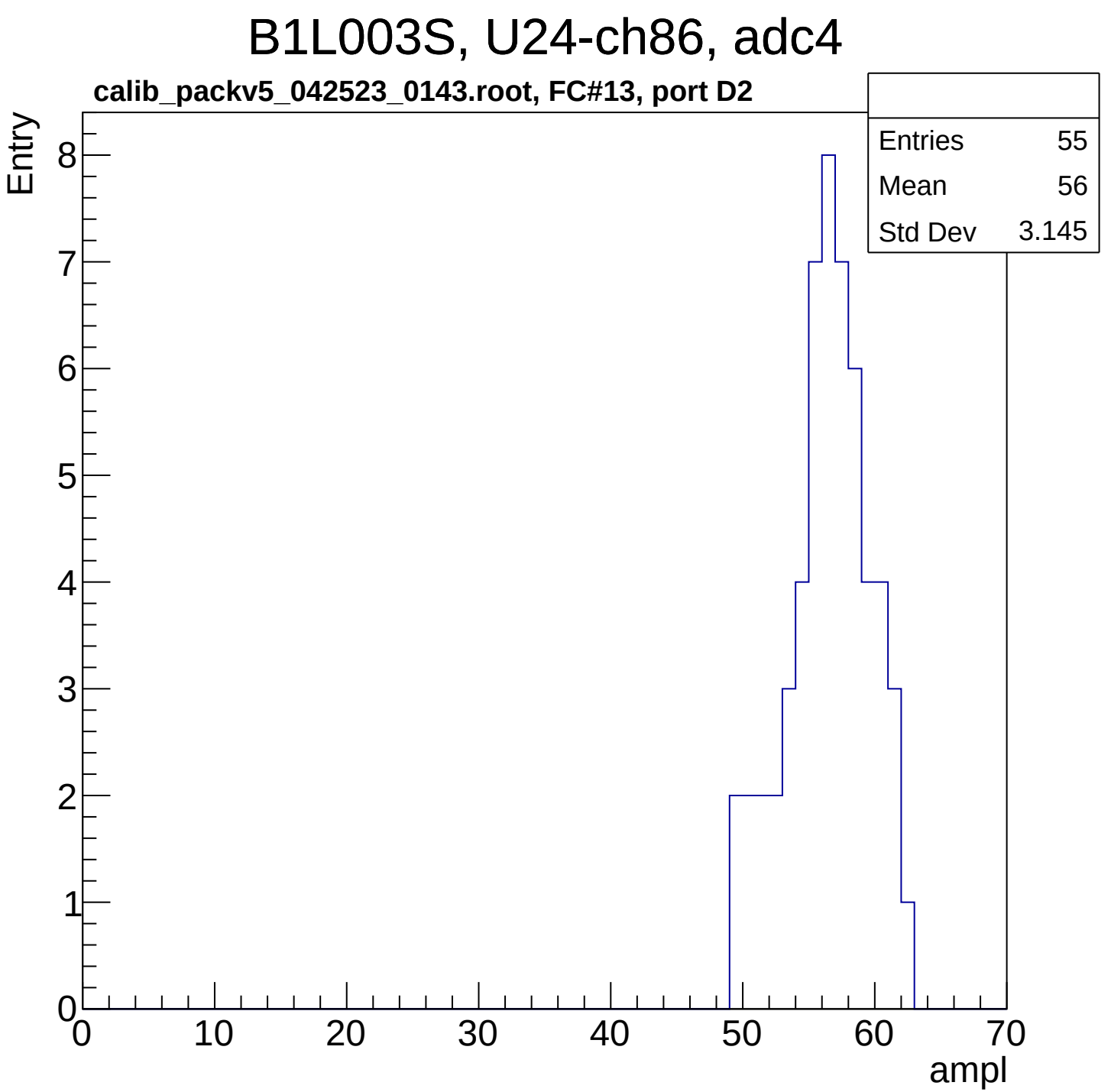
B1L003S, U24-ch86, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 56
Std Dev 3.145
Entry
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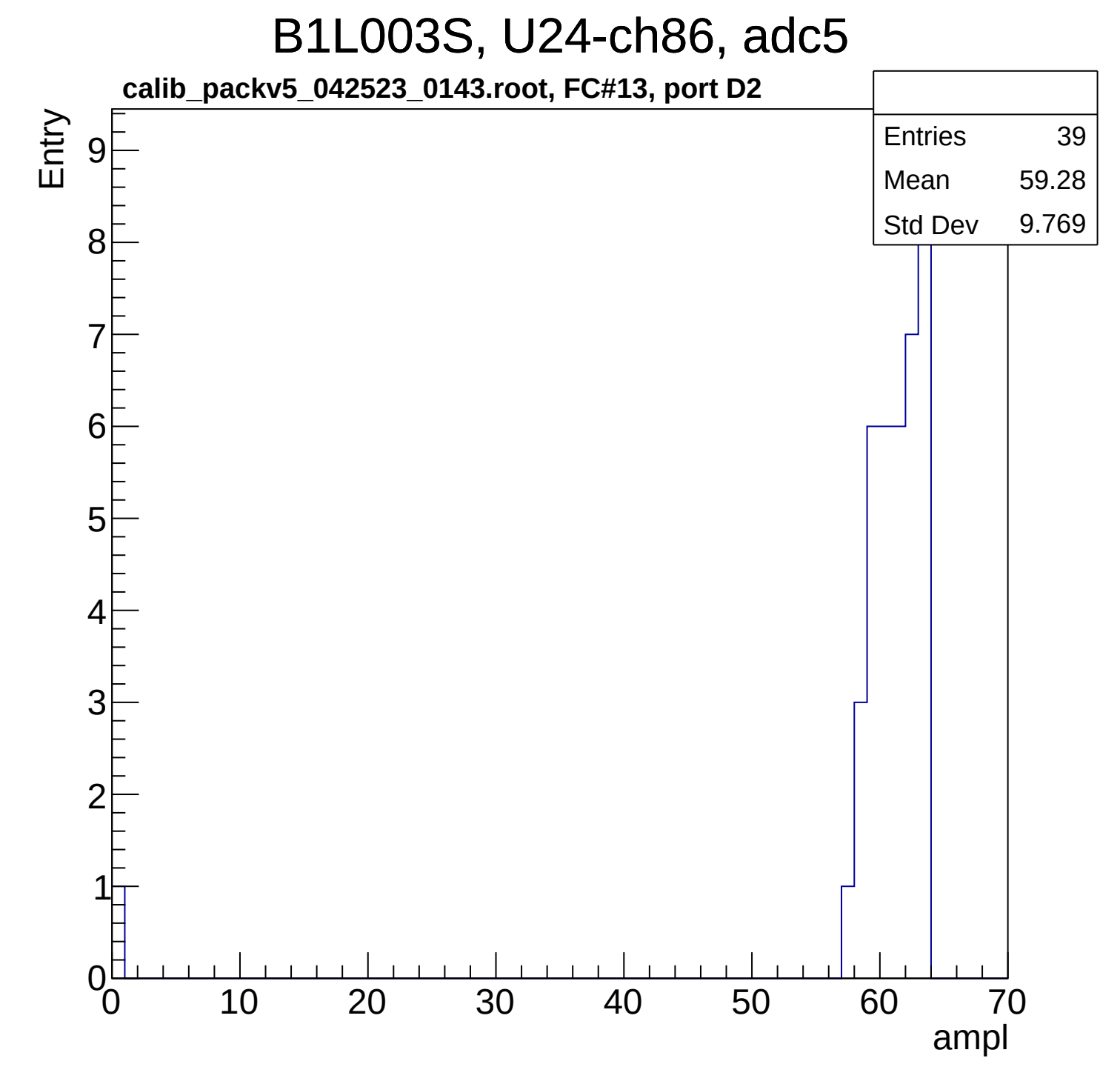
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch86, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 59.28
Std Dev 9.769
Entry
ampl
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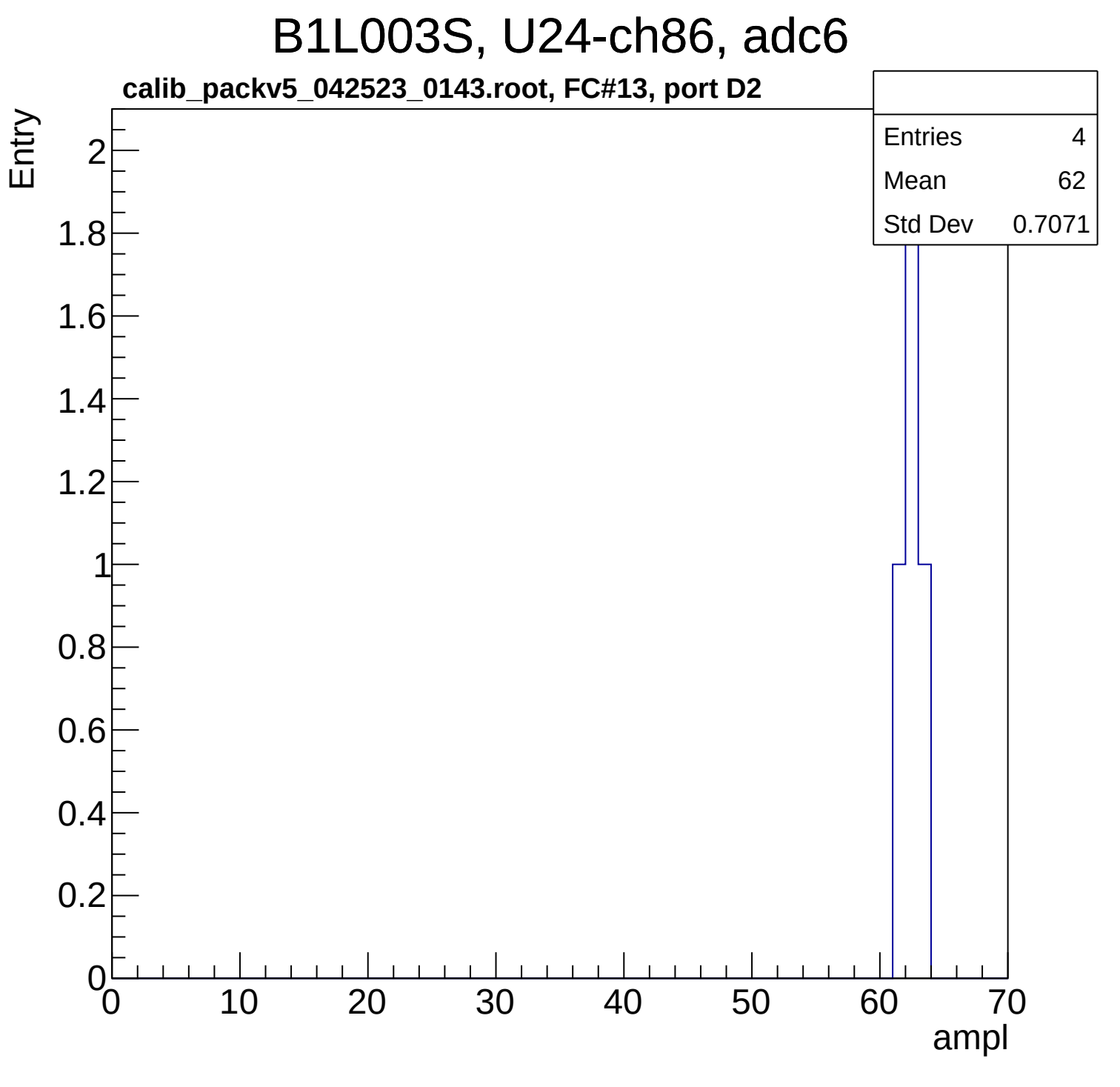
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch86, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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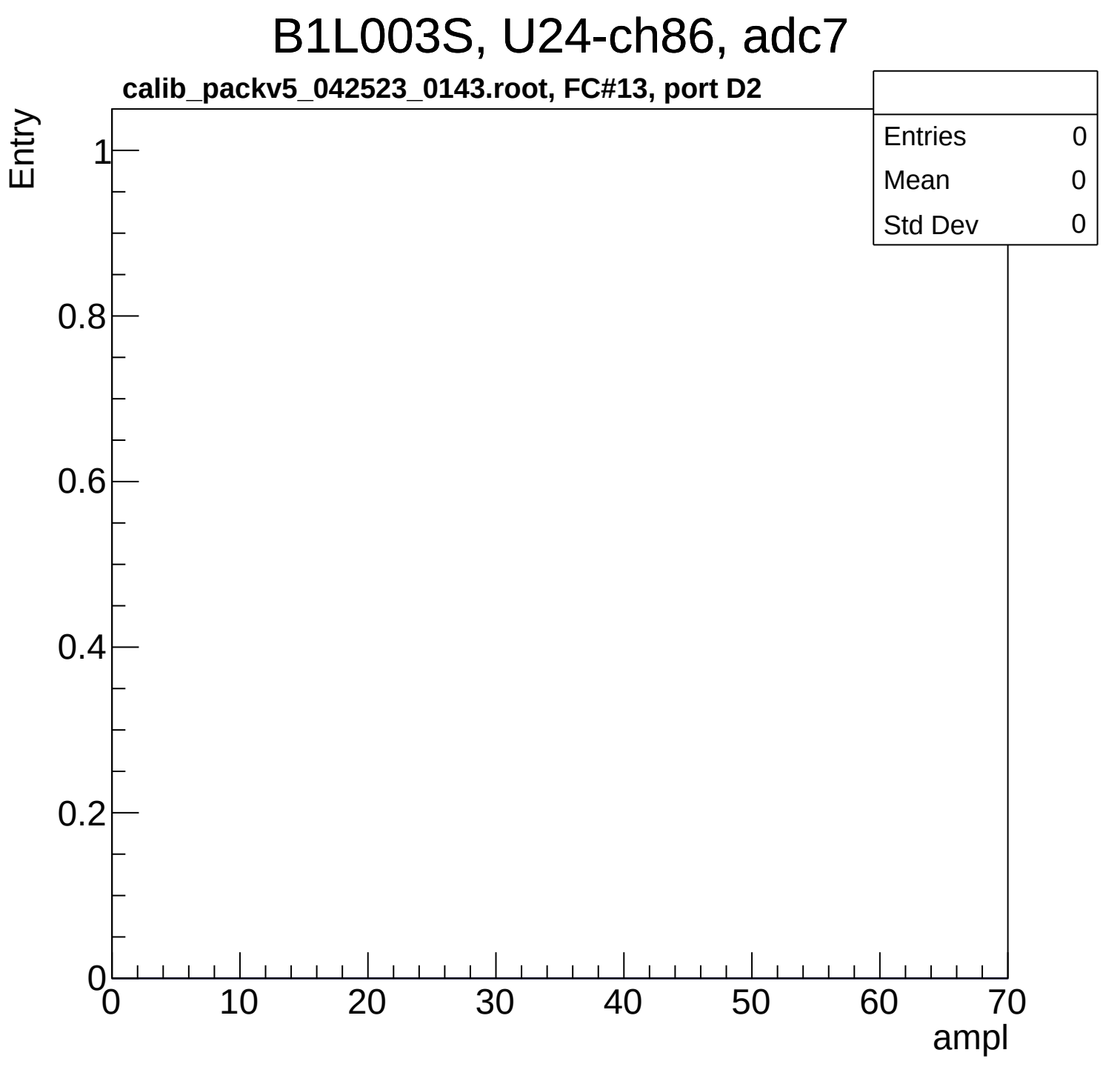
B1L003S, U24-ch86, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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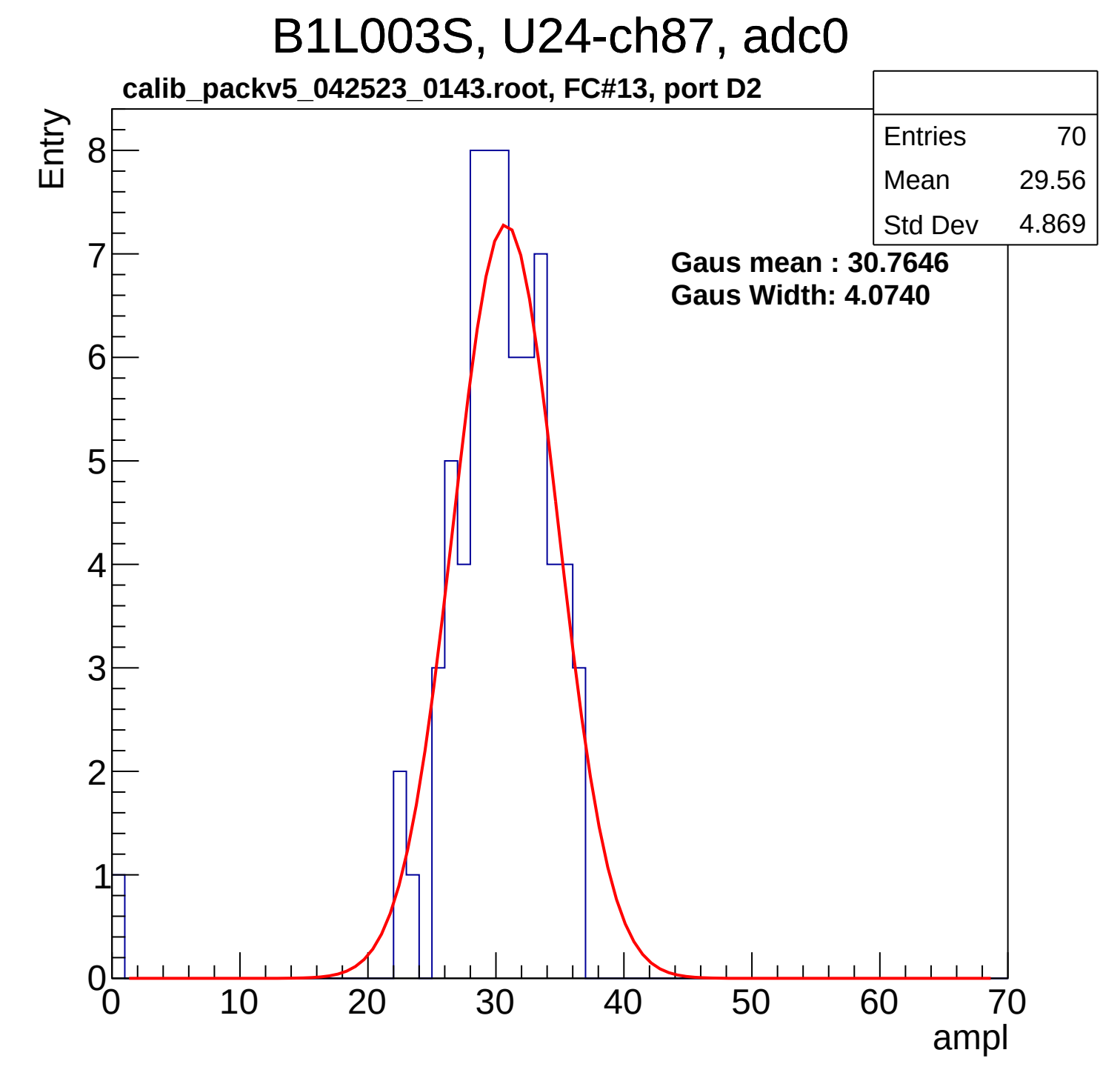
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch87, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 29.56
Std Dev 4.869
Gaus mean : 30.7646
Gaus Width: 4.0740
Entry
ampl
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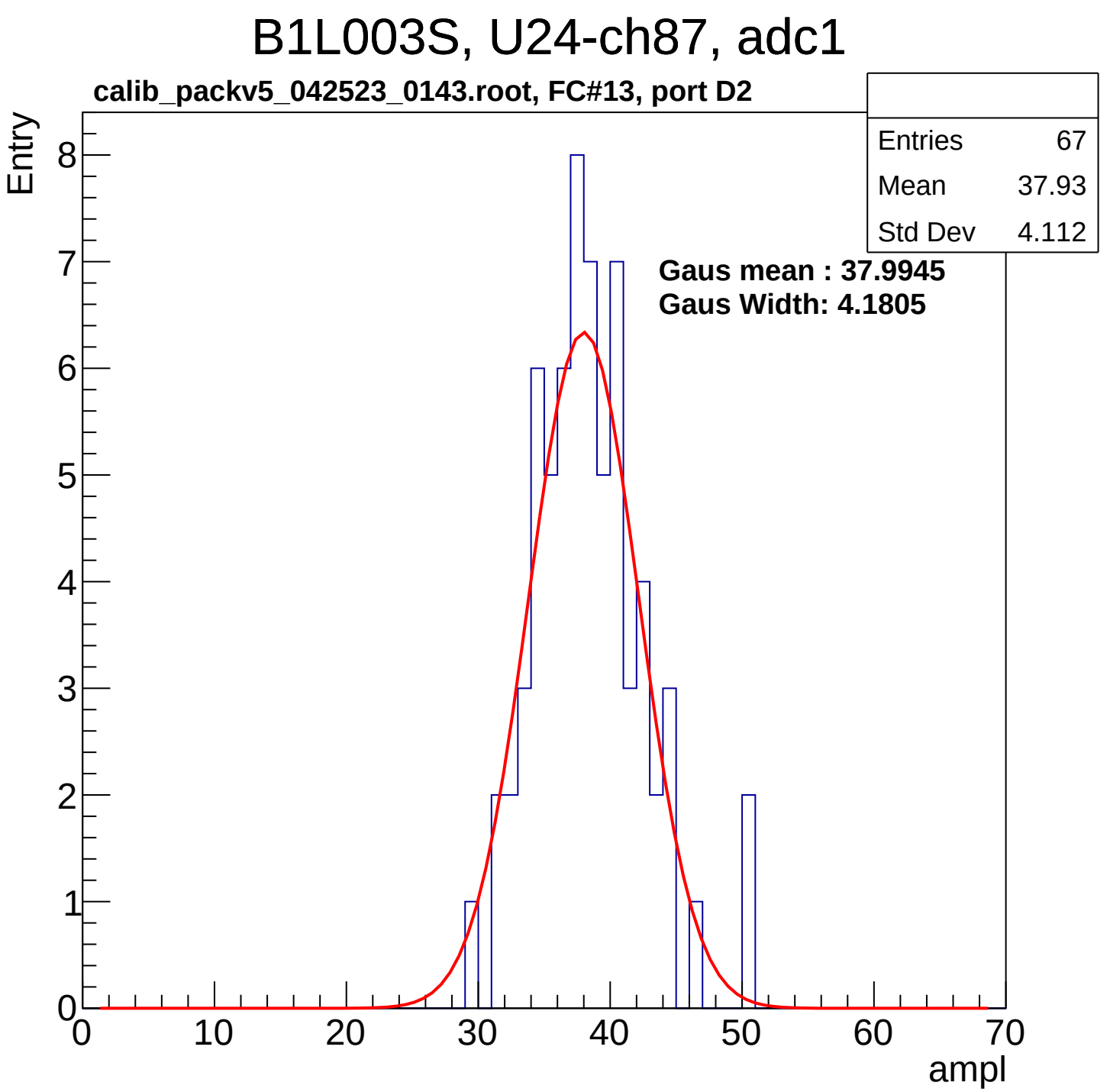
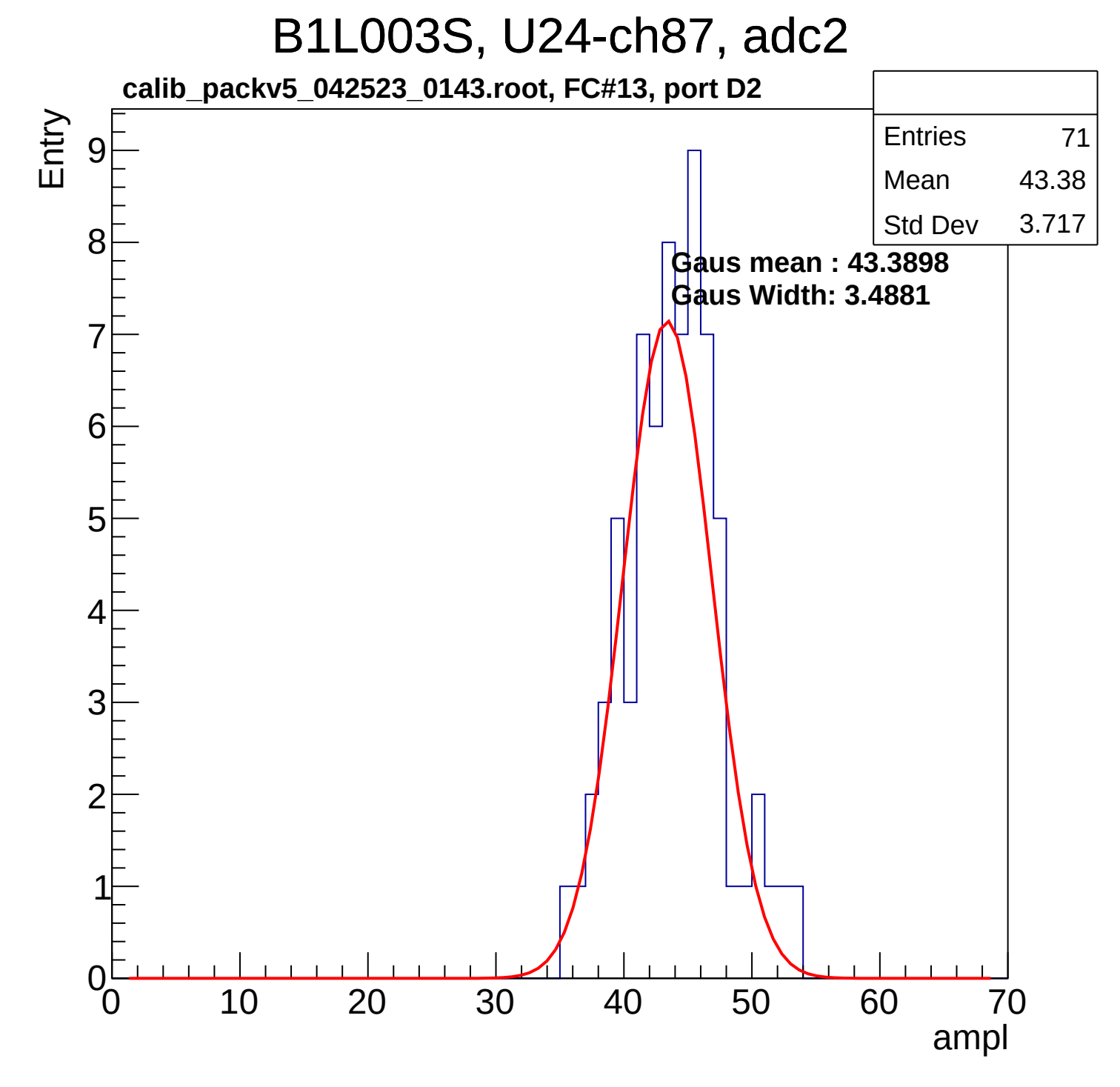
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch87, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.93
Std Dev 4.112
Gaus mean : 37.9945
Gaus Width: 4.1805
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch87, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 43.38
Std Dev 3.717
Gaus mean : 43.3898
Gaus Width: 3.4881
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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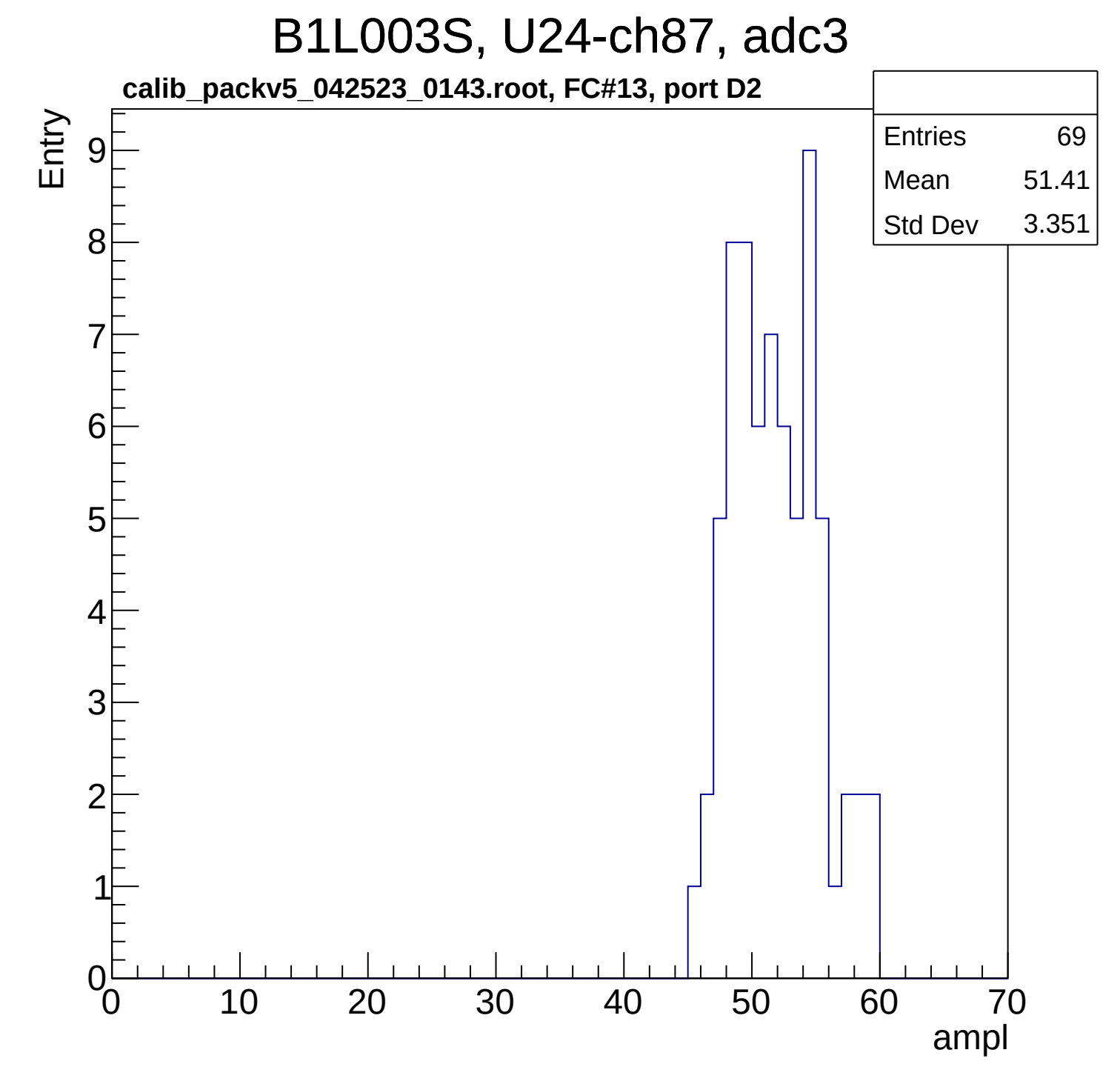
B1L003S, U24-ch87, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 51.41
Std Dev 3.351
Entry
ampl
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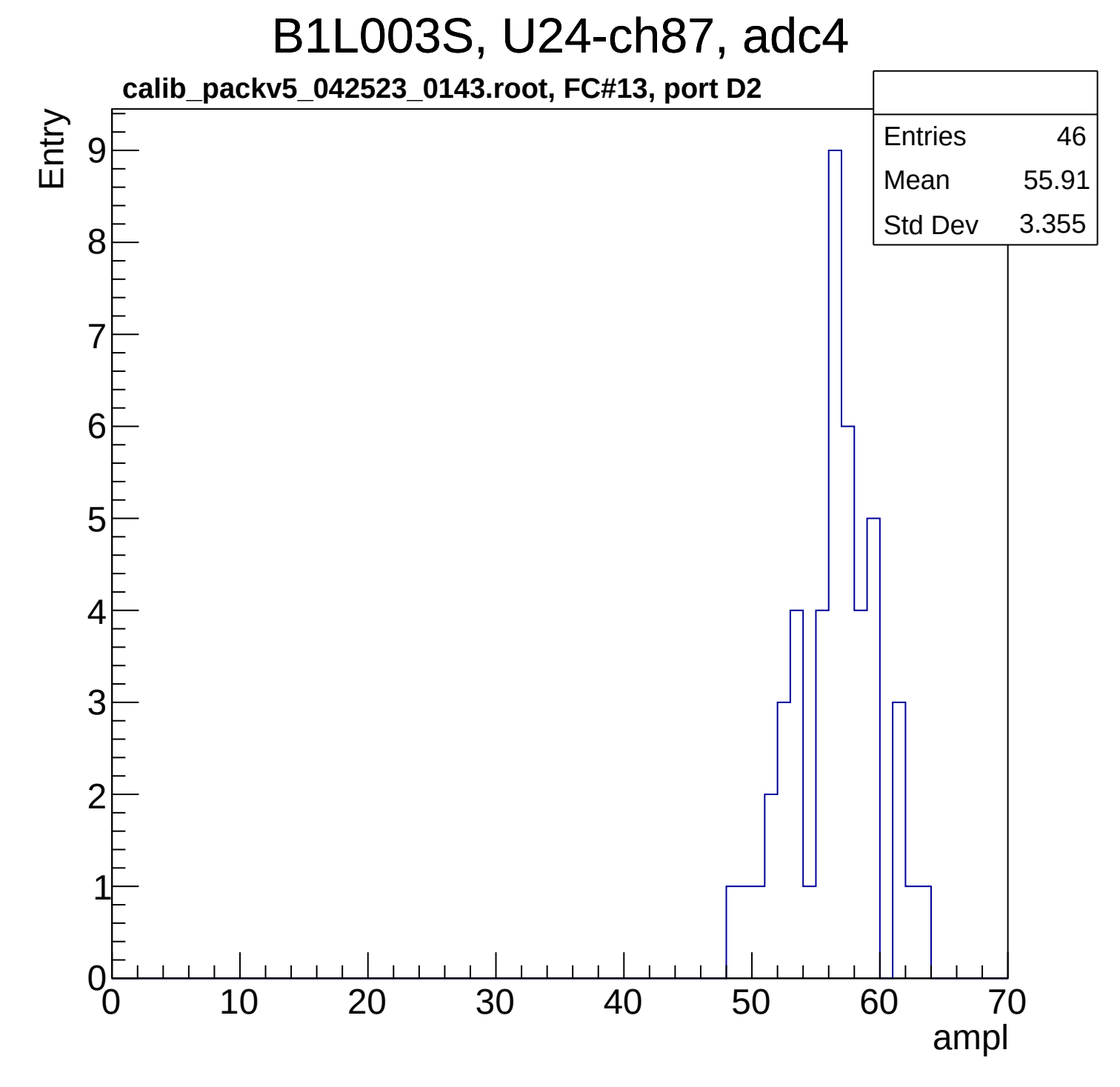
B1L003S, U24-ch87, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 55.91
Std Dev 3.355
Entry
ampl
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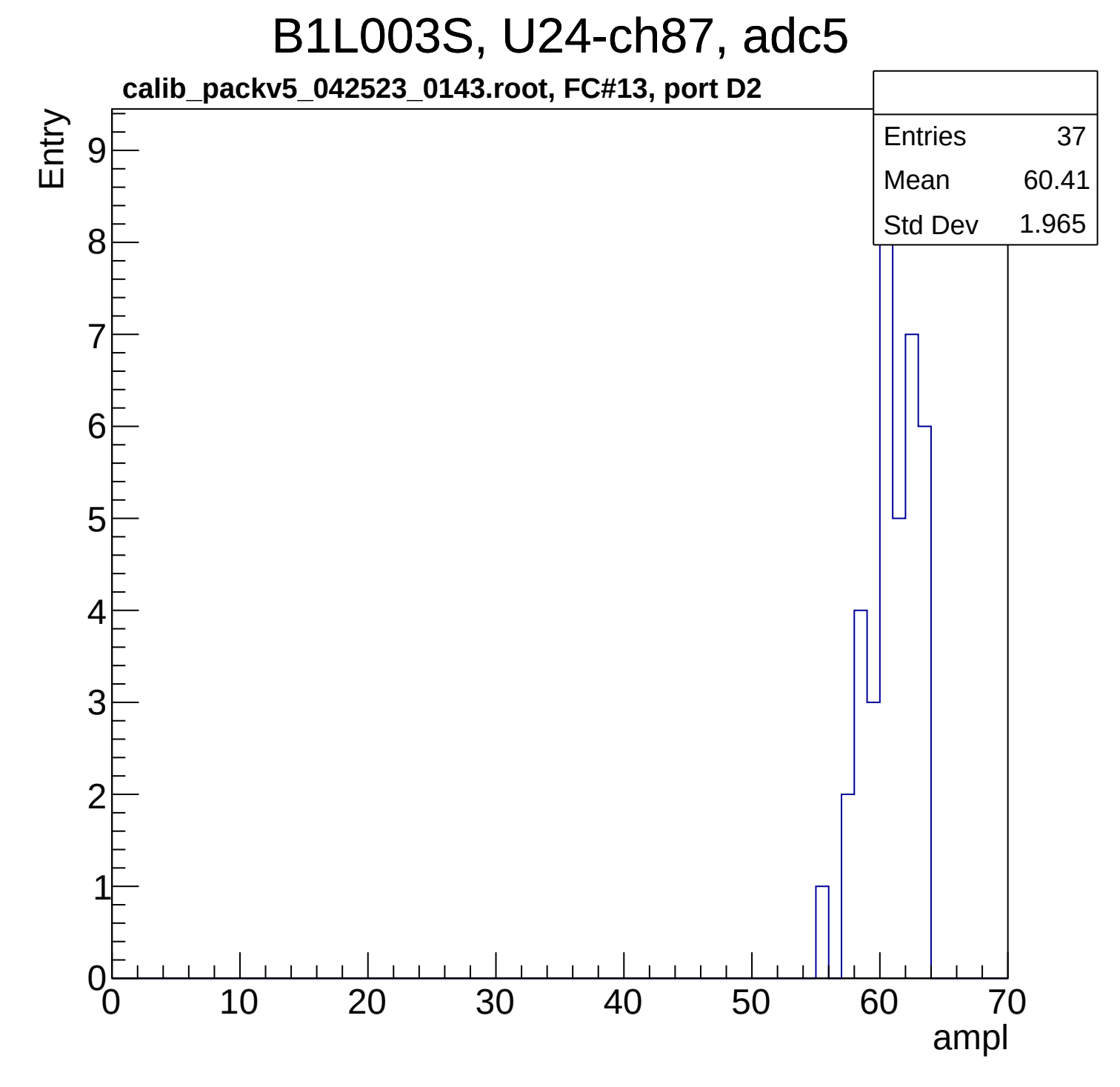
B1L003S, U24-ch87, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 60.41
Std Dev 1.965
Entry
ampl
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0
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60
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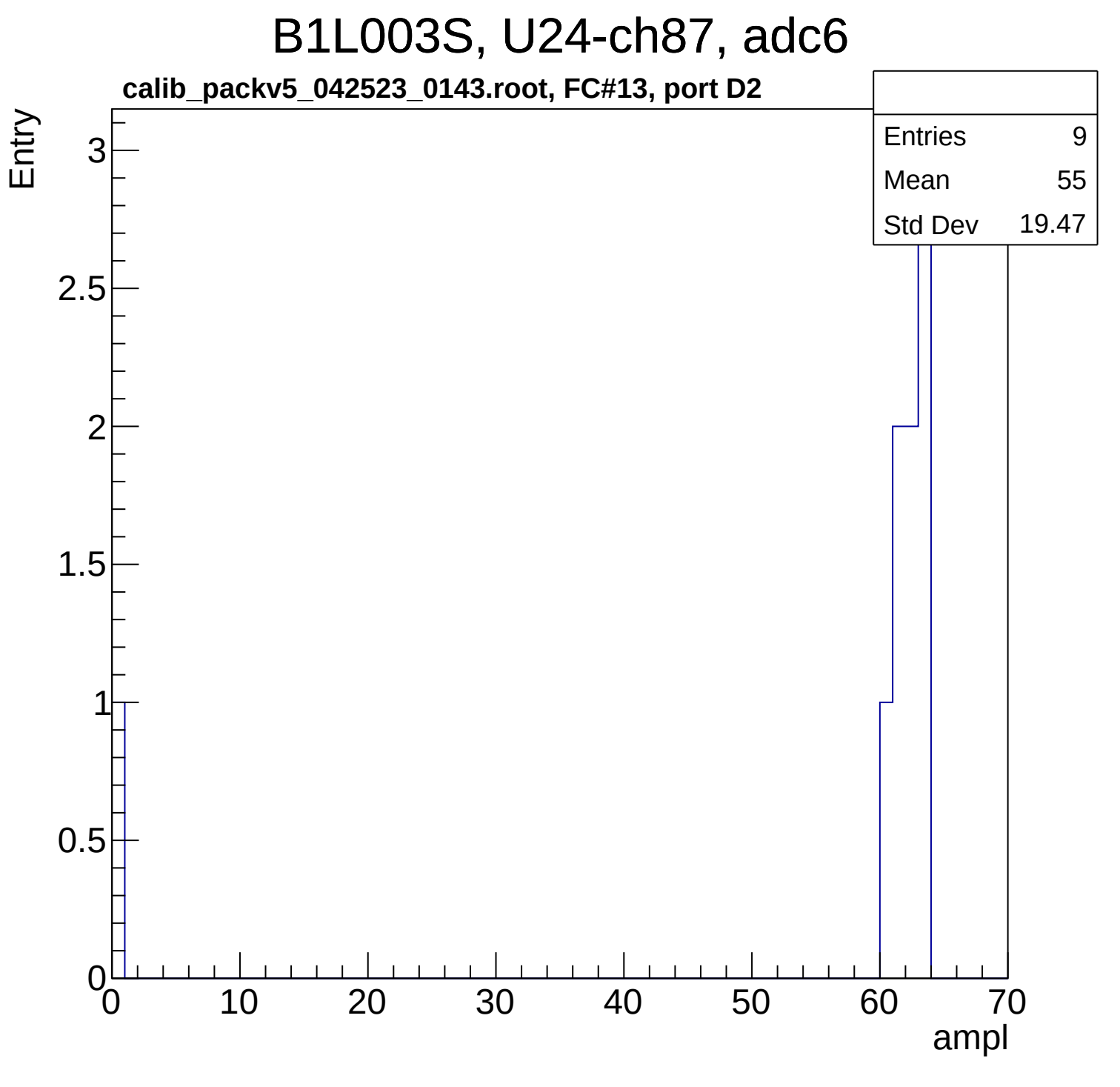
B1L003S, U24-ch87, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55
Std Dev 19.47
Entry
ampl
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0.5
1
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0
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60
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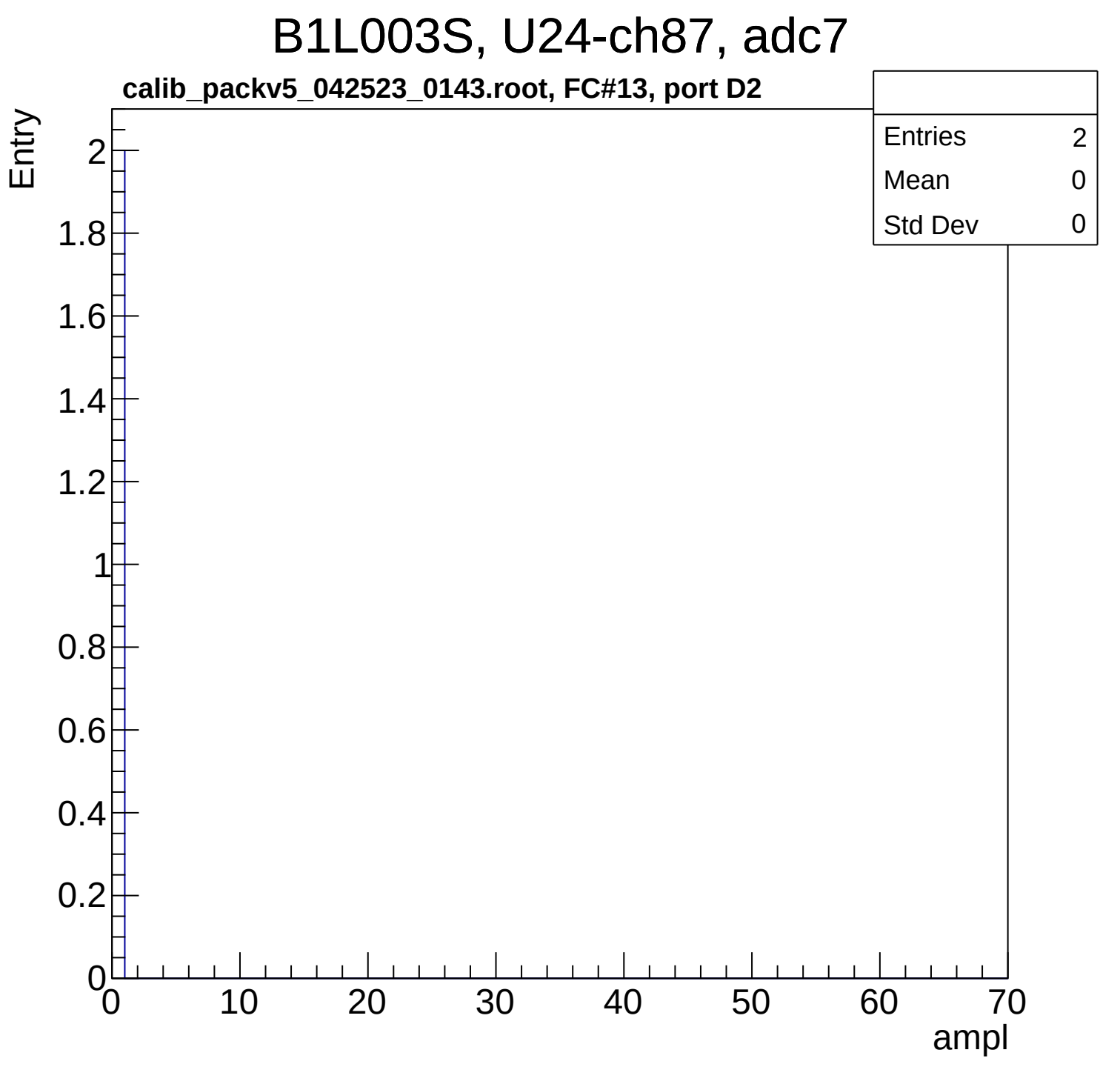
B1L003S, U24-ch87, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
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0
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60
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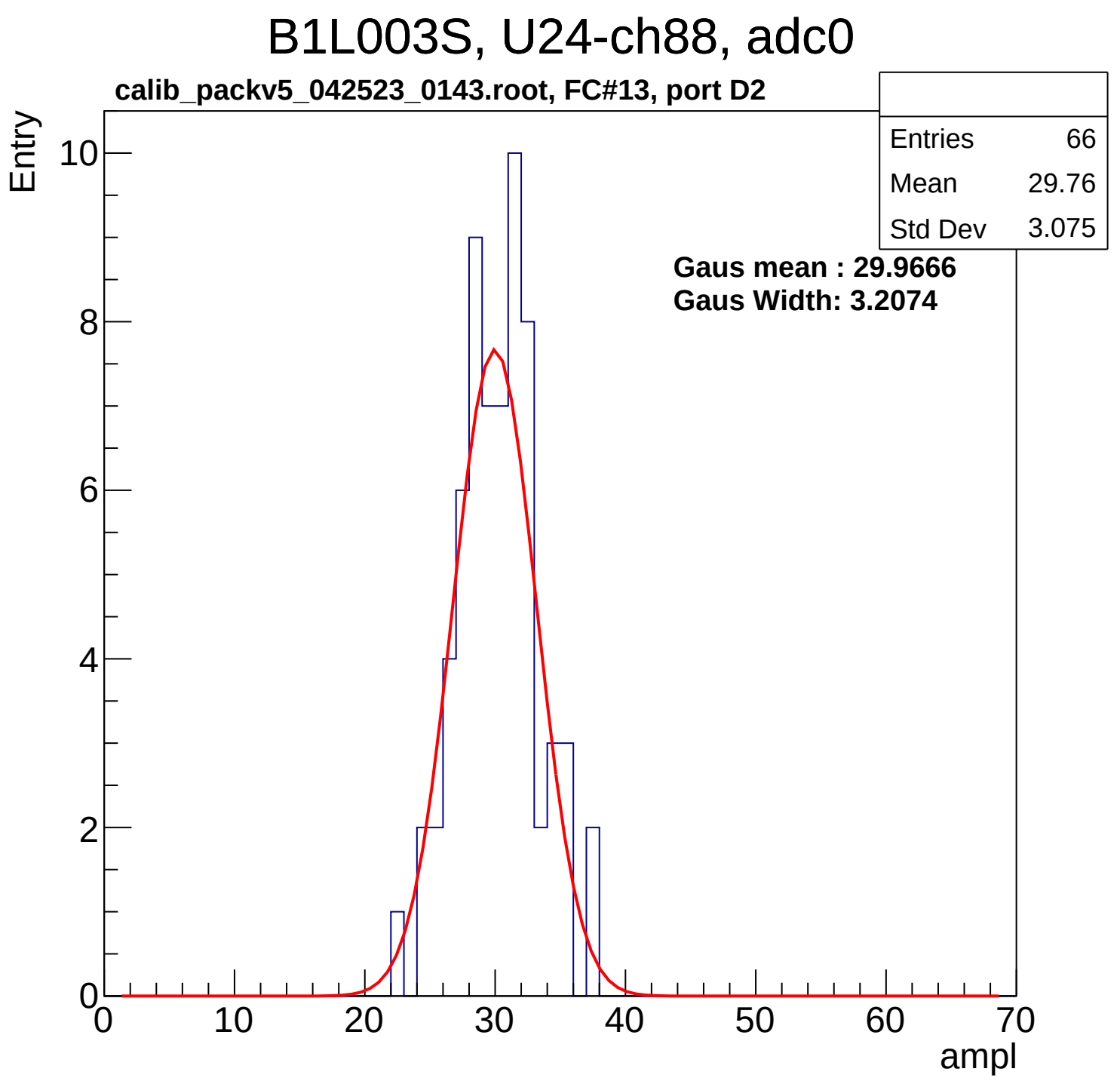
B1L003S, U24-ch88, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 29.76
Std Dev 3.075
Gaus mean : 29.9666
Gaus Width: 3.2074
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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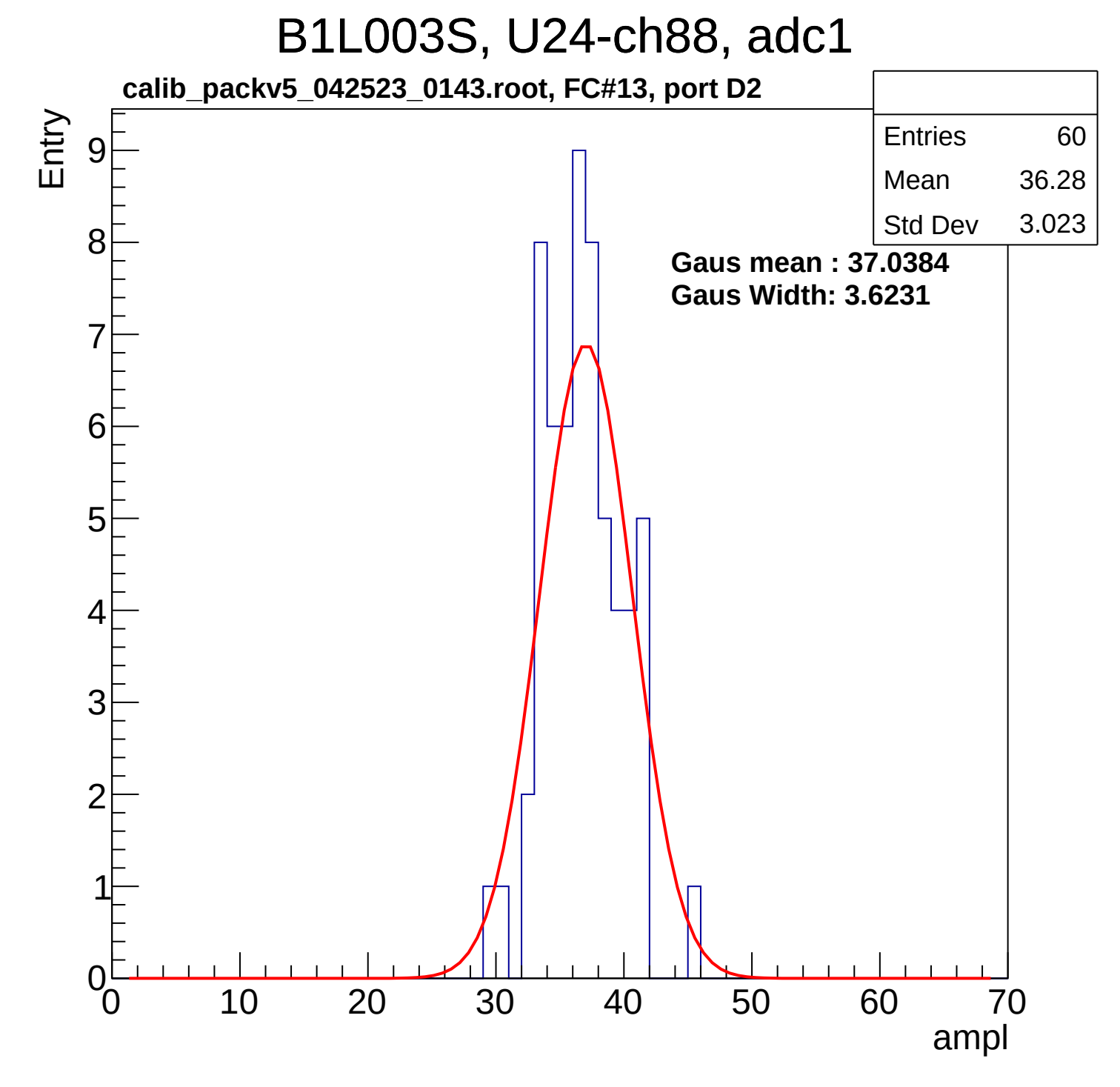
B1L003S, U24-ch88, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.28
Std Dev 3.023
Gaus mean : 37.0384
Gaus Width: 3.6231
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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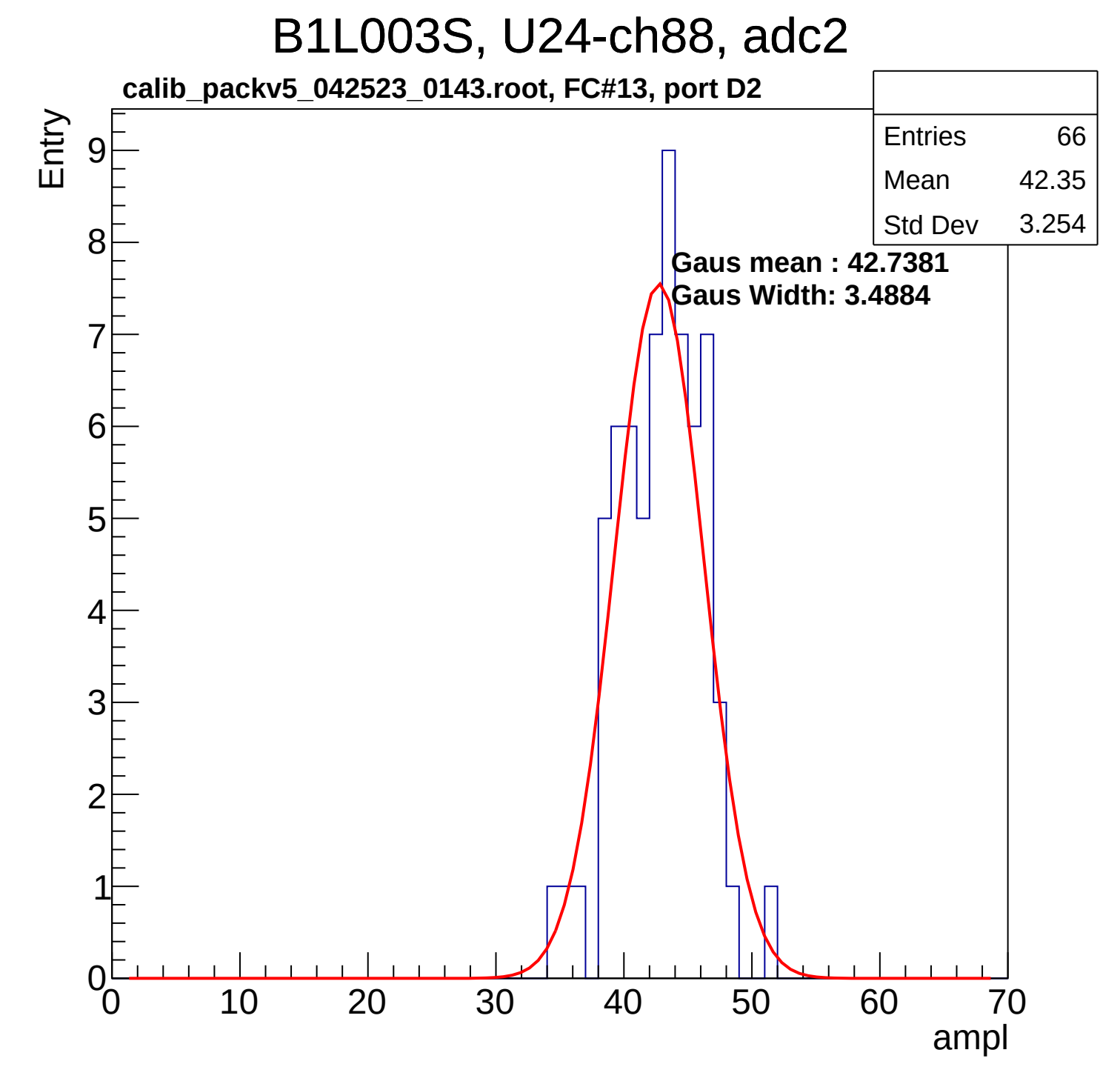
B1L003S, U24-ch88, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.35
Std Dev 3.254
Gaus mean : 42.7381
Gaus Width: 3.4884
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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60
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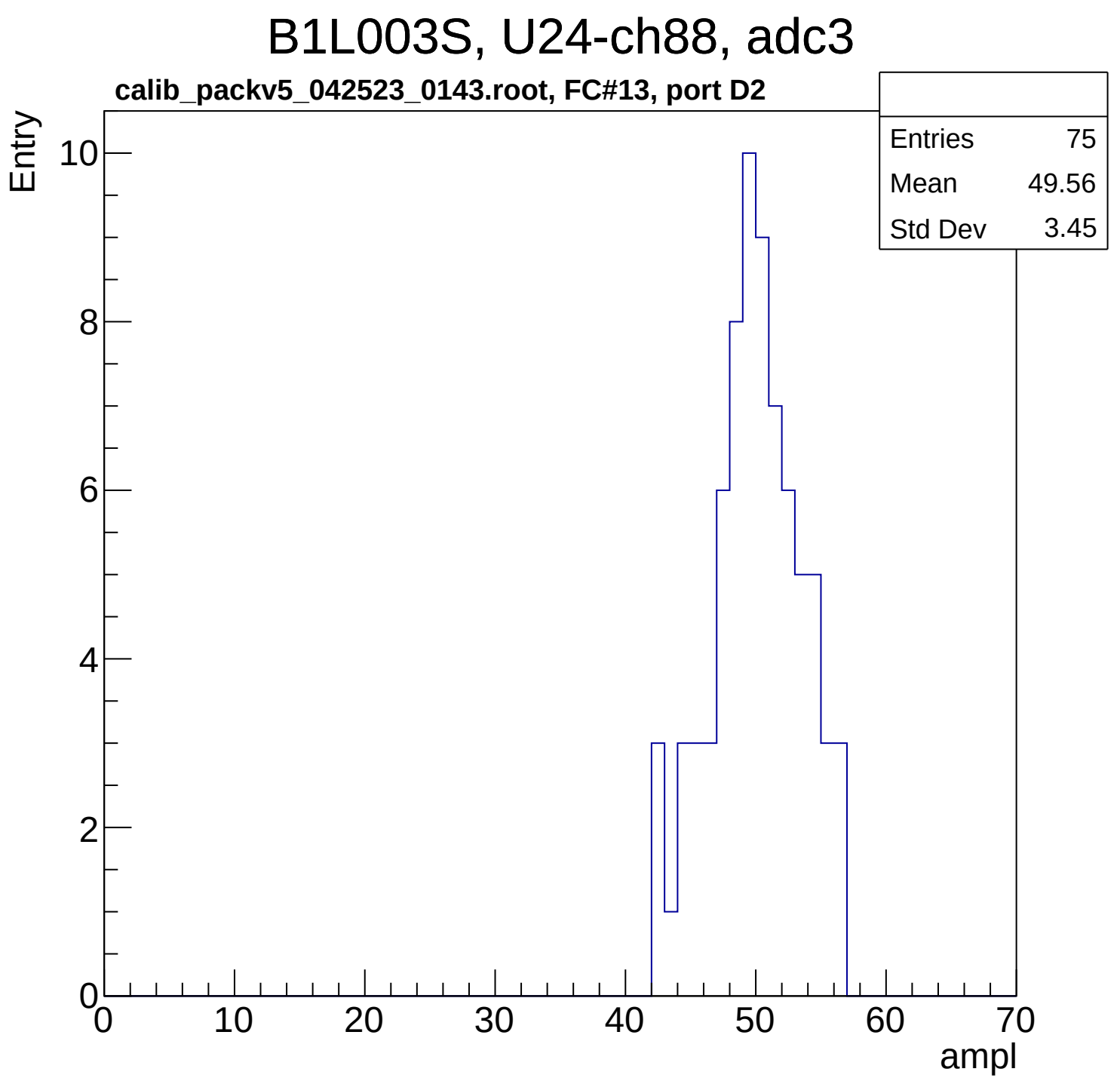
B1L003S, U24-ch88, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 49.56
Std Dev 3.45
Entry
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4
6
8
10
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10
20
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50
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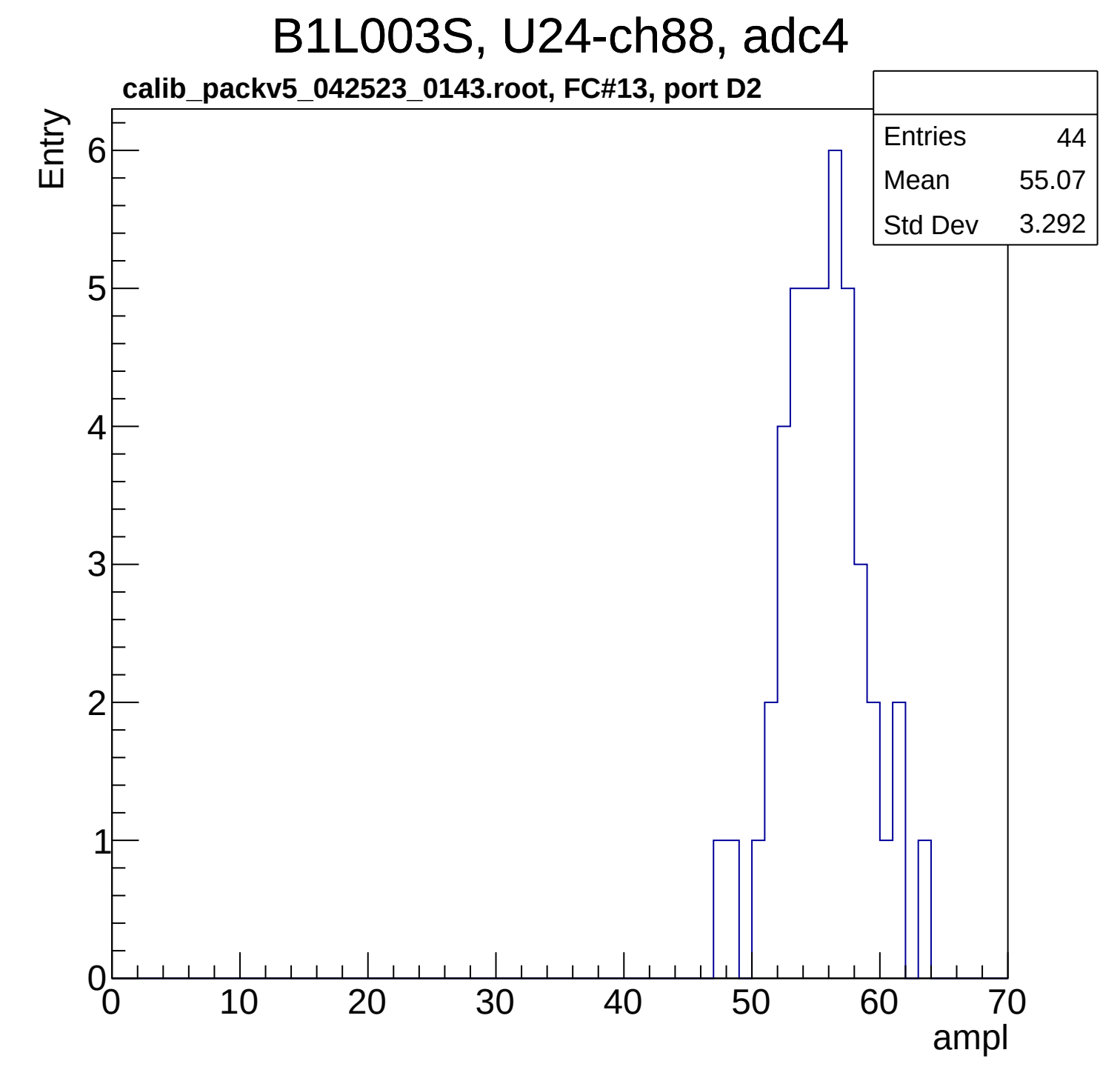
70
ampl


B1L003S, U24-ch88, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 55.07
Std Dev 3.292
Entry
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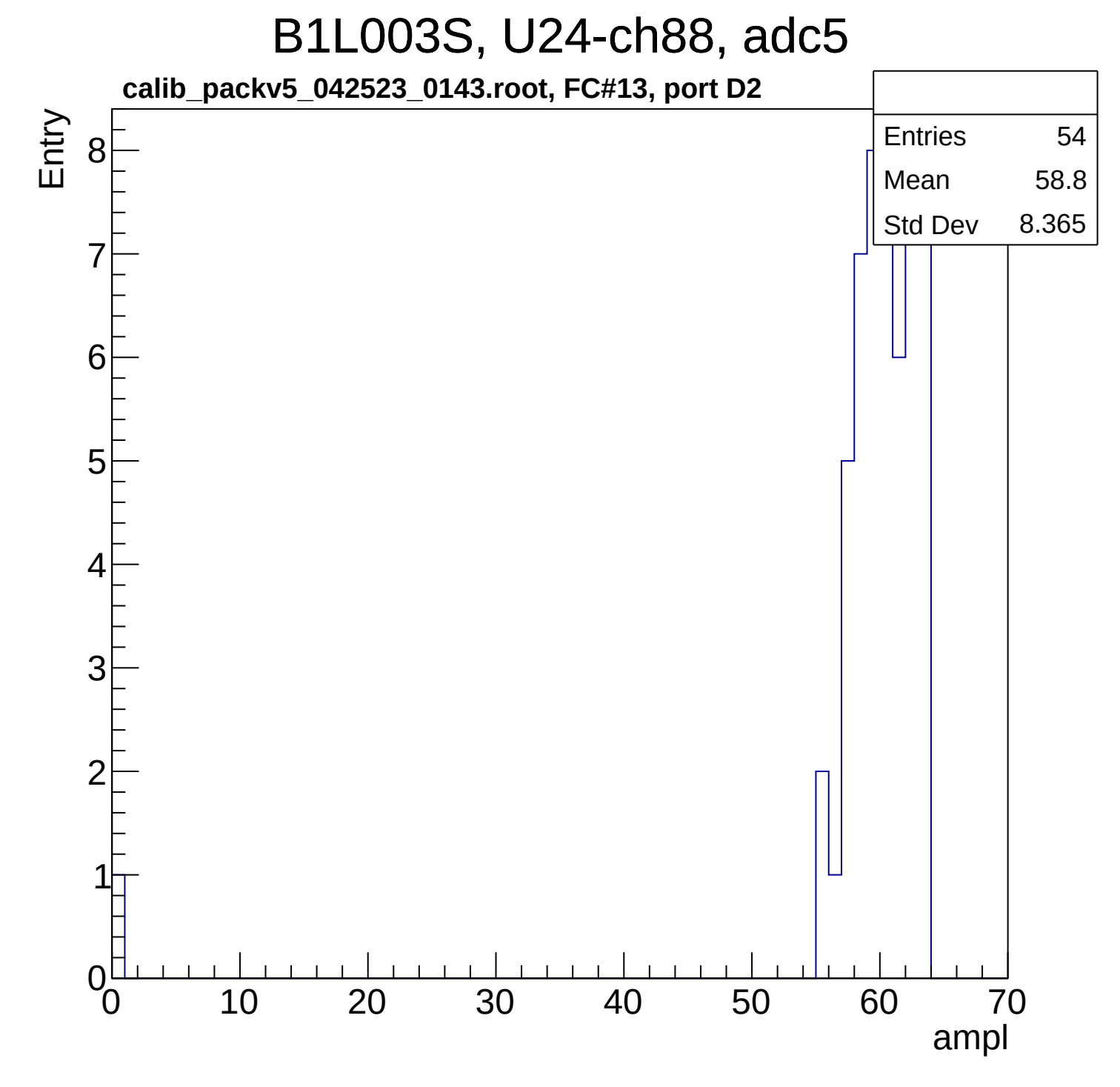
60
70
ampl


B1L003S, U24-ch88, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.8
Std Dev 8.365
Entry
ampl
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4
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7
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20
30
40
50
60
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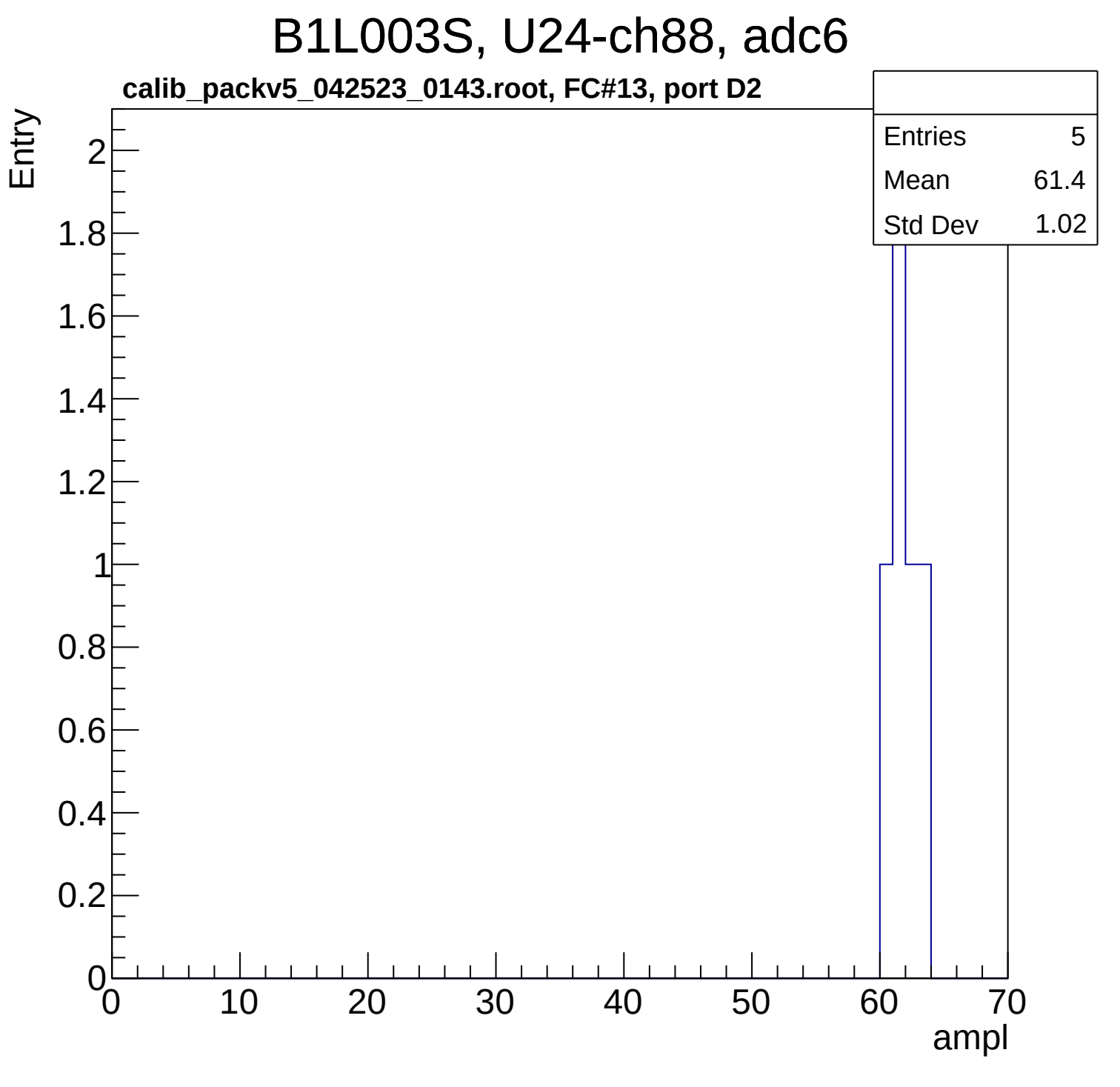
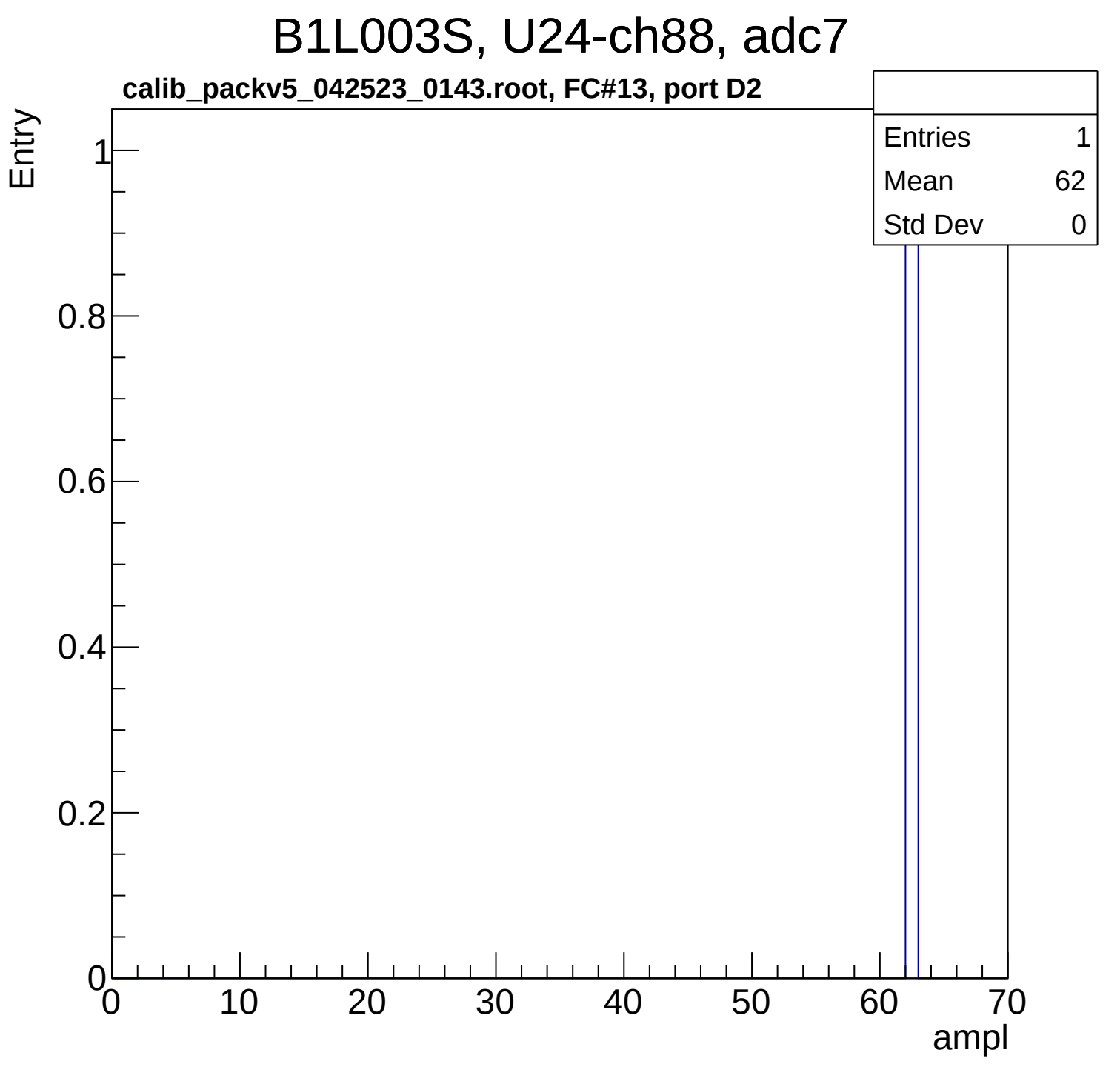
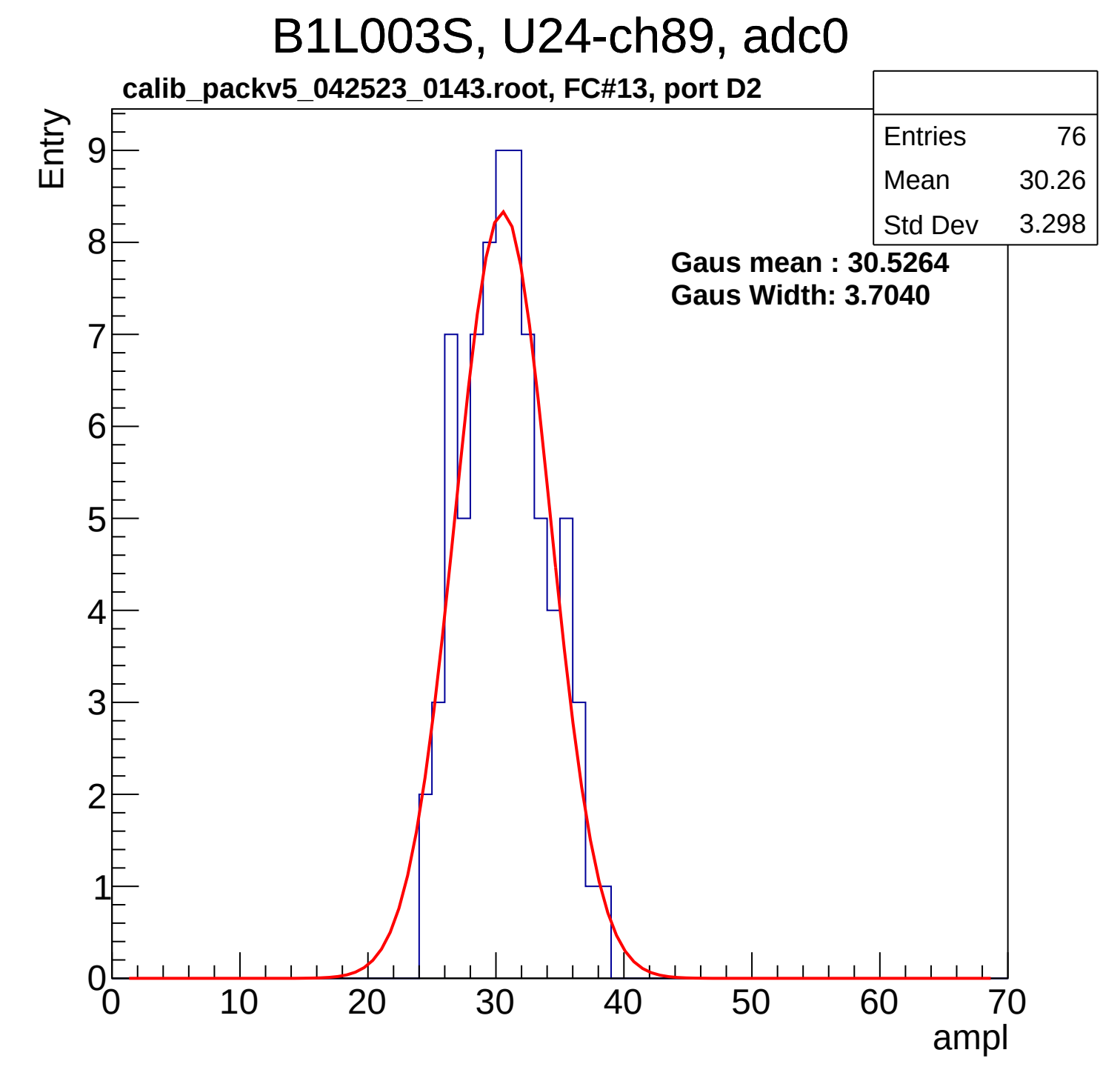

B1L003S, U24-ch88, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.4
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch88, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch89, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 30.26
Std Dev 3.298
Gaus mean : 30.5264
Gaus Width: 3.7040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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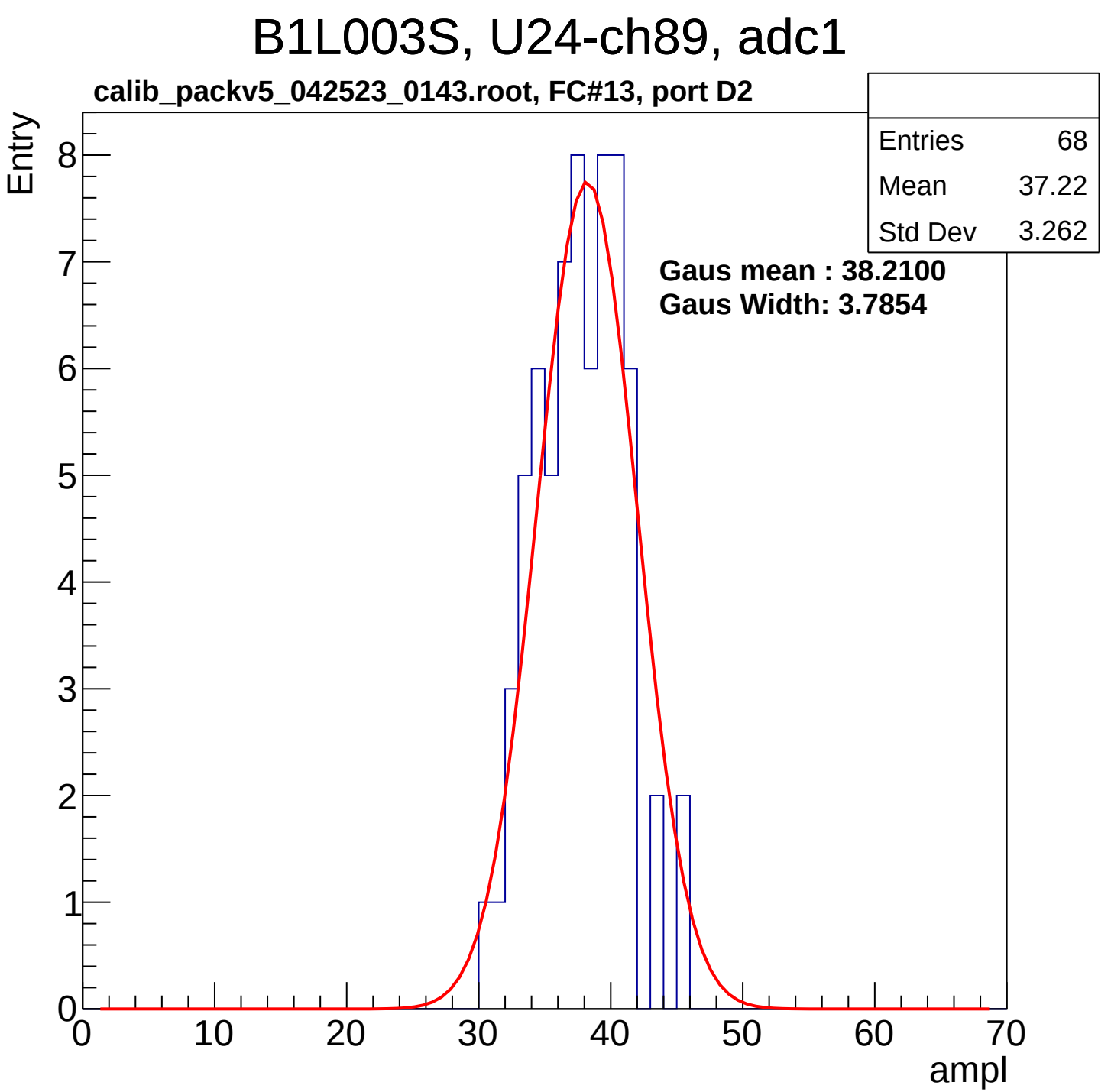
B1L003S, U24-ch89, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 37.22
Std Dev 3.262
Gaus mean : 38.2100
Gaus Width: 3.7854
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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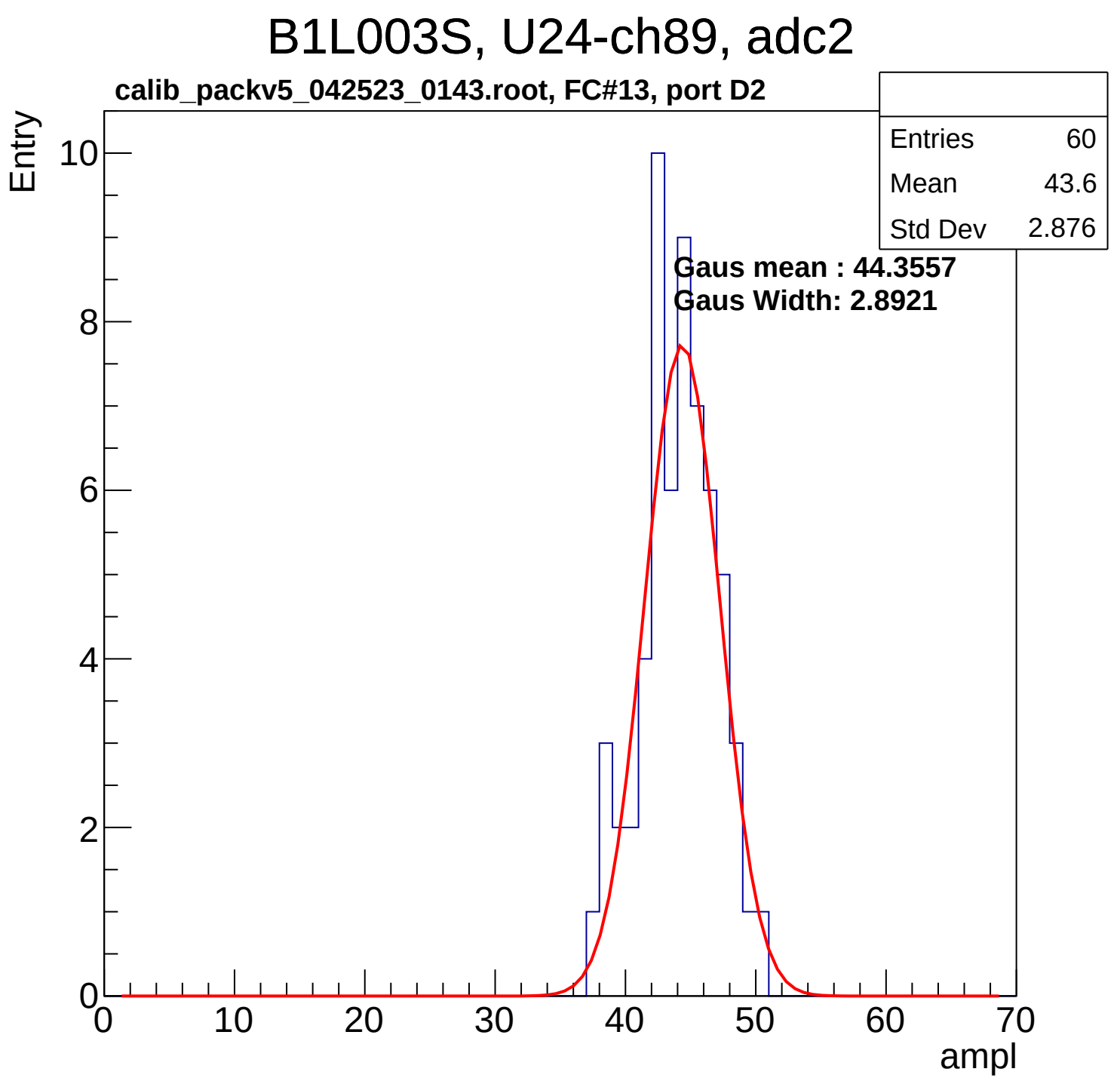
B1L003S, U24-ch89, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.6
Std Dev 2.876
Gaus mean : 44.3557
Gaus Width: 2.8921
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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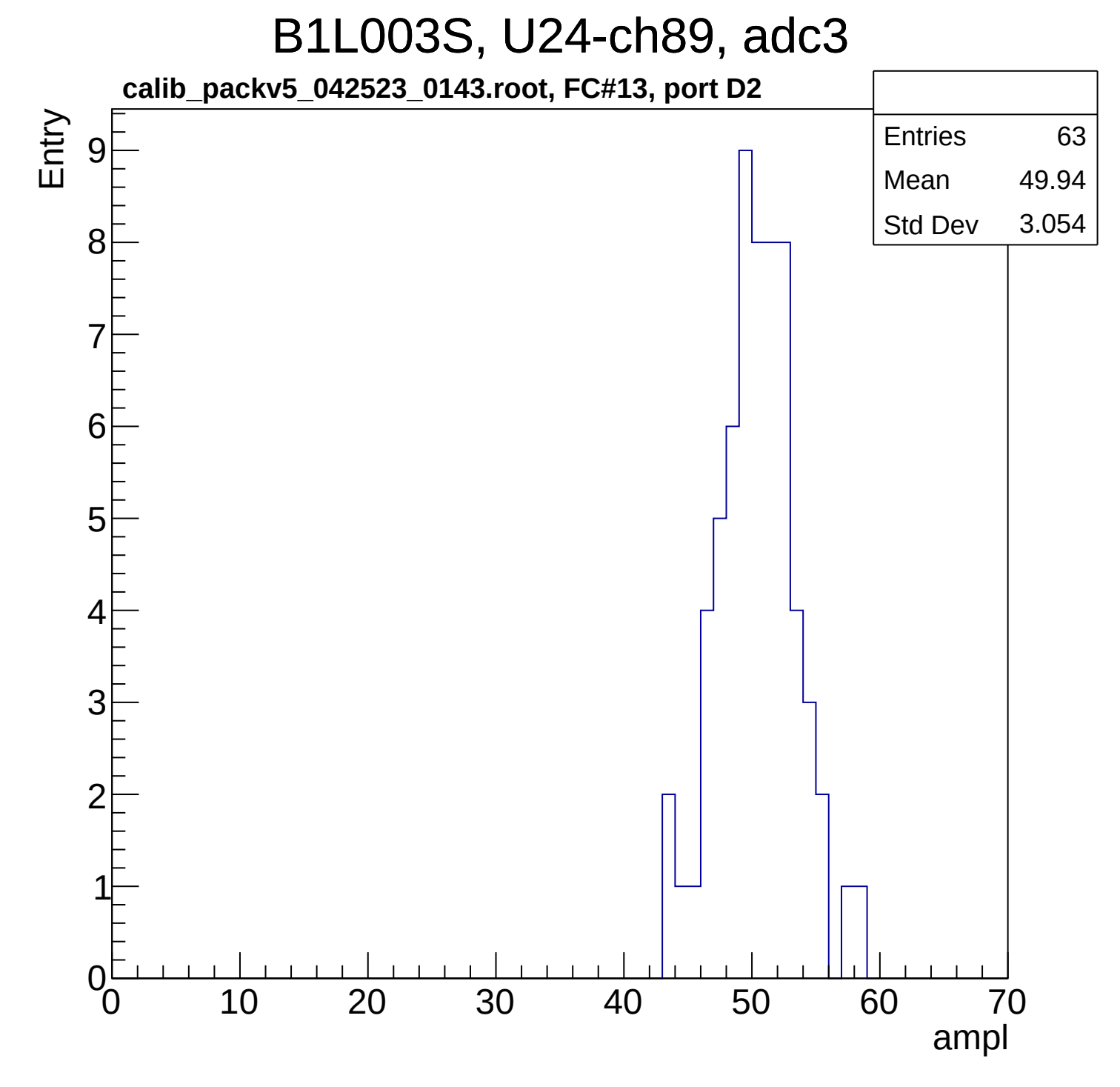
B1L003S, U24-ch89, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.94
Std Dev 3.054
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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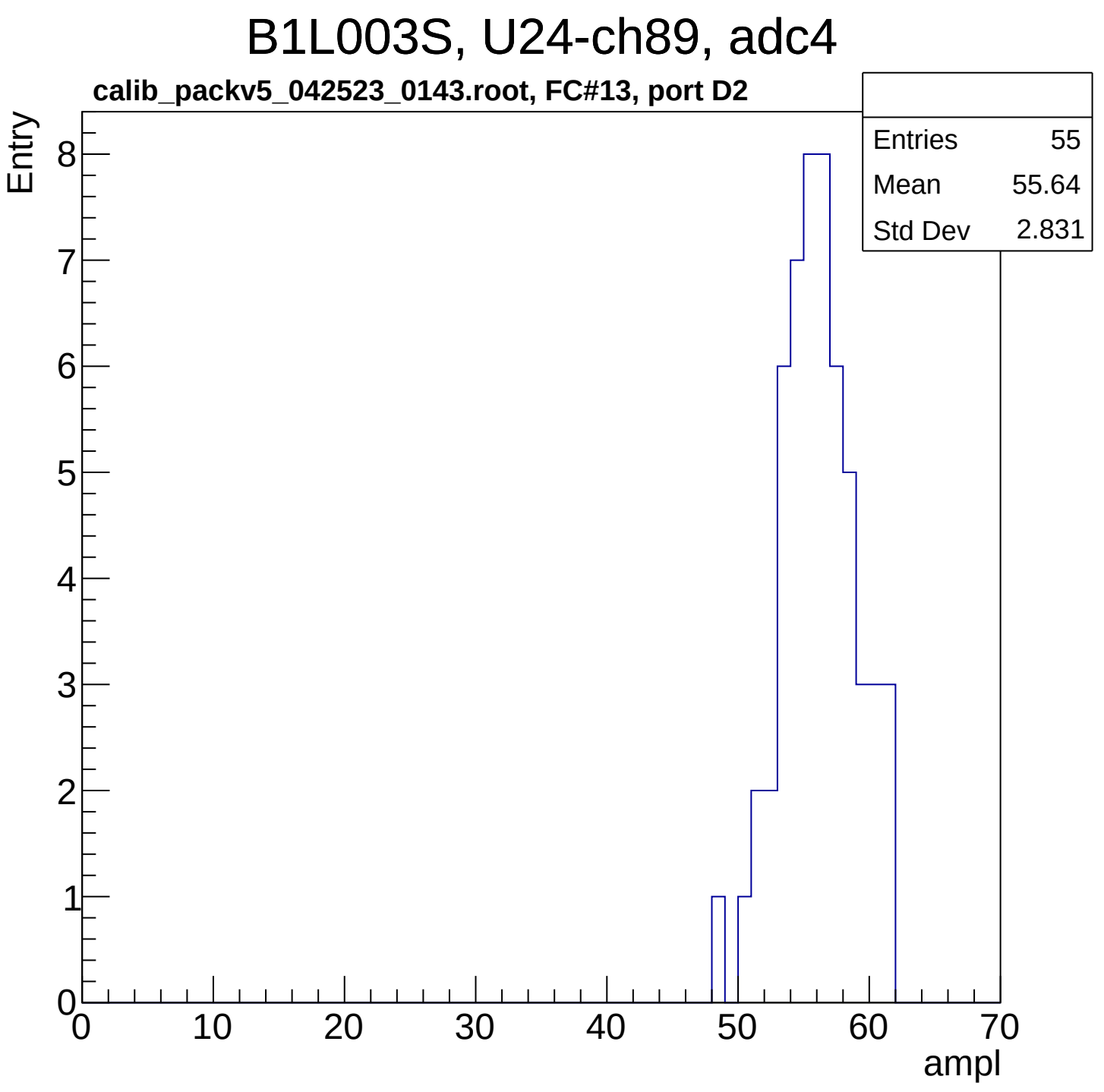
B1L003S, U24-ch89, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55.64
Std Dev 2.831
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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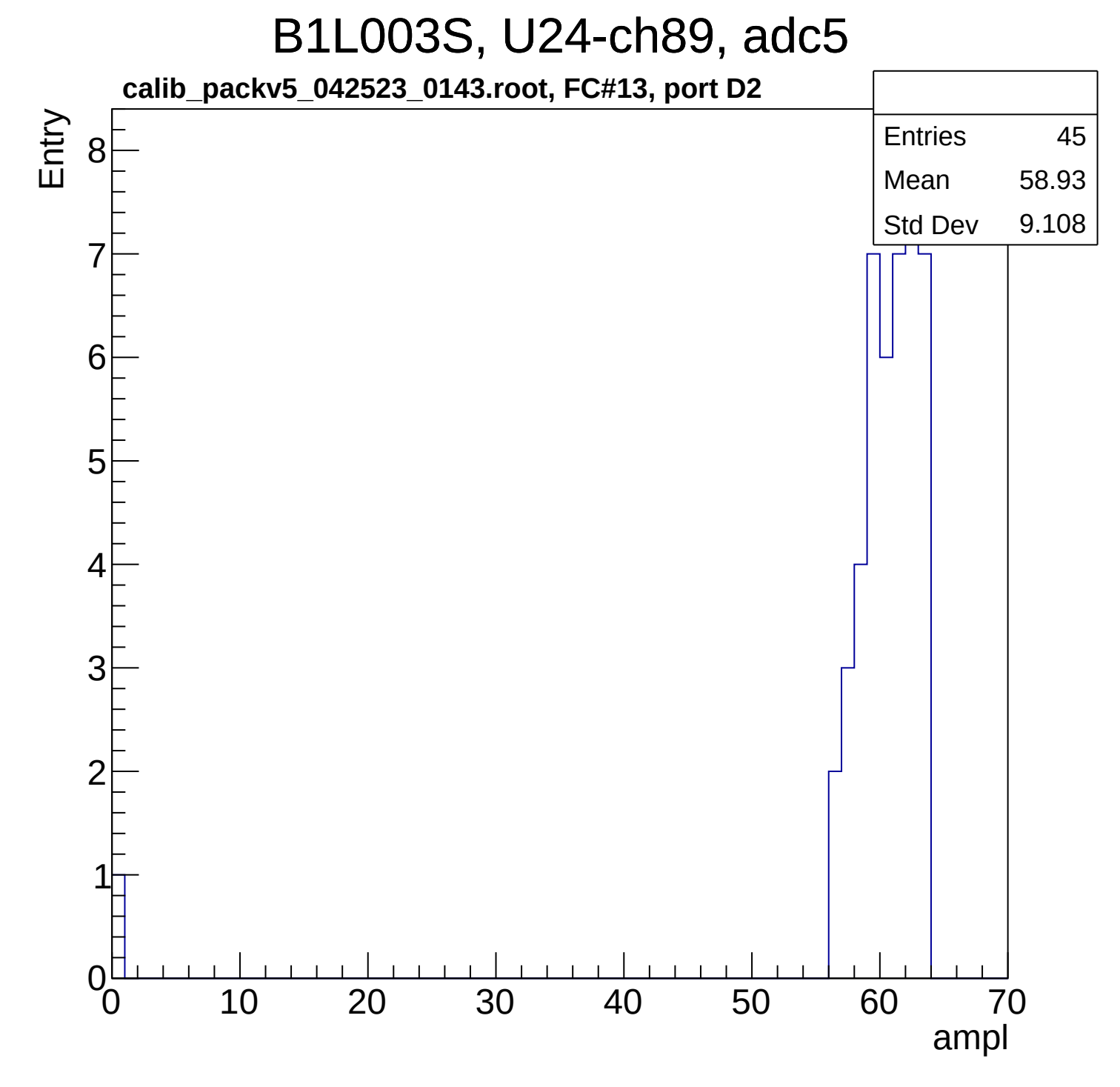
B1L003S, U24-ch89, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.93
Std Dev 9.108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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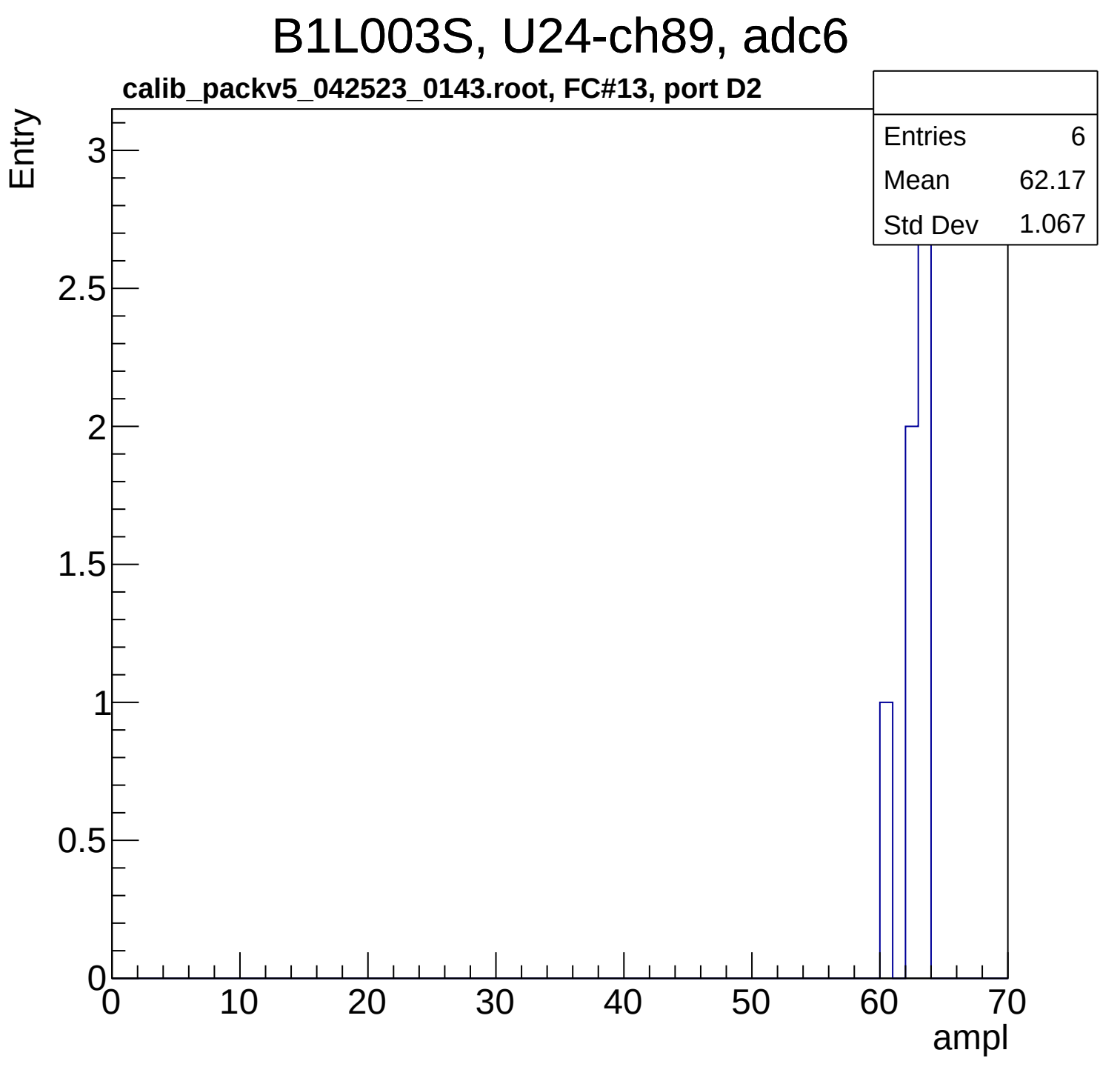
B1L003S, U24-ch89, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
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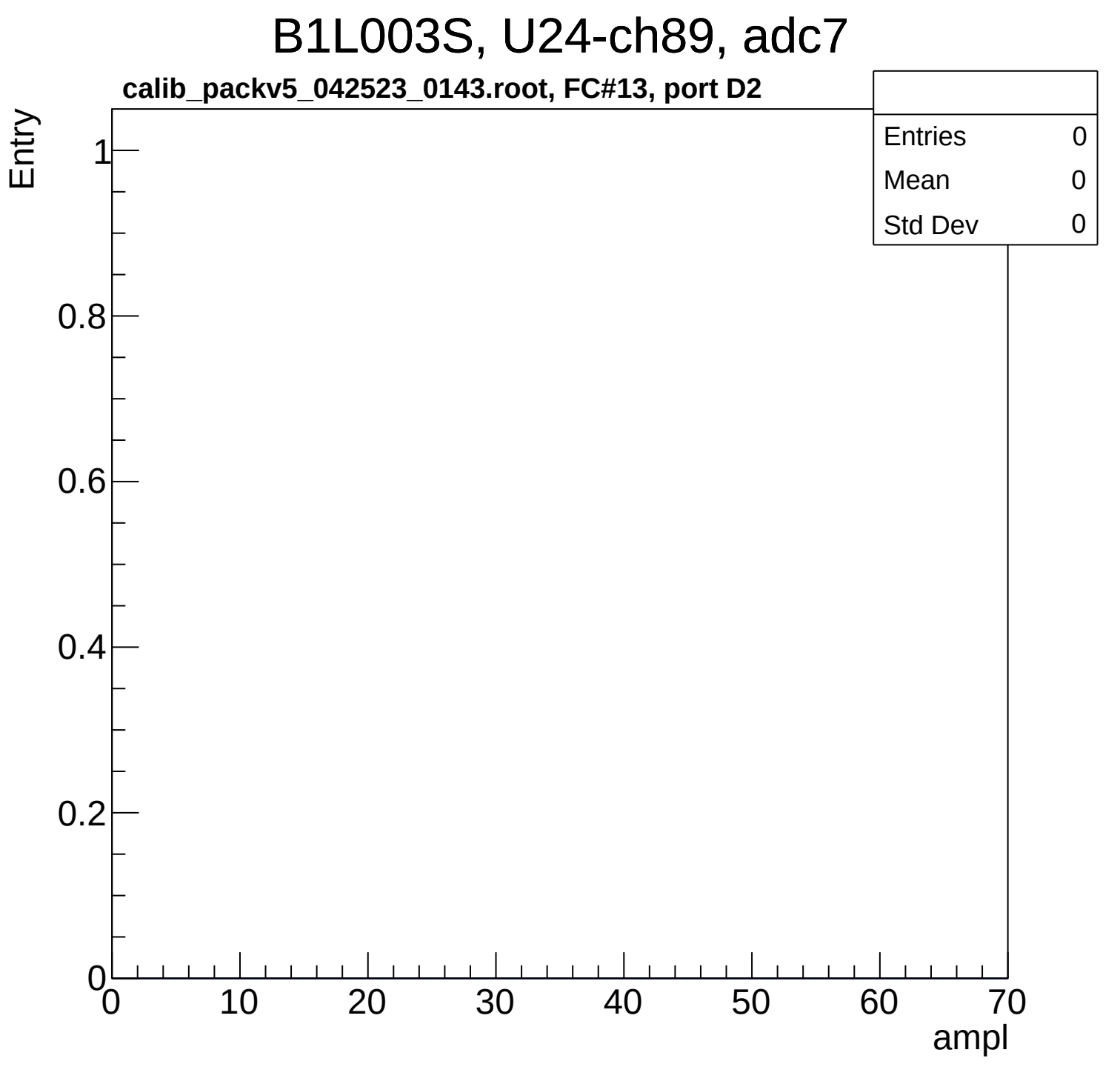
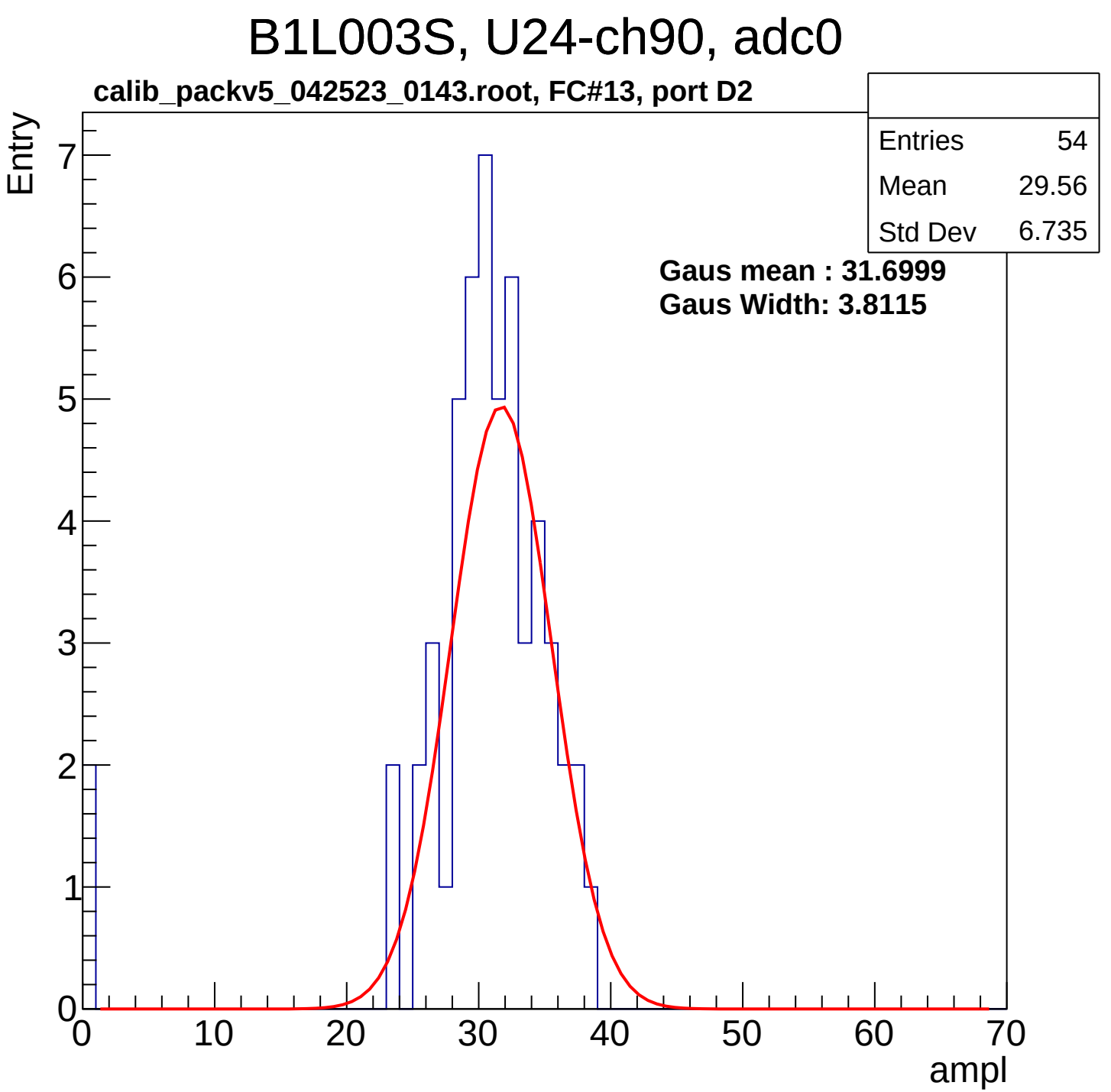
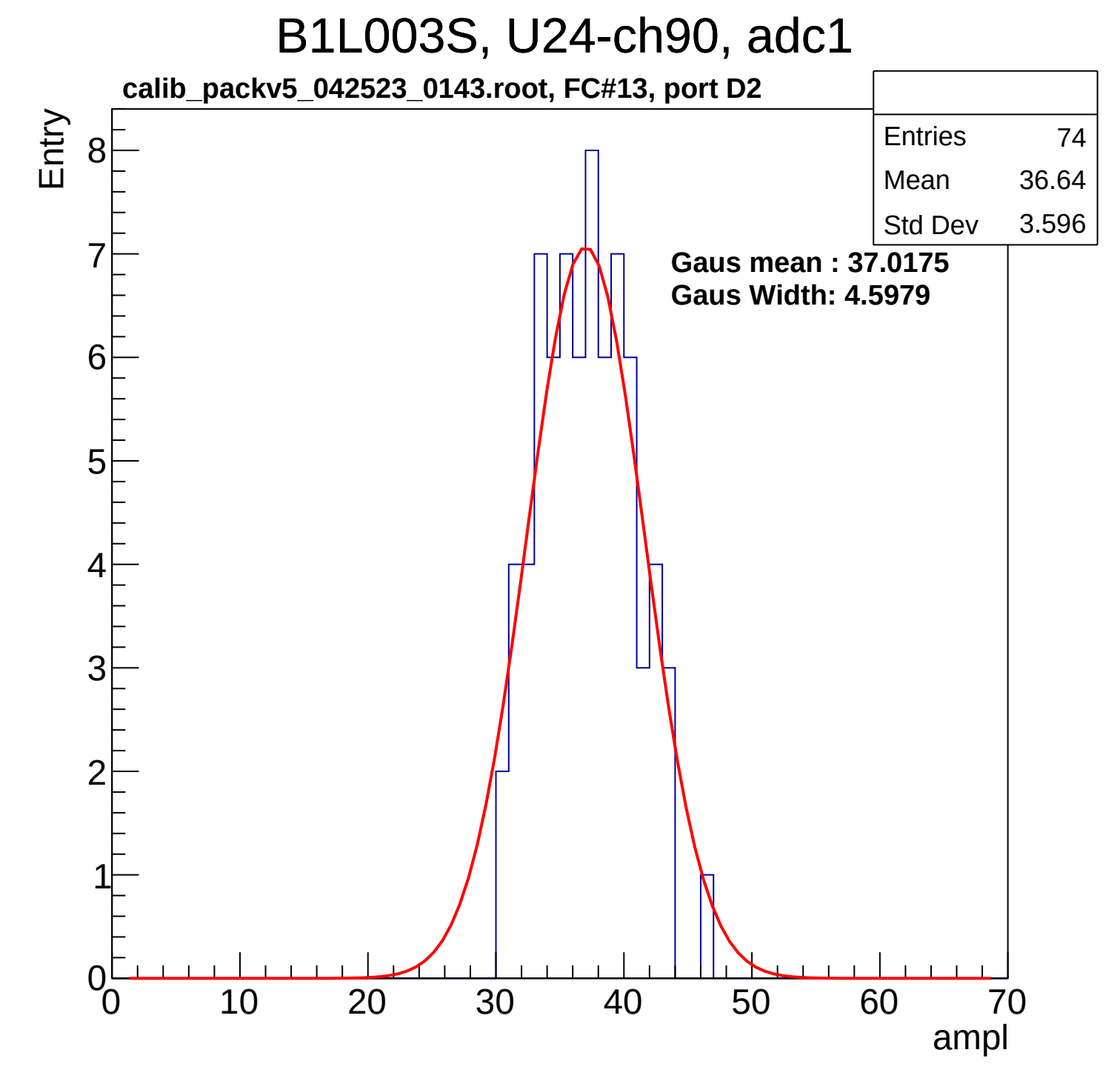
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch89, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch90, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.56
Std Dev 6.735
Gaus mean : 31.6999
Gaus Width: 3.8115
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch90, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.64
Std Dev 3.596
Gaus mean : 37.0175
Gaus Width: 4.5979
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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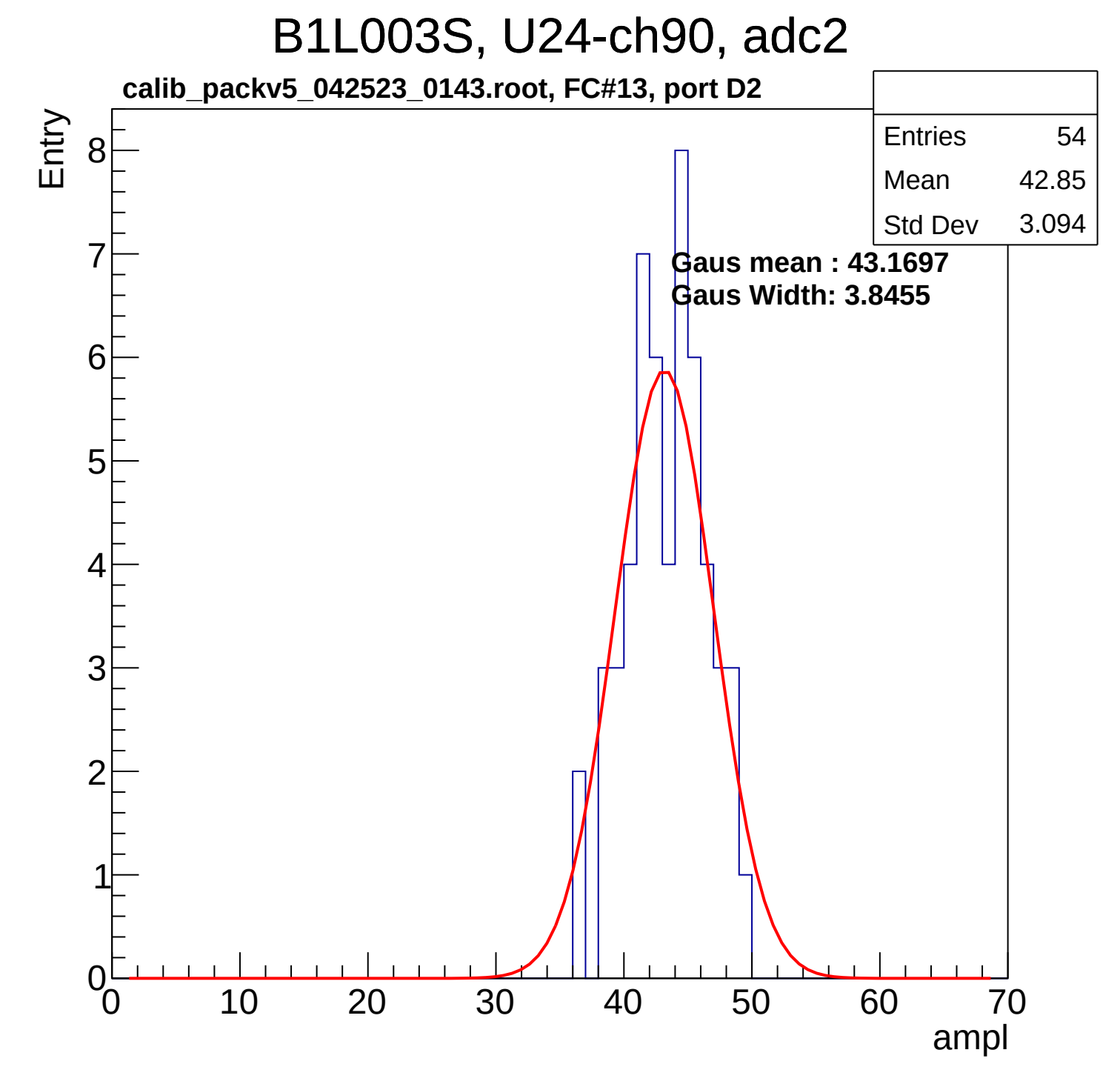
B1L003S, U24-ch90, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.85
Std Dev 3.094
Gaus mean : 43.1697
Gaus Width: 3.8455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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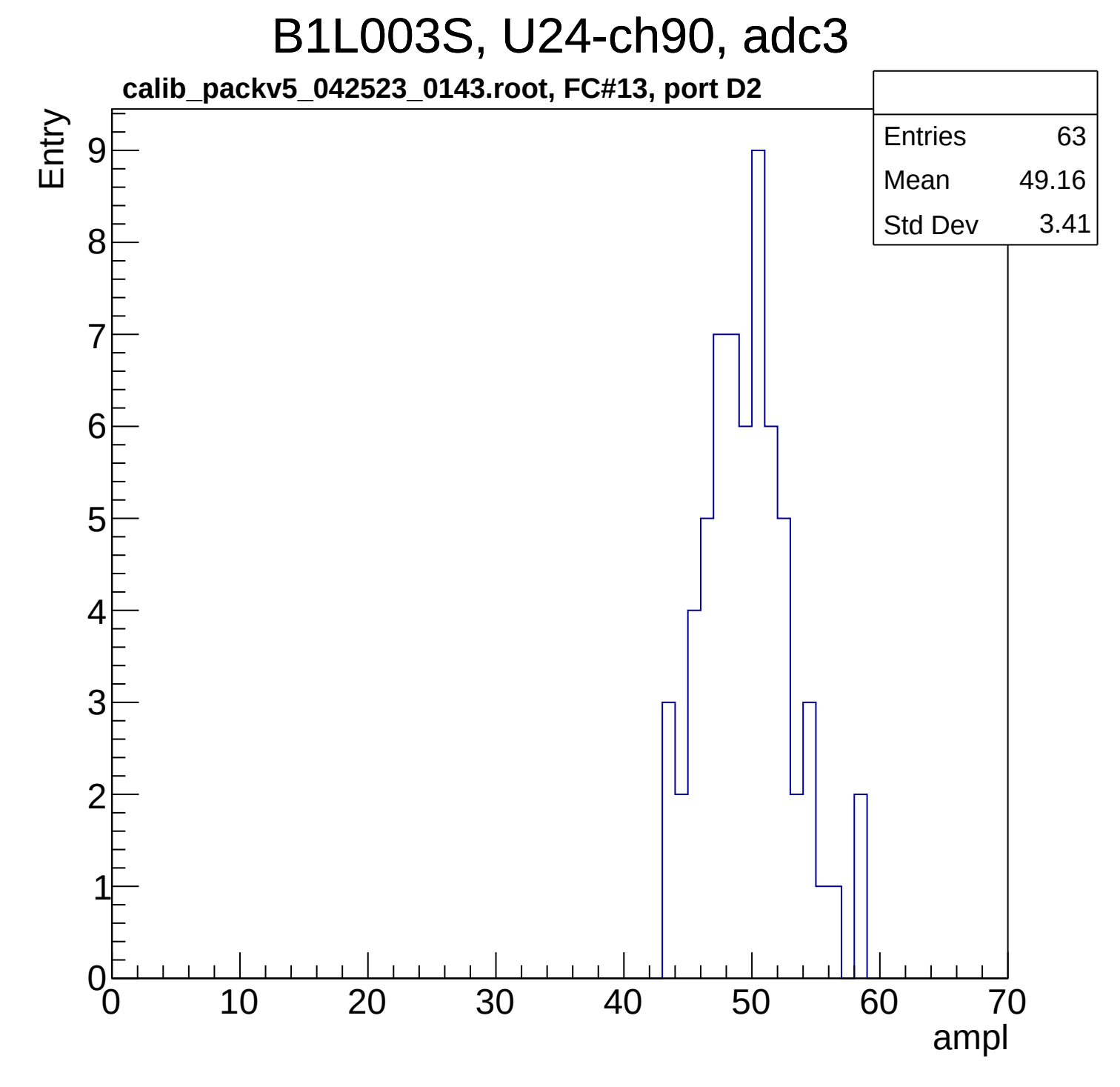
B1L003S, U24-ch90, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 49.16
Std Dev 3.41
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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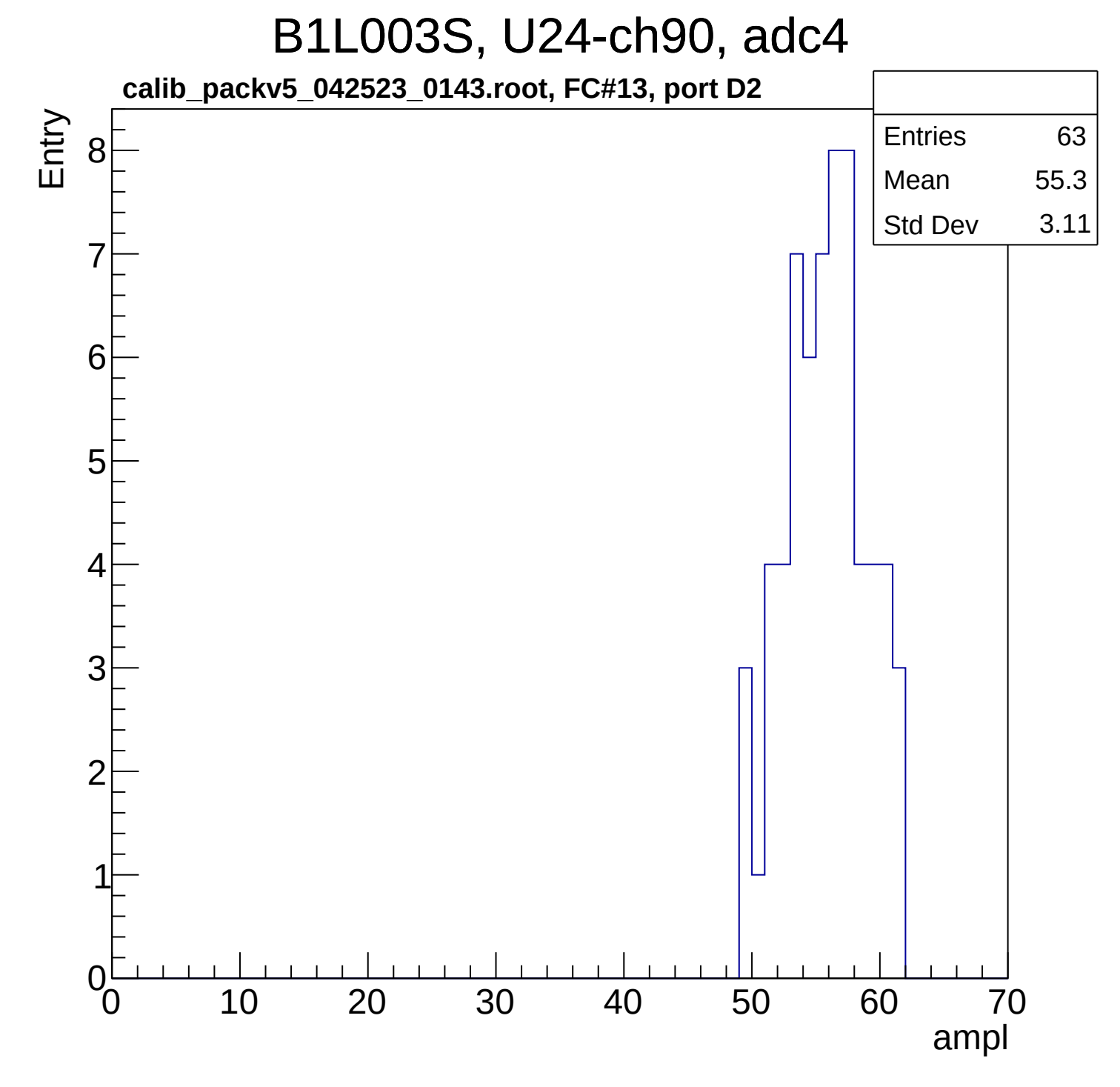
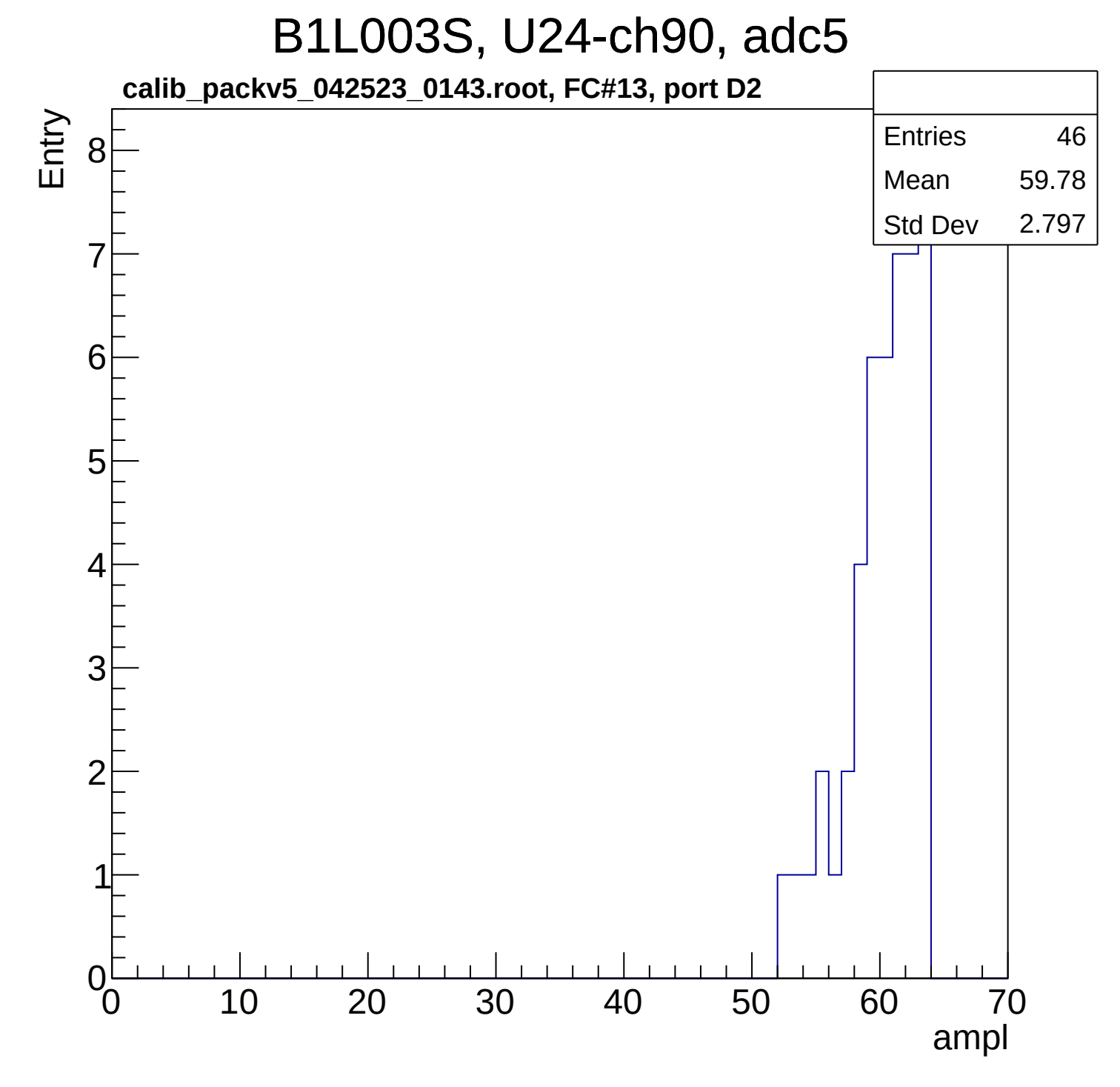

B1L003S, U24-ch90, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.3
Std Dev 3.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch90, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.78
Std Dev 2.797
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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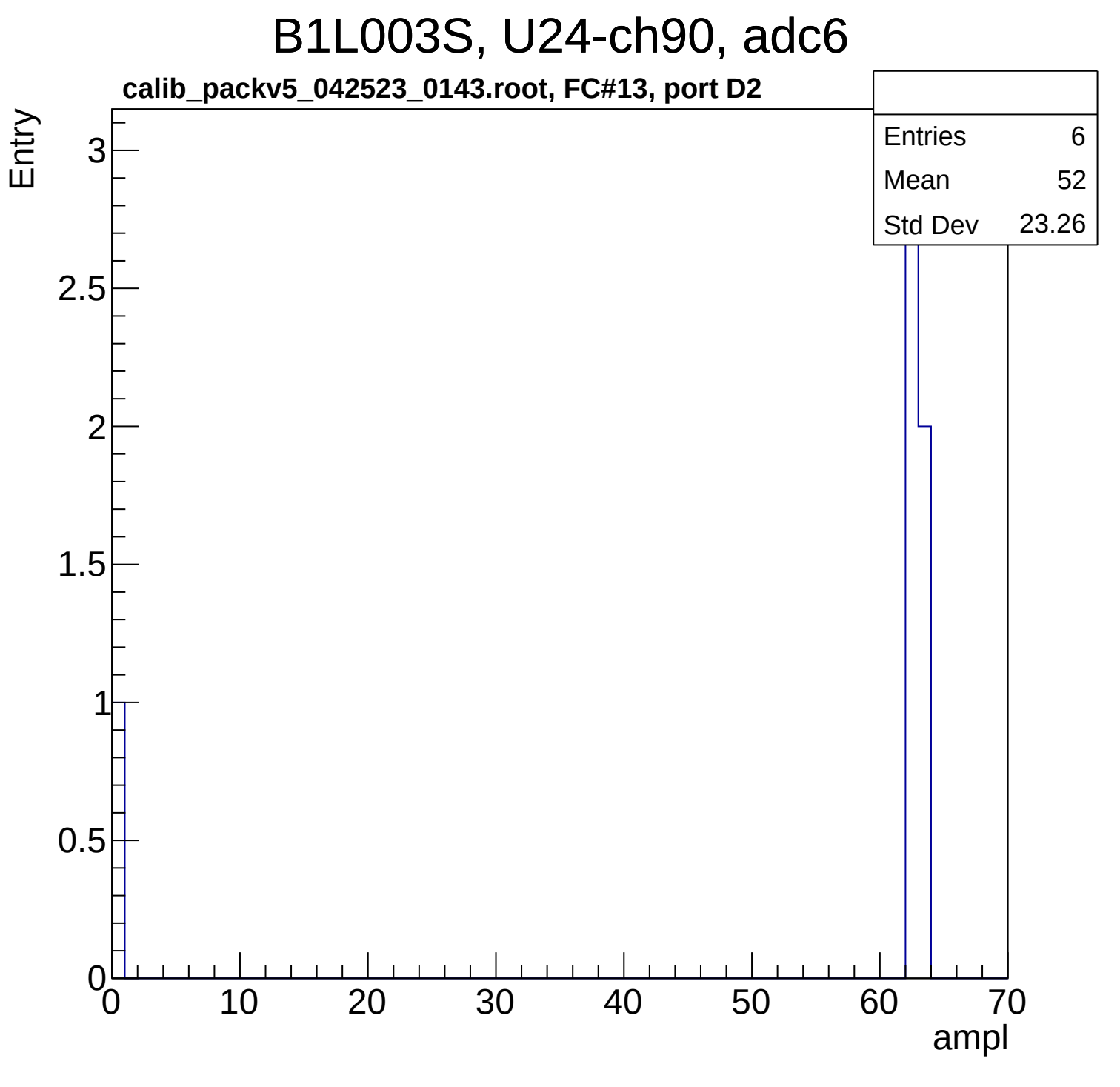
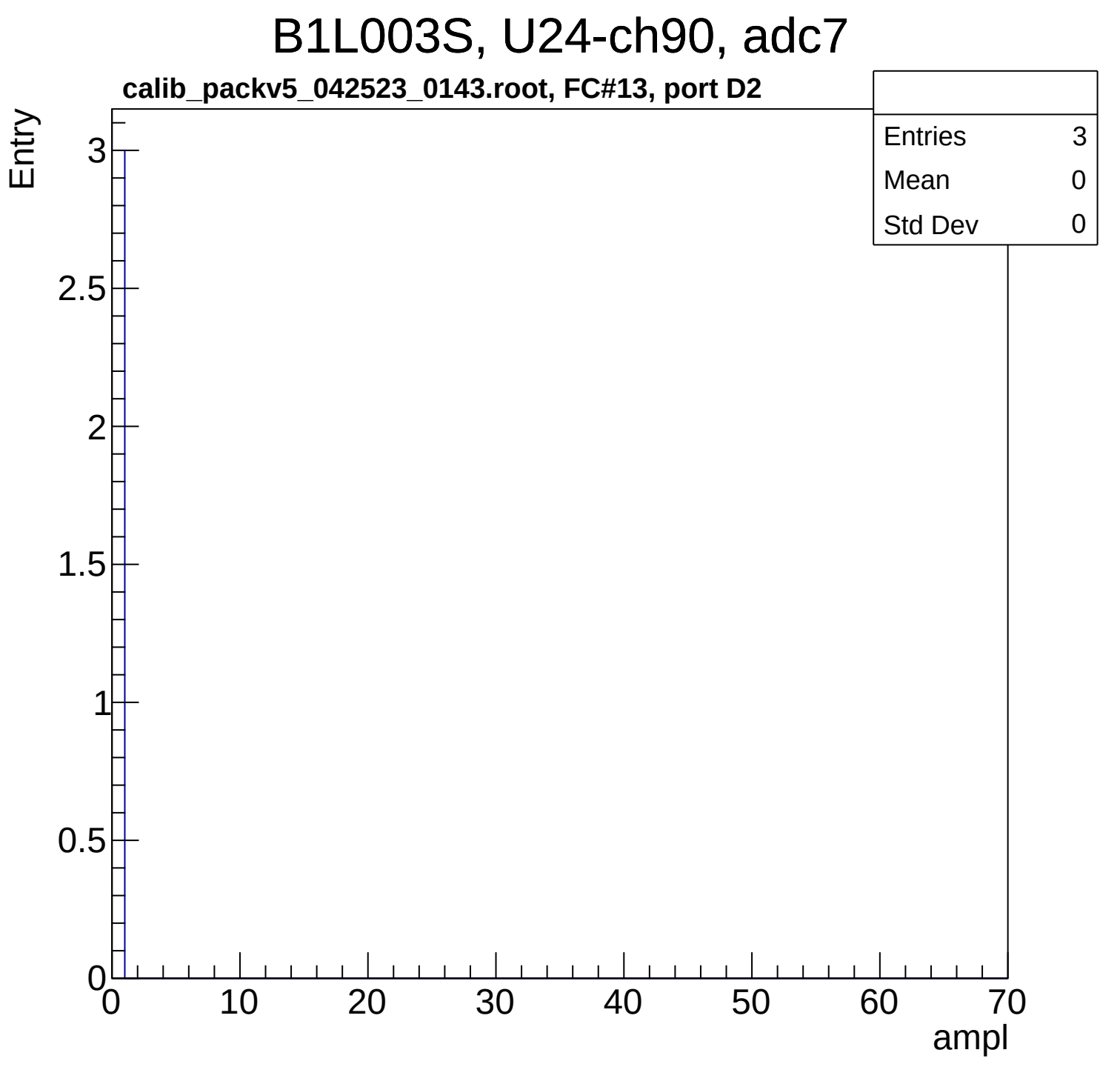
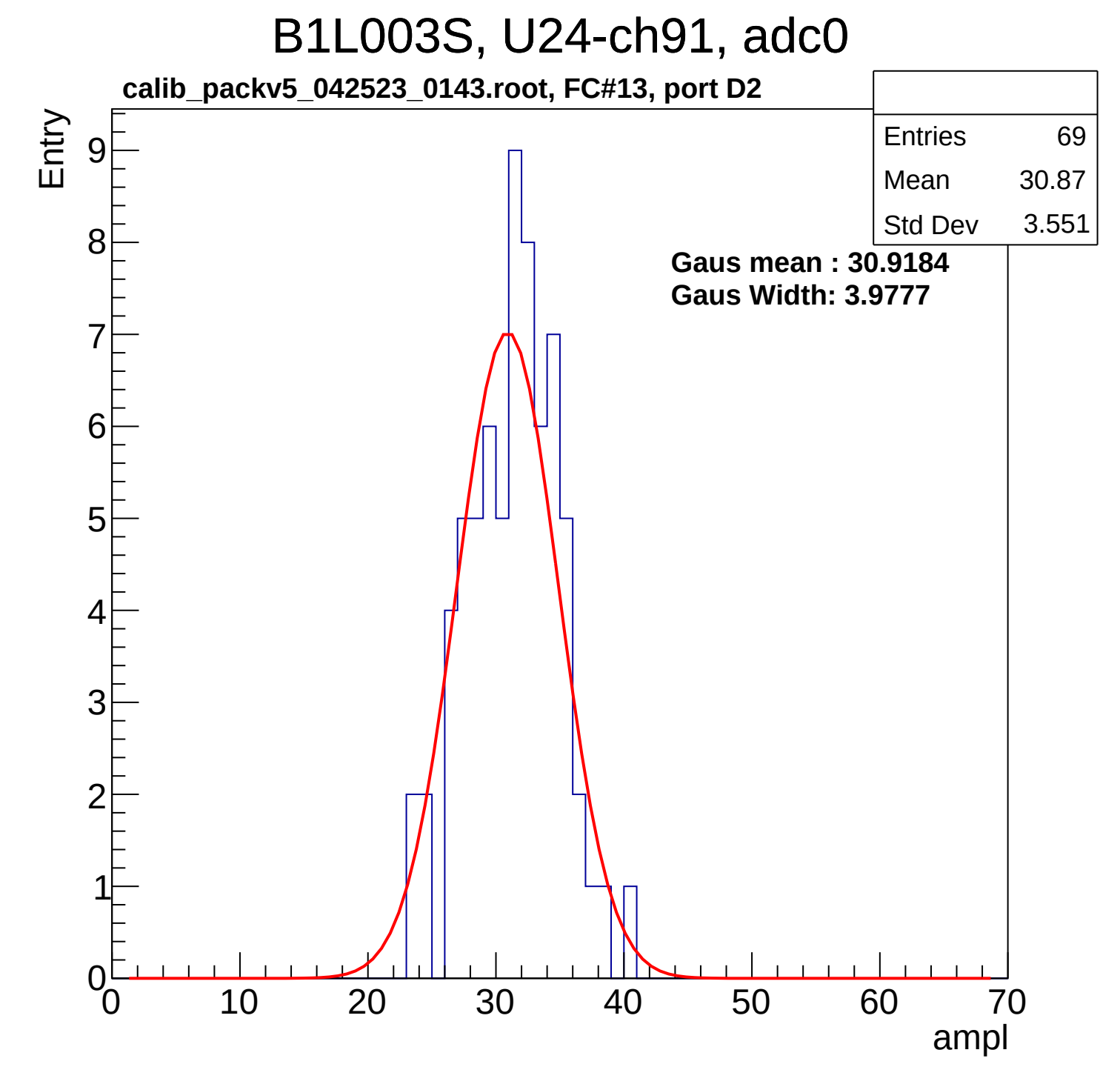
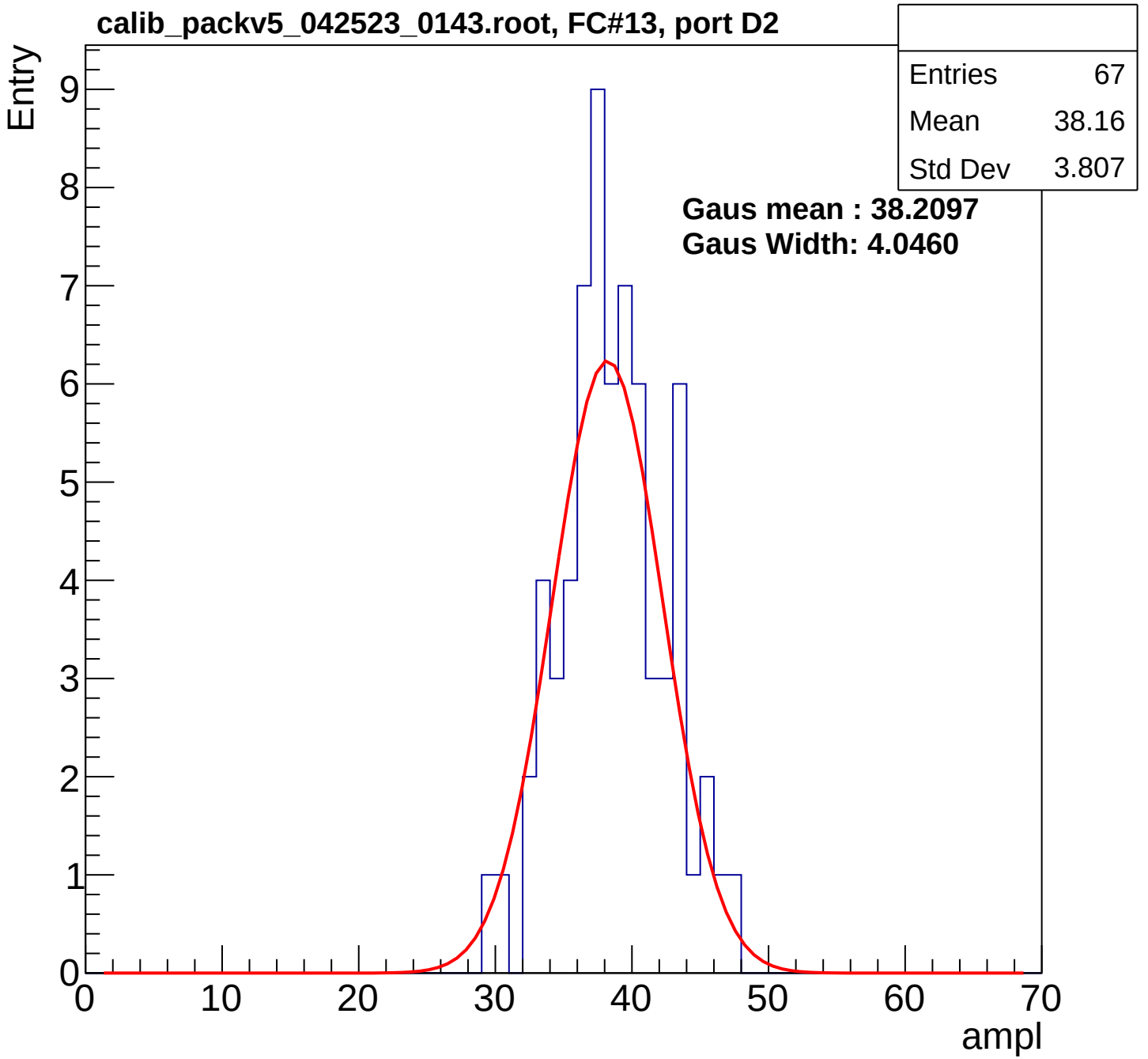

B1L003S, U24-ch90, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 52
Std Dev 23.26
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch90, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch91, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 30.87
Std Dev 3.551
Gaus mean : 30.9184
Gaus Width: 3.9777
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch91, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 38.16
Std Dev 3.807
Gaus mean : 38.2097
Gaus Width: 4.0460
Entry
ampl
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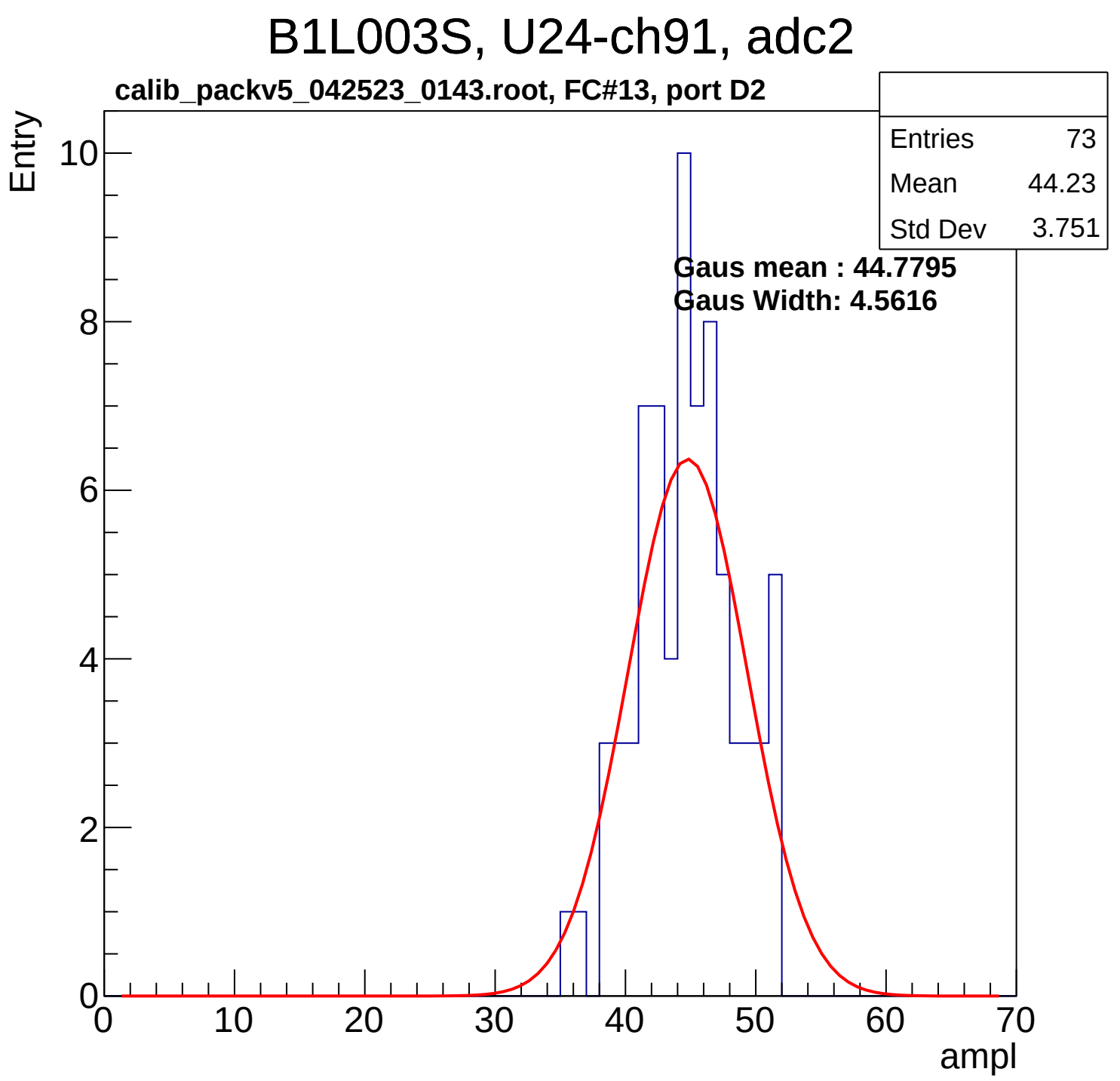
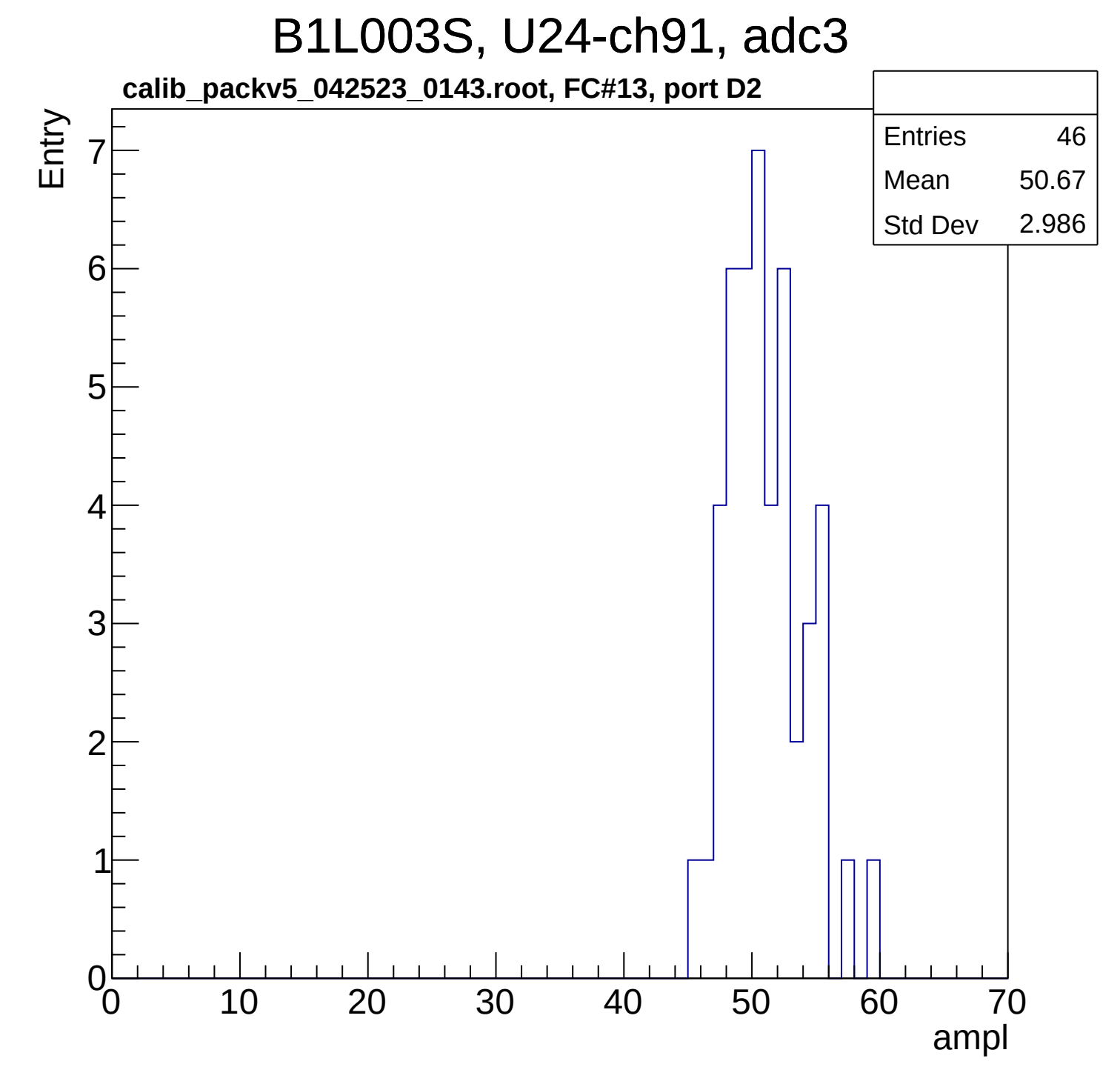
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch91, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 44.23
Std Dev 3.751
Gaus mean : 44.7795
Gaus Width: 4.5616
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch91, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 50.67
Std Dev 2.986
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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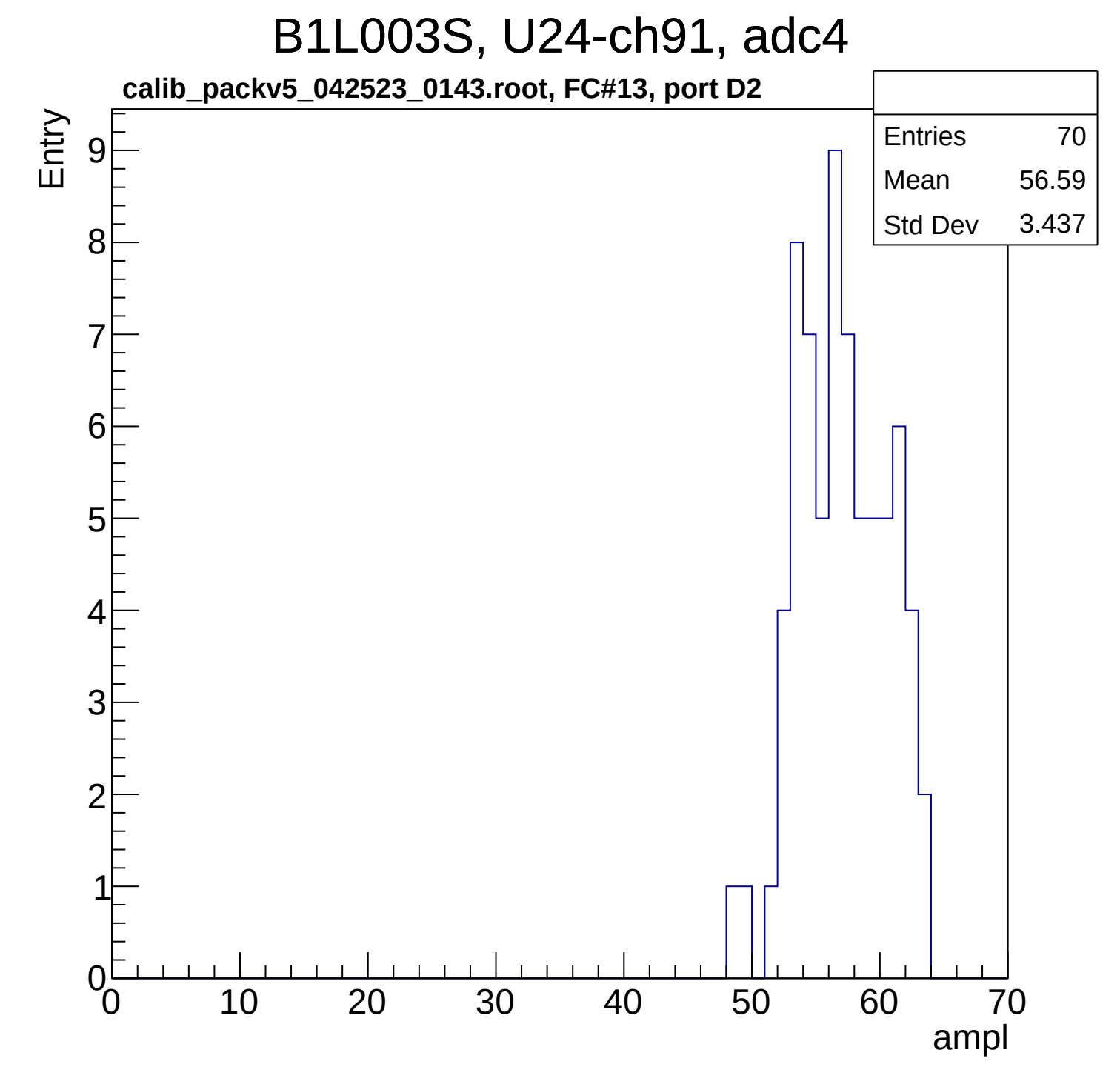
40
50
60
70


B1L003S, U24-ch91, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 56.59
Std Dev 3.437
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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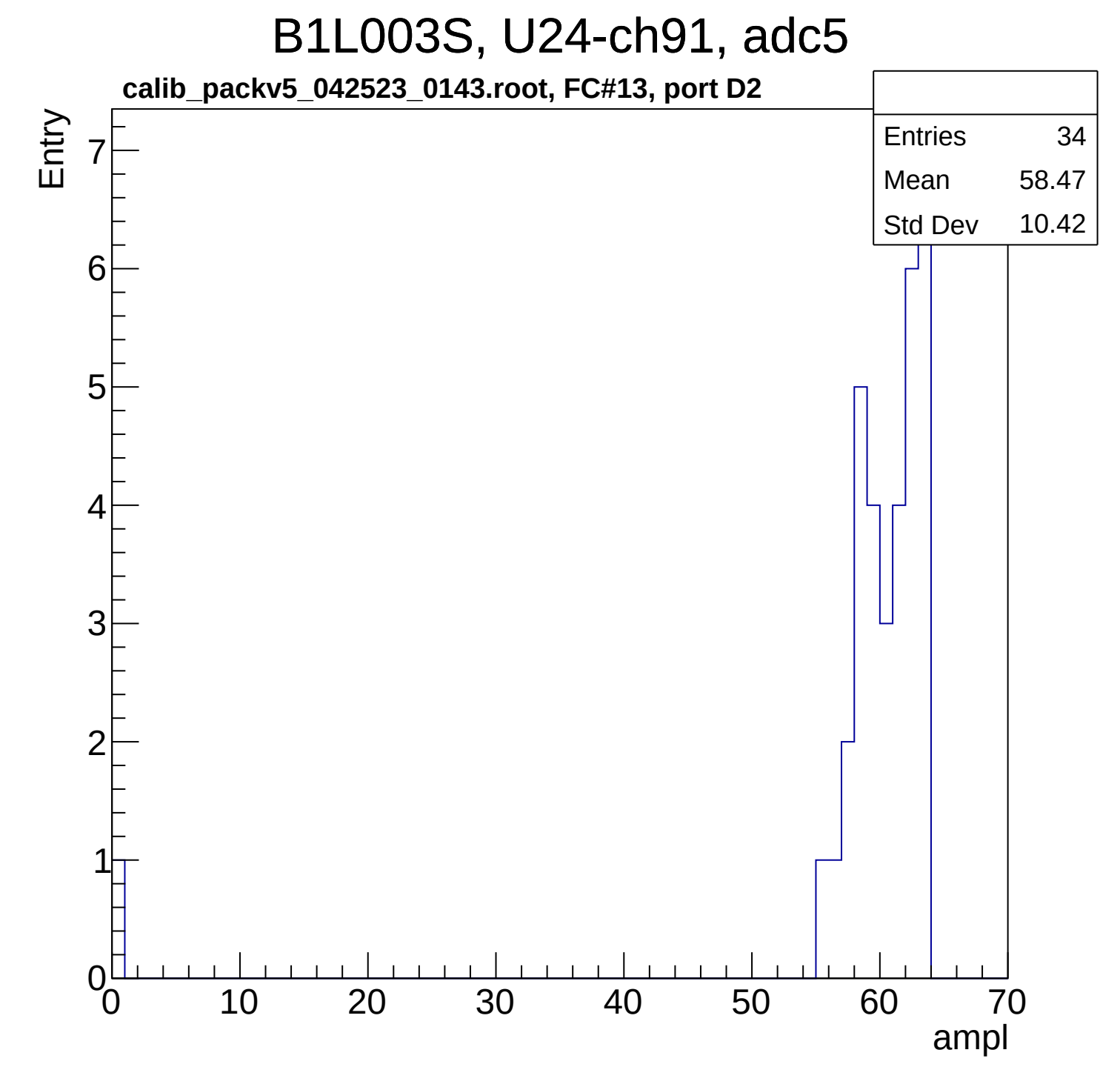
B1L003S, U24-ch91, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 58.47
Std Dev 10.42
Entry
ampl
0
1
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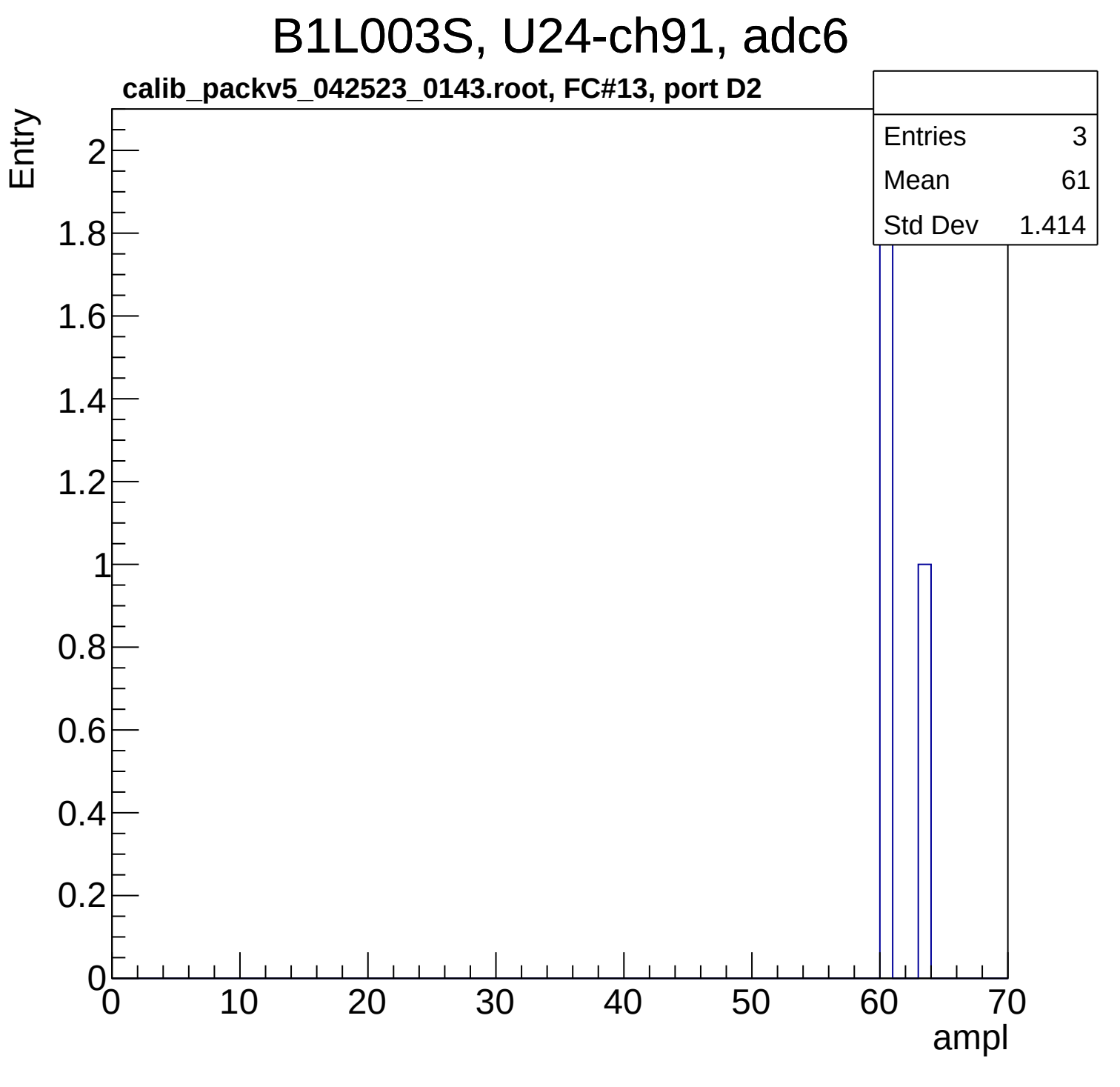
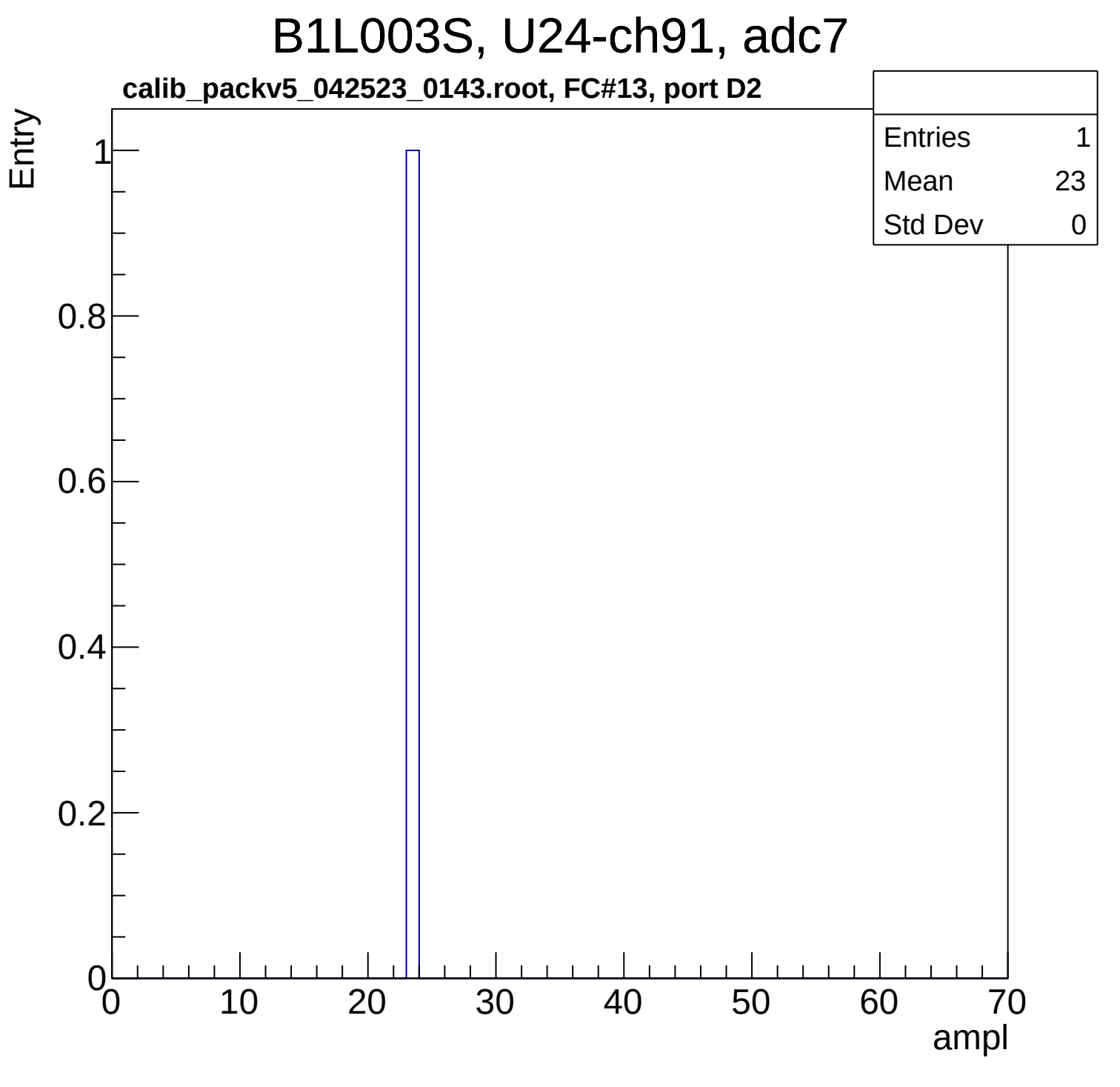
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch91, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 61
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch91, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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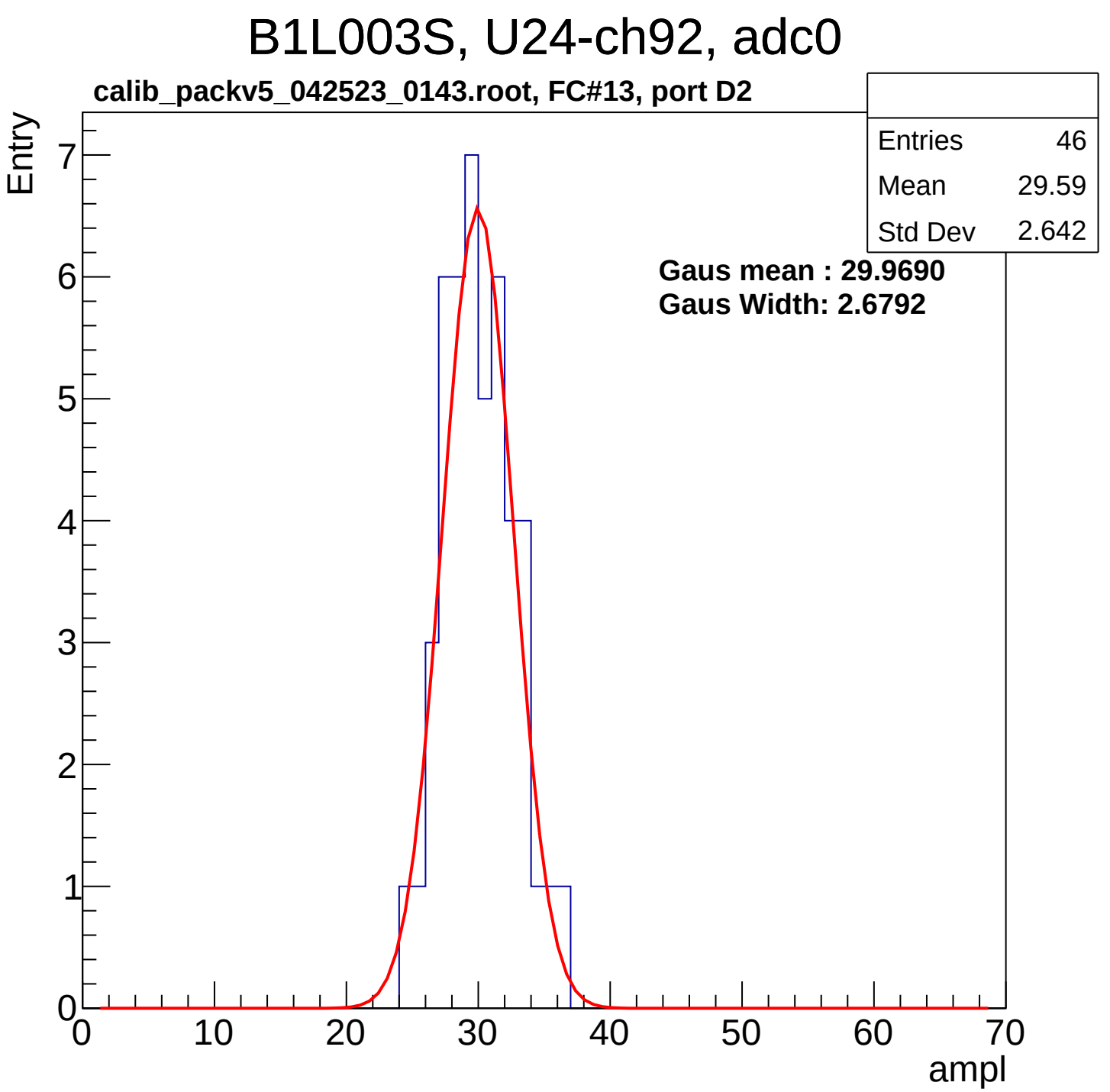
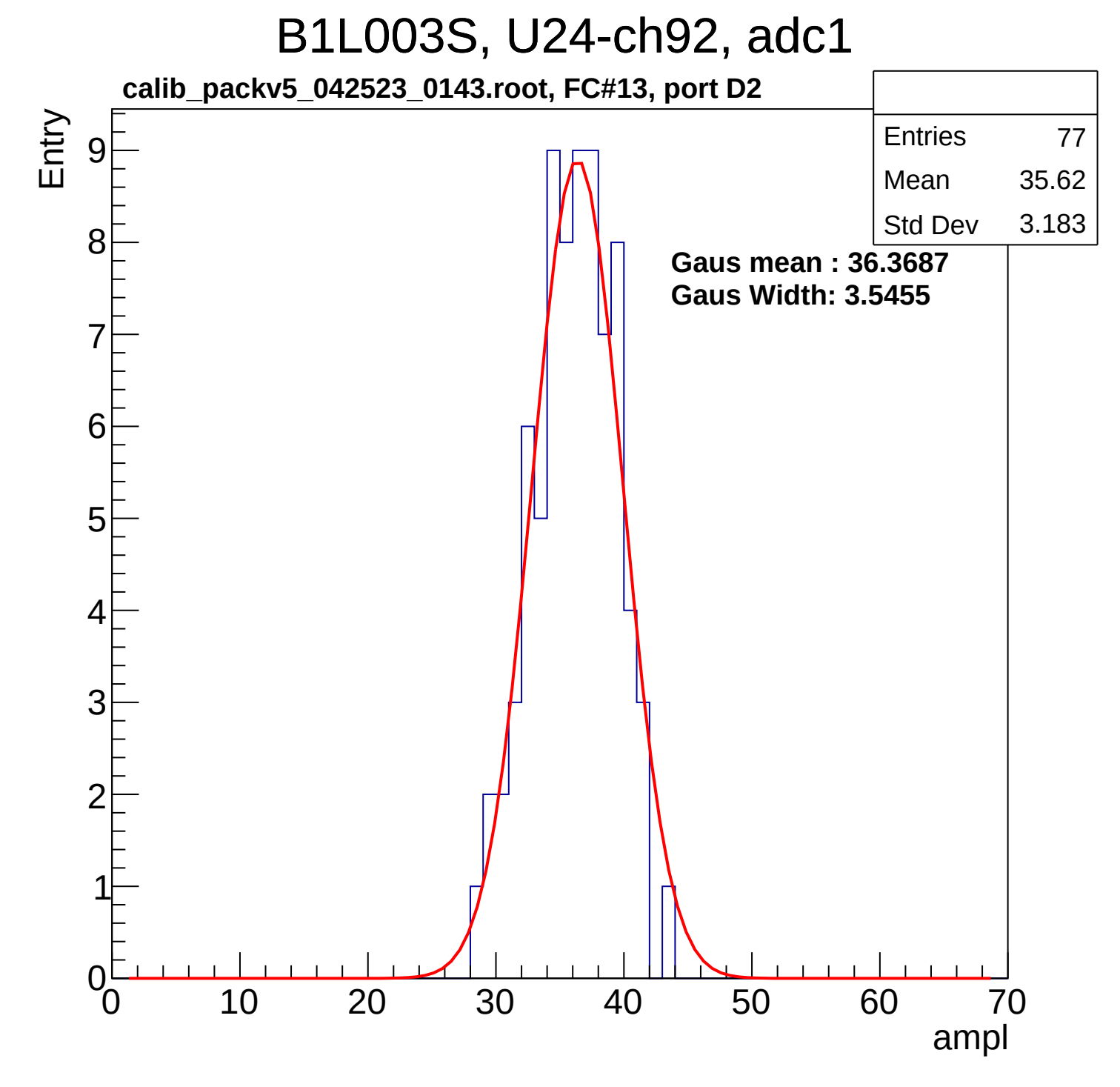
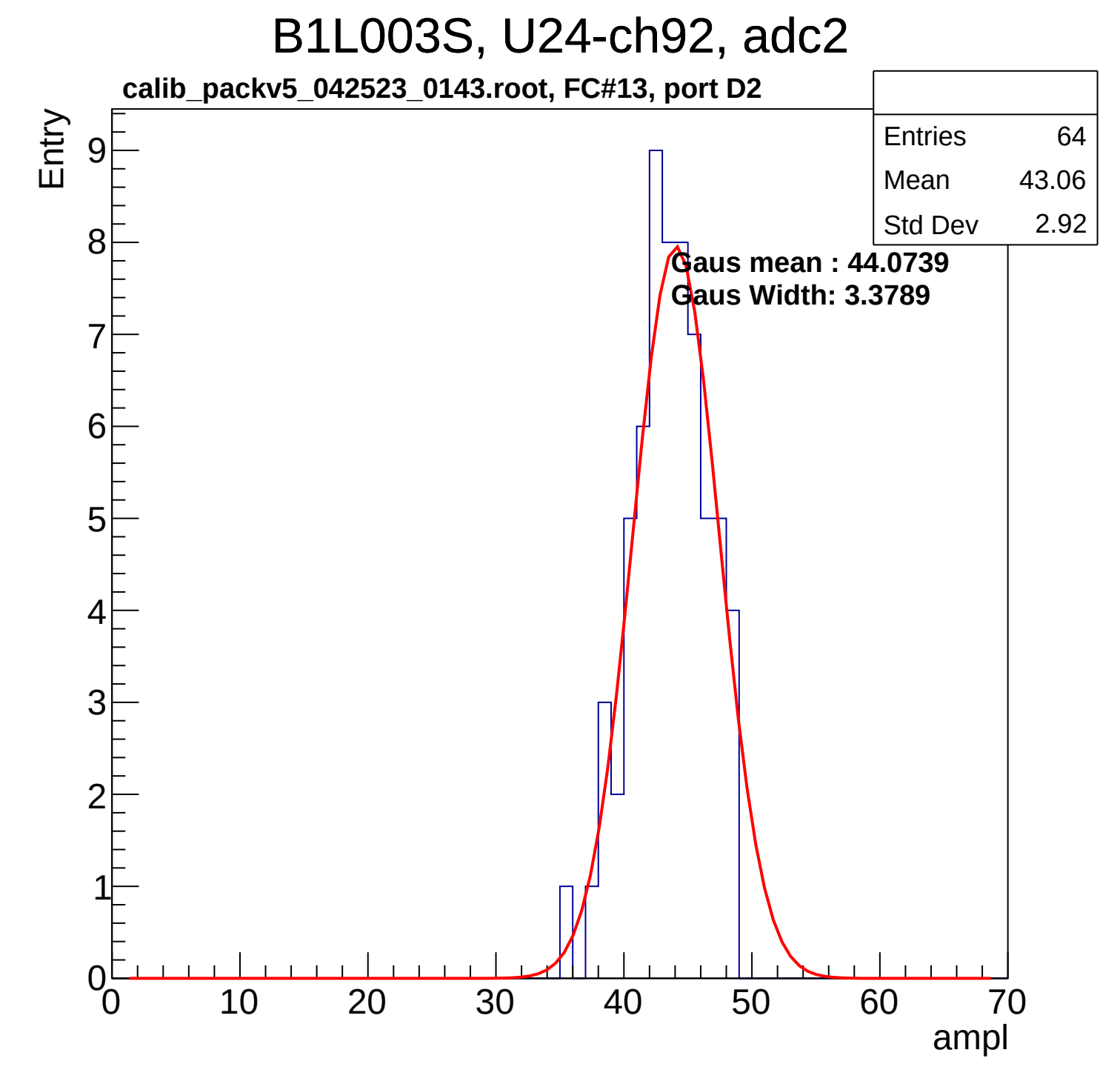
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch92, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 29.59
Std Dev 2.642
Gaus mean : 29.9690
Gaus Width: 2.6792
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch92, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 35.62
Std Dev 3.183
Gaus mean : 36.3687
Gaus Width: 3.5455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch92, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.06
Std Dev 2.92
Gaus mean : 44.0739
Gaus Width: 3.3789
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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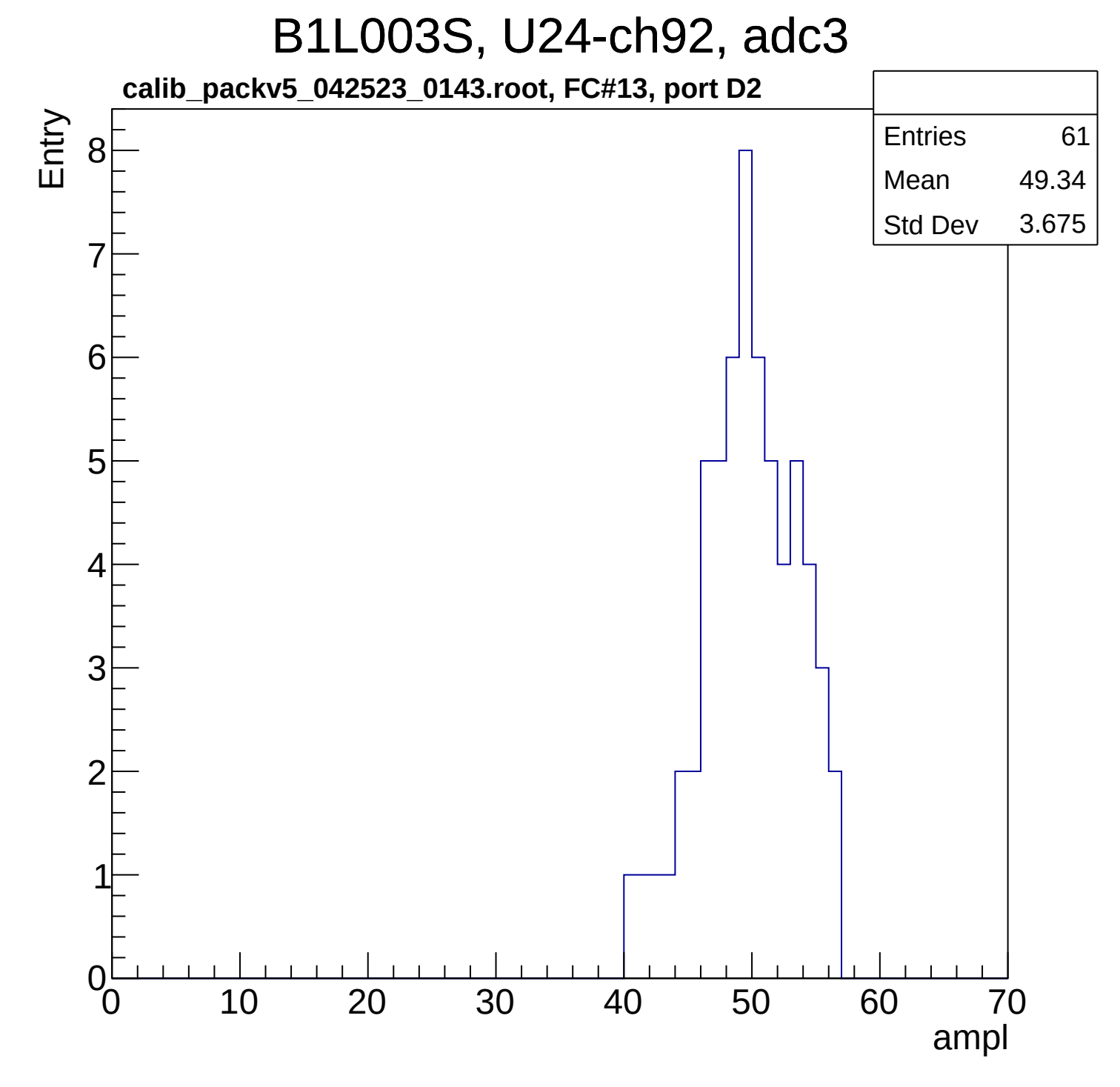
B1L003S, U24-ch92, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.34
Std Dev 3.675
Entry
ampl
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1
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3
4
5
6
7
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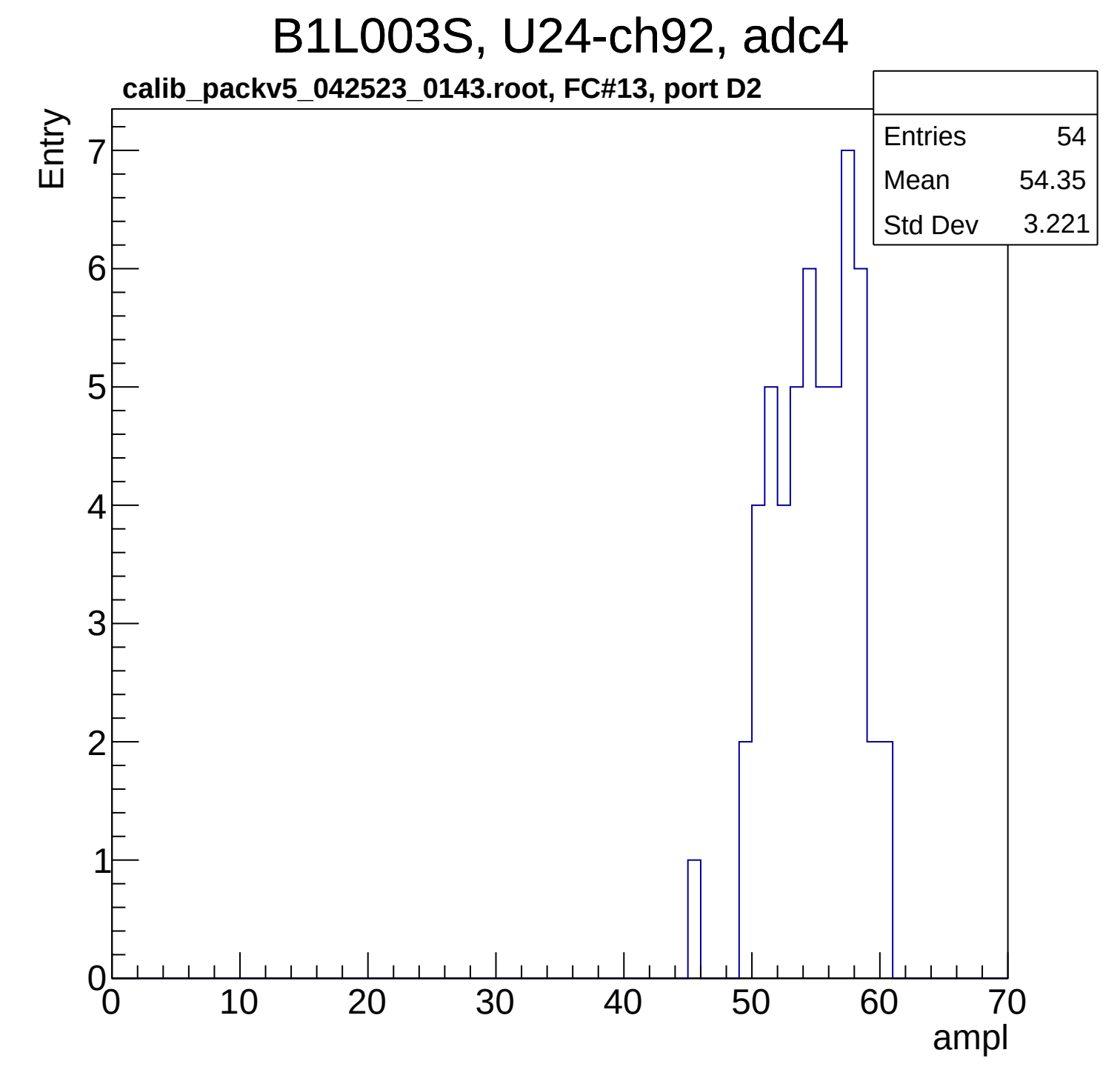
0
10
20
30
40
50
60
70


B1L003S, U24-ch92, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.35
Std Dev 3.221
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
50
60
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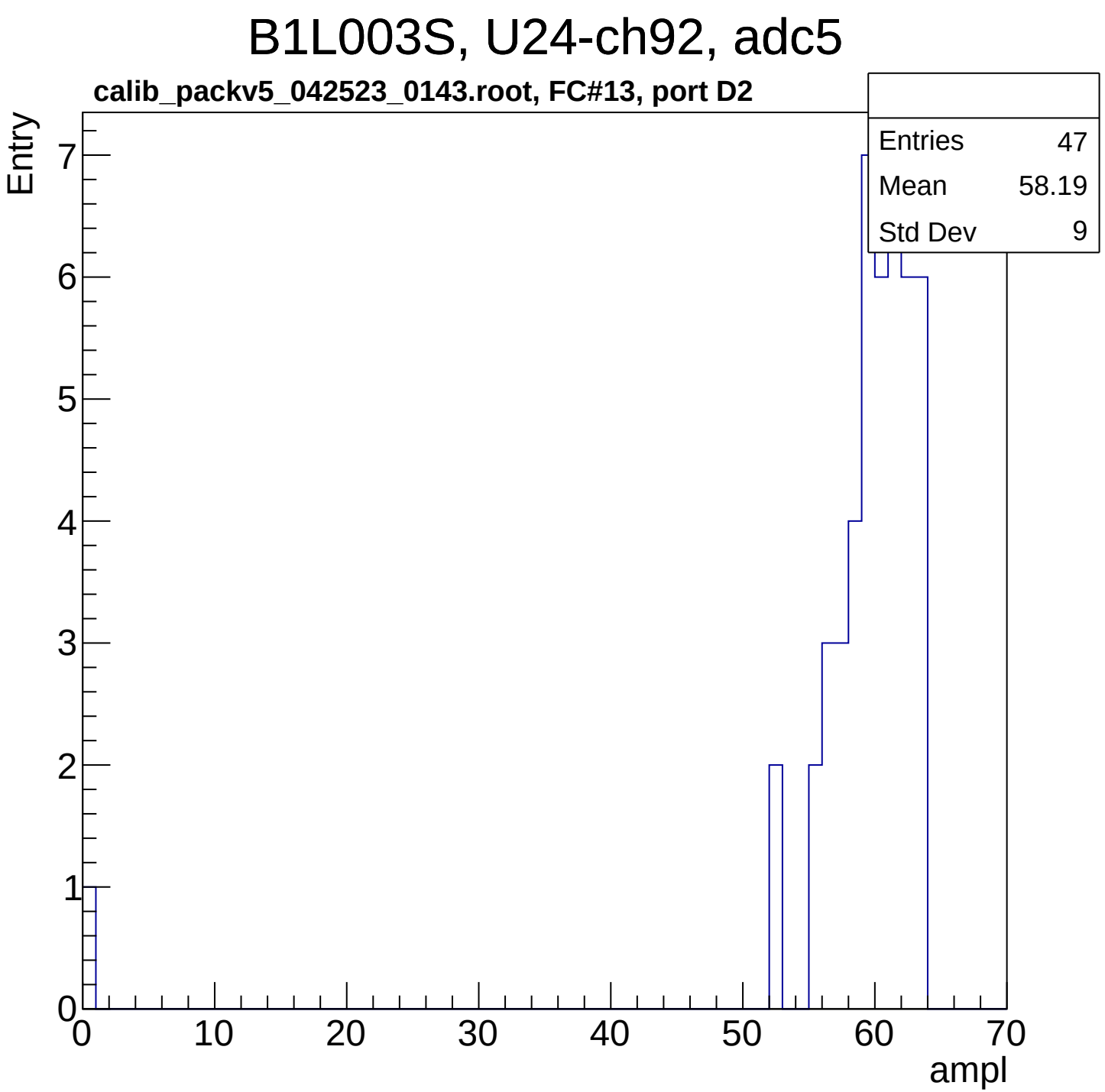
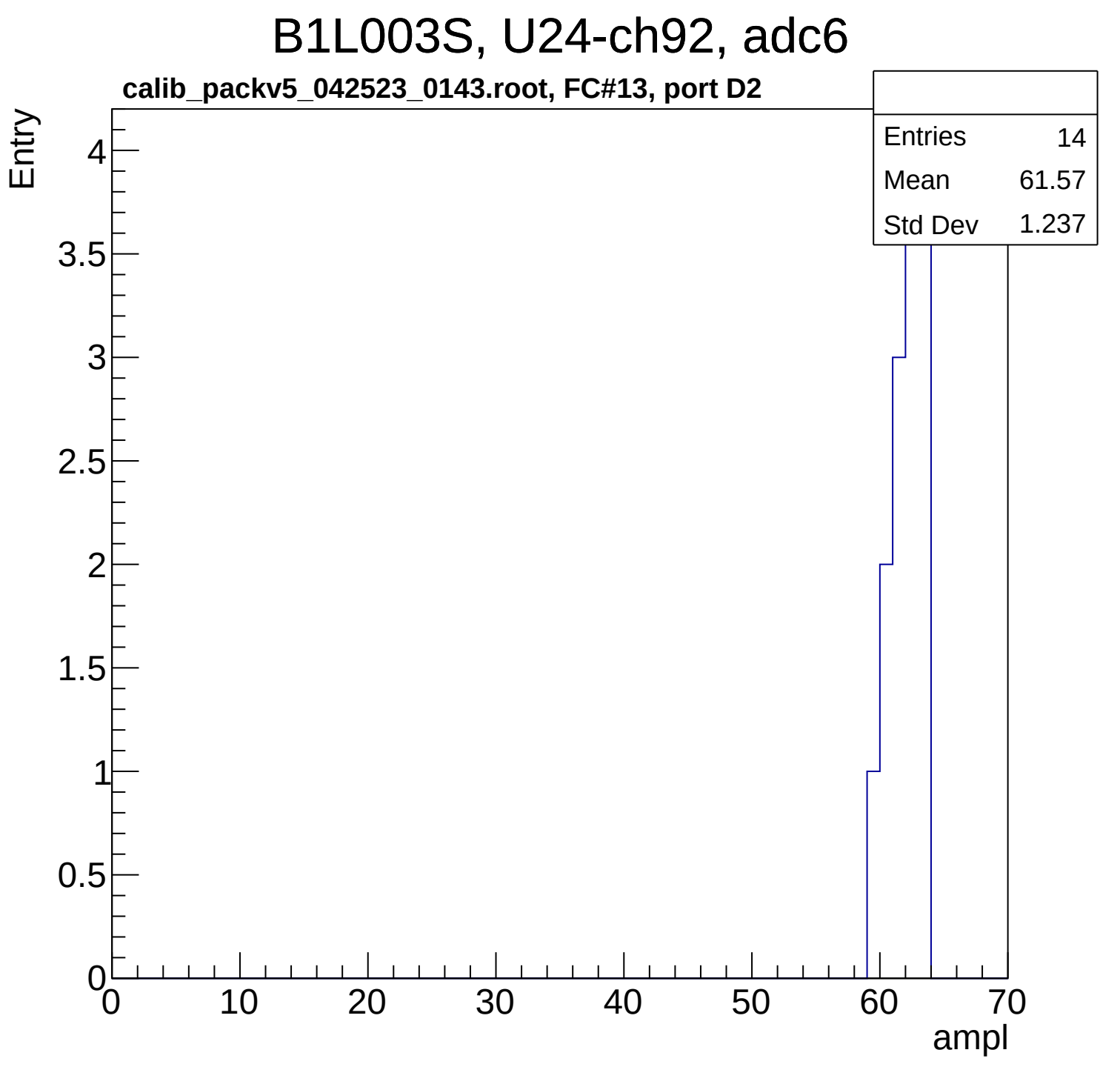

B1L003S, U24-ch92, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.19
Std Dev 9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch92, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.57
Std Dev 1.237
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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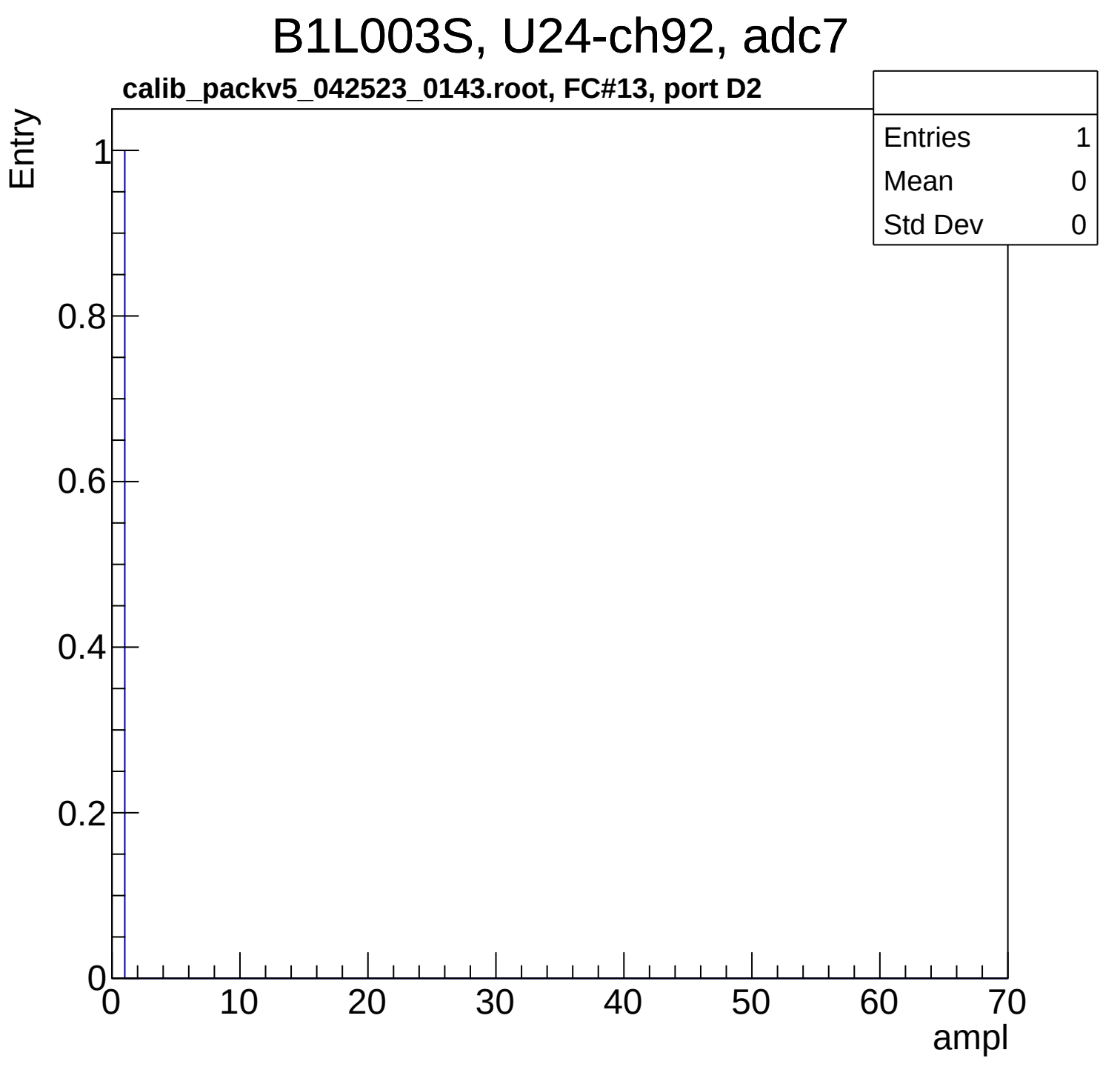
B1L003S, U24-ch92, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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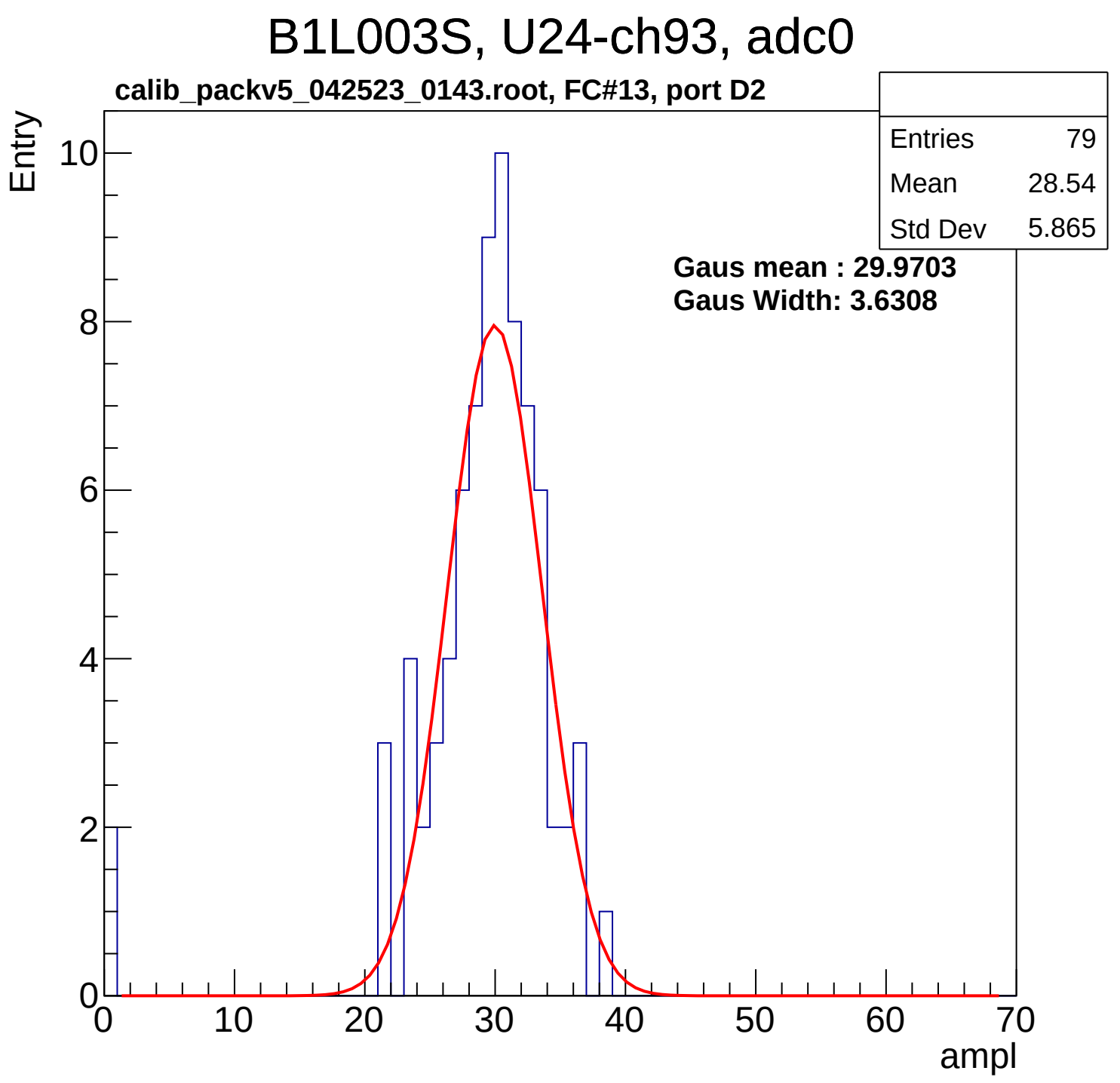
30
40
50
60
70
ampl


B1L003S, U24-ch93, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 28.54
Std Dev 5.865
Gaus mean : 29.9703
Gaus Width: 3.6308
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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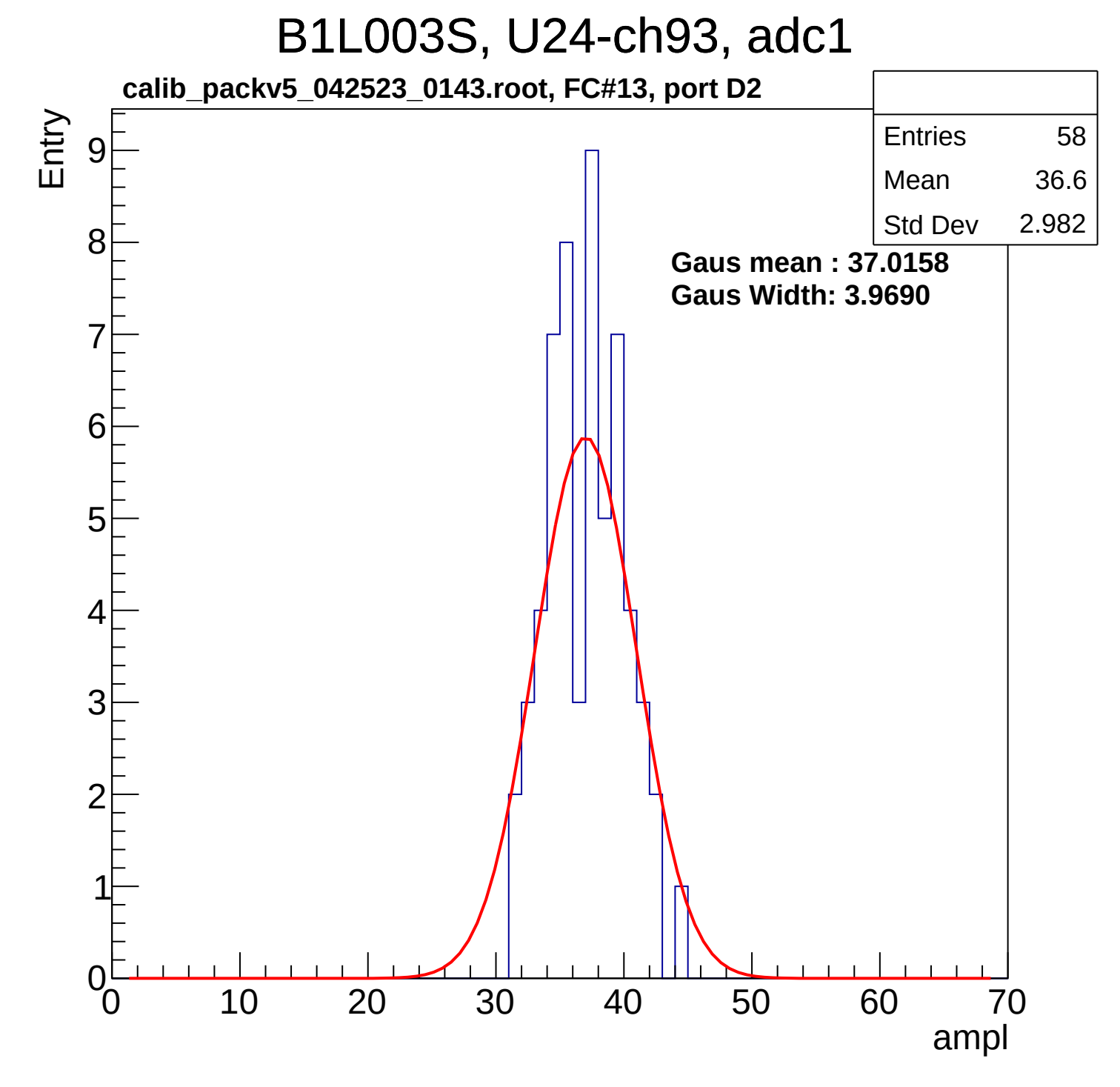
B1L003S, U24-ch93, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 36.6
Std Dev 2.982
Gaus mean : 37.0158
Gaus Width: 3.9690
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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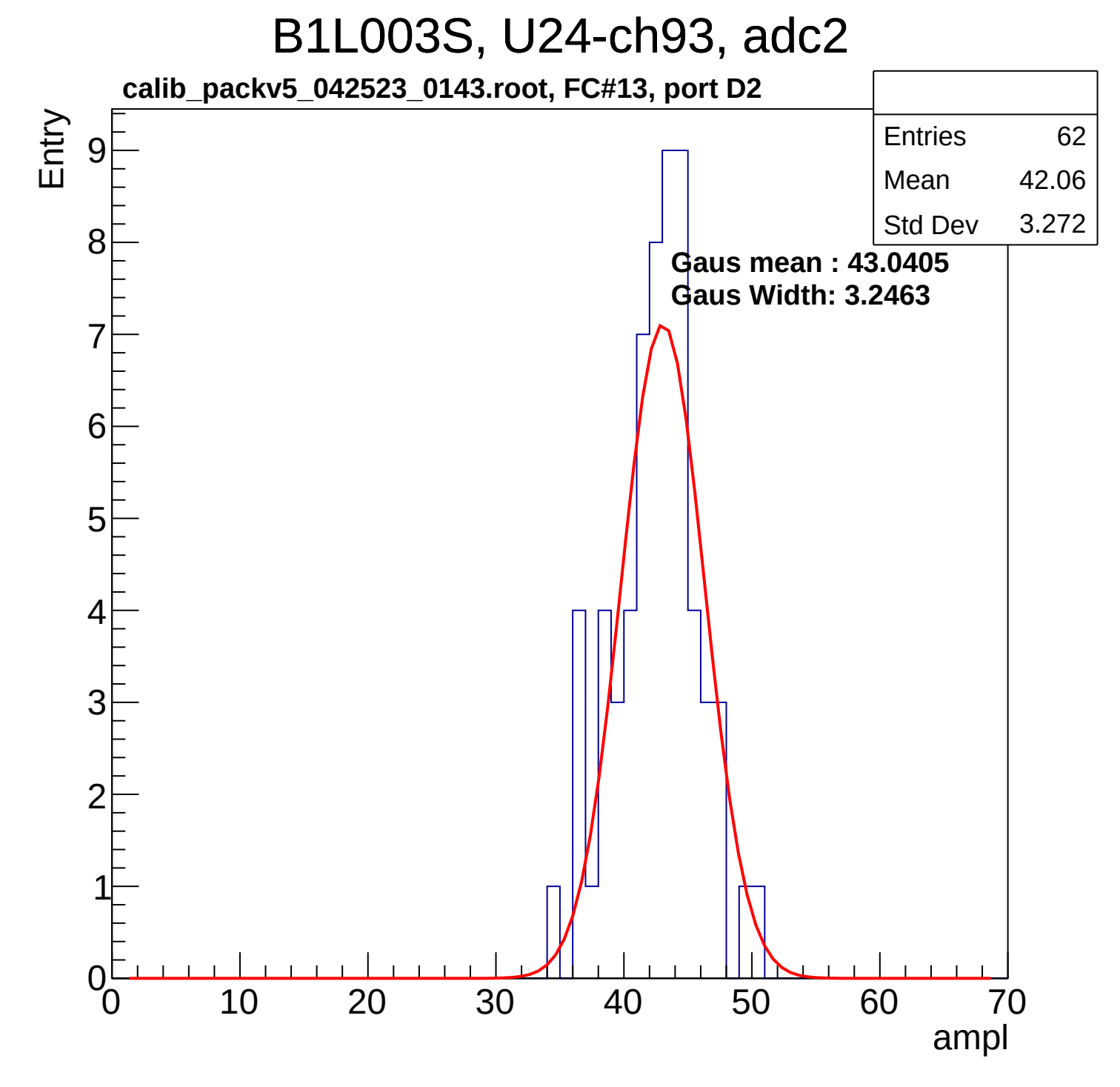
B1L003S, U24-ch93, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.06
Std Dev 3.272
Gaus mean : 43.0405
Gaus Width: 3.2463
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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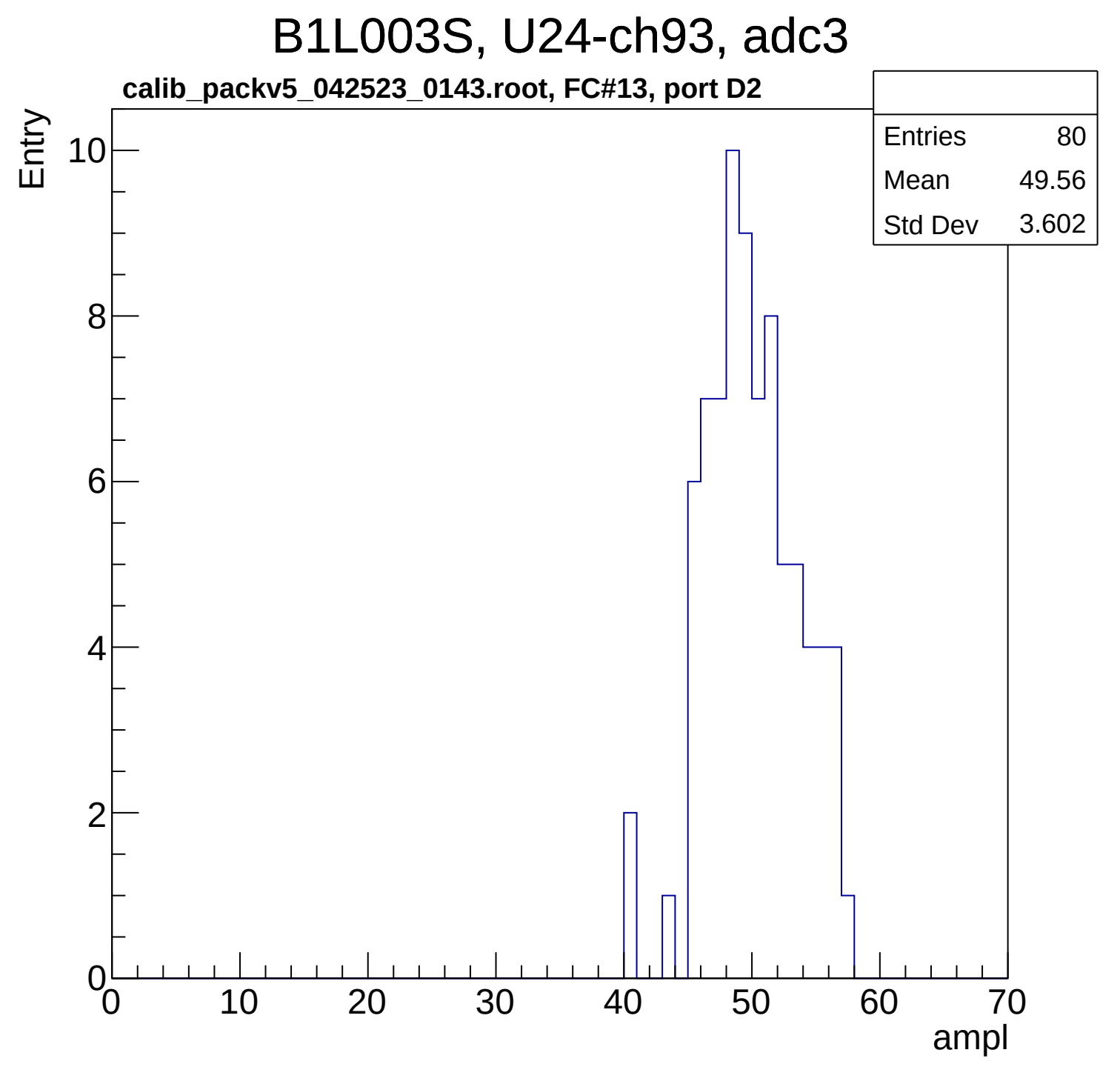
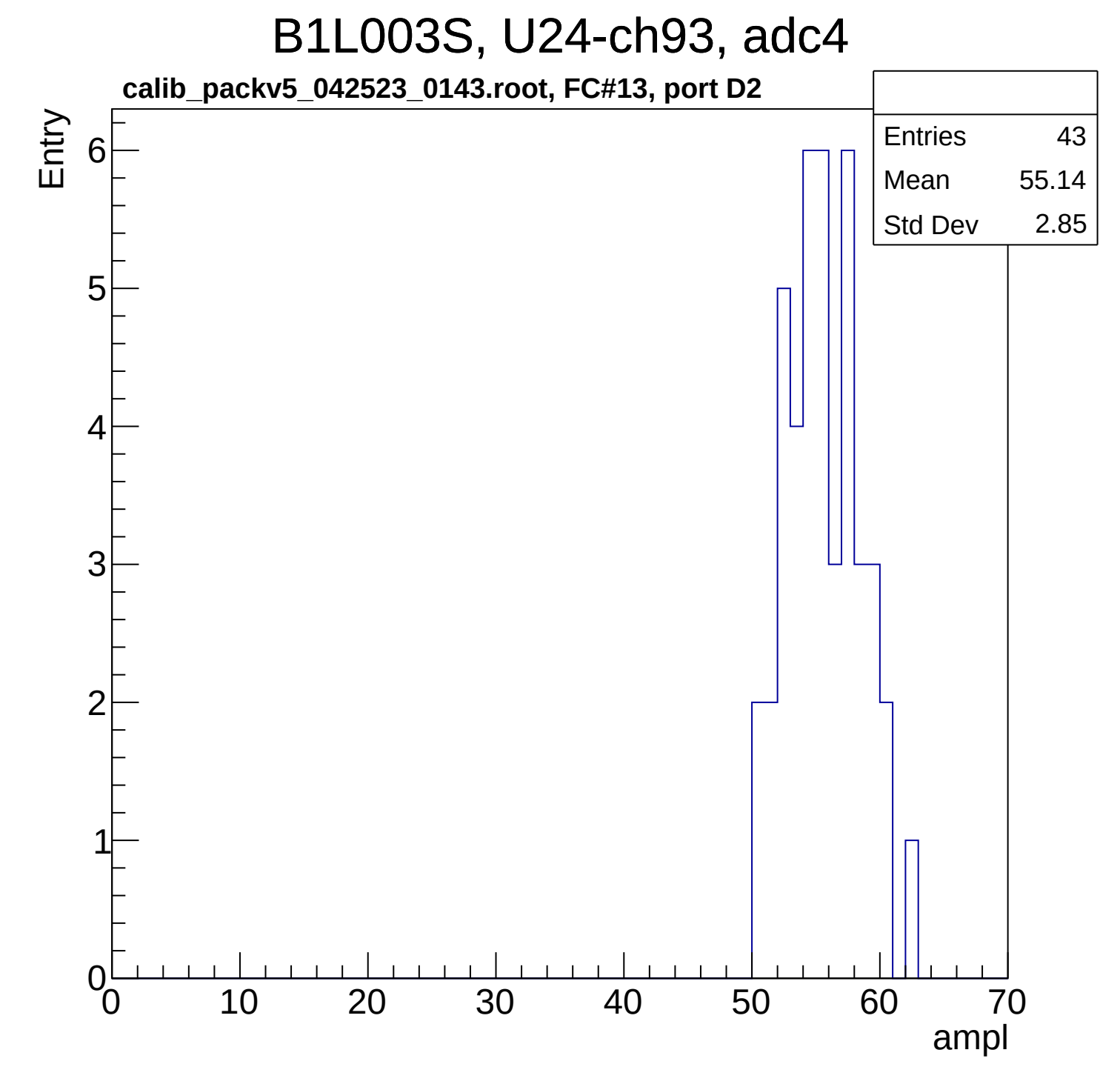
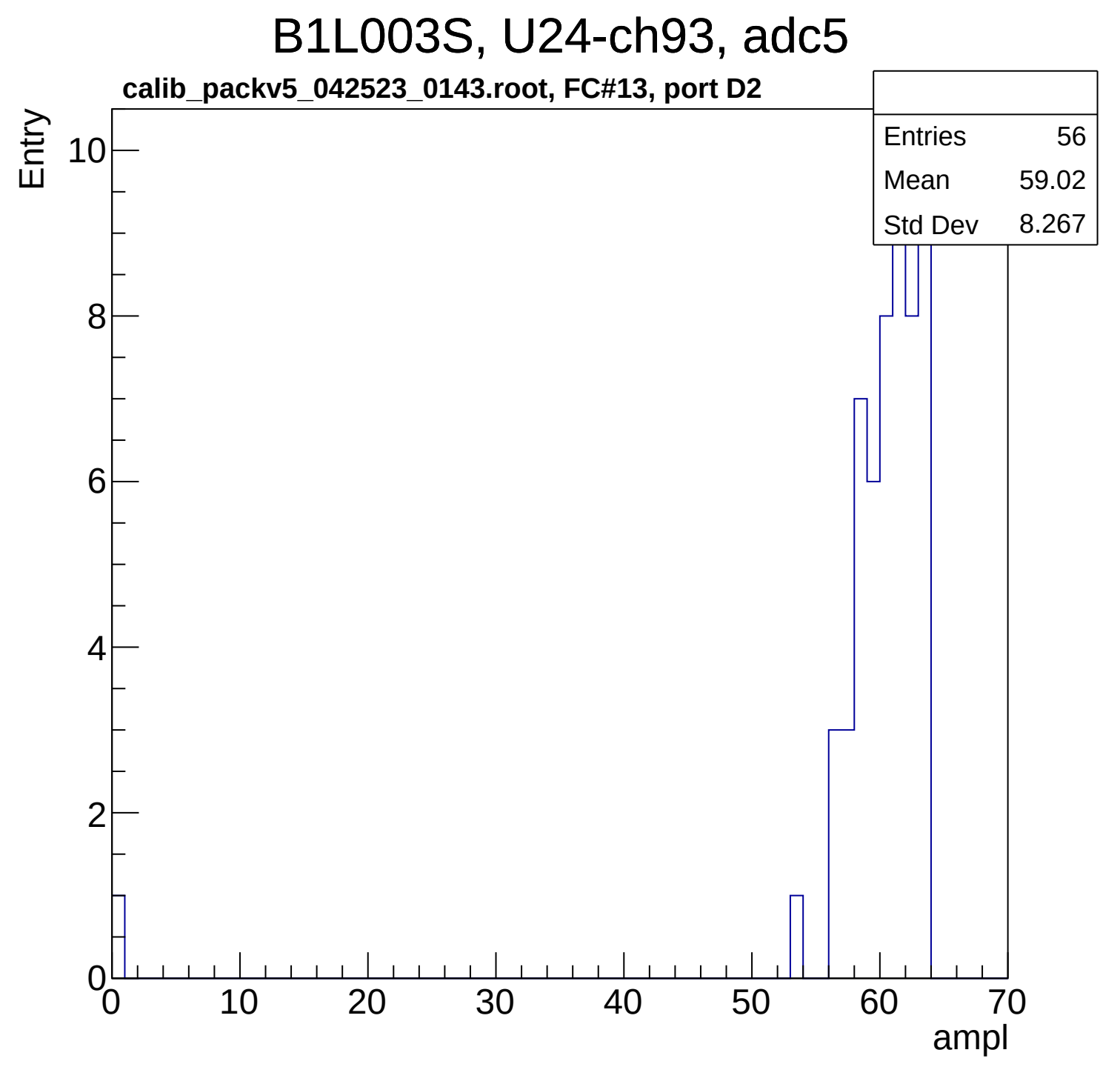

B1L003S, U24-ch93, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 49.56
Std Dev 3.602
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch93, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 55.14
Std Dev 2.85
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch93, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.02
Std Dev 8.267
Entry
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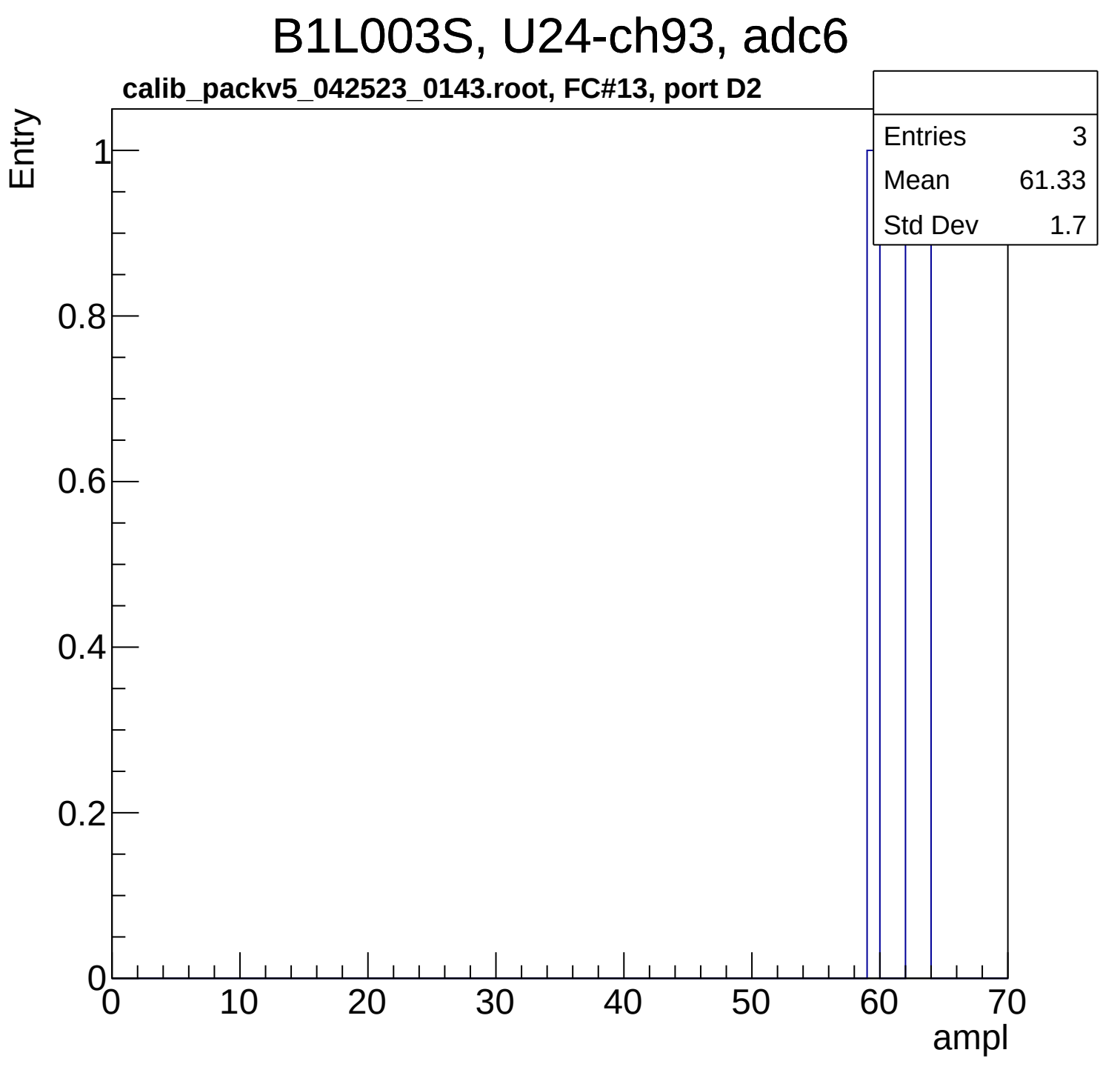
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch93, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 61.33
Std Dev 1.7
Entry
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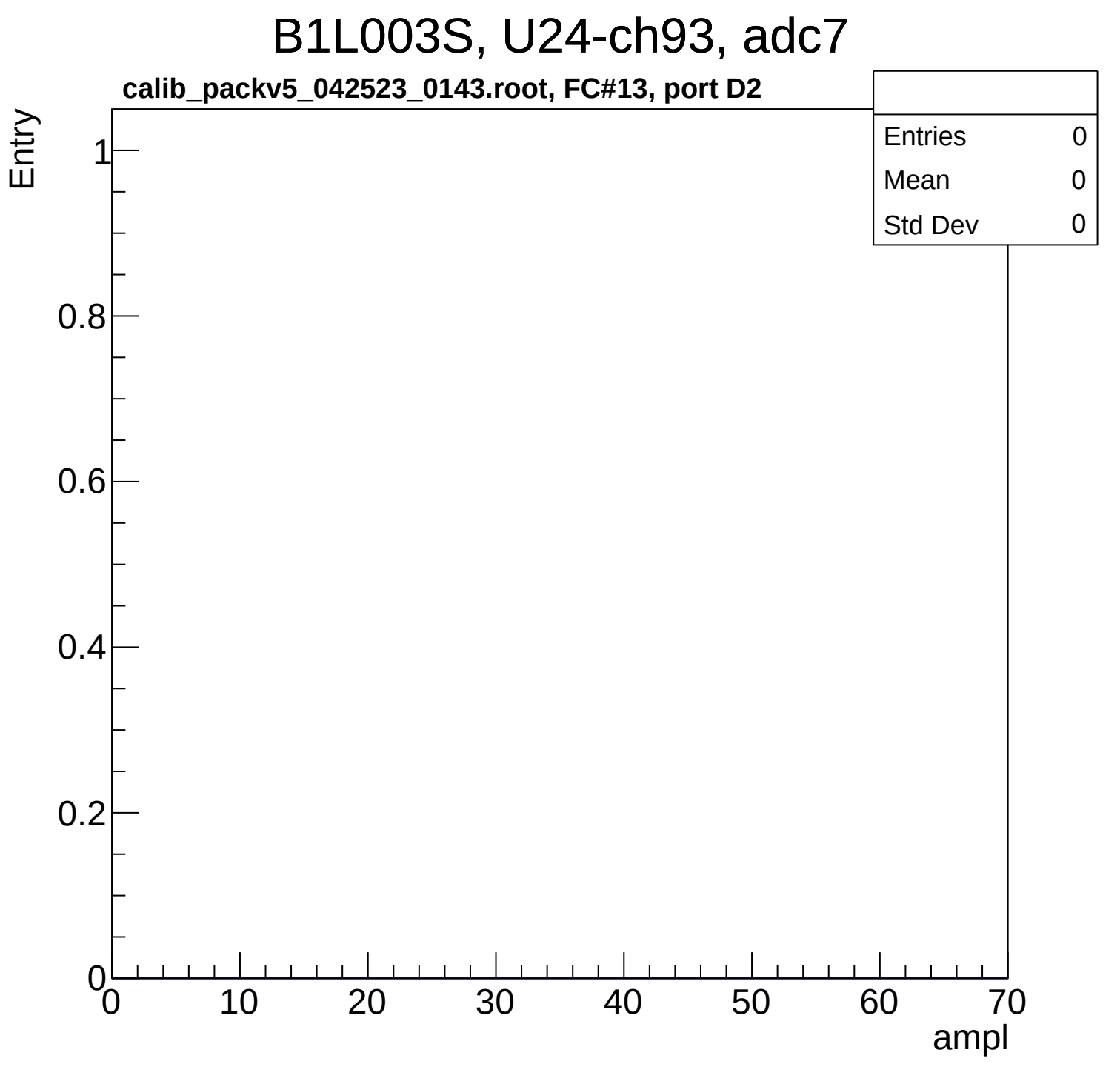
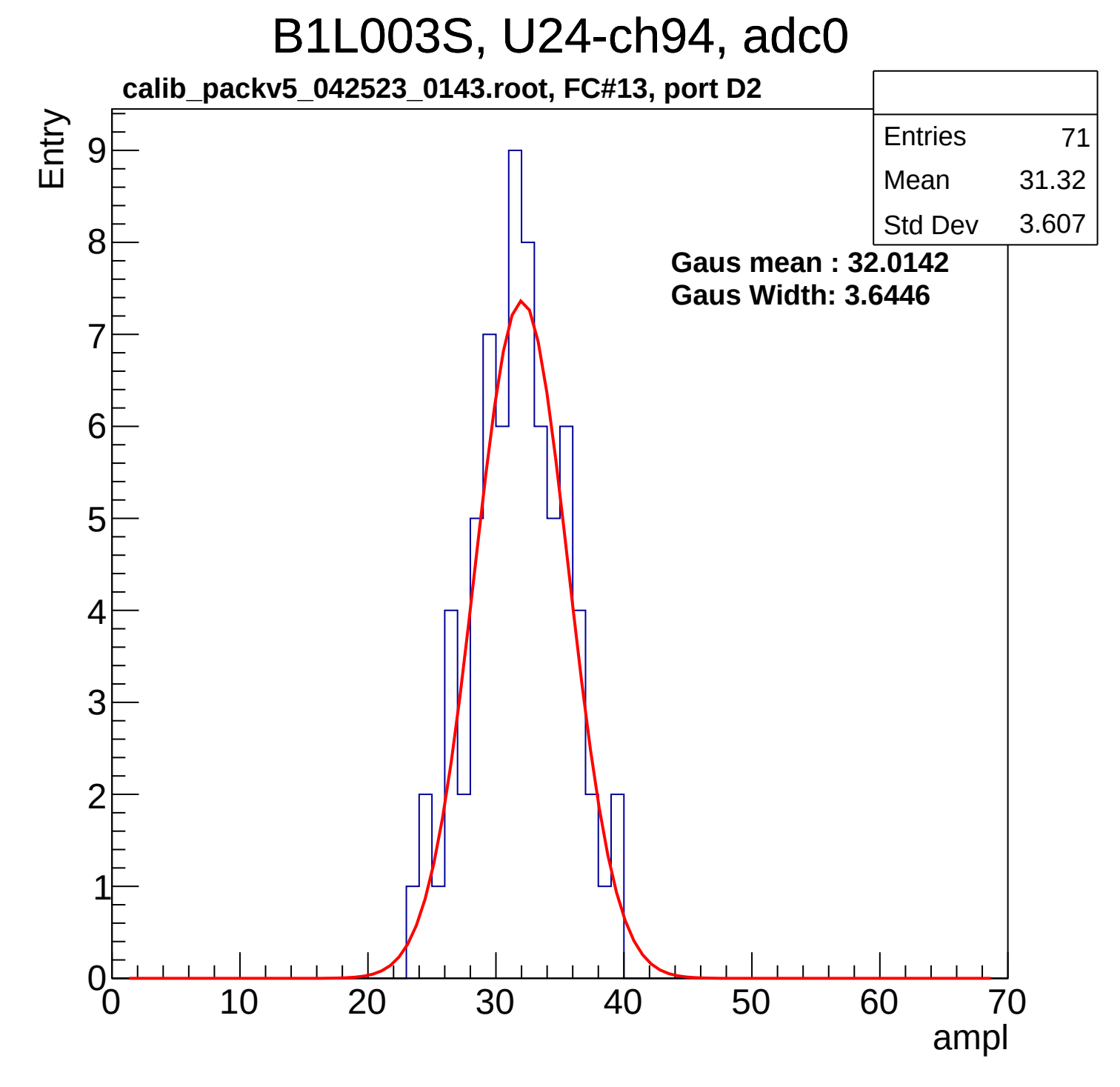
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch93, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch94, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 31.32
Std Dev 3.607
Gaus mean : 32.0142
Gaus Width: 3.6446
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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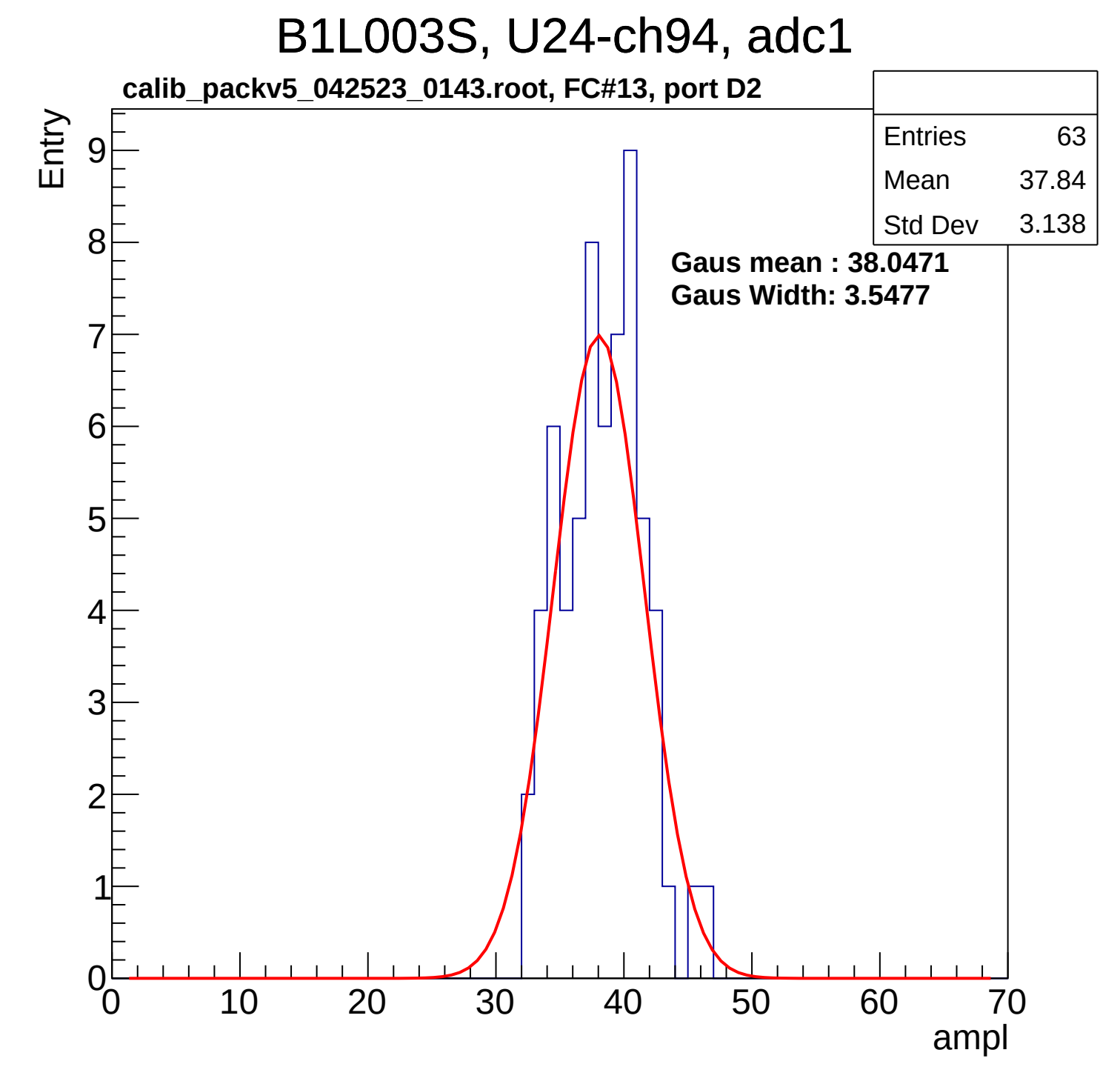
B1L003S, U24-ch94, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 37.84
Std Dev 3.138
Gaus mean : 38.0471
Gaus Width: 3.5477
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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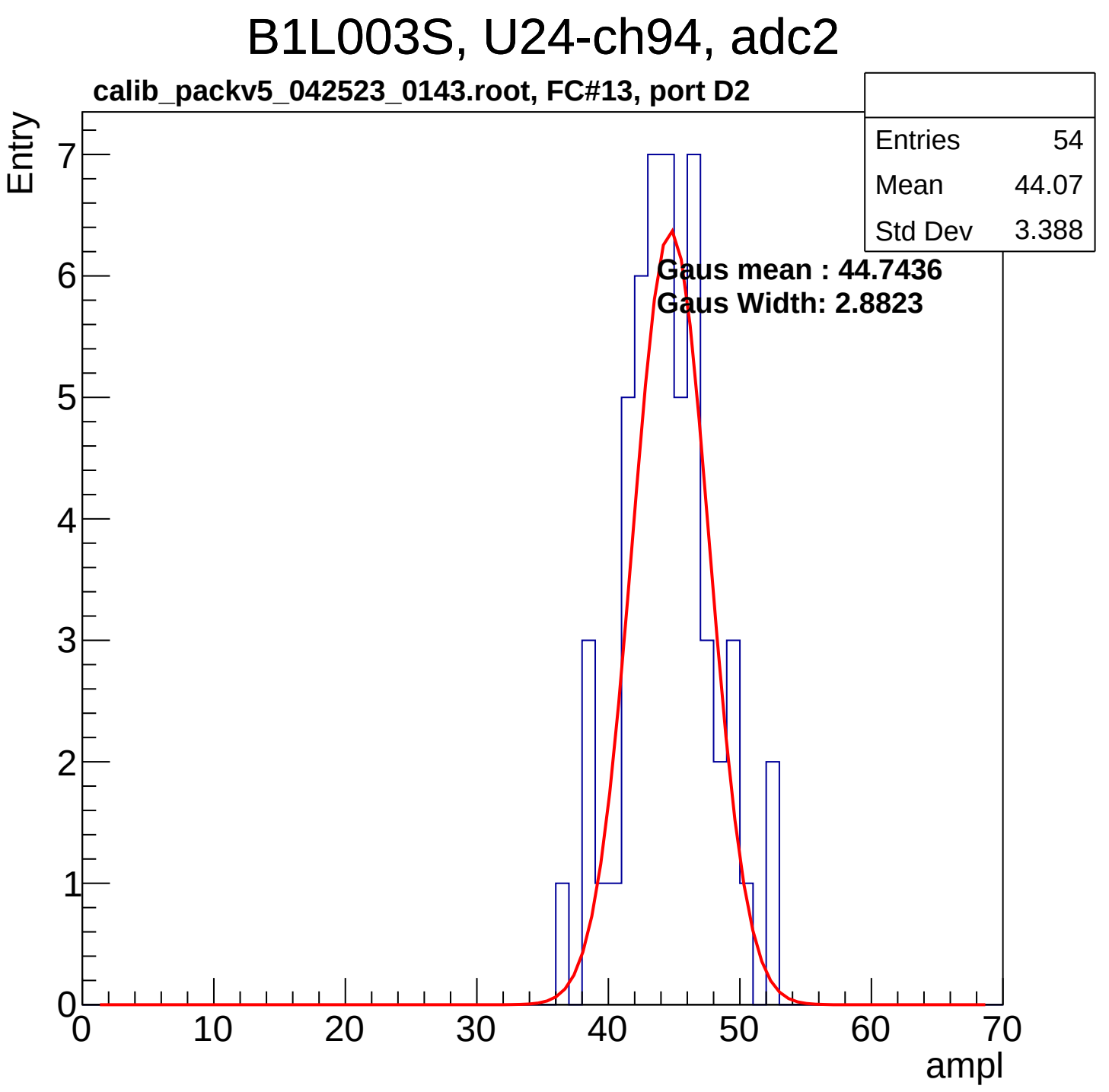
B1L003S, U24-ch94, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 44.07
Std Dev 3.388
Gaus mean : 44.7436
Gaus Width: 2.8823
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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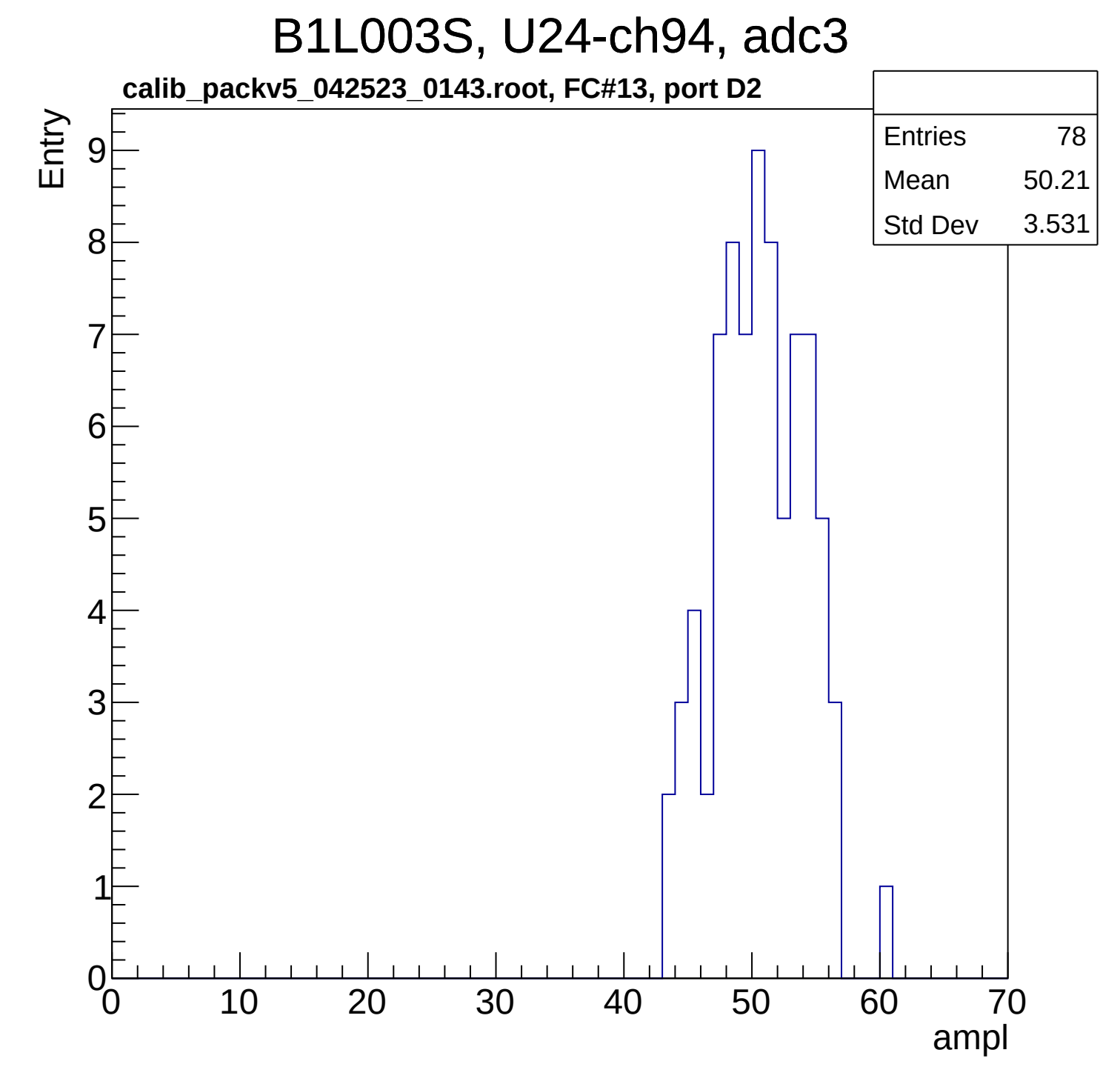
B1L003S, U24-ch94, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 50.21
Std Dev 3.531
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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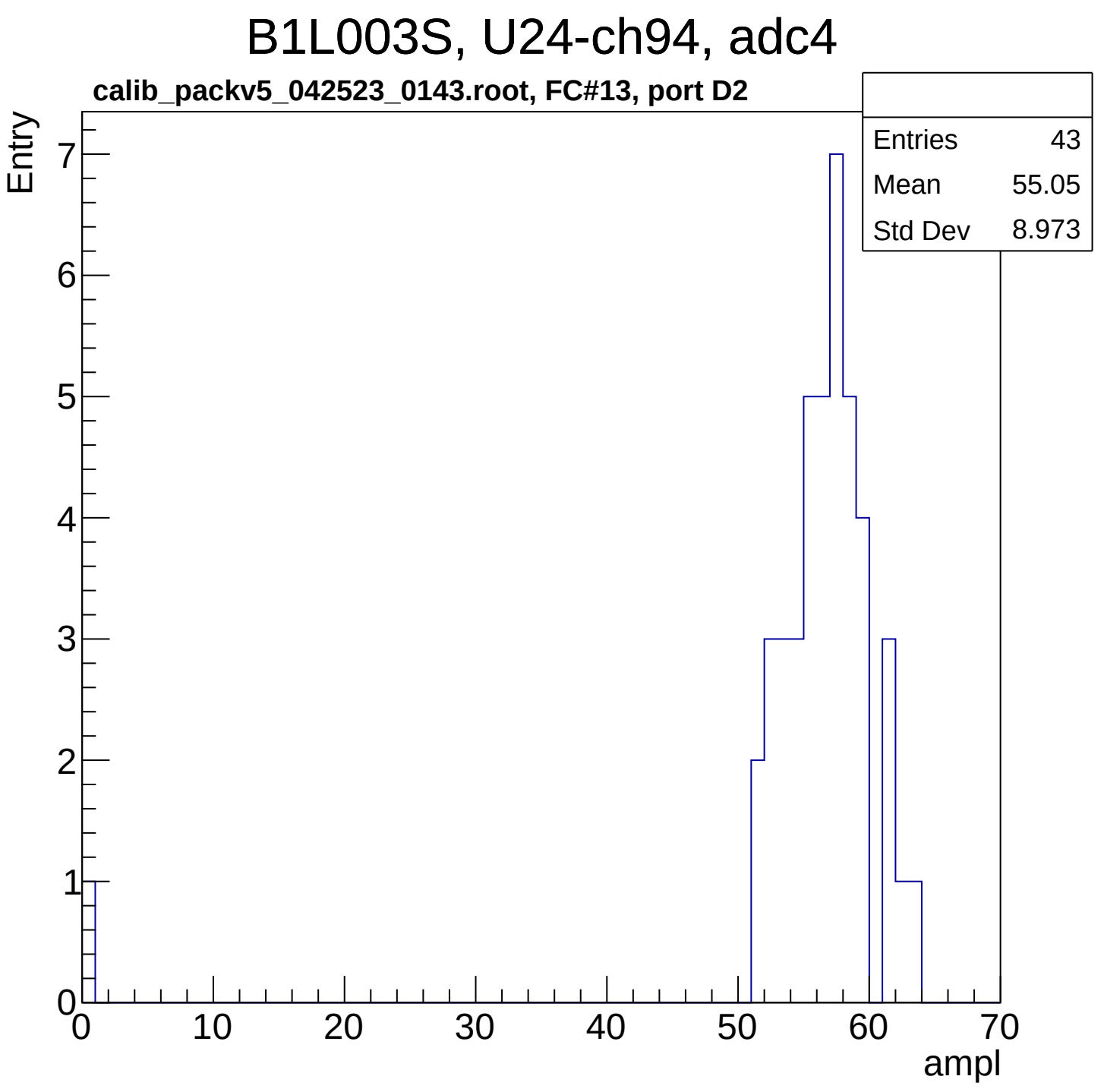
40
50
60
70


B1L003S, U24-ch94, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 55.05
Std Dev 8.973
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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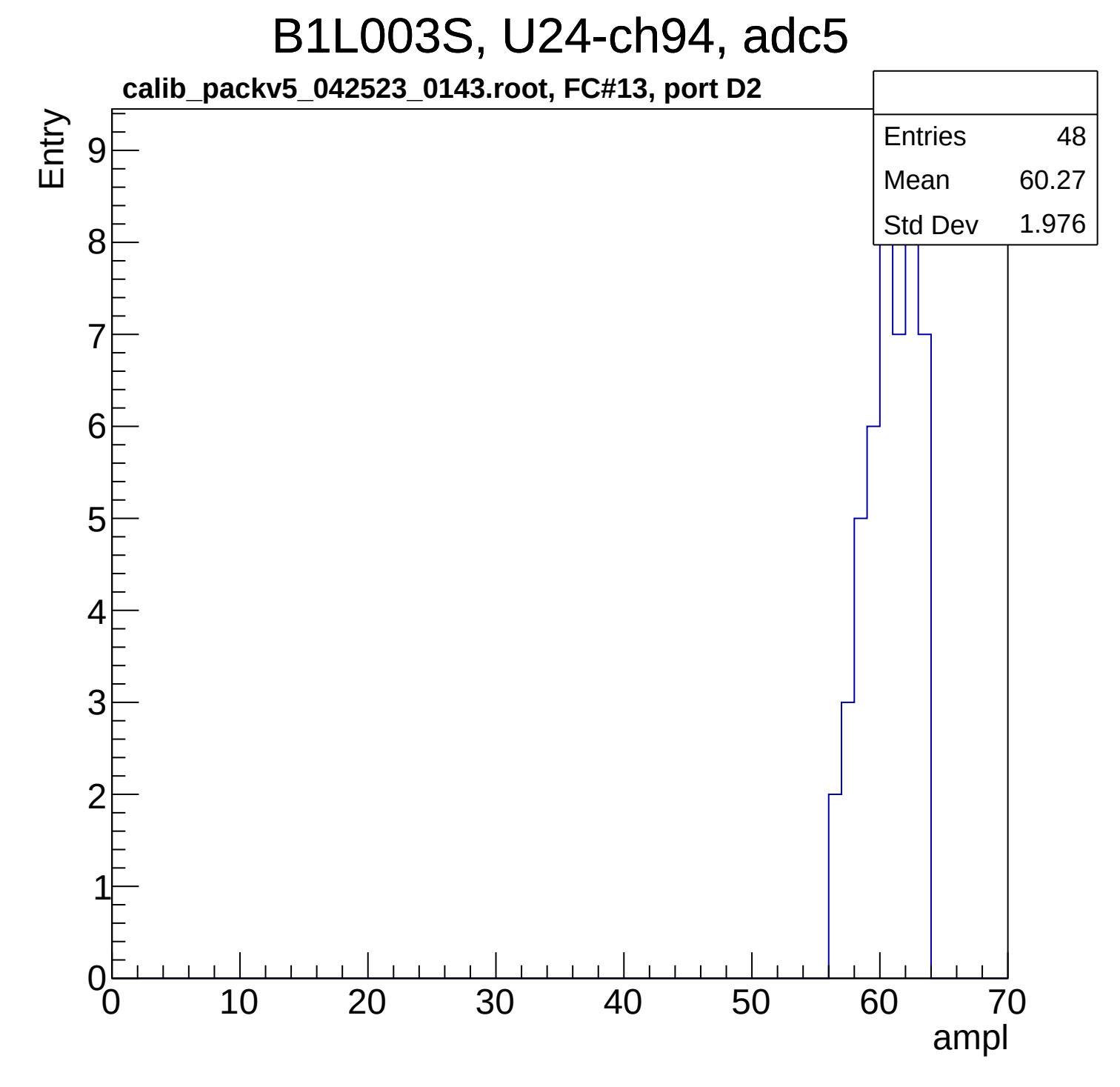
B1L003S, U24-ch94, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 60.27
Std Dev 1.976
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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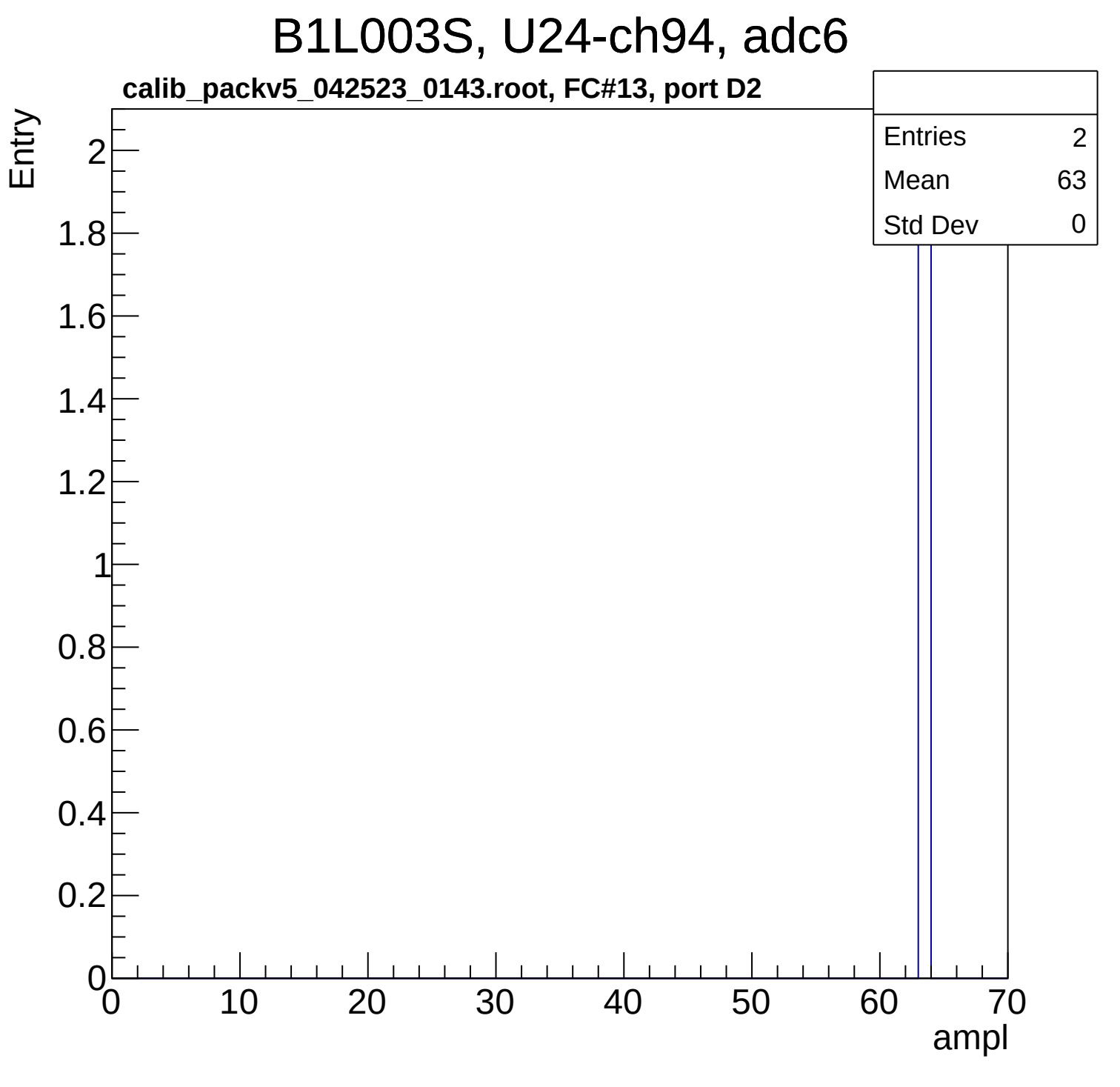
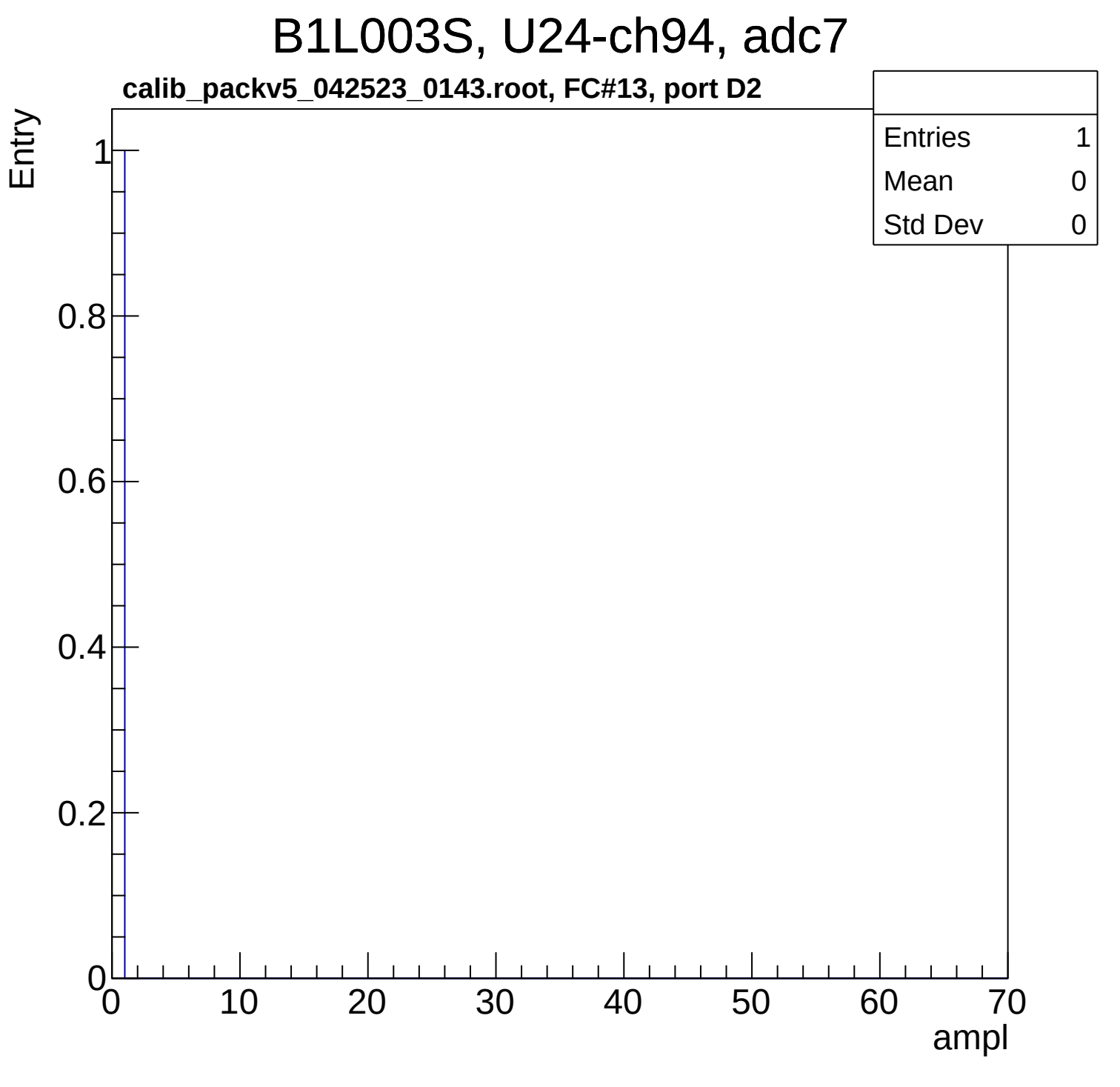

B1L003S, U24-ch94, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch94, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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0
10
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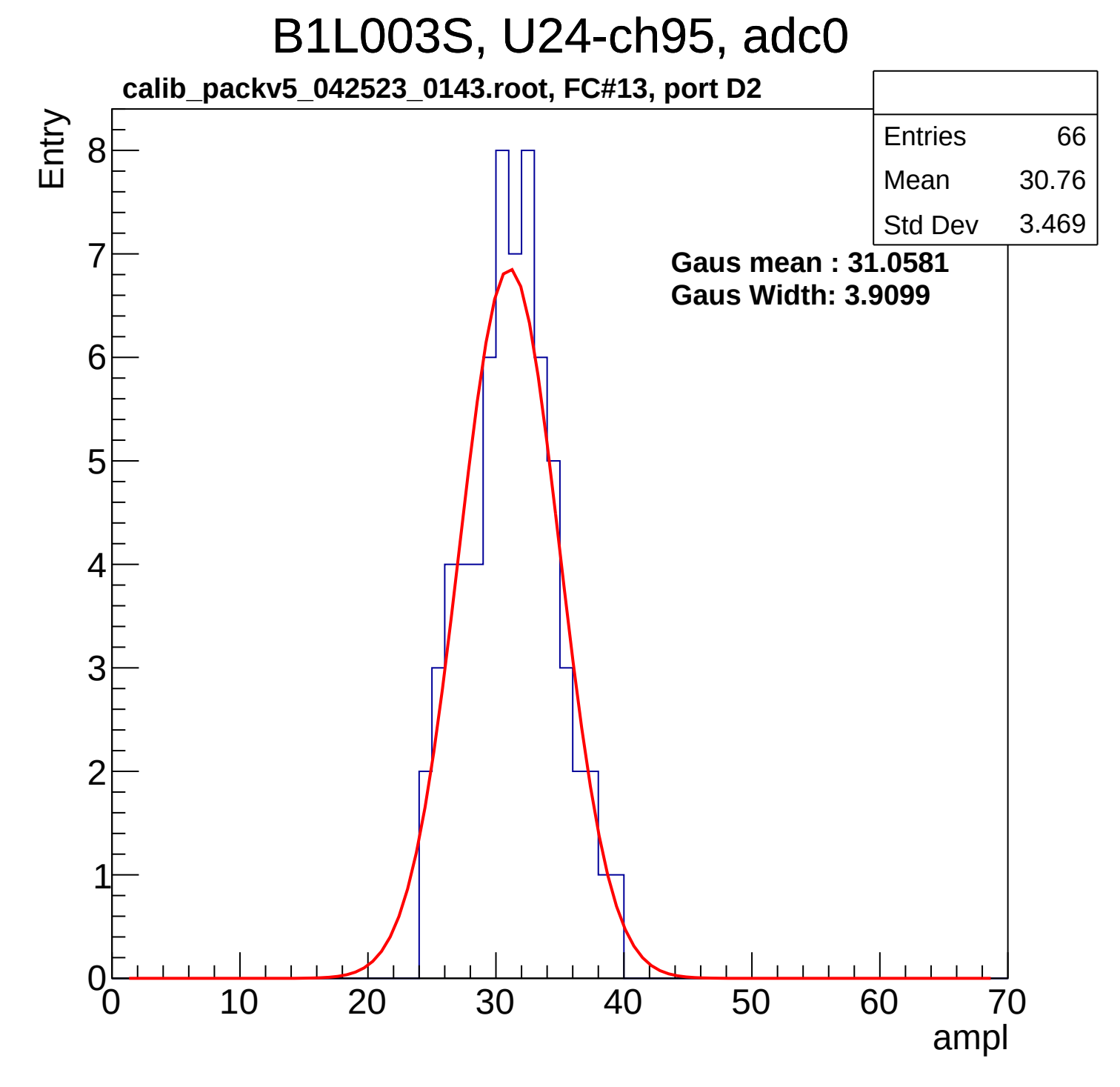
60
70
ampl


B1L003S, U24-ch95, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.76
Std Dev 3.469
Gaus mean : 31.0581
Gaus Width: 3.9099
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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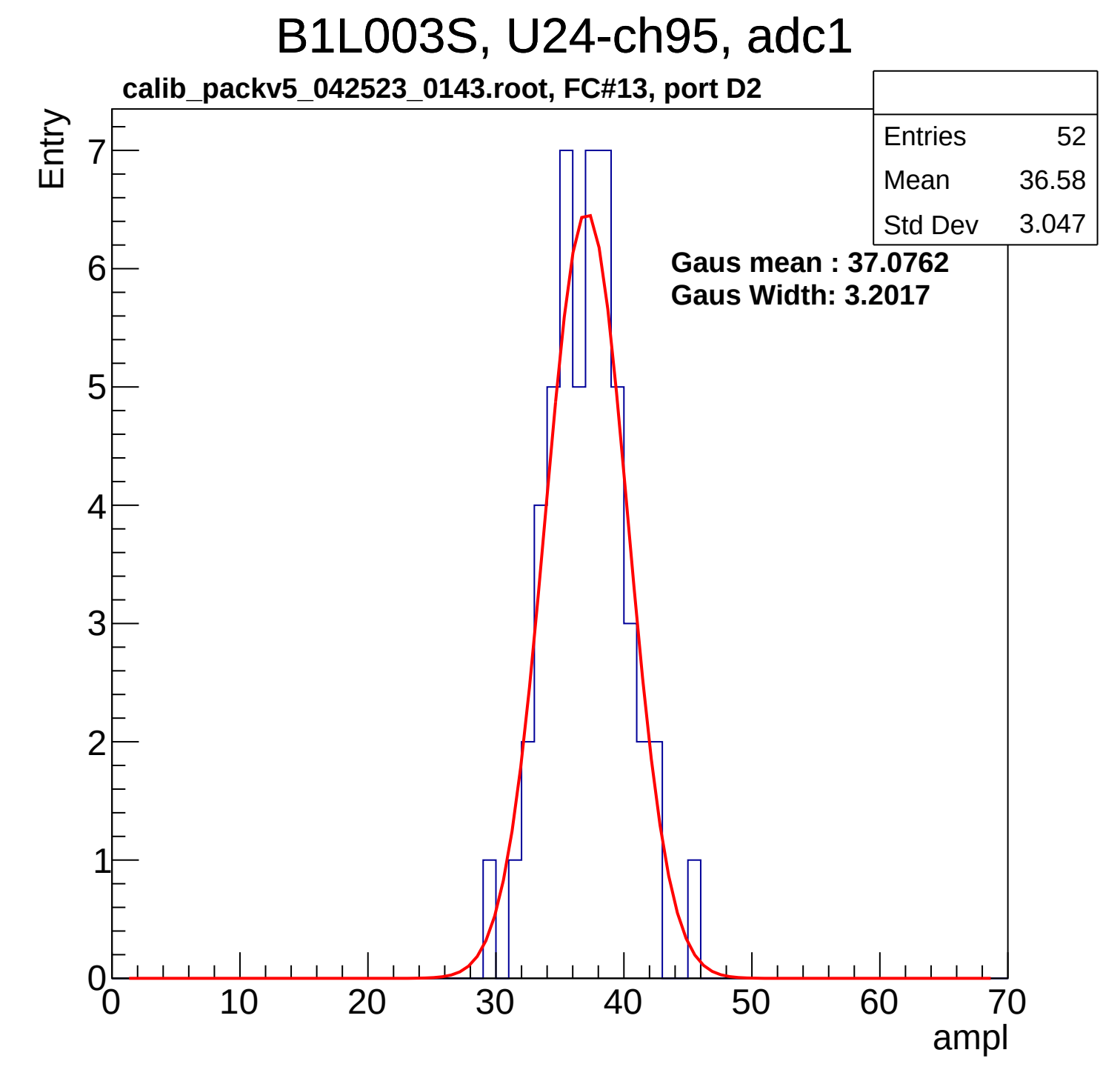
60
70


B1L003S, U24-ch95, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 36.58
Std Dev 3.047
Gaus mean : 37.0762
Gaus Width: 3.2017
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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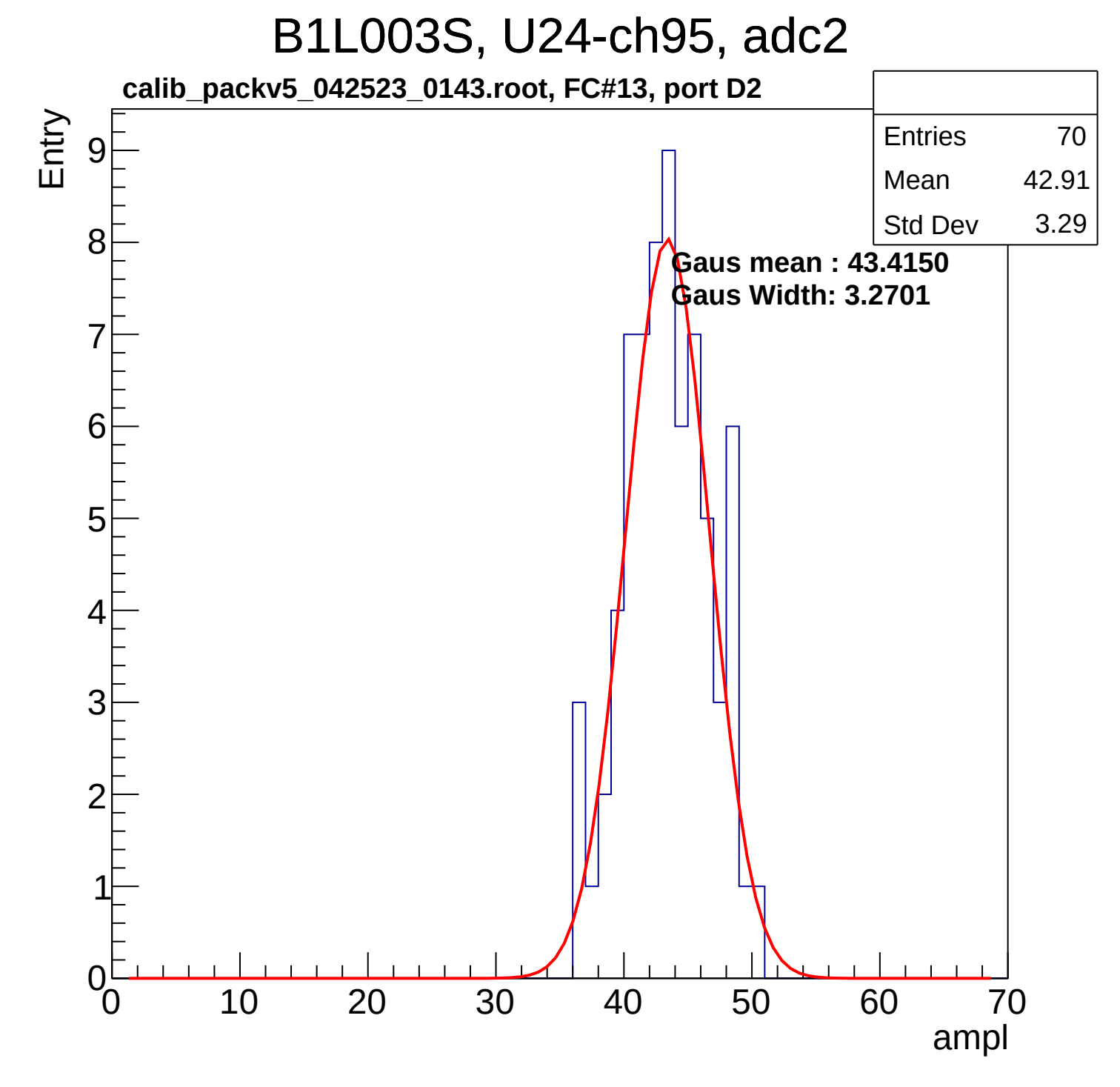
40
50
60
70


B1L003S, U24-ch95, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.91
Std Dev 3.29
Gaus mean : 43.4150
Gaus Width: 3.2701
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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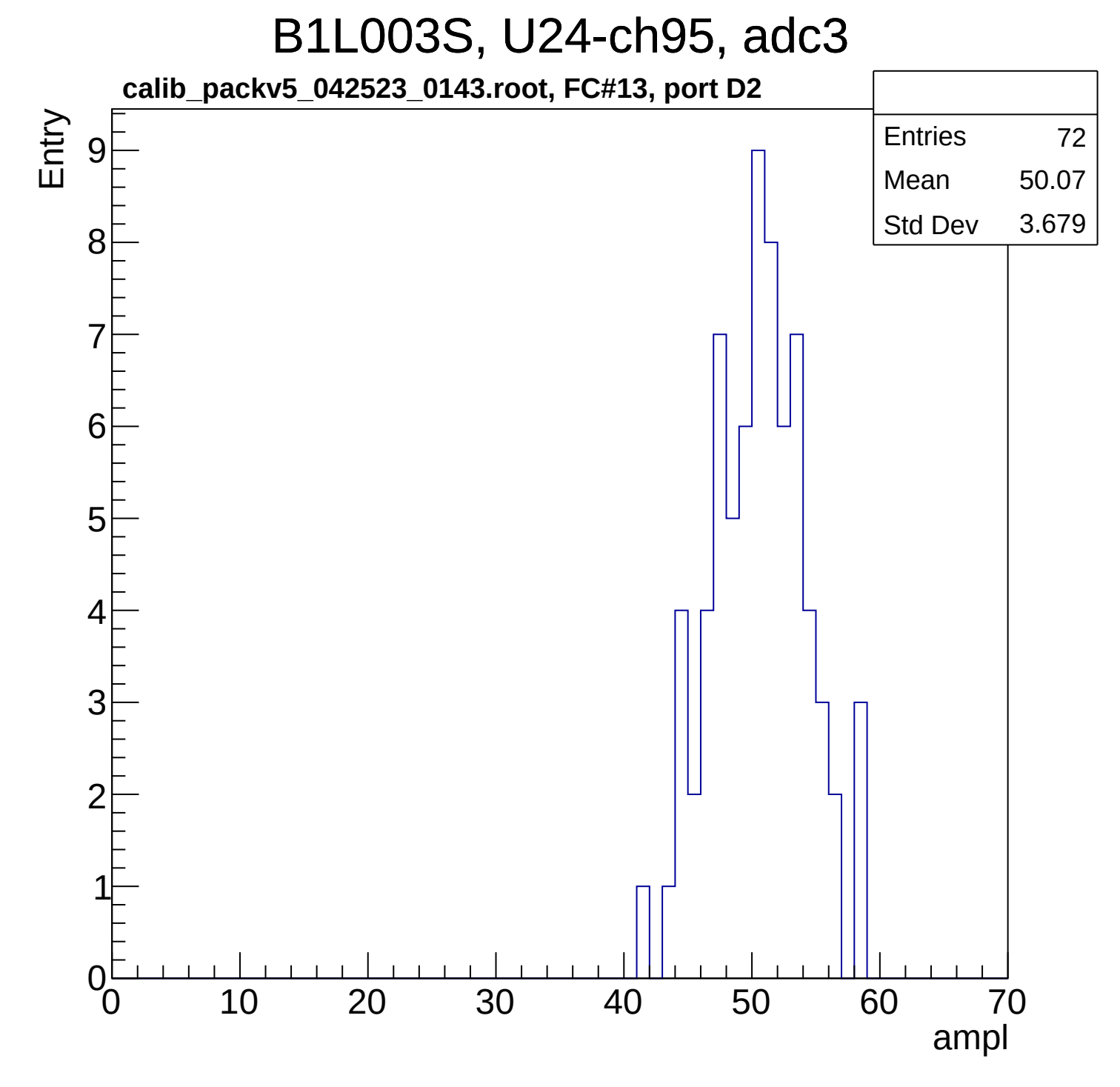
B1L003S, U24-ch95, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 50.07
Std Dev 3.679
Entry
ampl
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5
6
7
8
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0
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60
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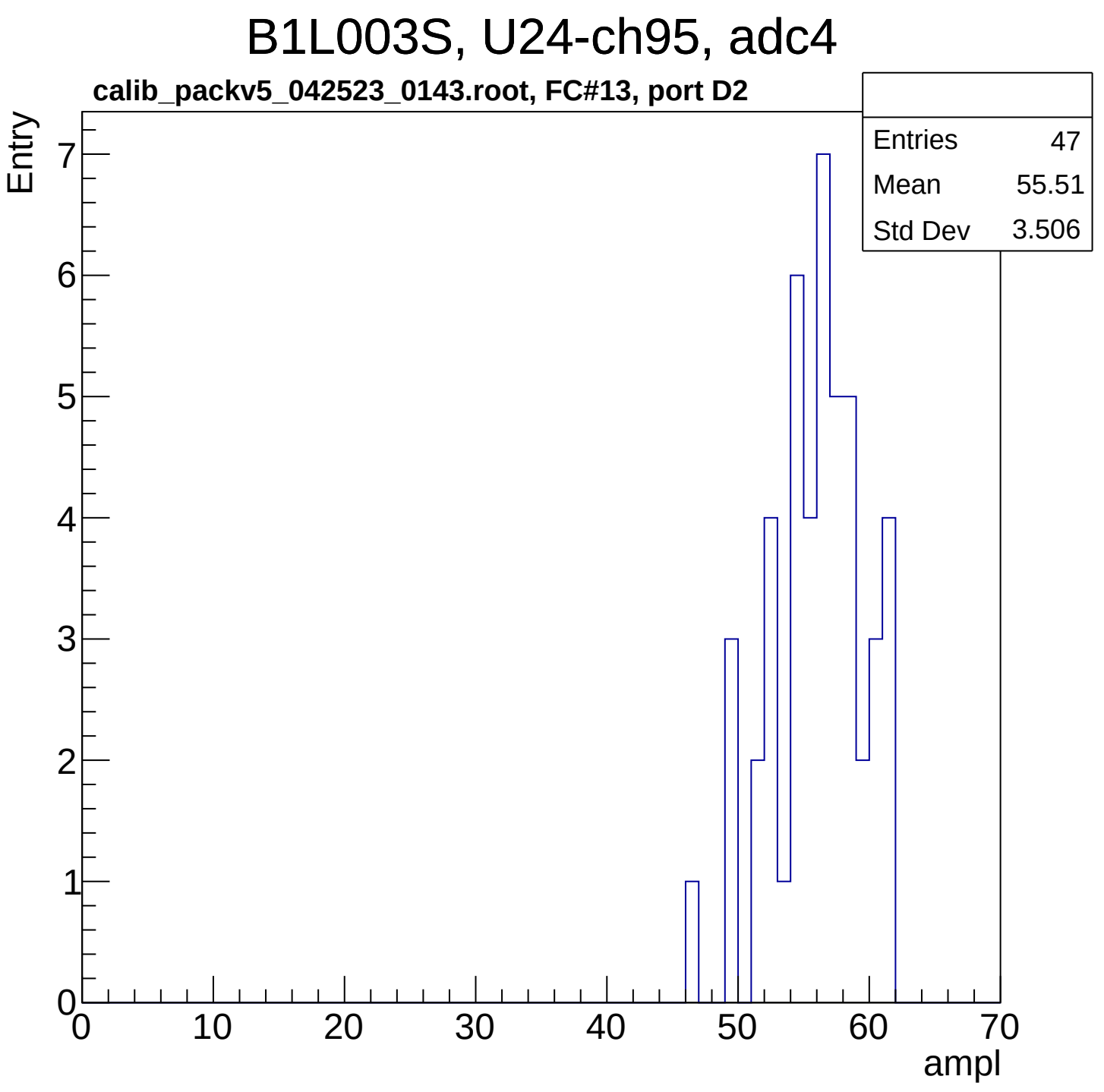
B1L003S, U24-ch95, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 55.51
Std Dev 3.506
Entry
ampl
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4
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6
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0
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60
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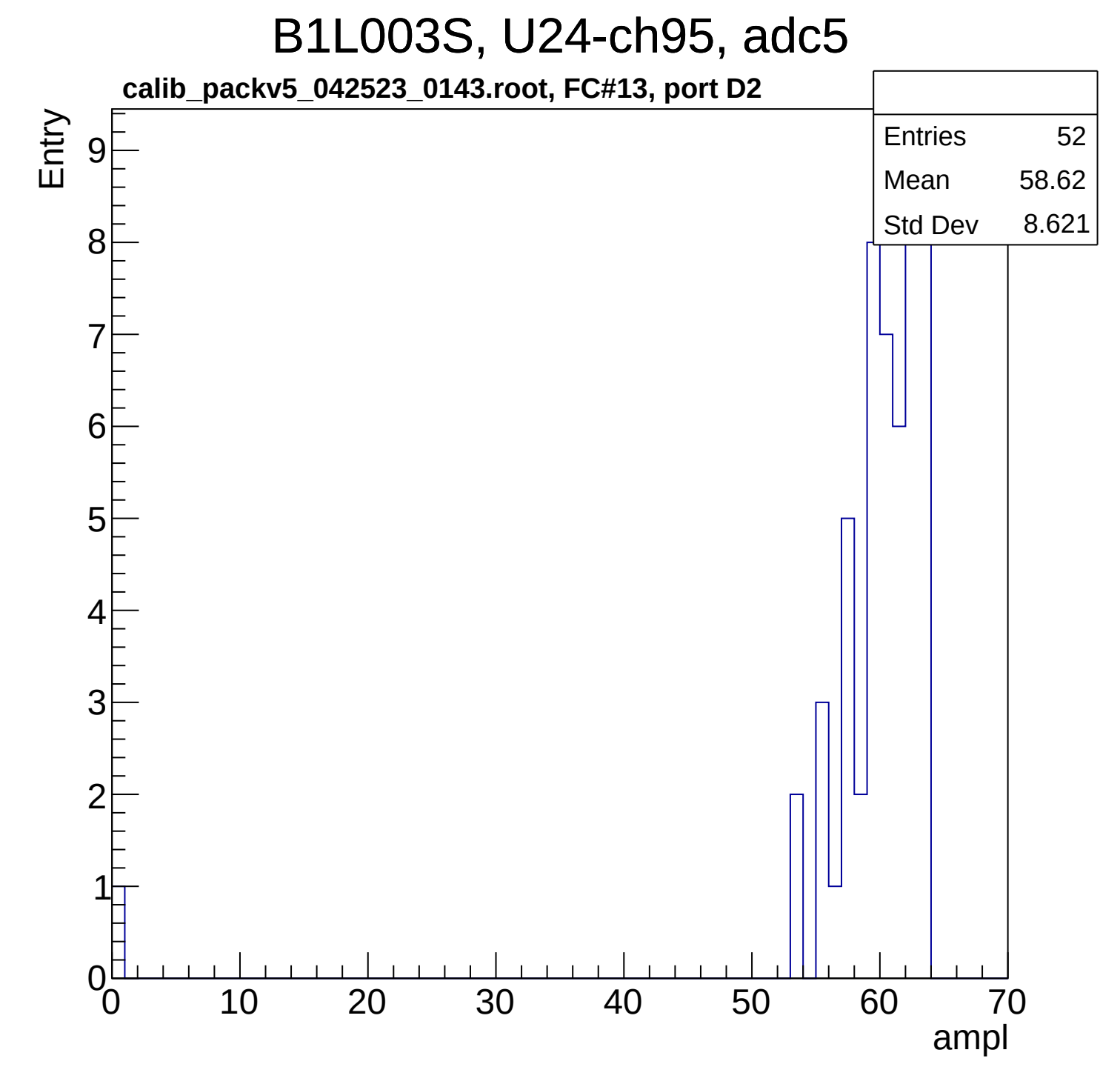
B1L003S, U24-ch95, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.62
Std Dev 8.621
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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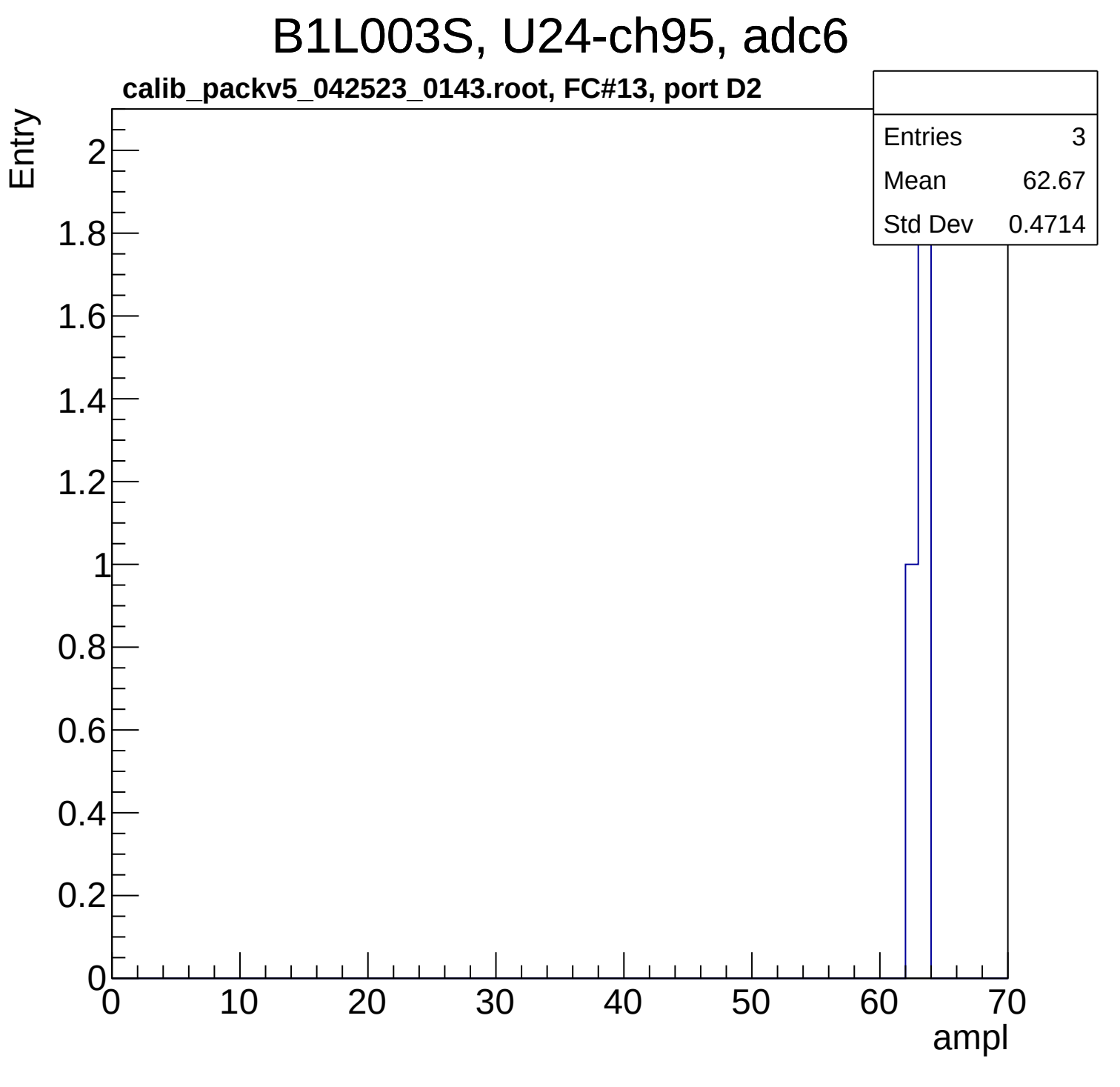
B1L003S, U24-ch95, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
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60
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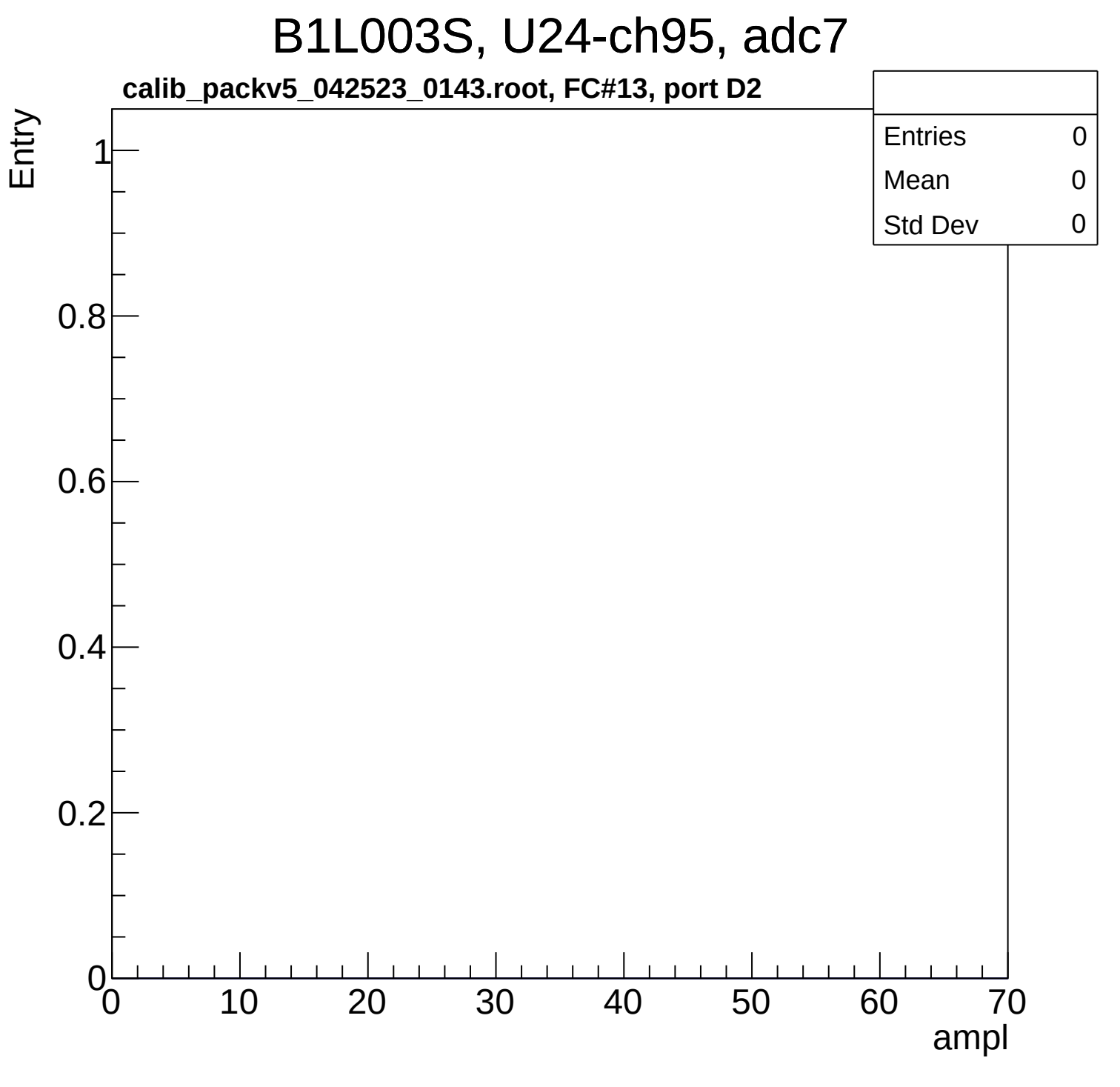
B1L003S, U24-ch95, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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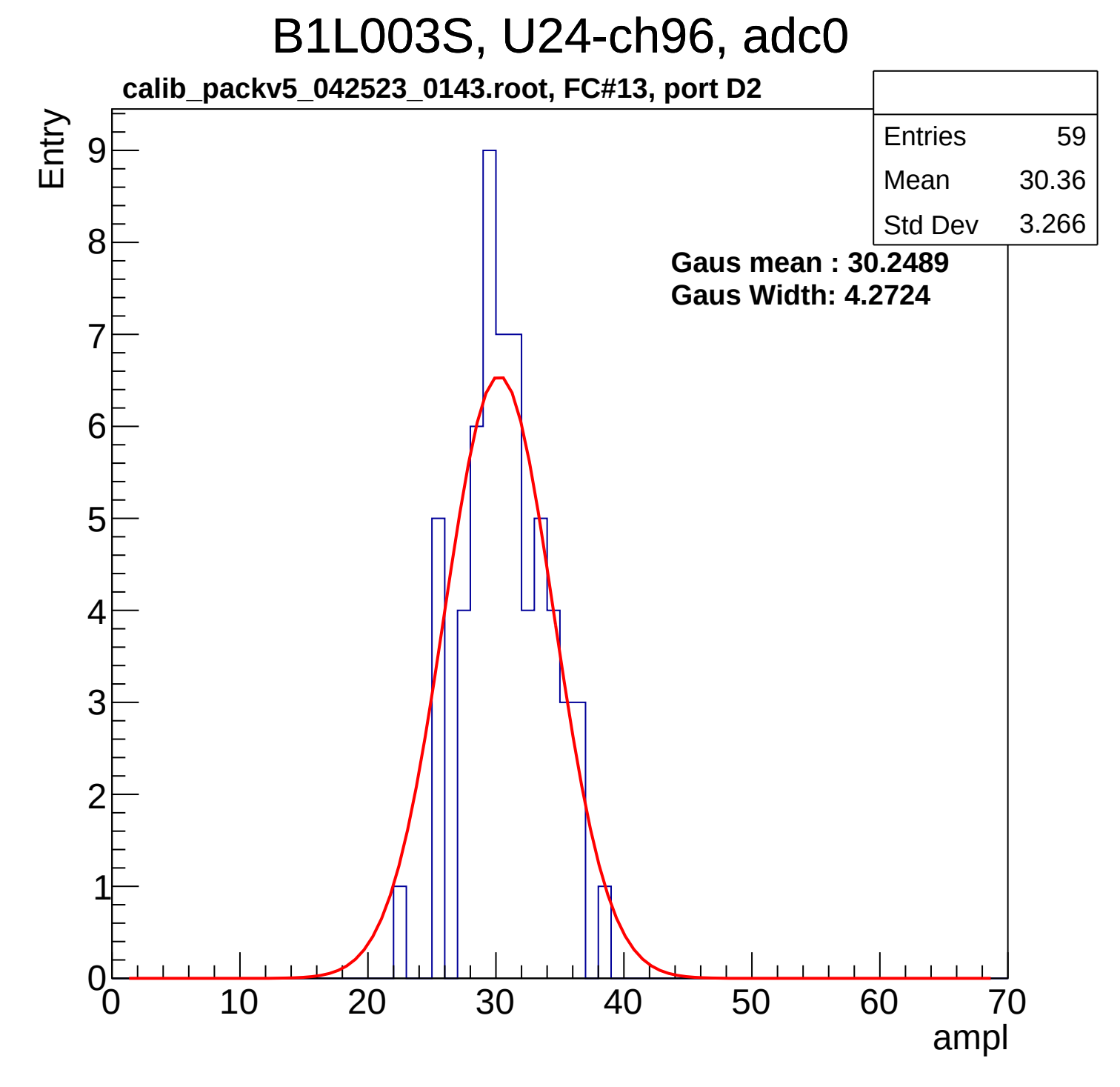
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch96, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 30.36
Std Dev 3.266
Gaus mean : 30.2489
Gaus Width: 4.2724
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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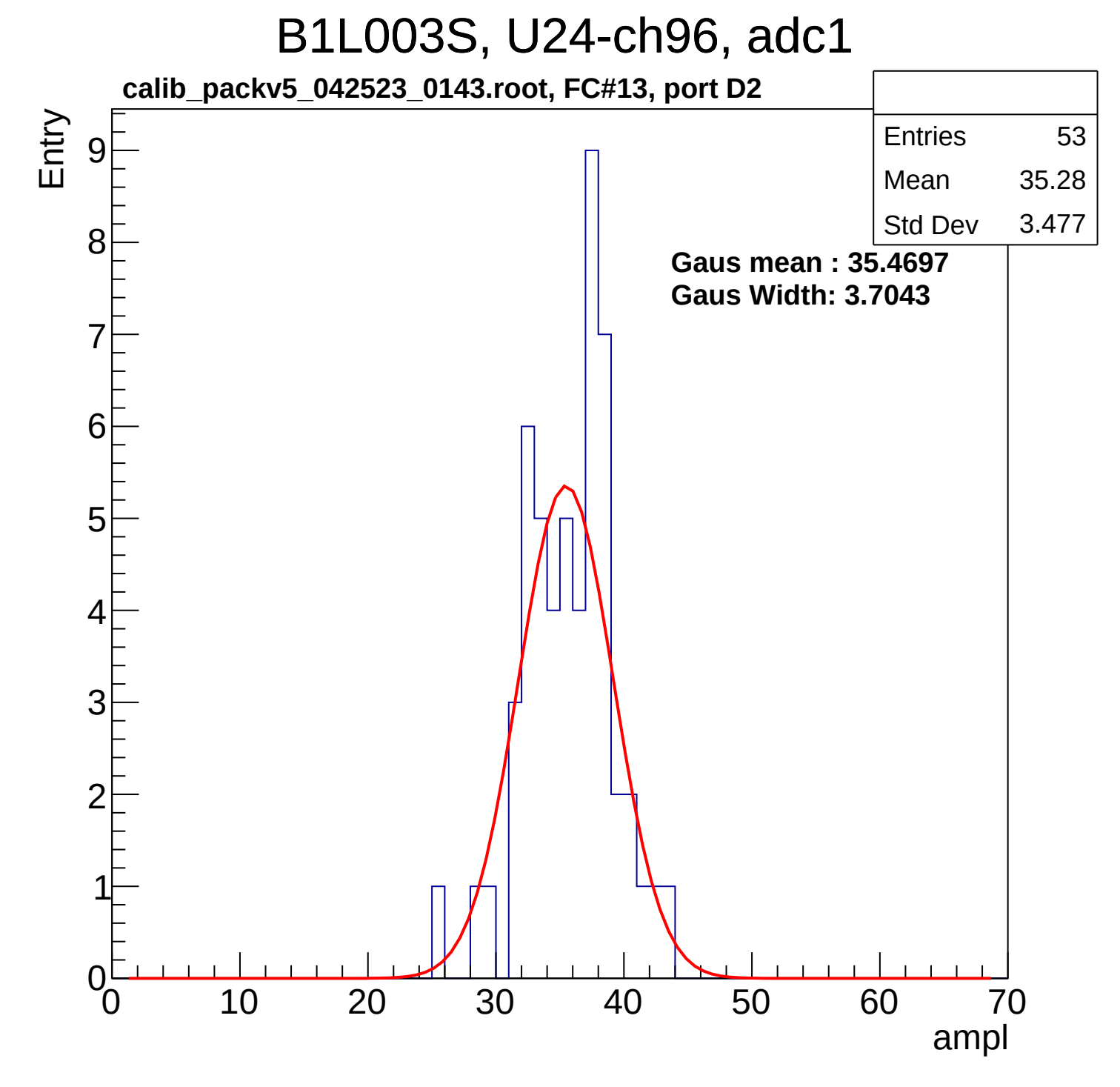
B1L003S, U24-ch96, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 35.28
Std Dev 3.477
Gaus mean : 35.4697
Gaus Width: 3.7043
Entry
ampl
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10
20
30
40
50
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1
2
3
4
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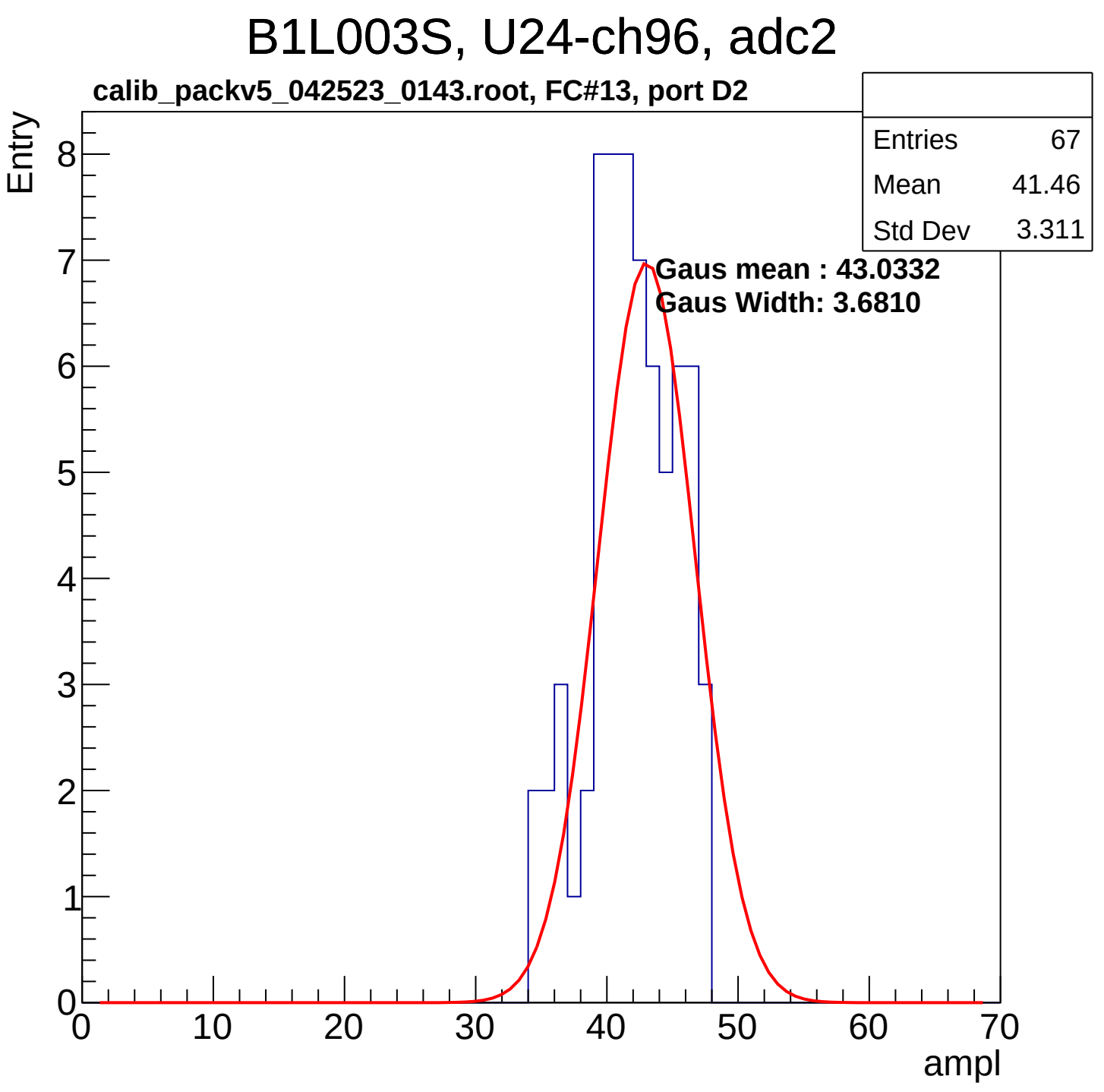
6
7
8
9


B1L003S, U24-ch96, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 41.46
Std Dev 3.311
Gaus mean : 43.0332
Gaus Width: 3.6810
Entry
ampl
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1
2
3
4
5
6
7
8
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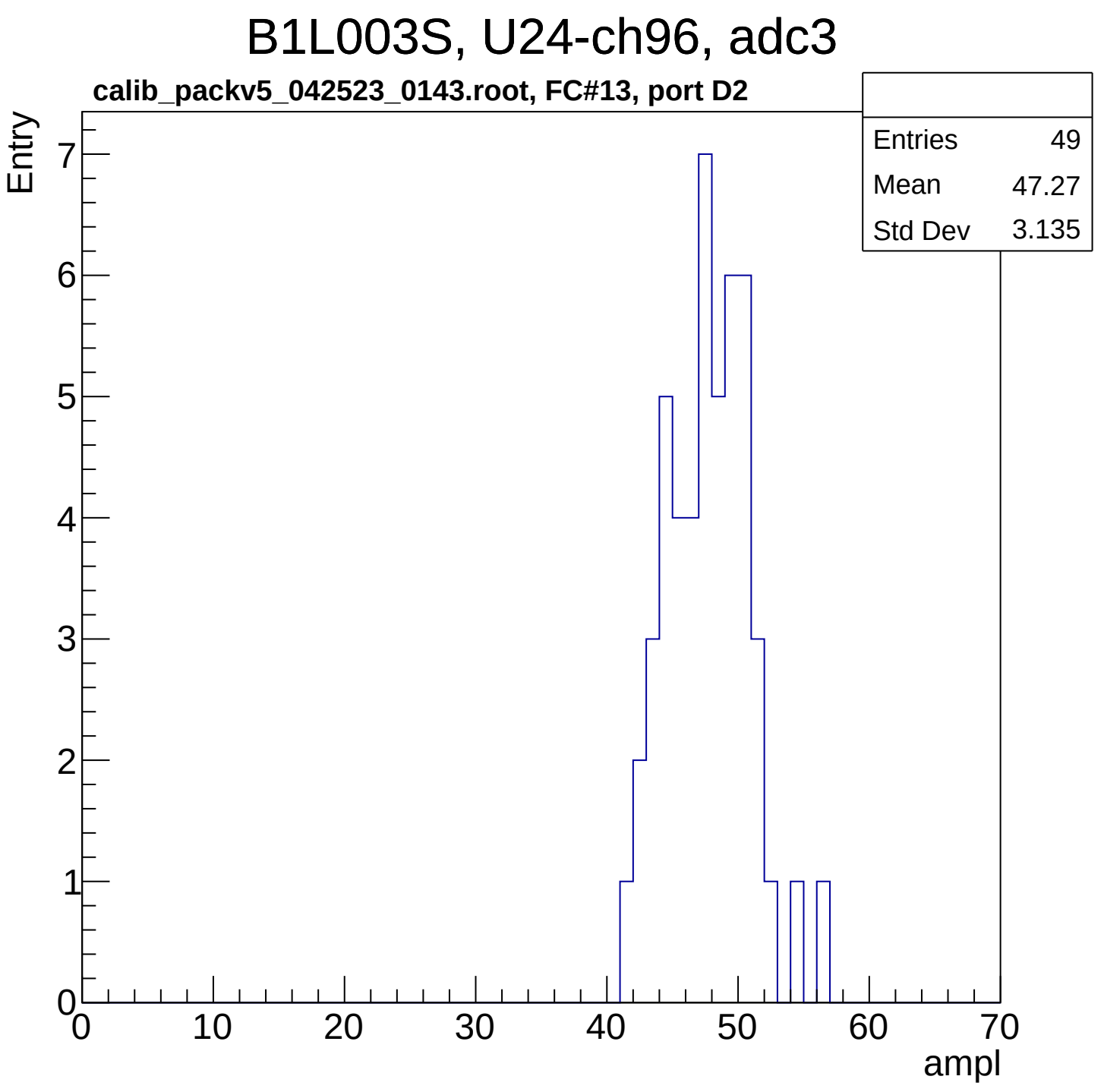
B1L003S, U24-ch96, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 47.27
Std Dev 3.135
Entry
ampl
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3
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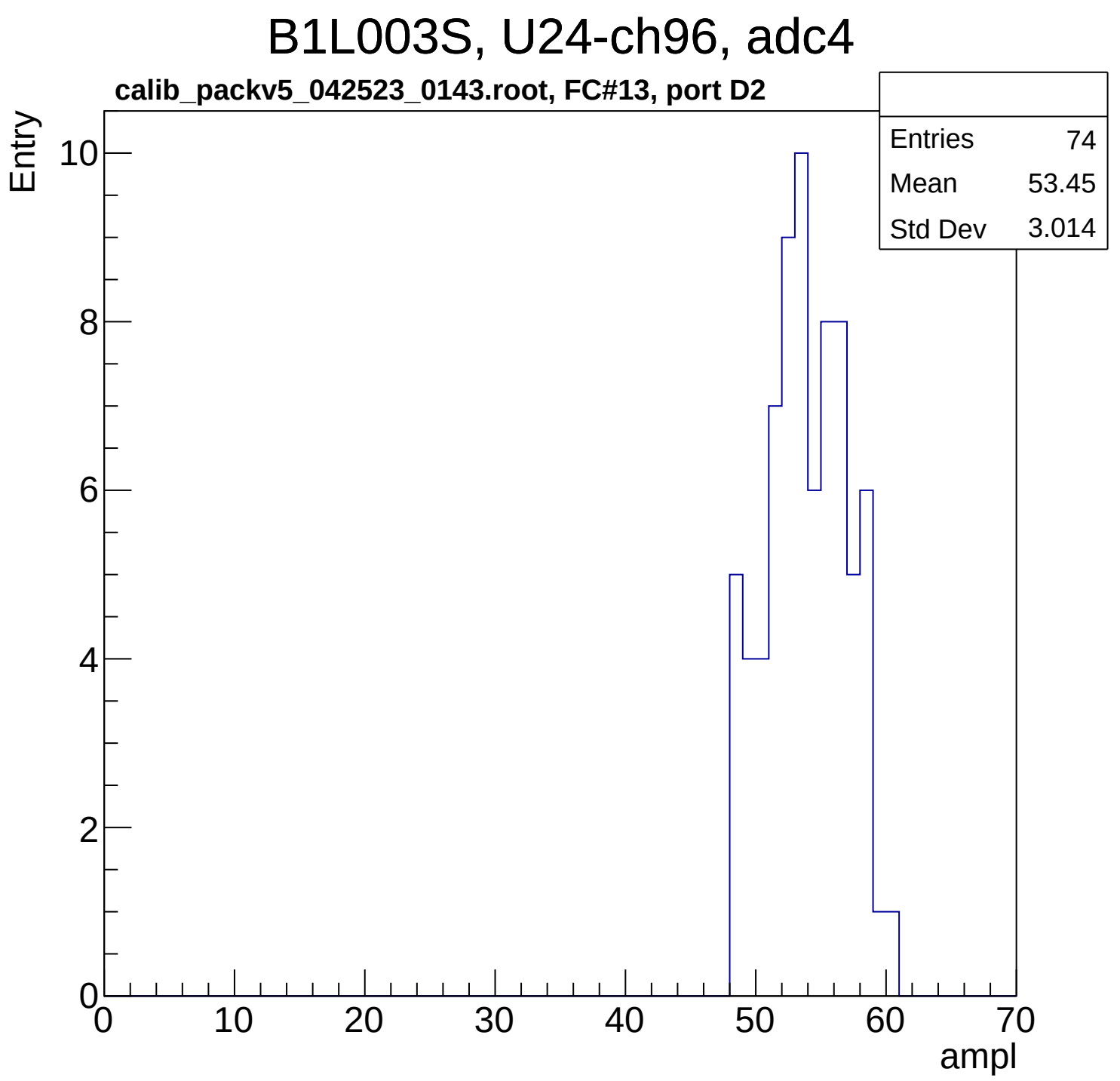
B1L003S, U24-ch96, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 53.45
Std Dev 3.014
Entry
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2
4
6
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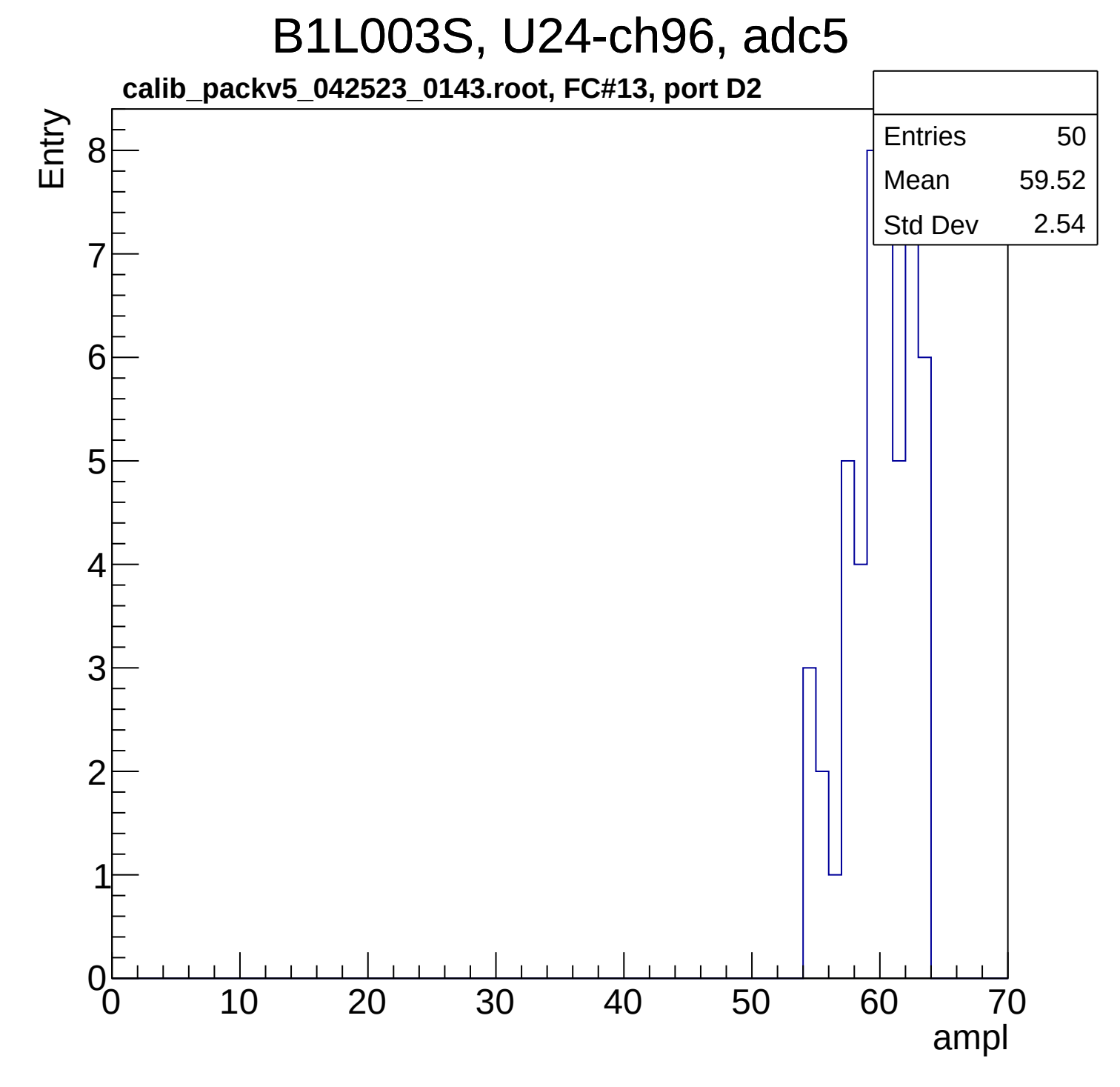
60
70
ampl


B1L003S, U24-ch96, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.52
Std Dev 2.54
Entry
ampl
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1
2
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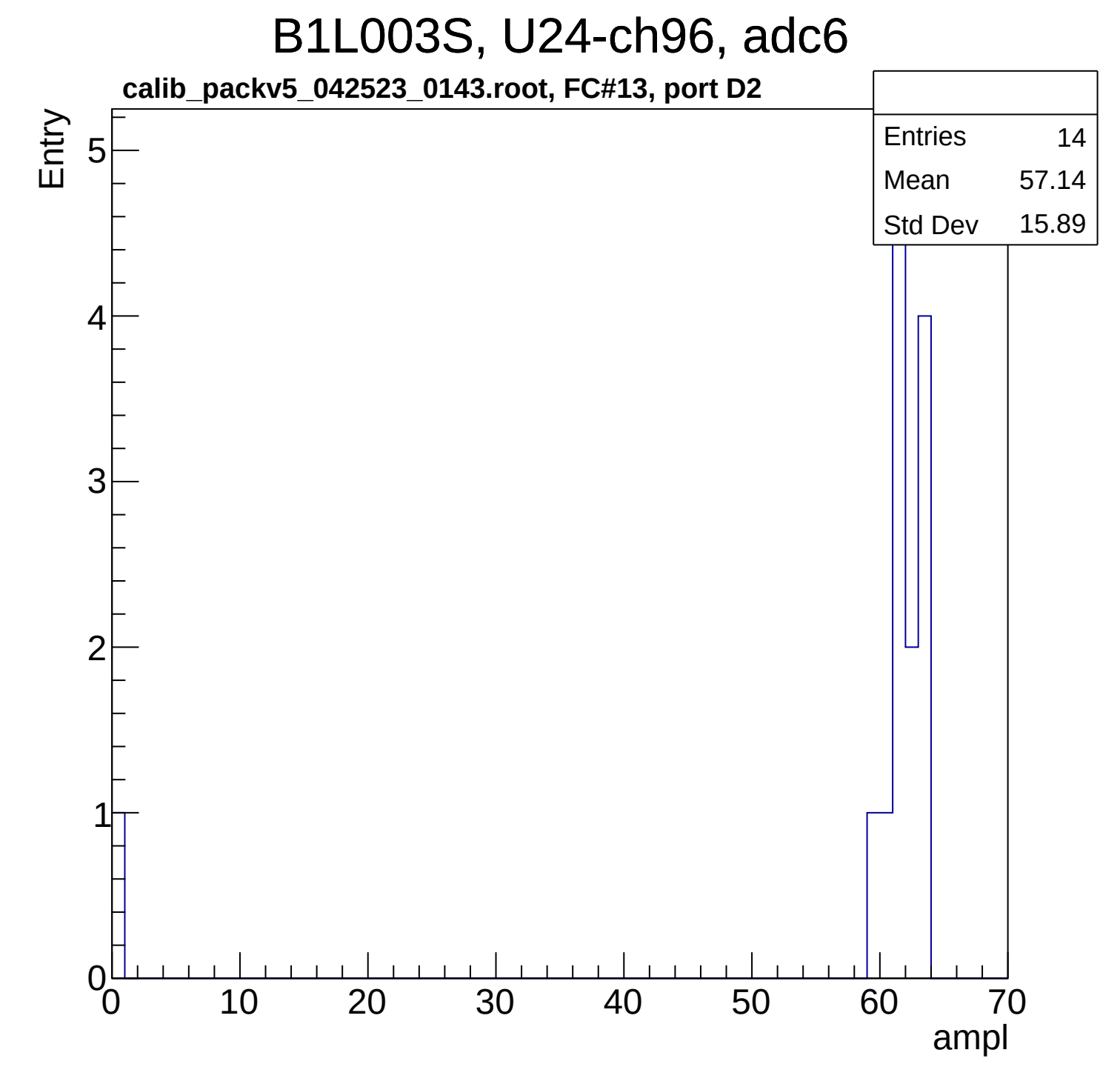
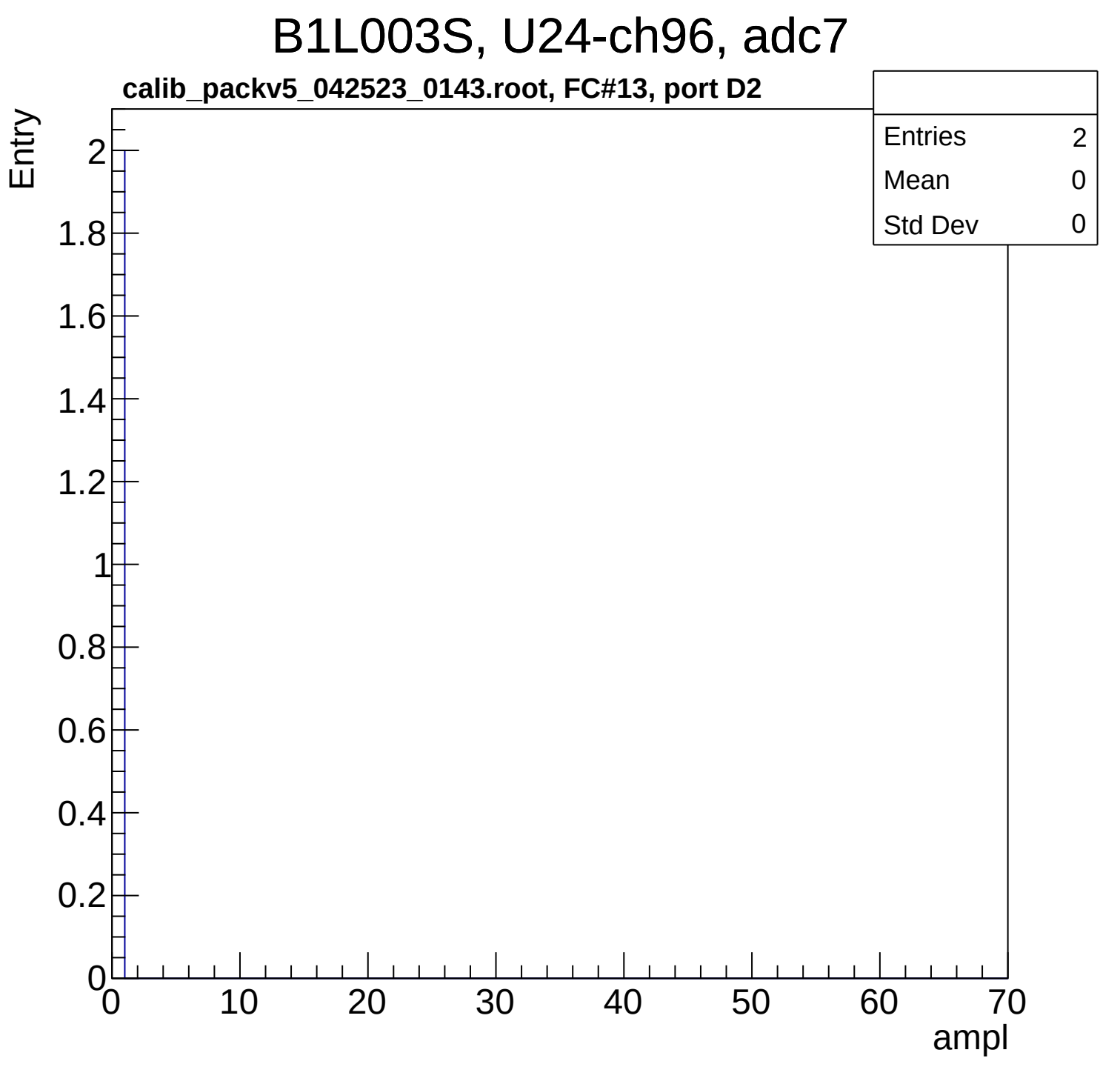

B1L003S, U24-ch96, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.14
Std Dev 15.89
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch96, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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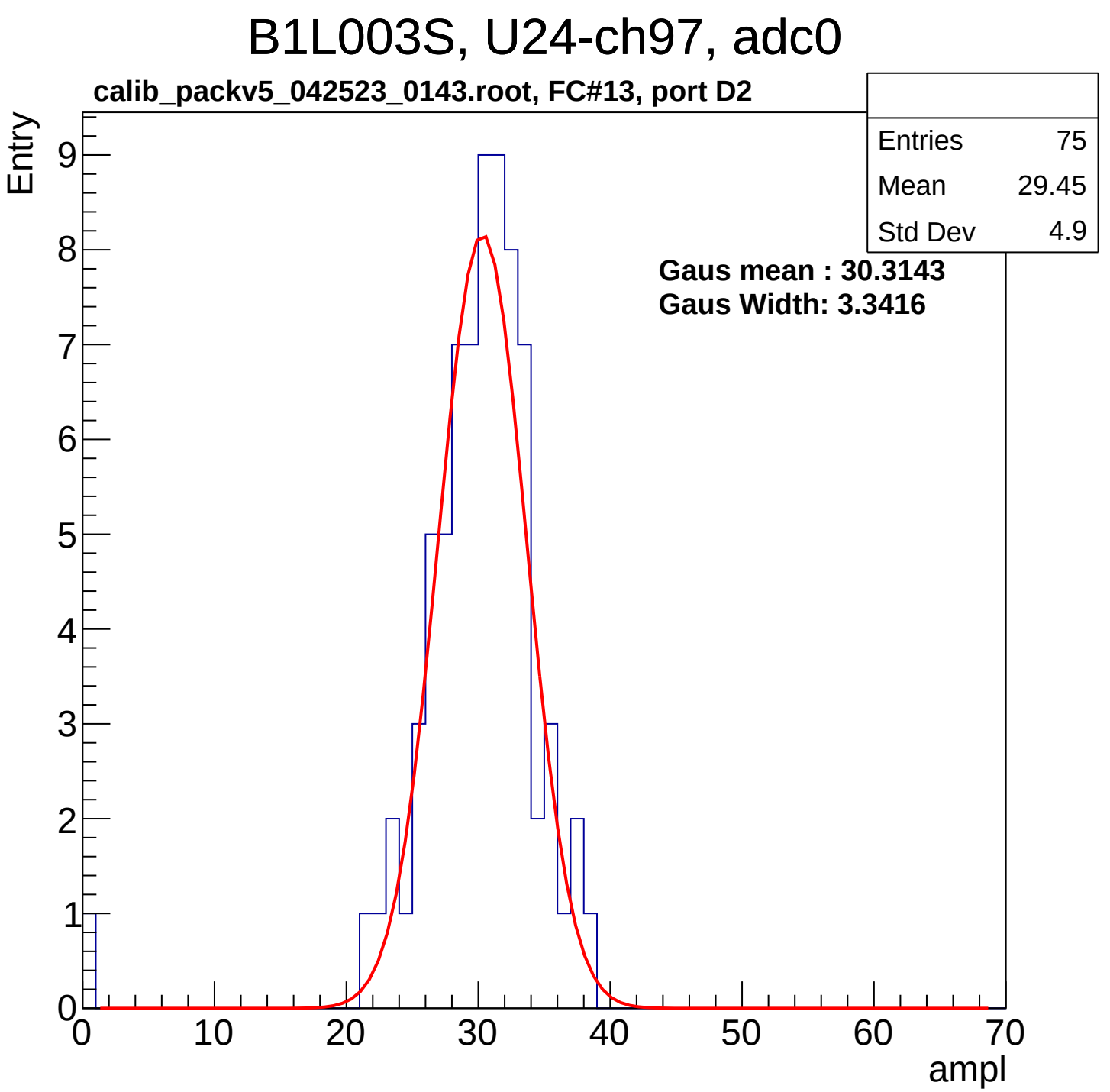
B1L003S, U24-ch97, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 29.45
Std Dev 4.9
Gaus mean : 30.3143
Gaus Width: 3.3416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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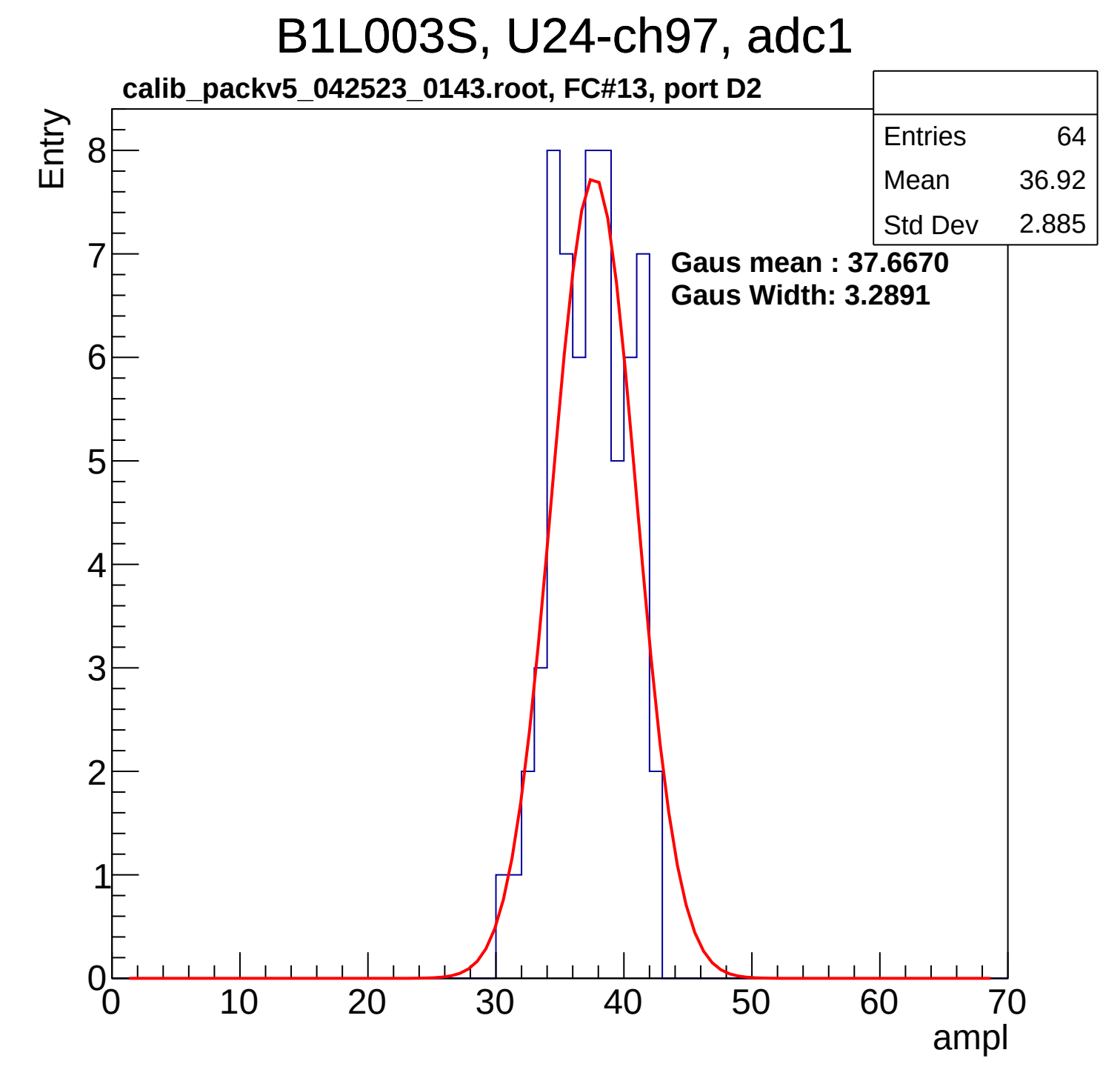
B1L003S, U24-ch97, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.92
Std Dev 2.885
Gaus mean : 37.6670
Gaus Width: 3.2891
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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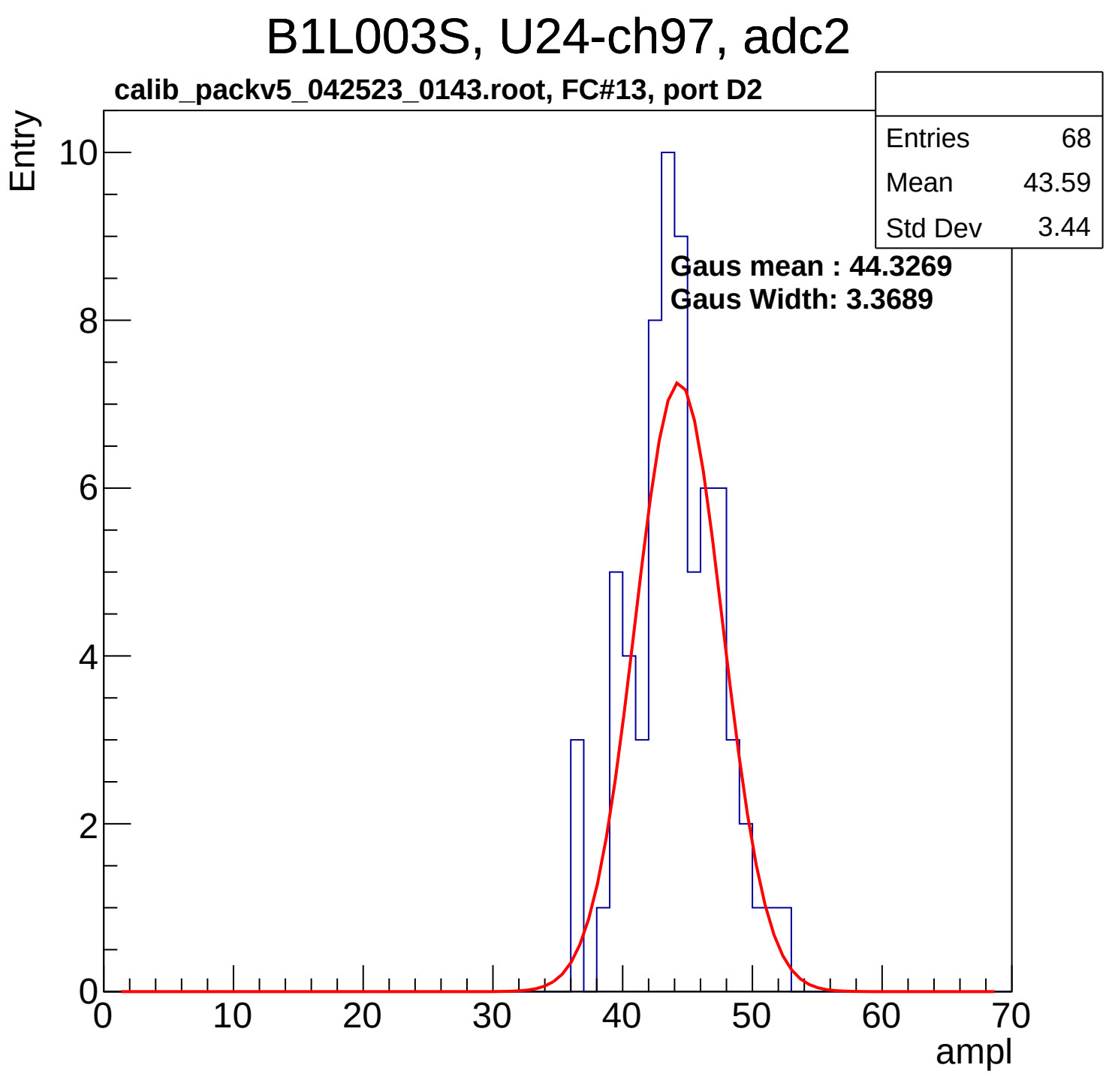
B1L003S, U24-ch97, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 43.59
Std Dev 3.44
Gaus mean : 44.3269
Gaus Width: 3.3689
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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60
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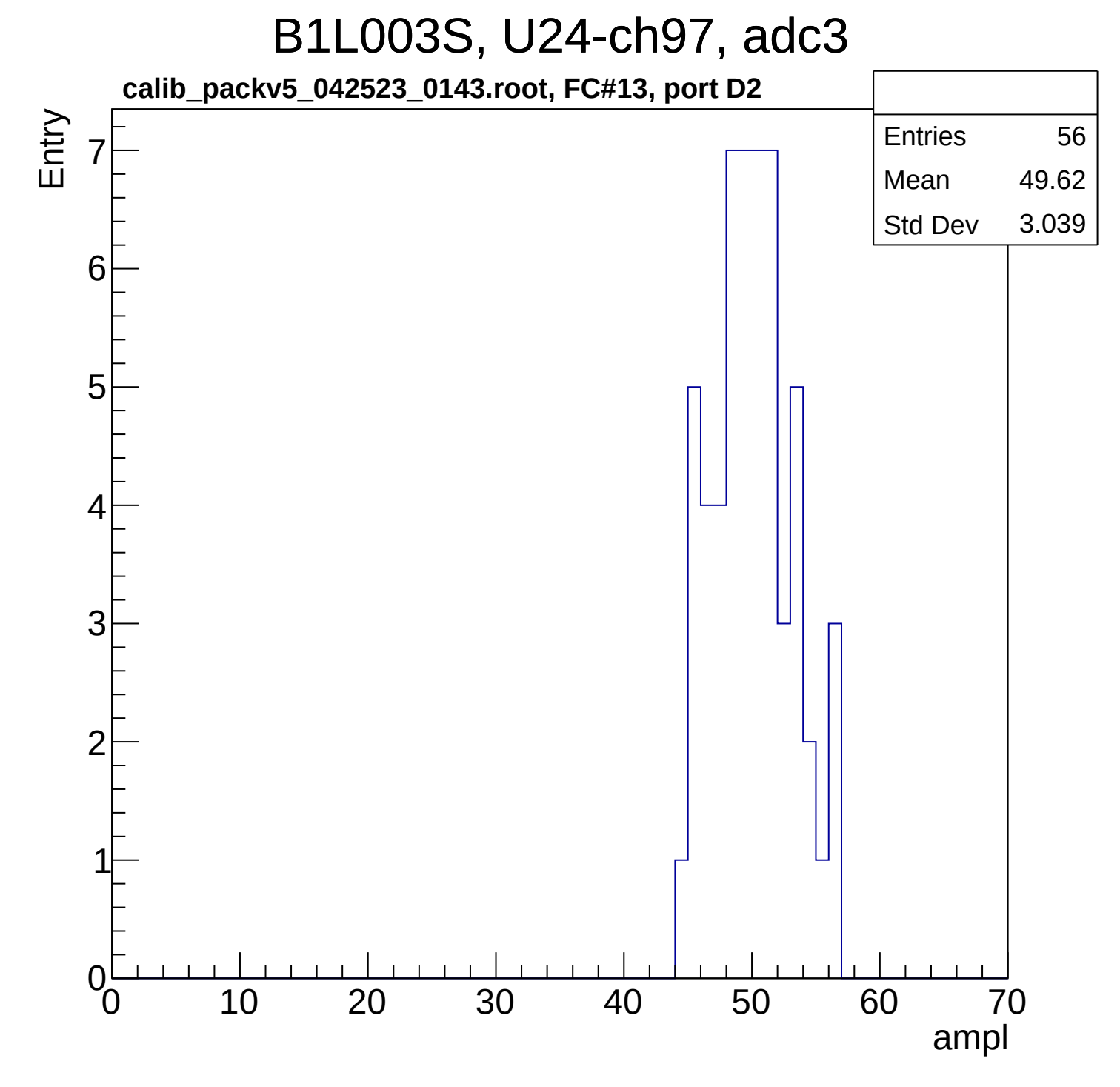
B1L003S, U24-ch97, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 49.62
Std Dev 3.039
Entry
ampl
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1
2
3
4
5
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60
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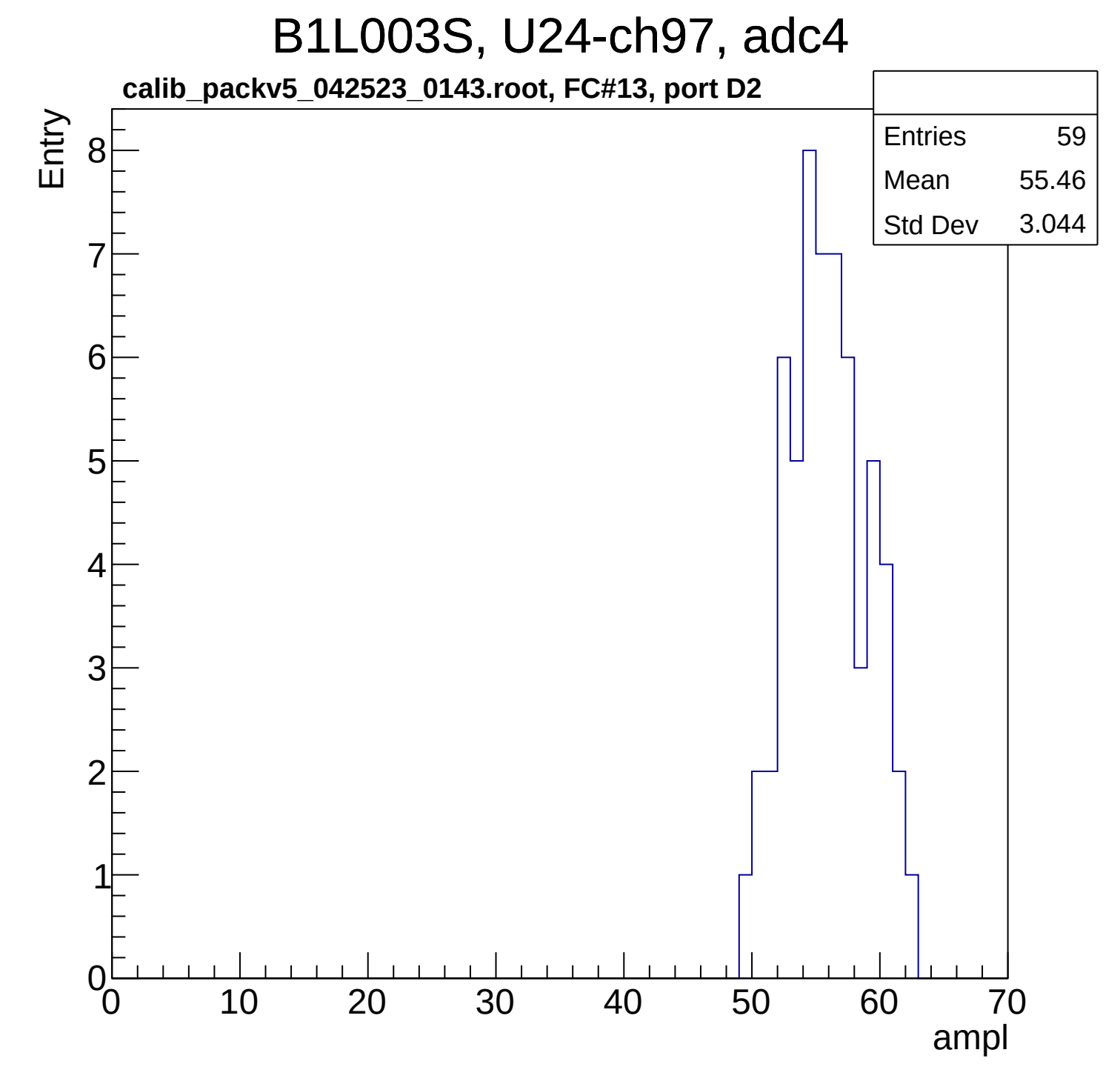
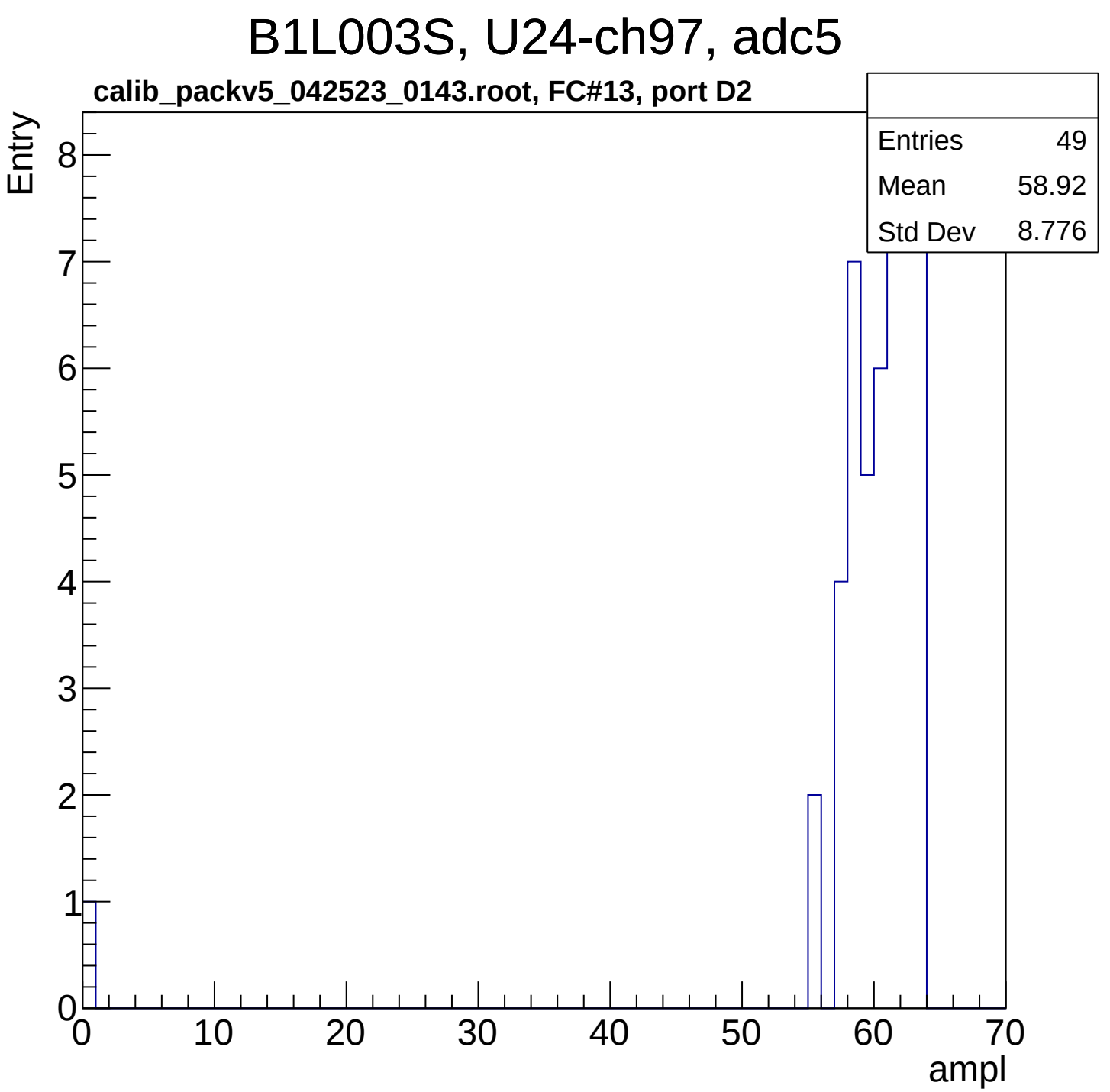

B1L003S, U24-ch97, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.46
Std Dev 3.044
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch97, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.92
Std Dev 8.776
Entry
0
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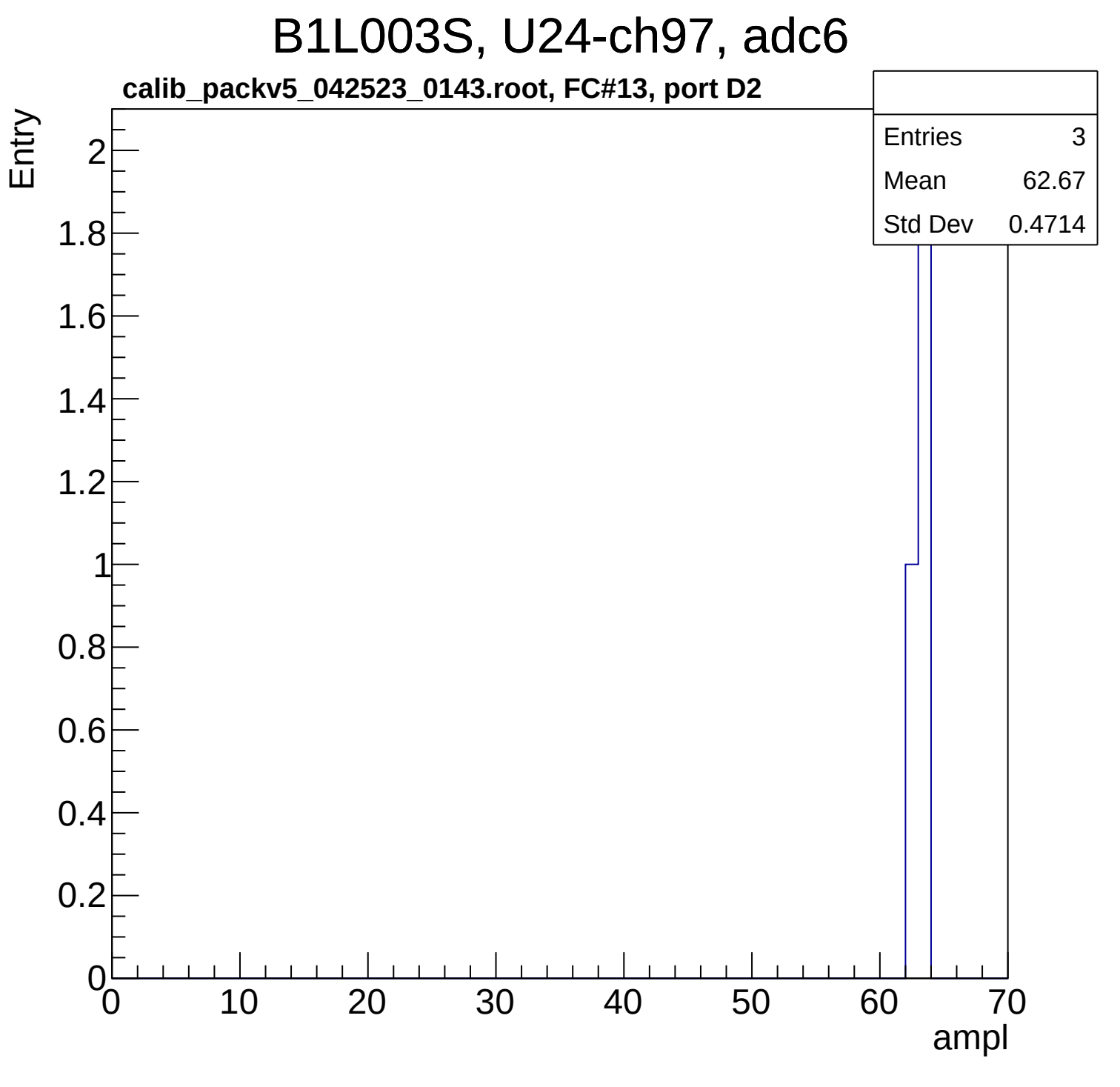
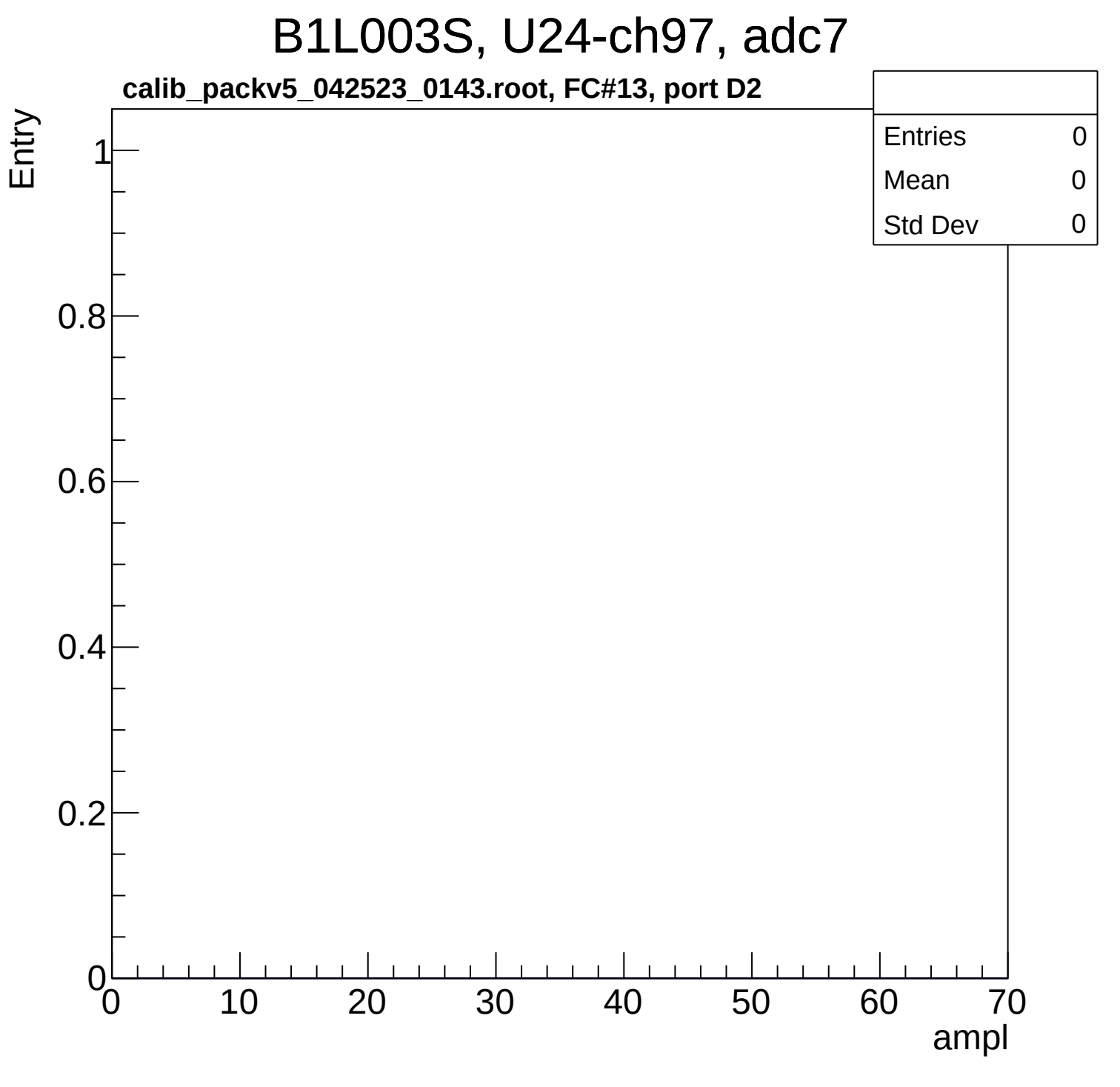
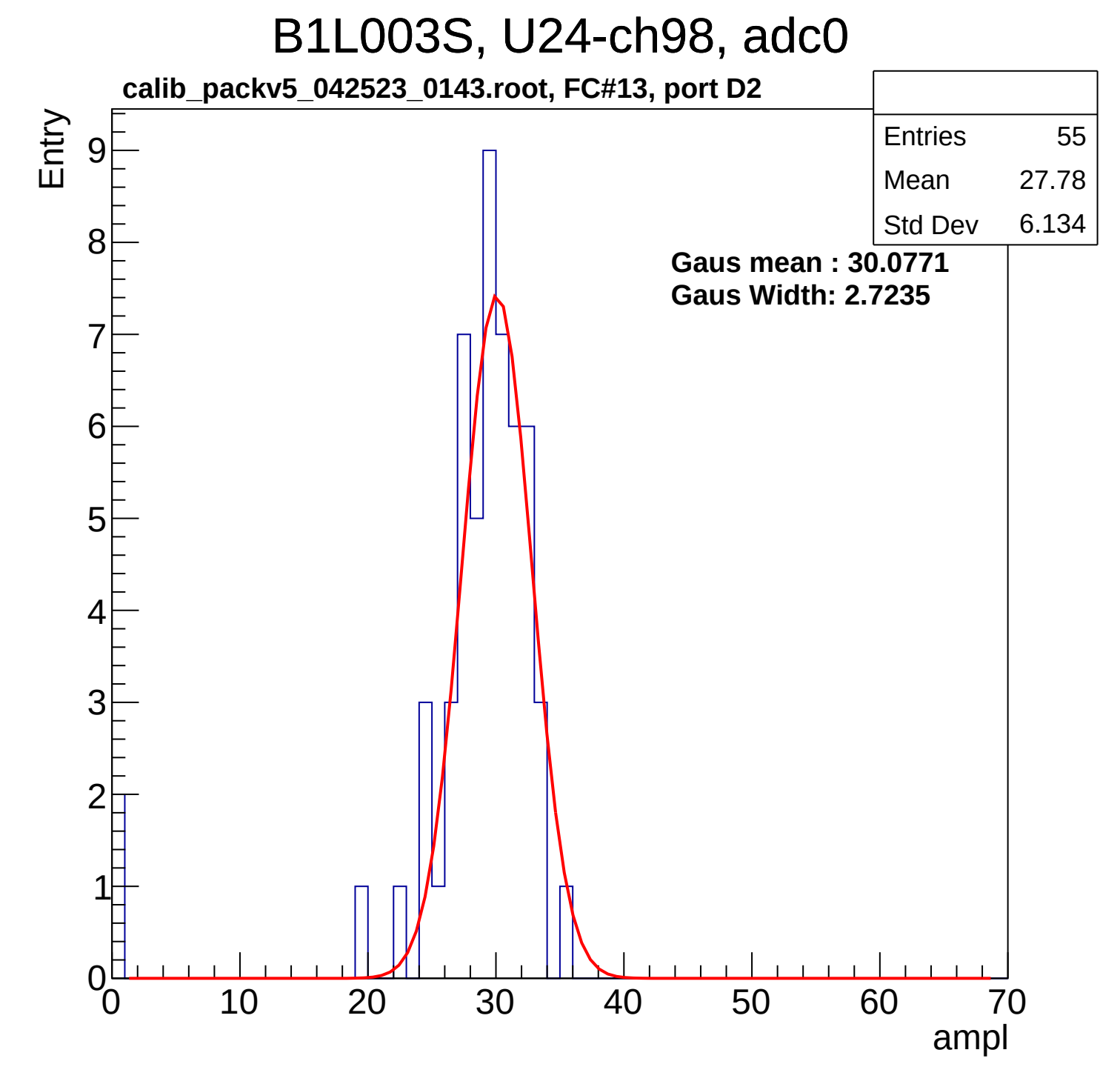
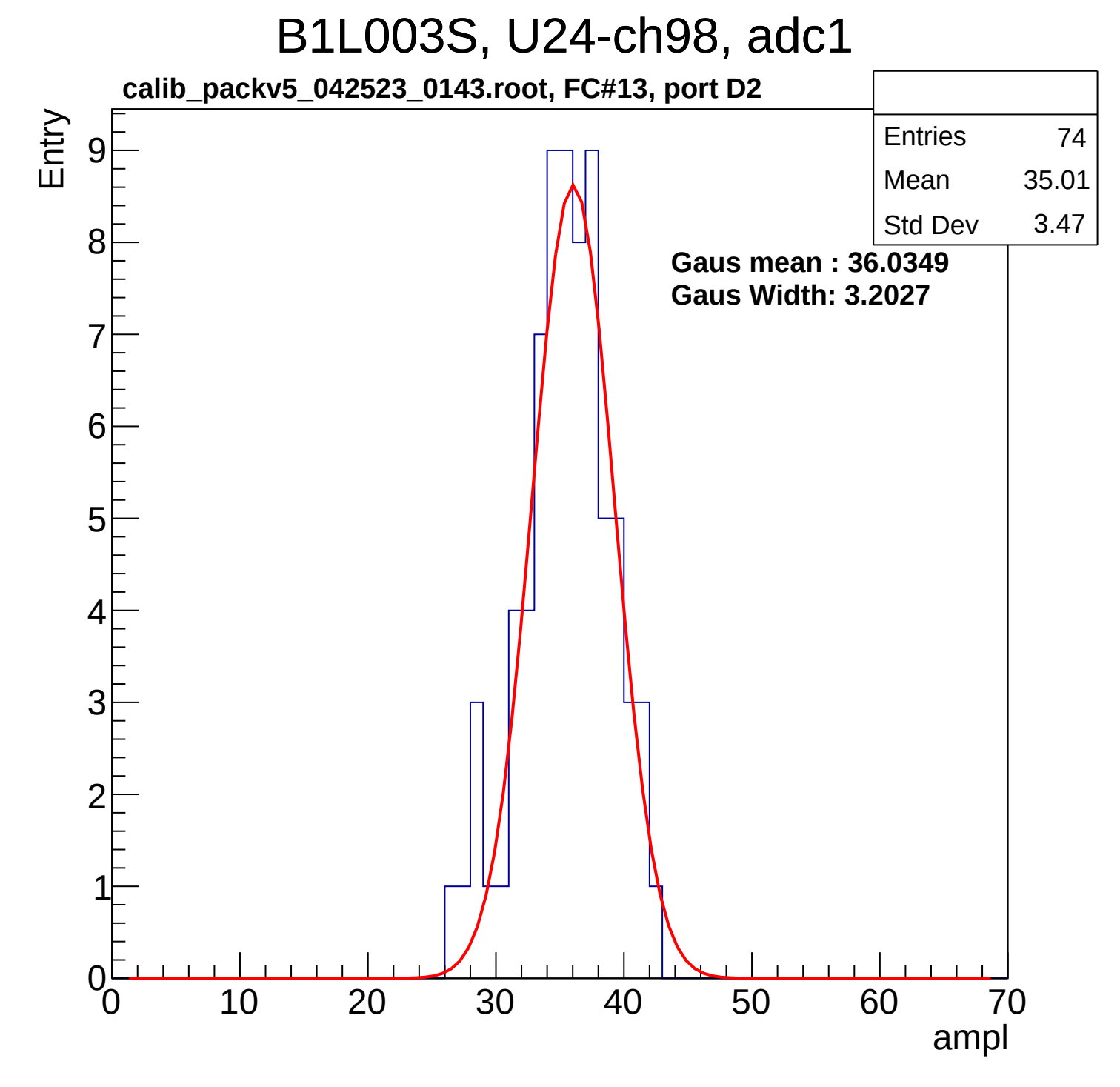
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch97, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch97, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch98, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 27.78
Std Dev 6.134
Gaus mean : 30.0771
Gaus Width: 2.7235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch98, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 35.01
Std Dev 3.47
Gaus mean : 36.0349
Gaus Width: 3.2027
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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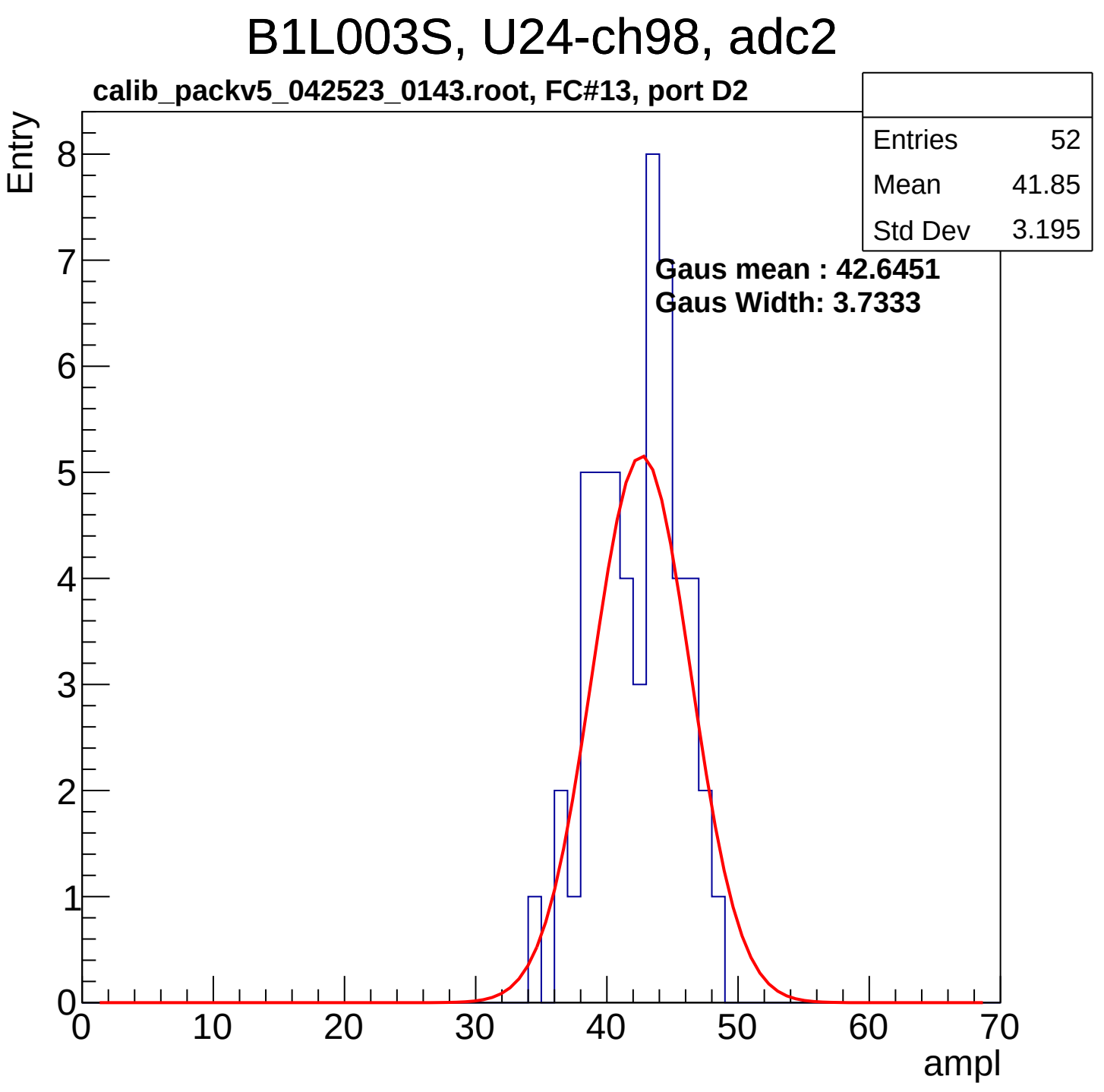
B1L003S, U24-ch98, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 41.85
Std Dev 3.195
Gaus mean : 42.6451
Gaus Width: 3.7333
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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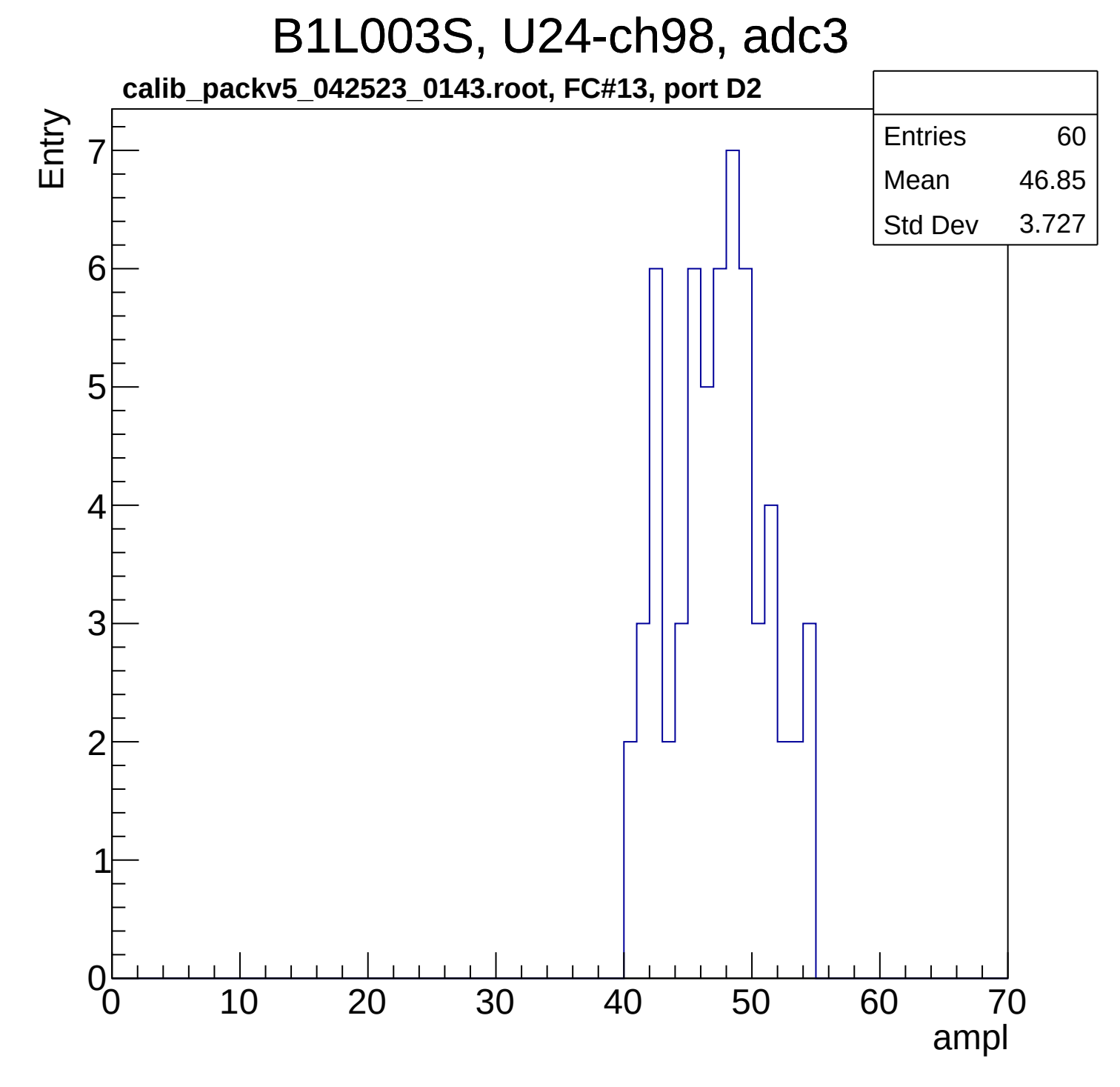
B1L003S, U24-ch98, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 46.85
Std Dev 3.727
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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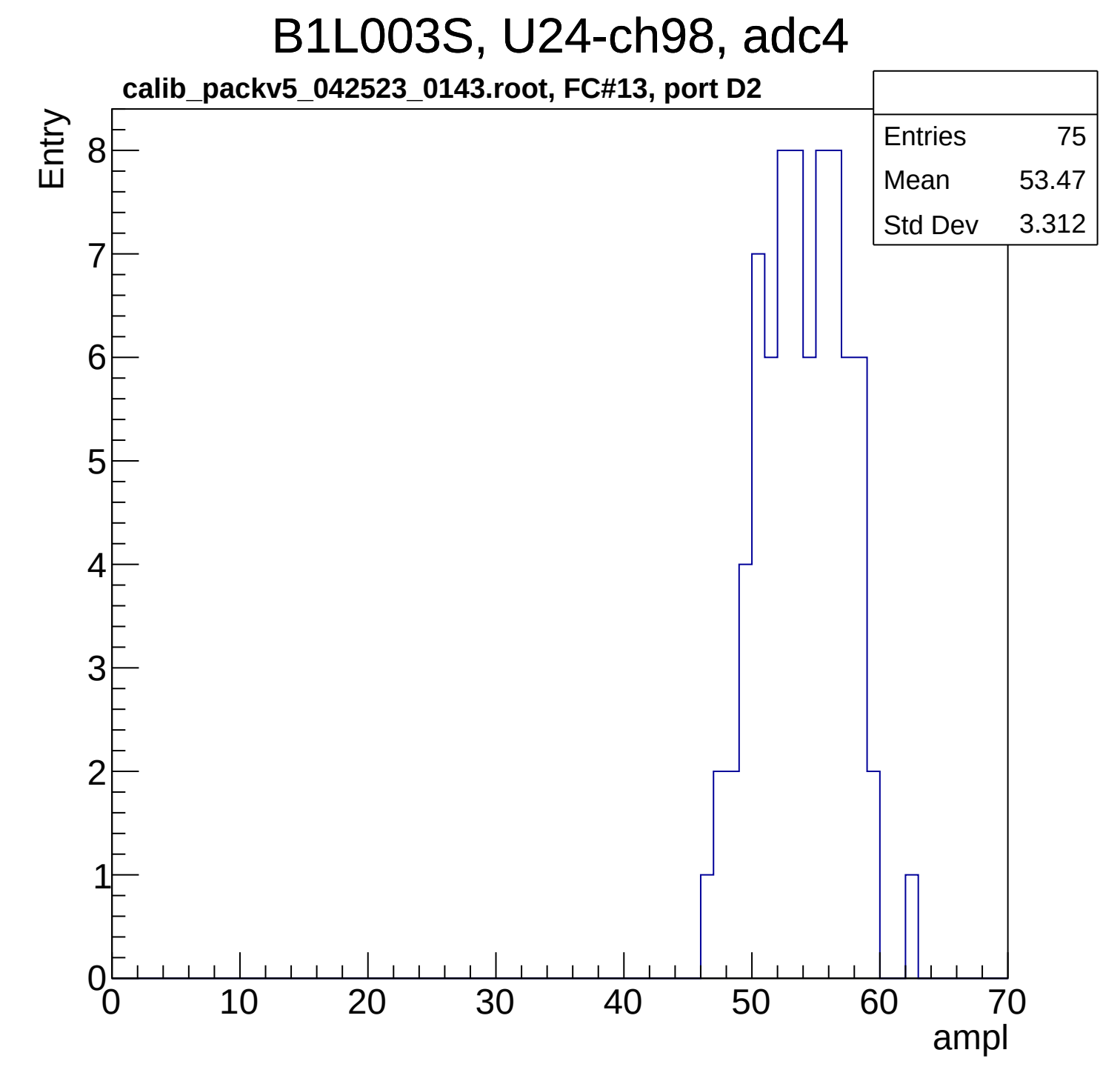
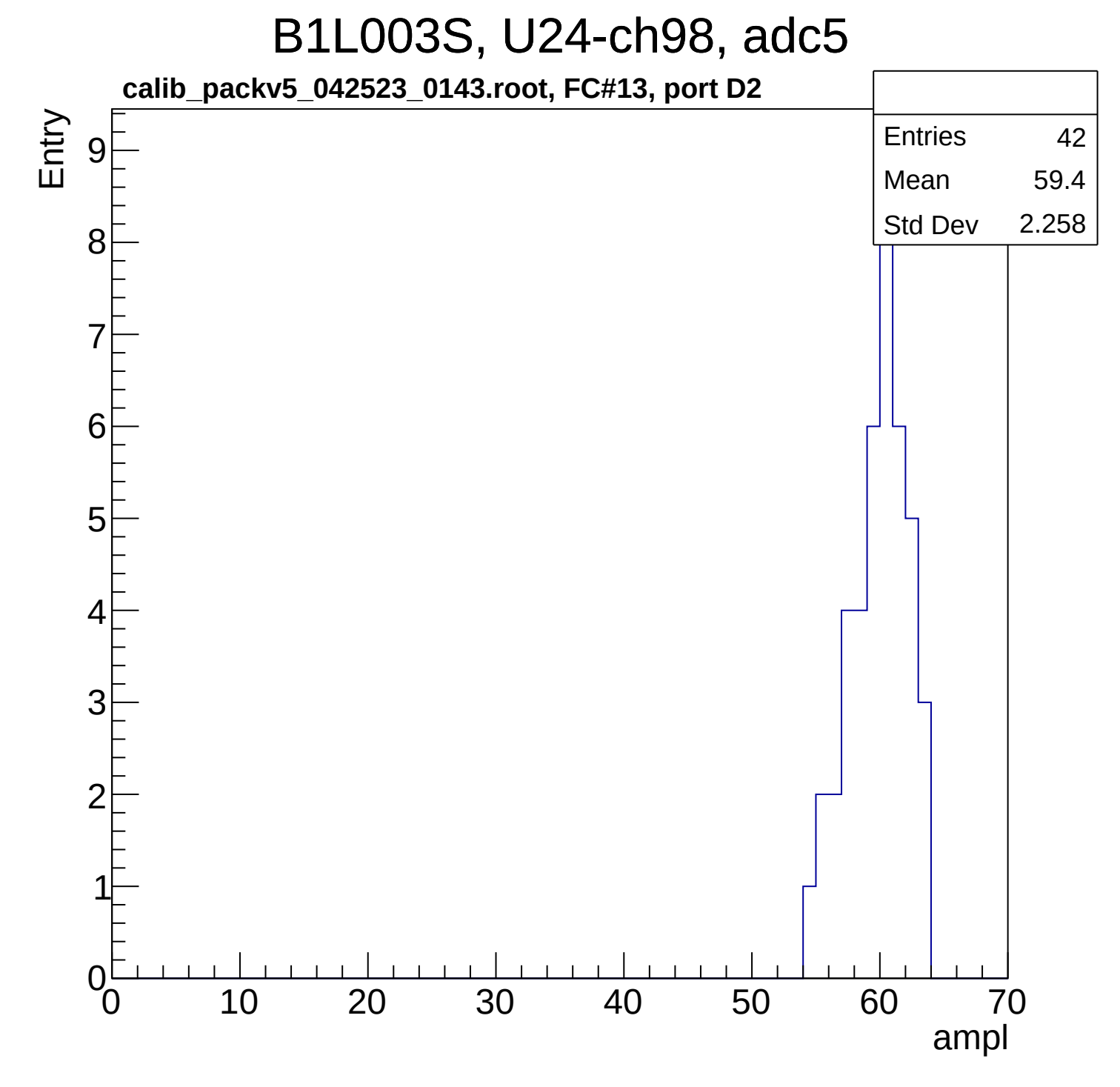

B1L003S, U24-ch98, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 53.47
Std Dev 3.312
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch98, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 59.4
Std Dev 2.258
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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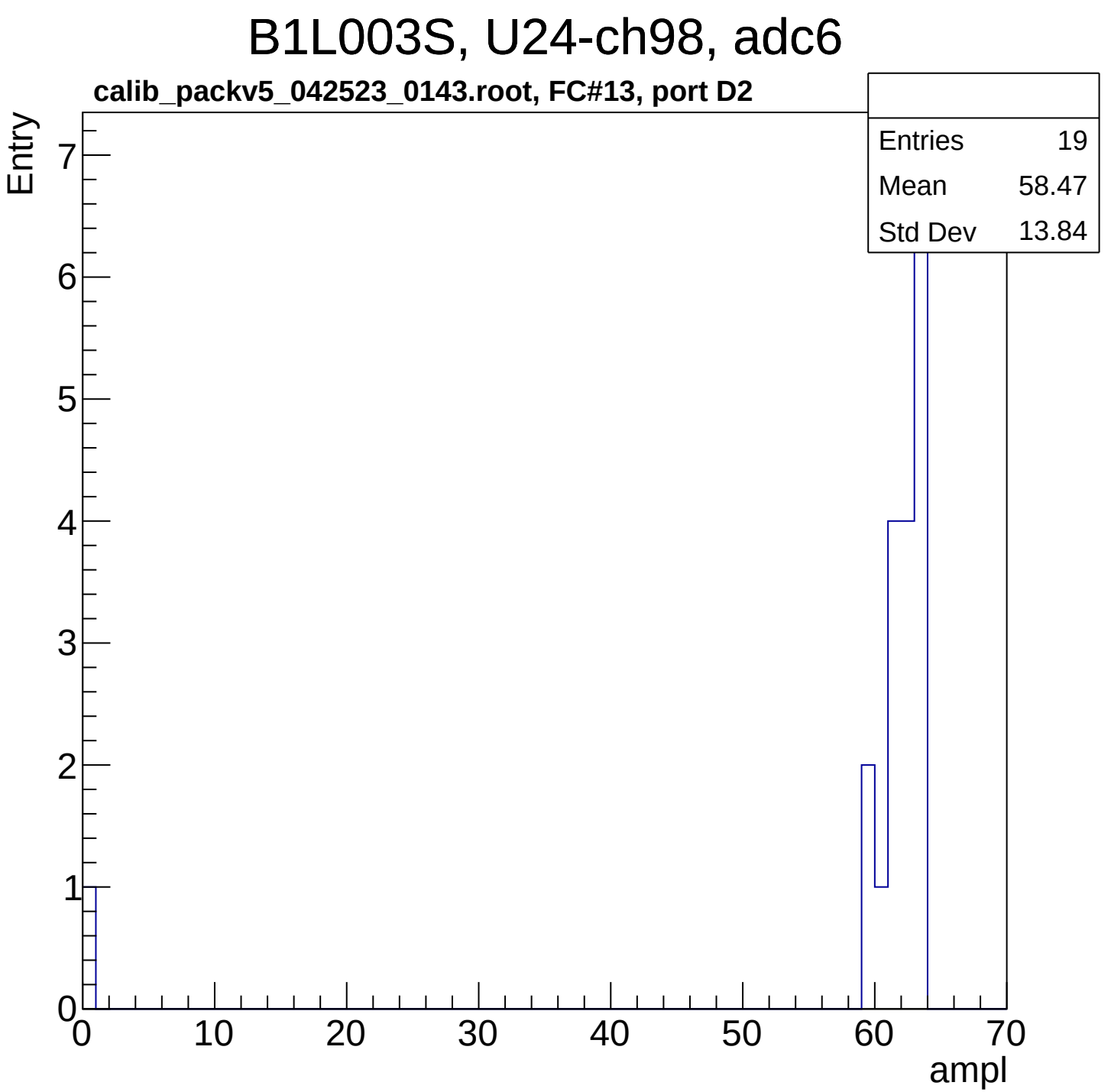
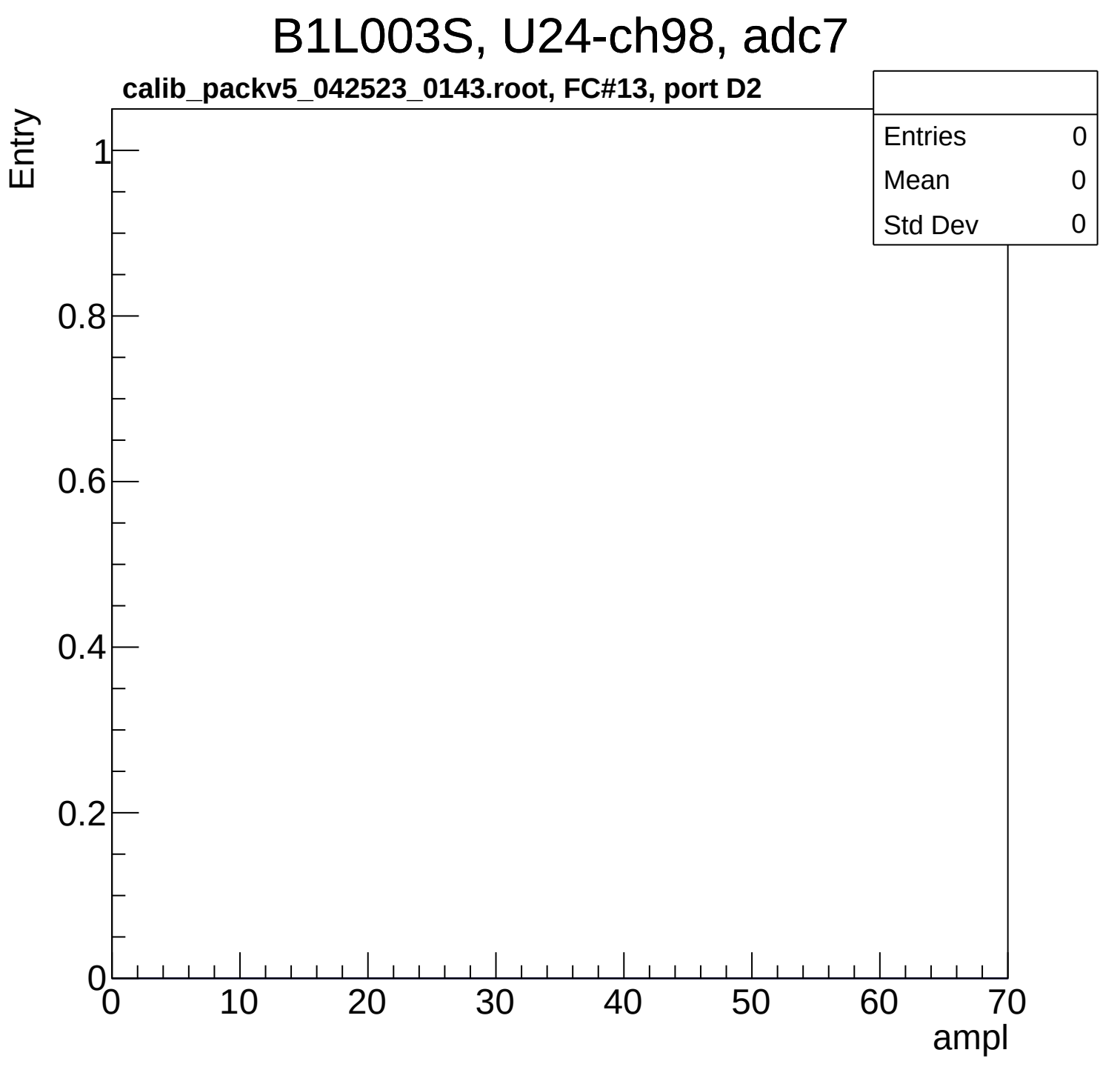

B1L003S, U24-ch98, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 58.47
Std Dev 13.84
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch98, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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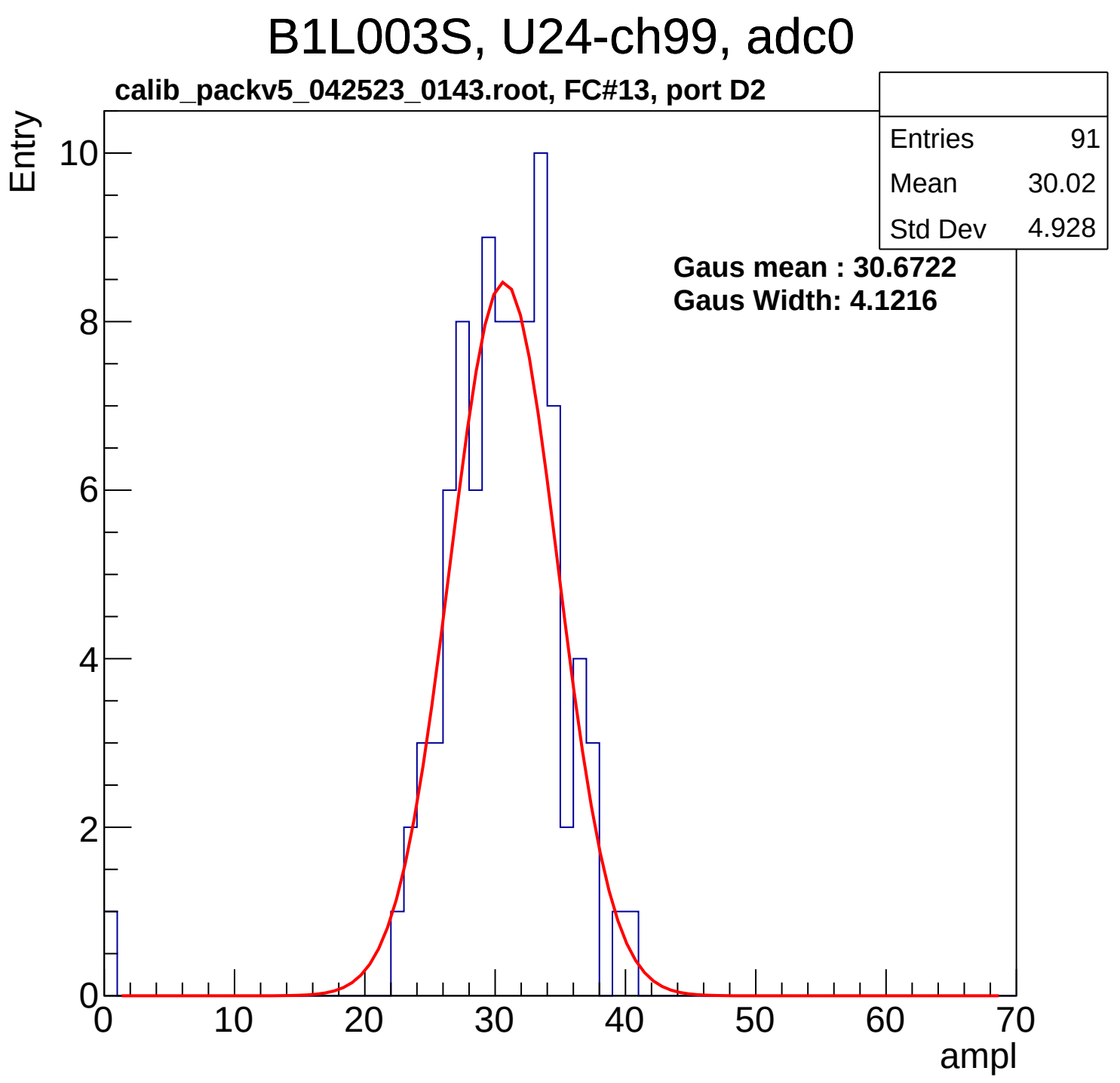
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch99, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 91
Mean 30.02
Std Dev 4.928
Gaus mean : 30.6722
Gaus Width: 4.1216
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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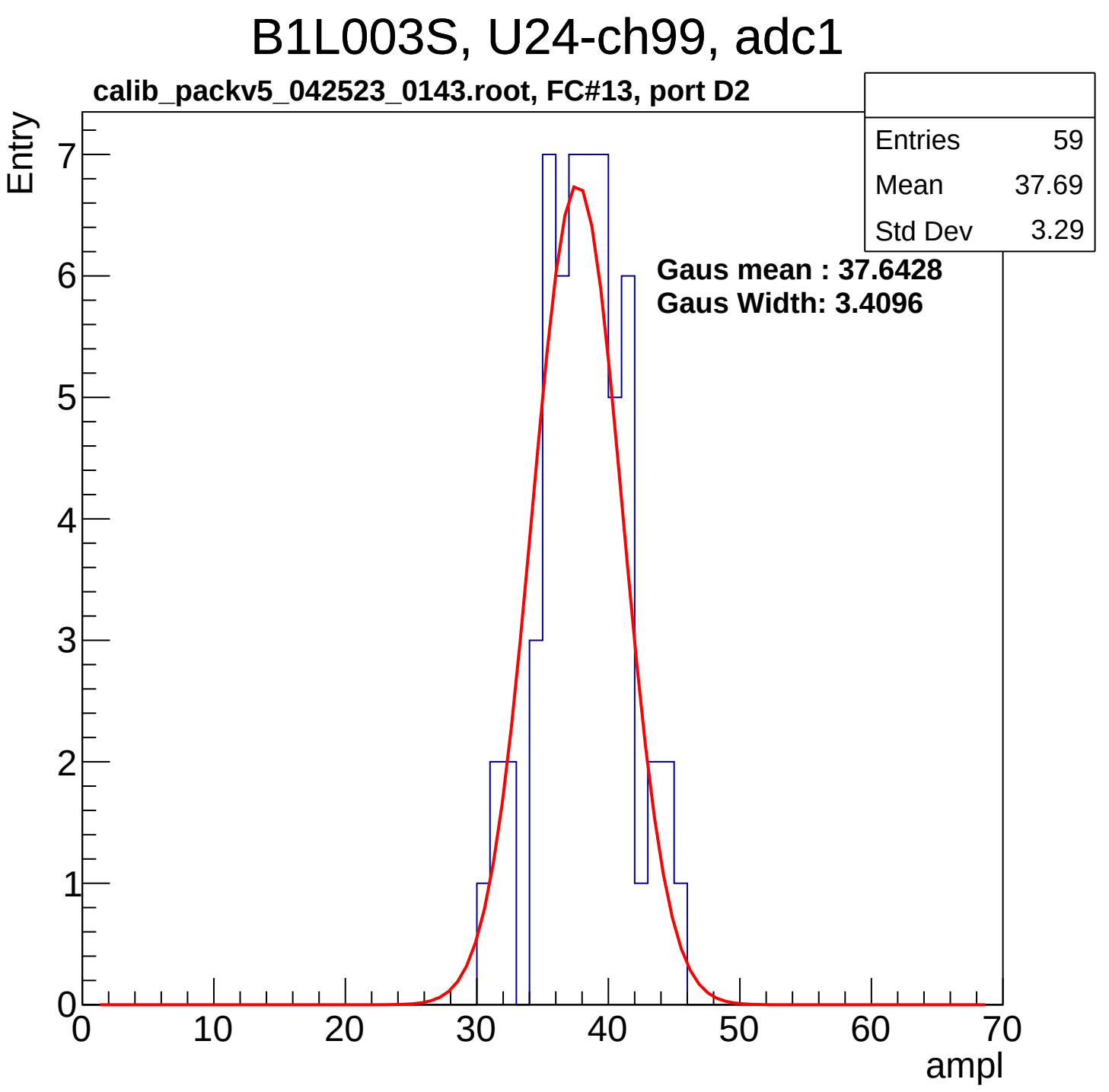
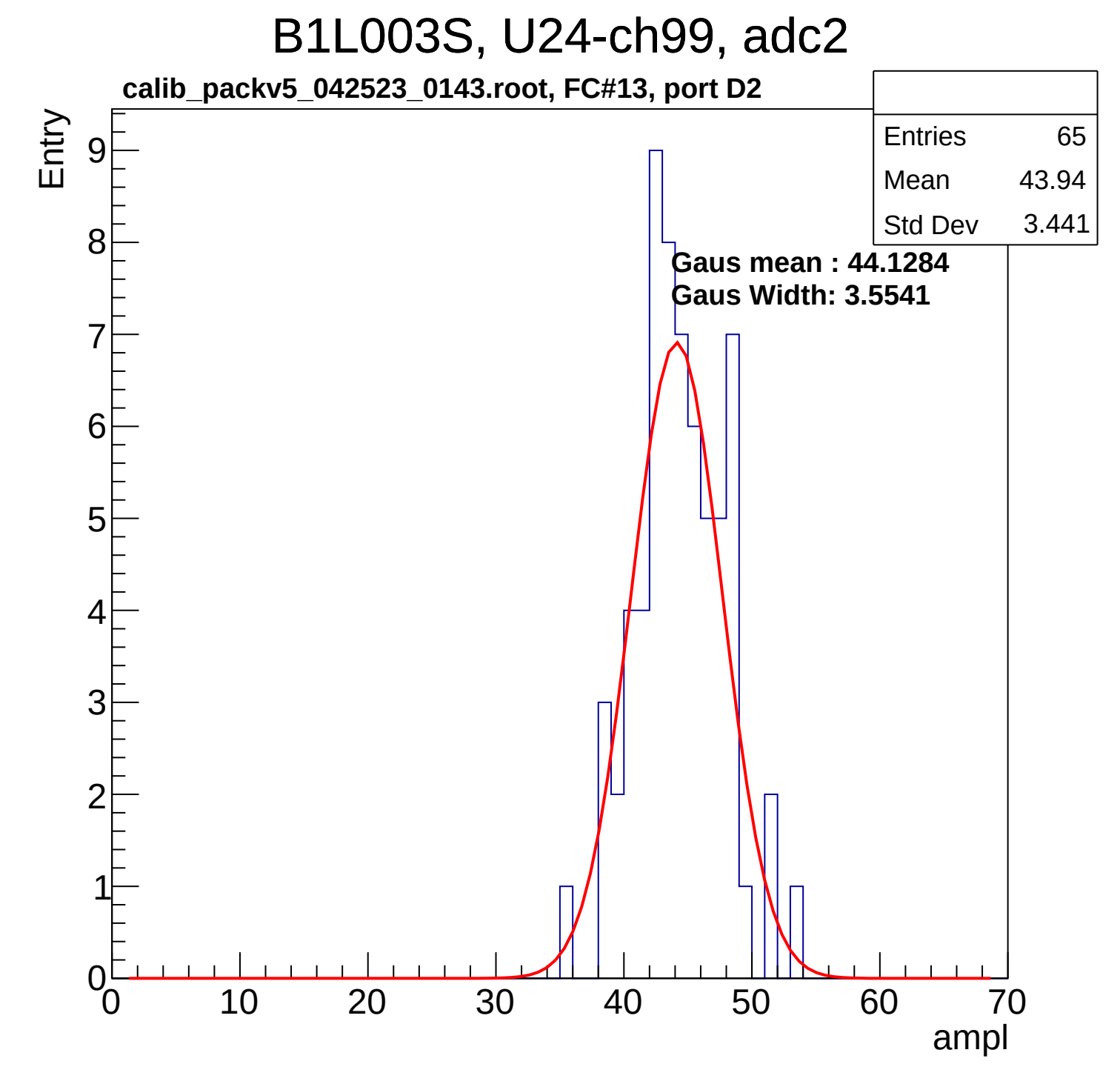

B1L003S, U24-ch99, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 37.69
Std Dev 3.29
Gaus mean : 37.6428
Gaus Width: 3.4096
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch99, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.94
Std Dev 3.441
Gaus mean : 44.1284
Gaus Width: 3.5541
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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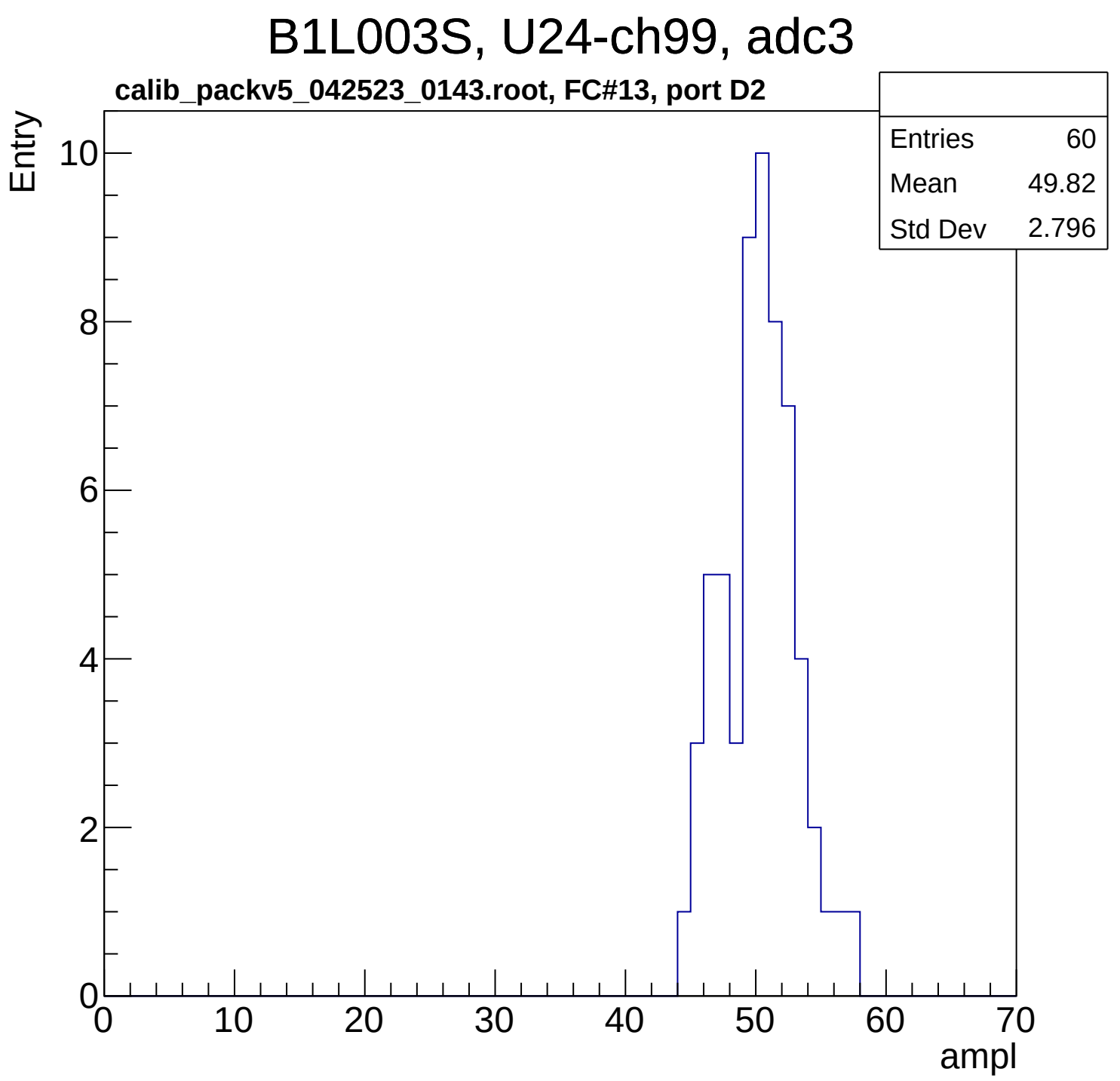
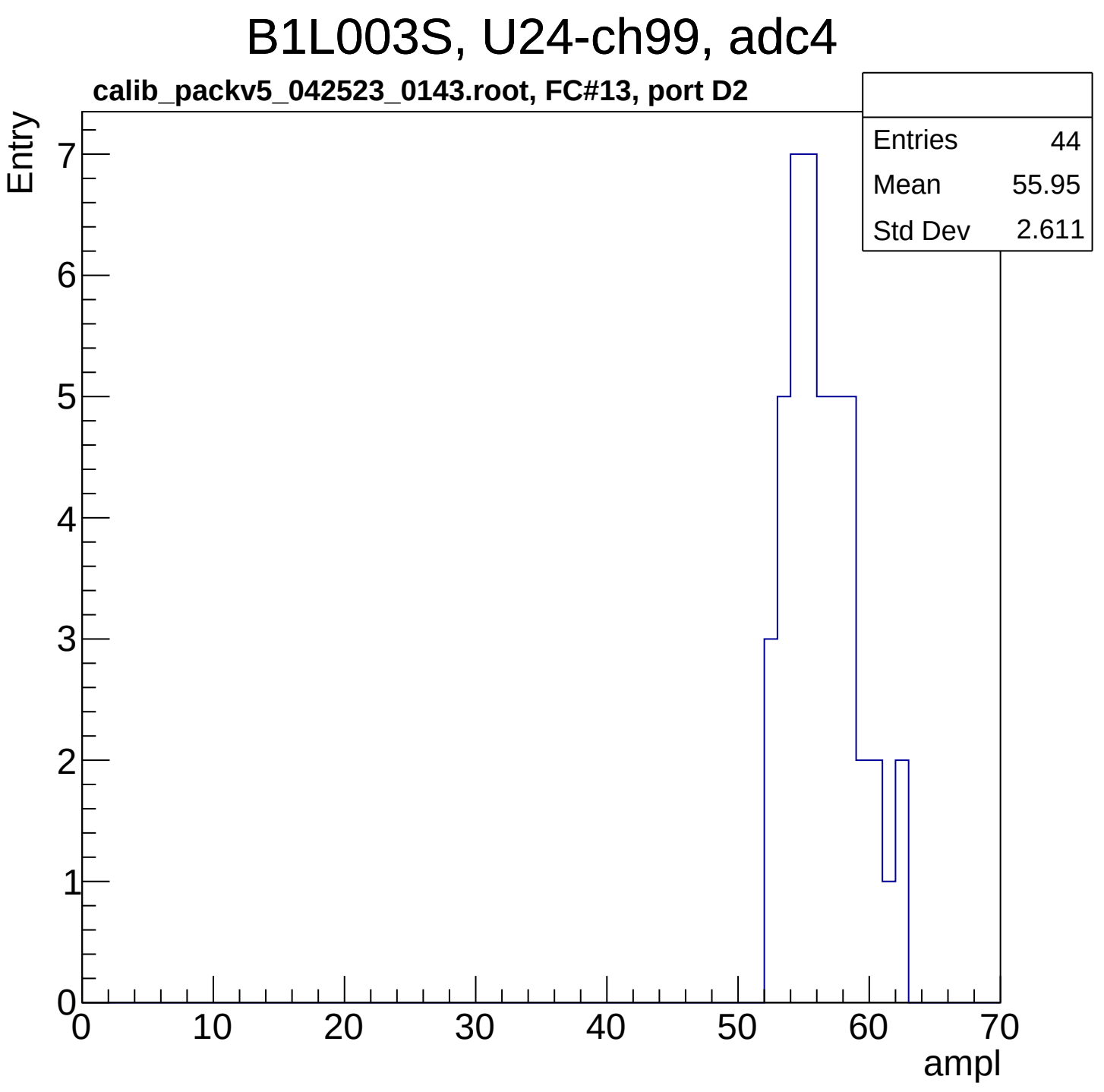

B1L003S, U24-ch99, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.82
Std Dev 2.796
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch99, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 55.95
Std Dev 2.611
Entry
ampl
0
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3
4
5
6
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0
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20
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40
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60
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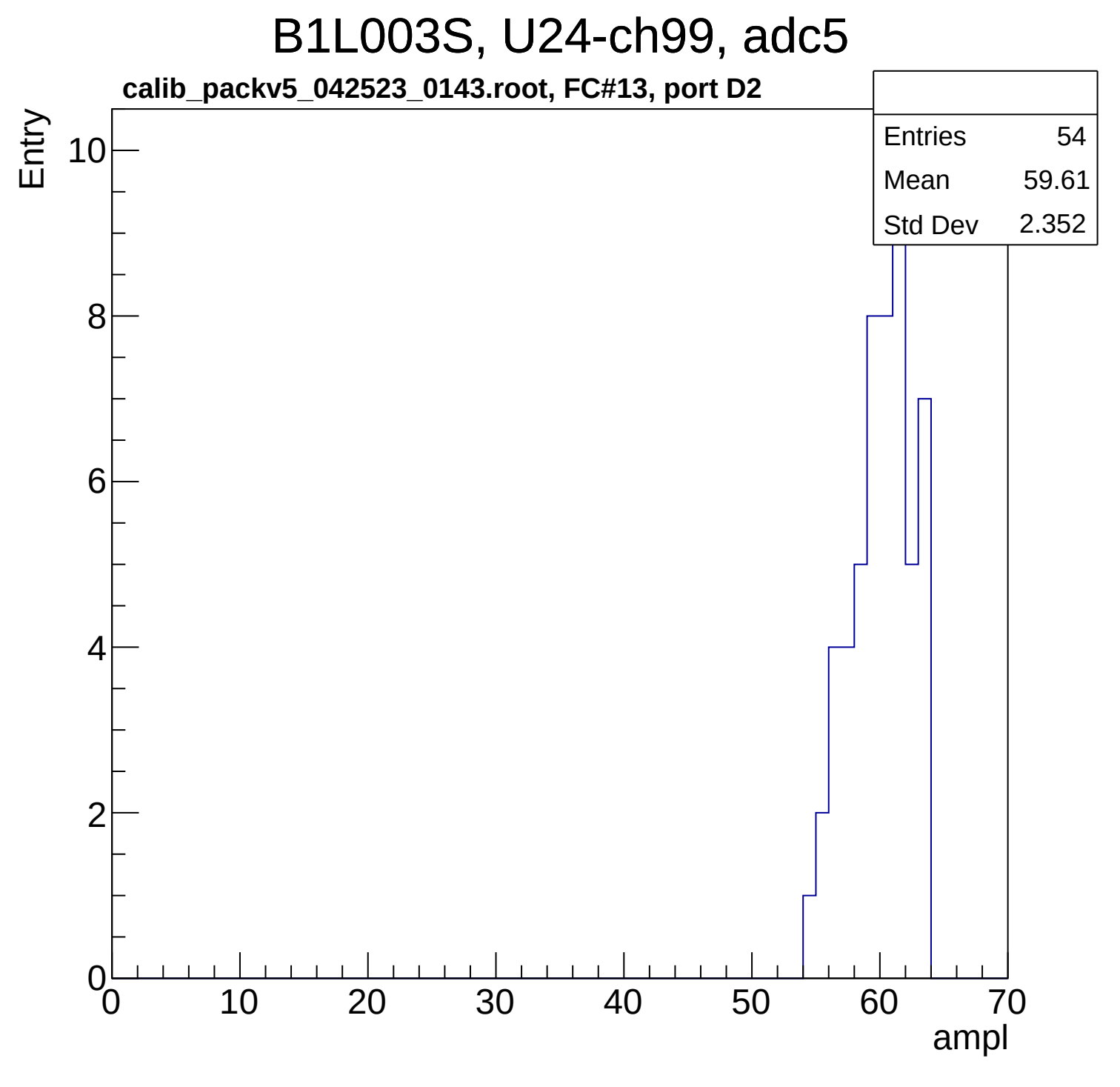
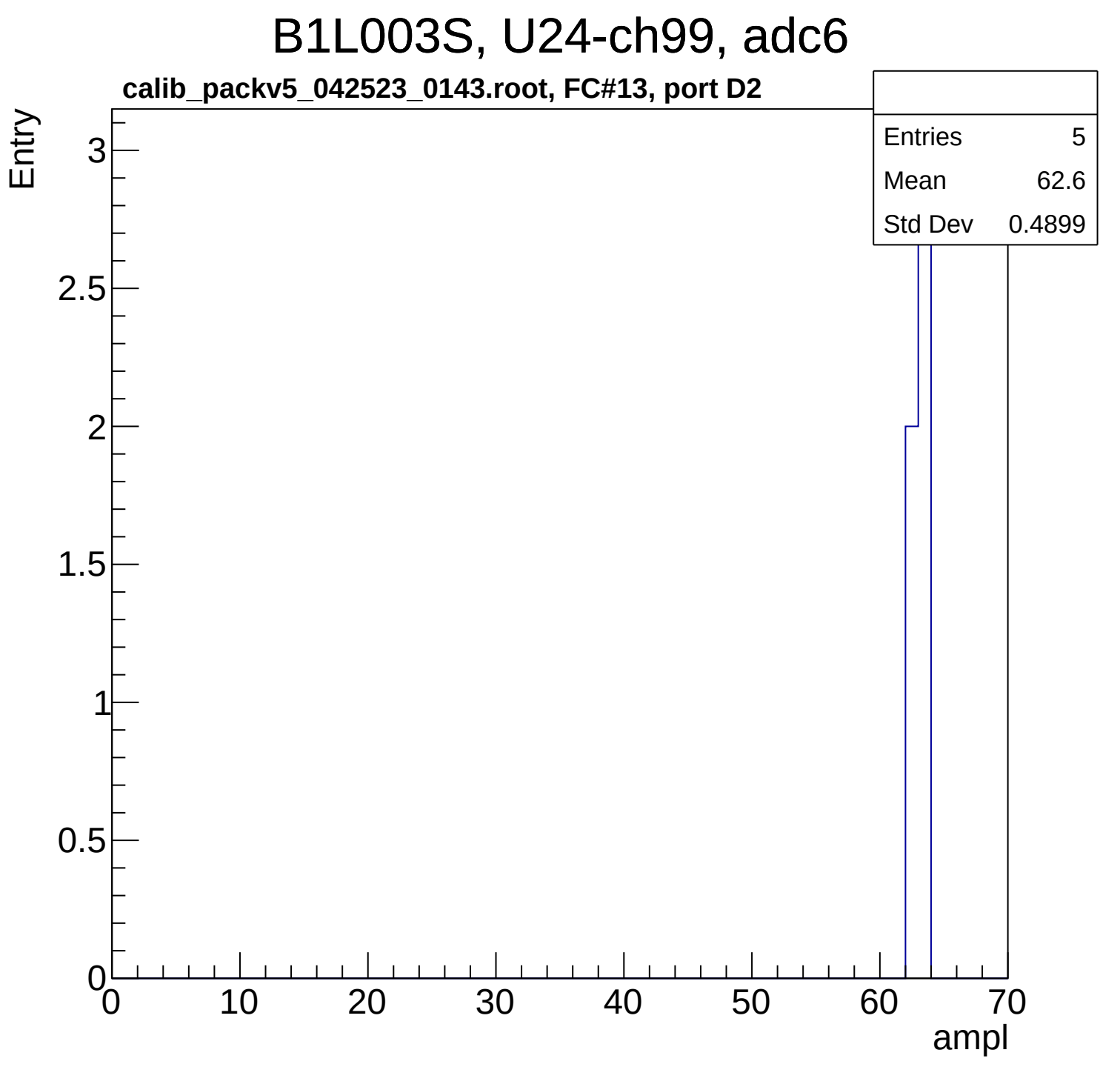

B1L003S, U24-ch99, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.61
Std Dev 2.352
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch99, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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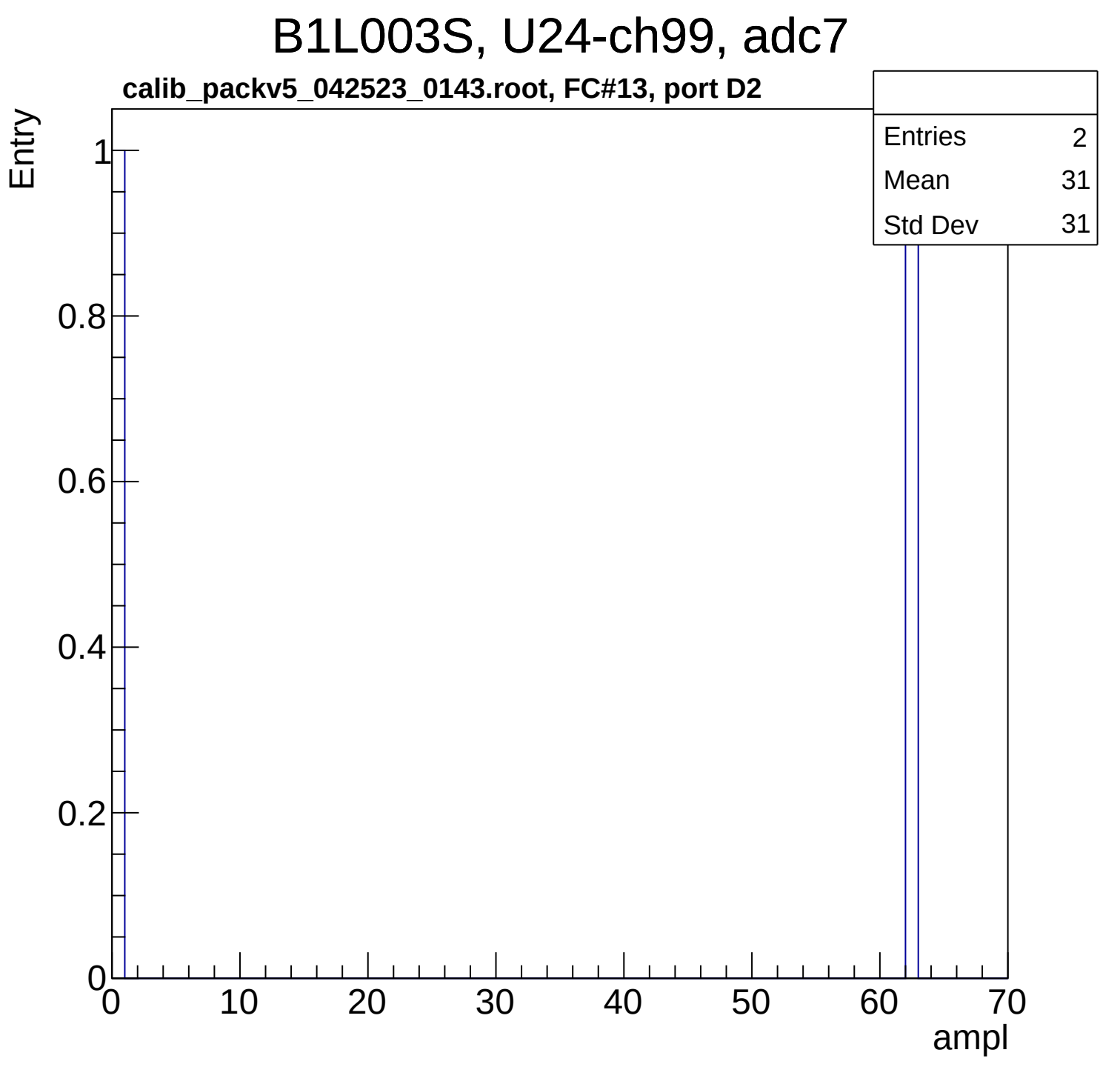
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch99, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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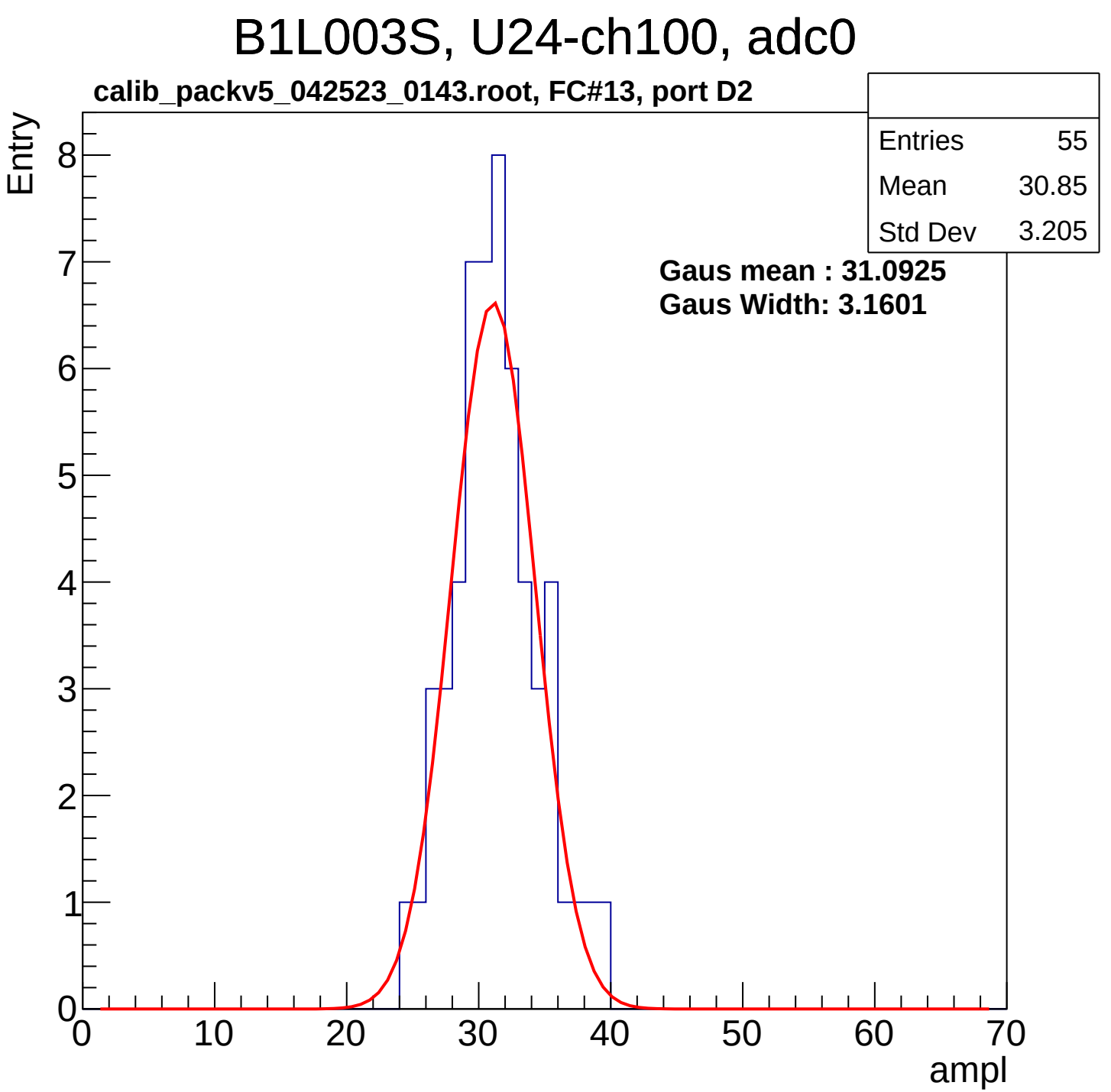
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch100, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 30.85
Std Dev 3.205
Gaus mean : 31.0925
Gaus Width: 3.1601
Entry
ampl
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3
4
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7
8
0
10
20
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40
50
60
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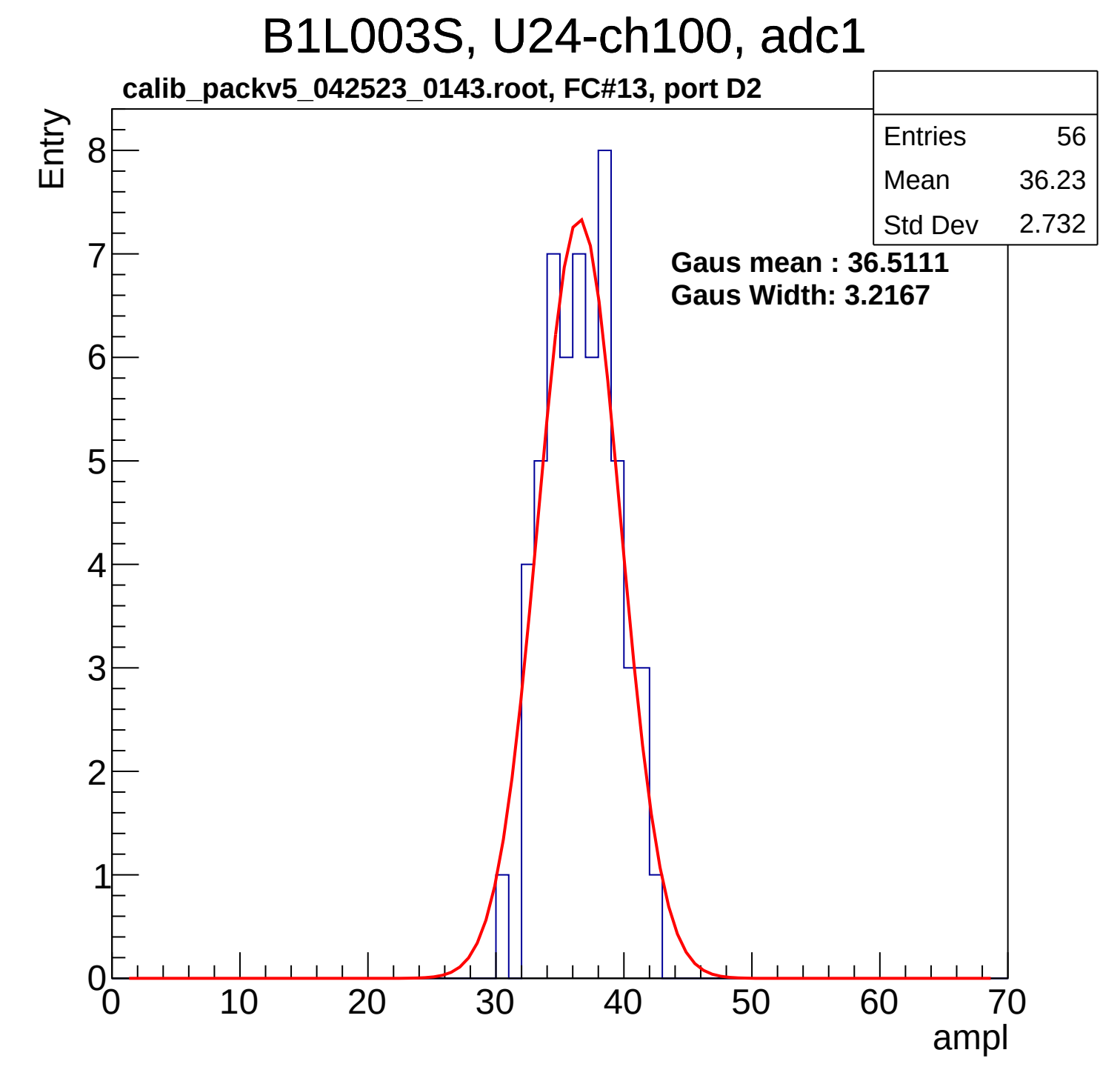
B1L003S, U24-ch100, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 36.23
Std Dev 2.732
Gaus mean : 36.5111
Gaus Width: 3.2167
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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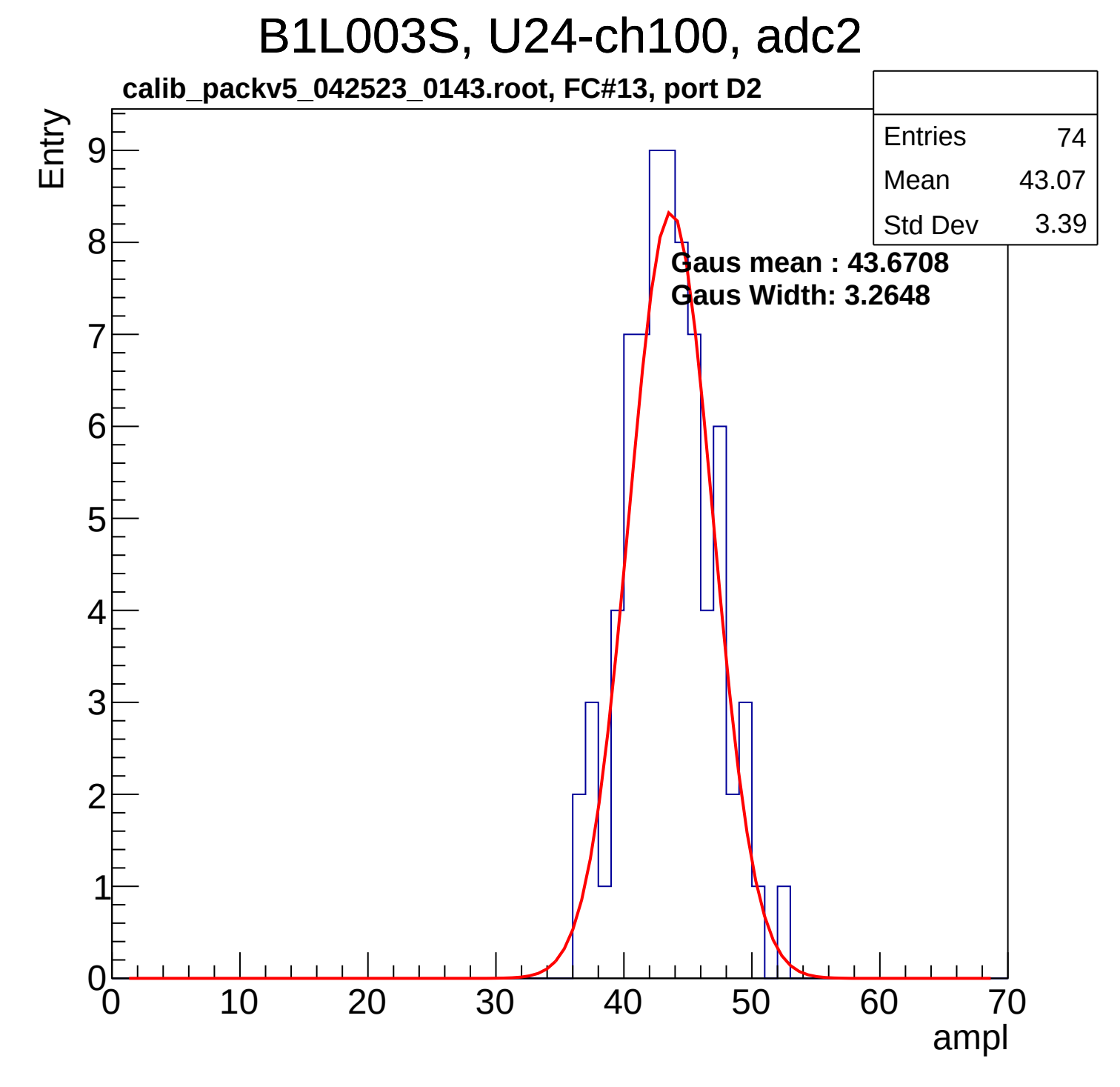
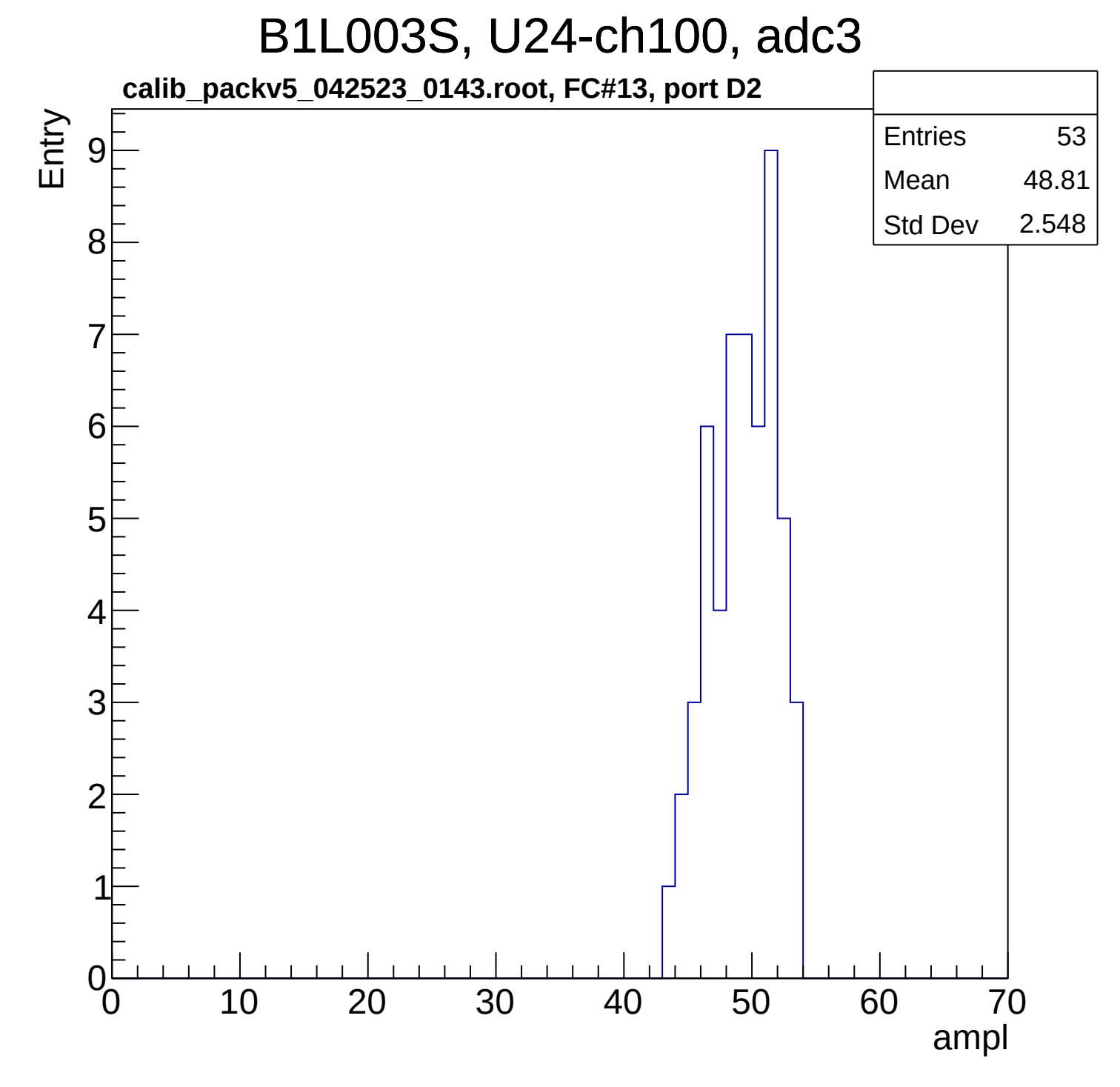
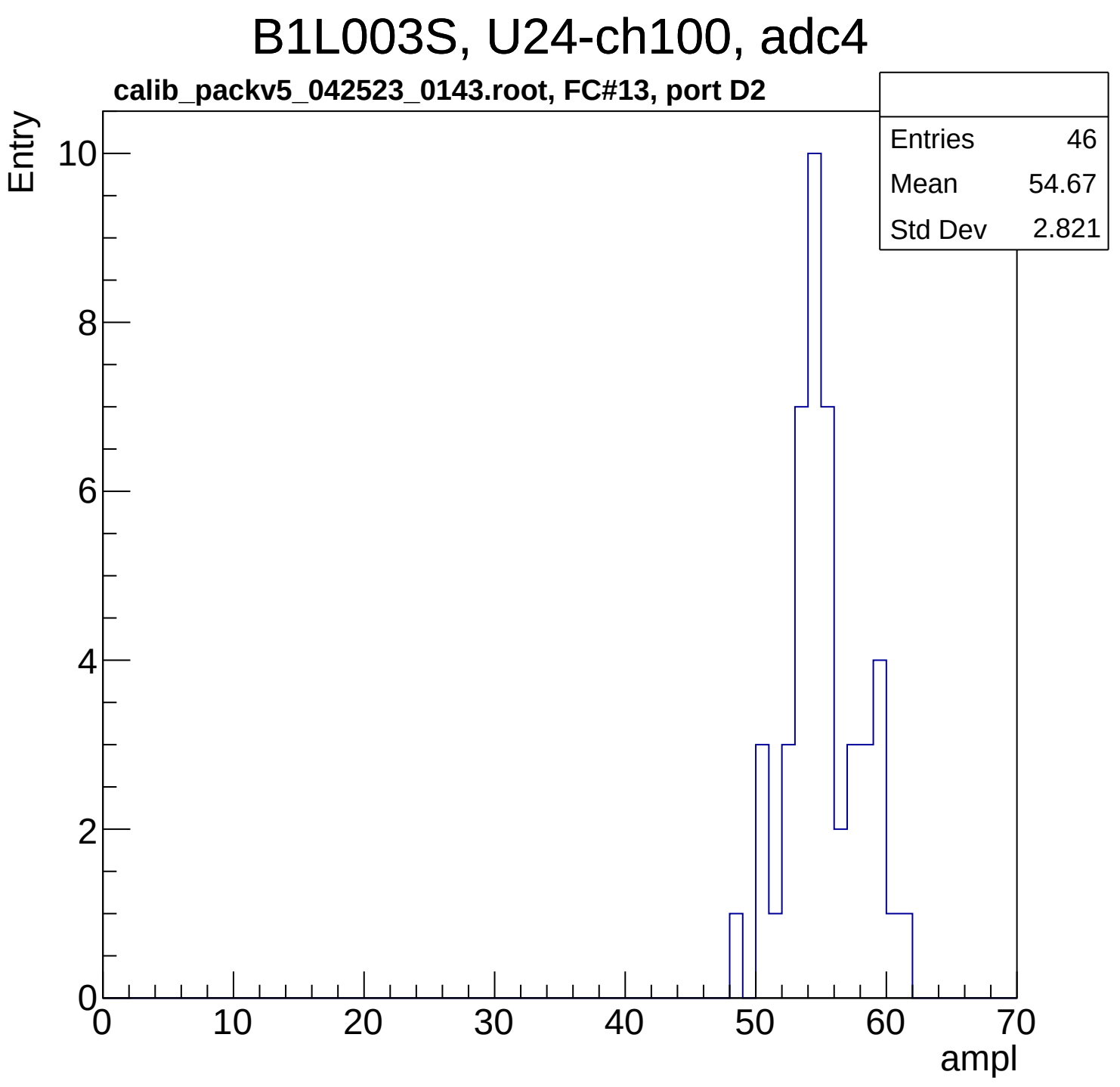
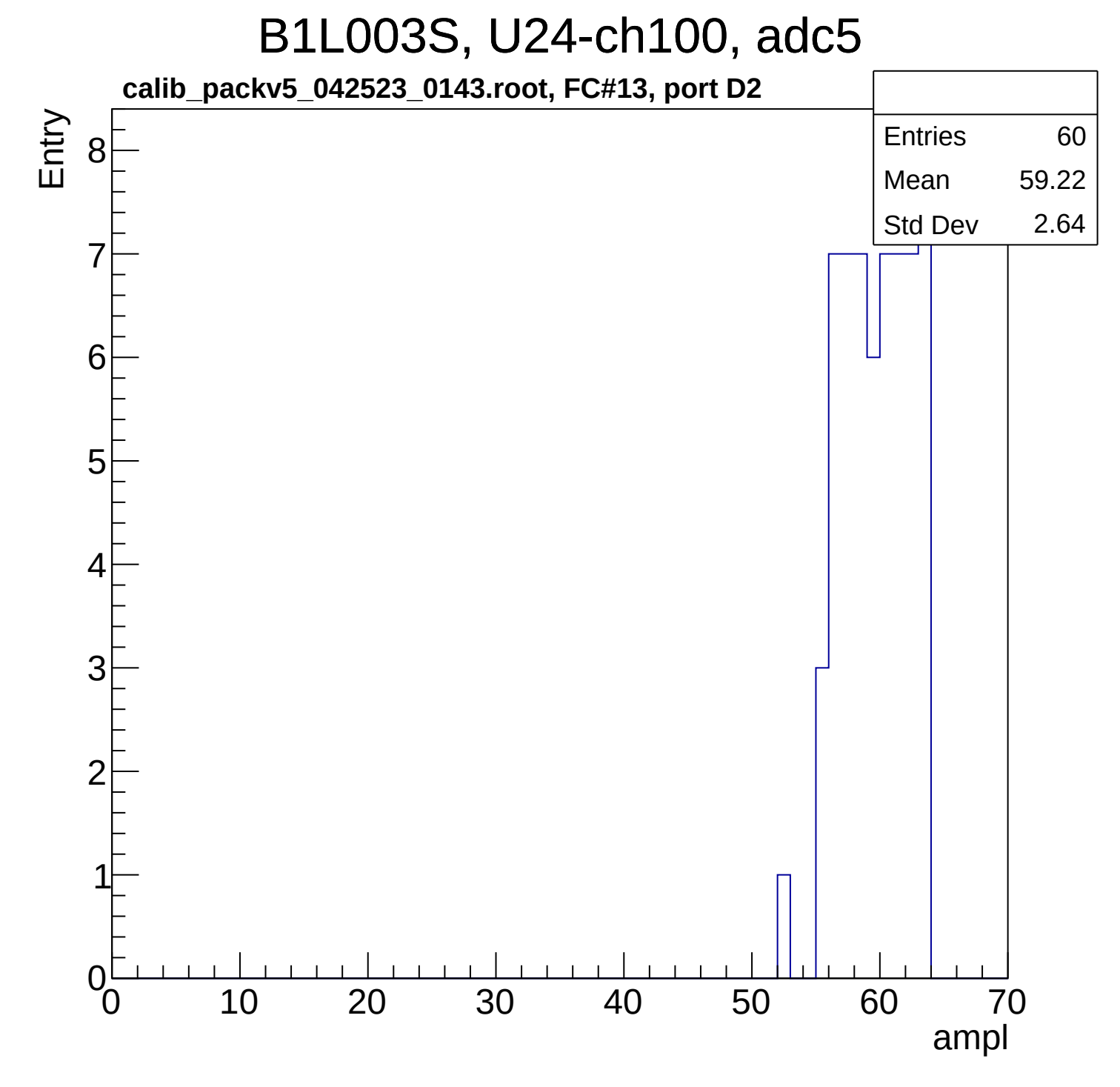

B1L003S, U24-ch100, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 43.07
Std Dev 3.39
Gaus mean : 43.6708
Gaus Width: 3.2648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch100, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.81
Std Dev 2.548
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch100, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 54.67
Std Dev 2.821
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch100, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 59.22
Std Dev 2.64
Entry
ampl
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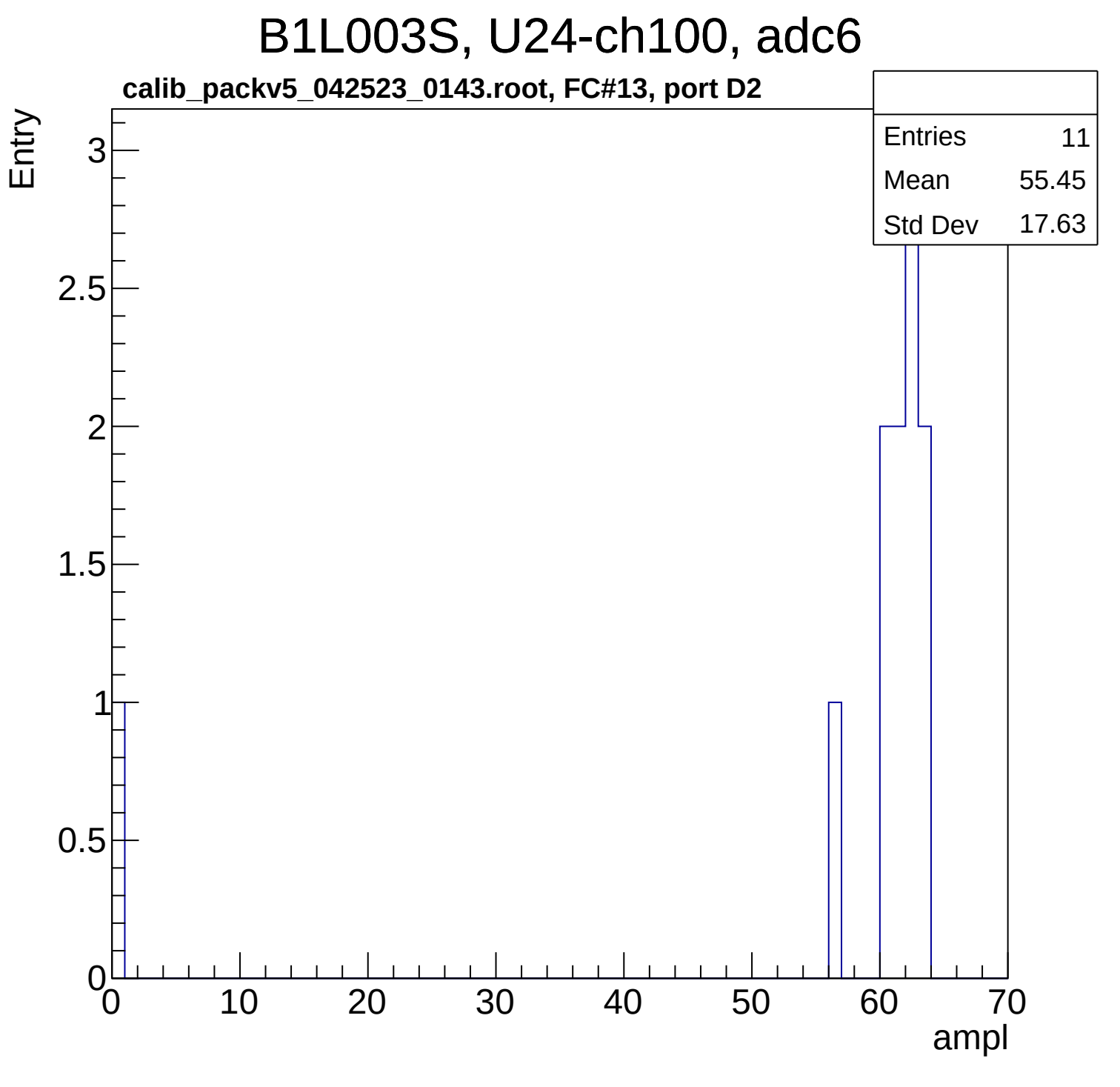
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch100, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.45
Std Dev 17.63
Entry
ampl
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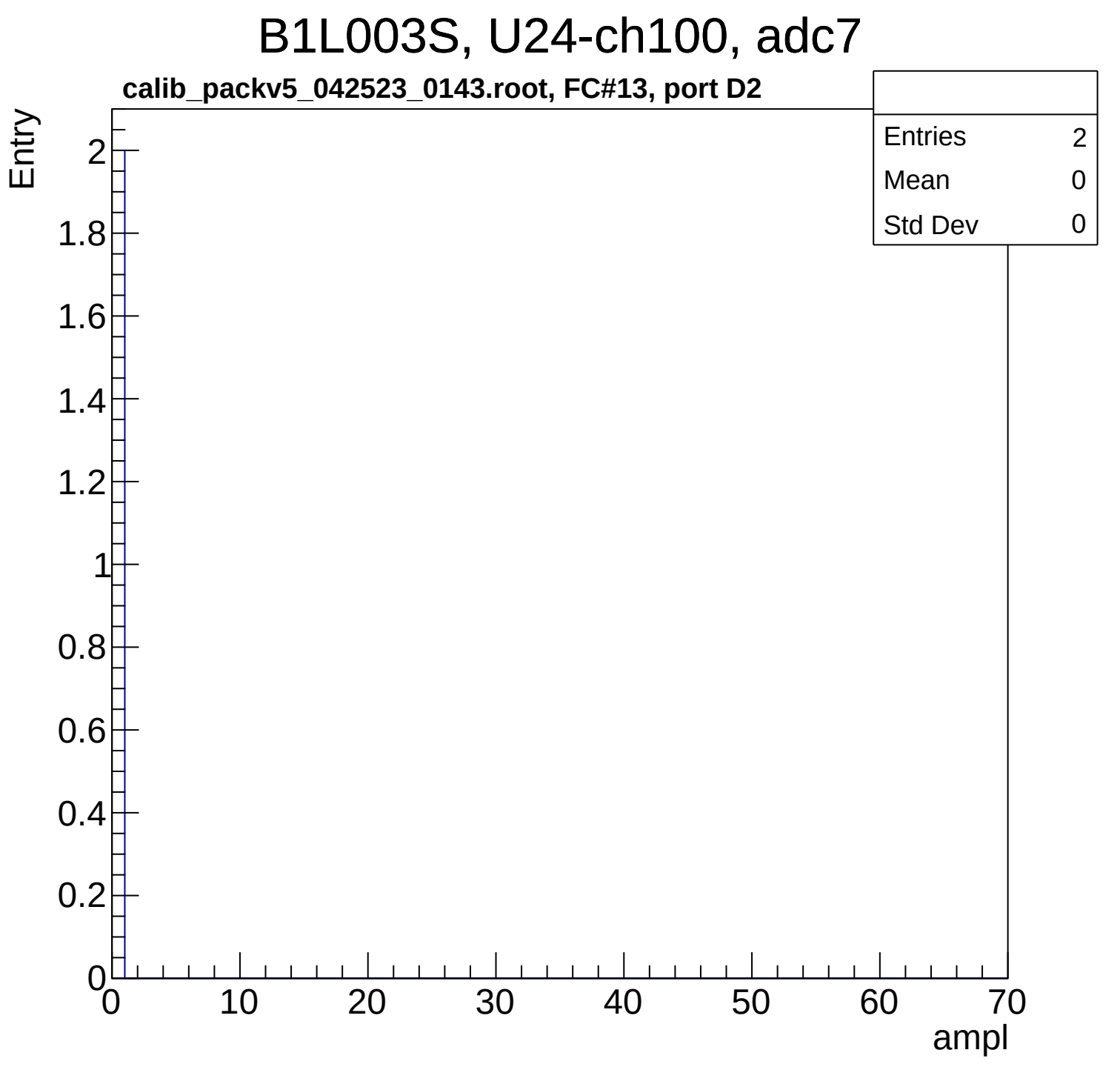
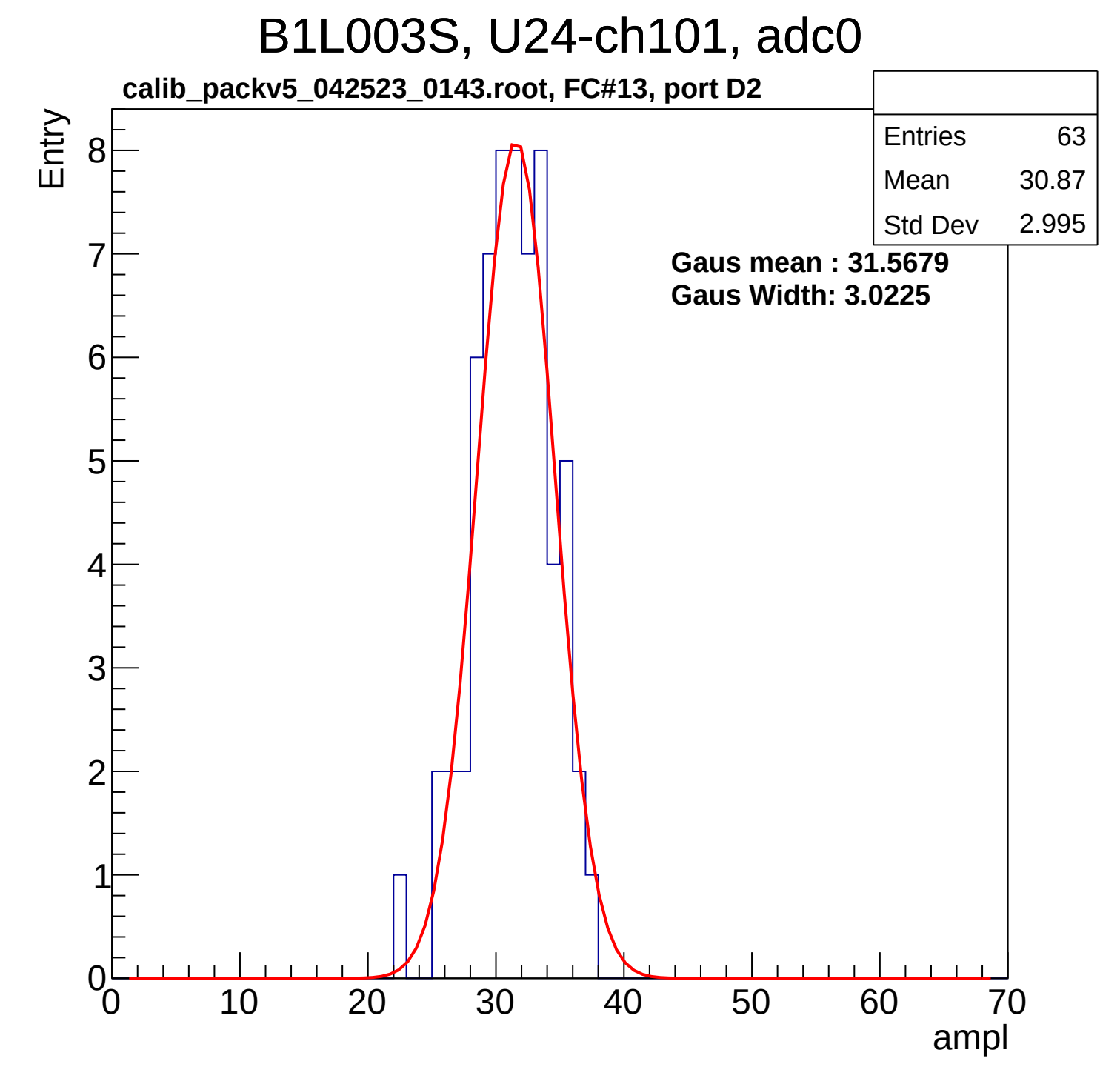
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch100, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch101, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 30.87
Std Dev 2.995
Gaus mean : 31.5679
Gaus Width: 3.0225
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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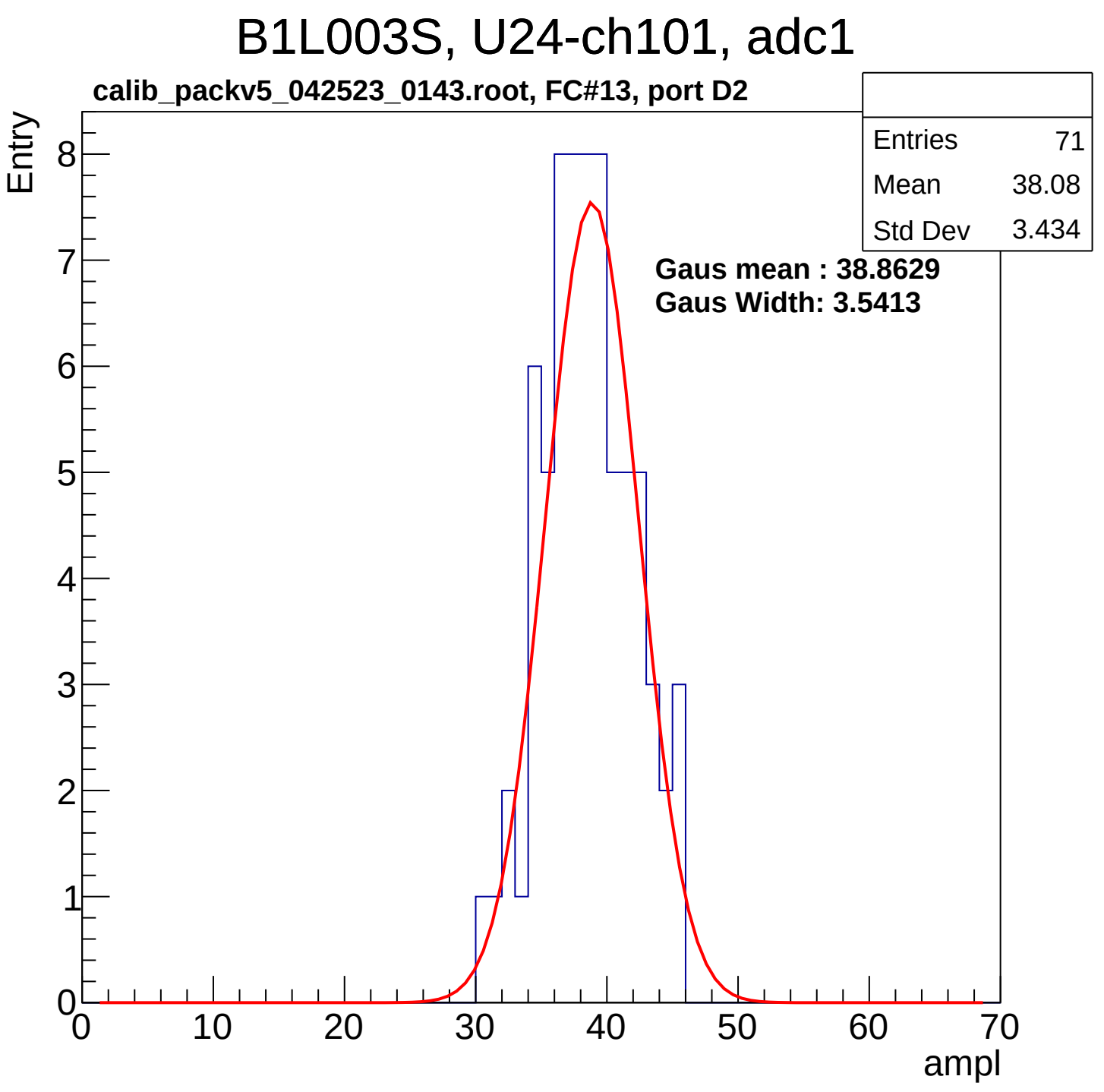
B1L003S, U24-ch101, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 38.08
Std Dev 3.434
Gaus mean : 38.8629
Gaus Width: 3.5413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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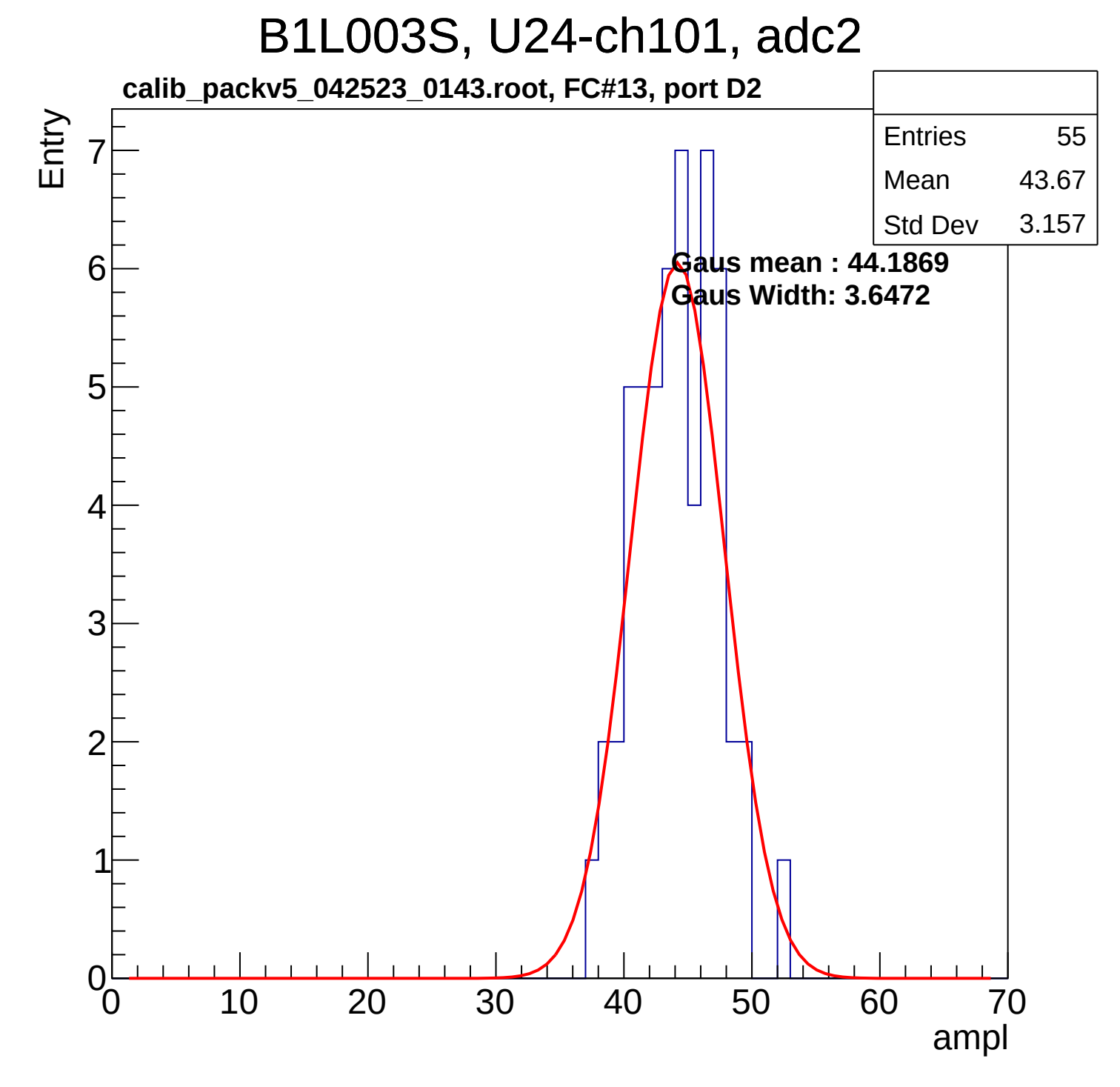
B1L003S, U24-ch101, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43.67
Std Dev 3.157
Gaus mean : 44.1869
Gaus Width: 3.6472
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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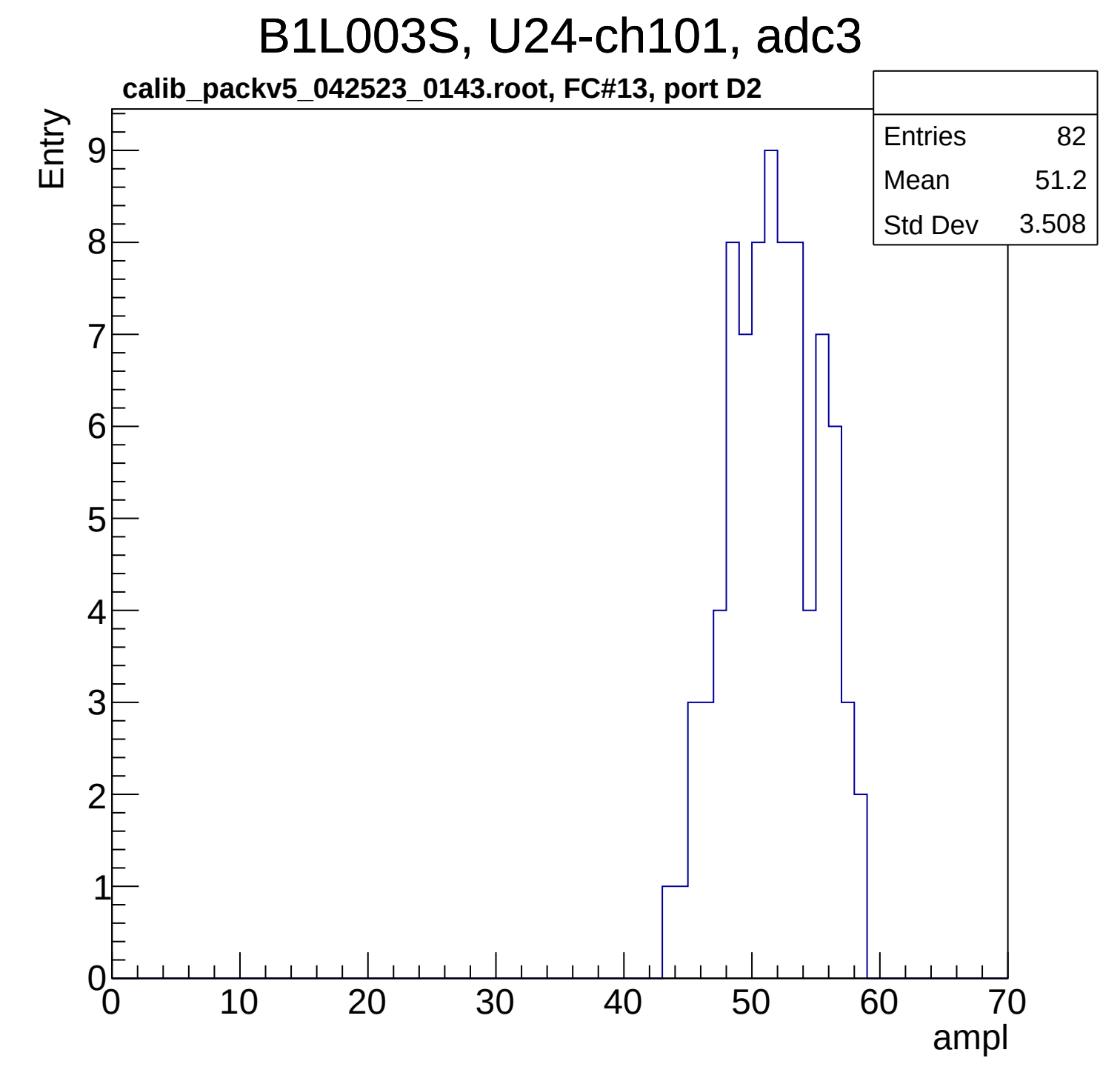
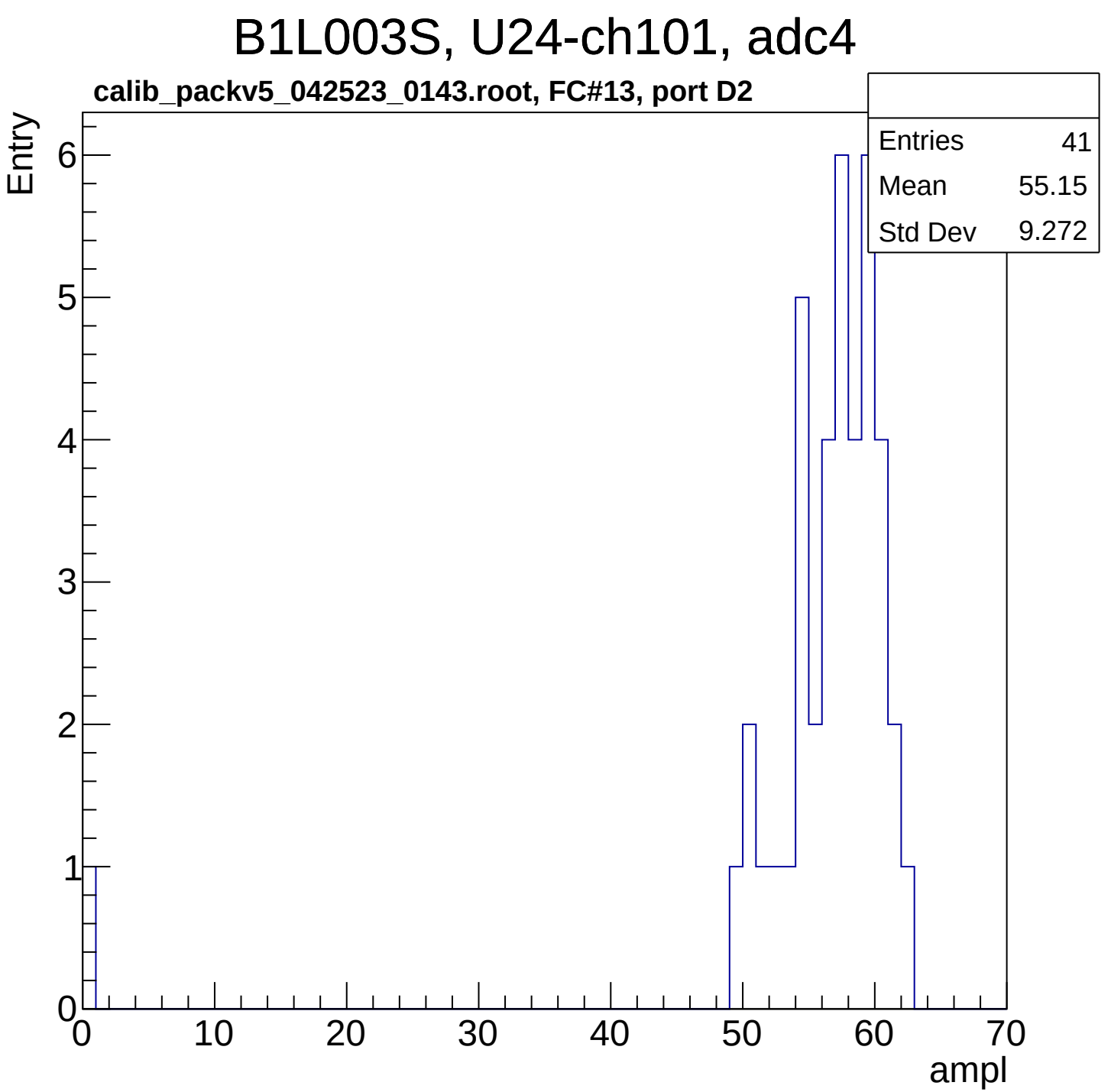
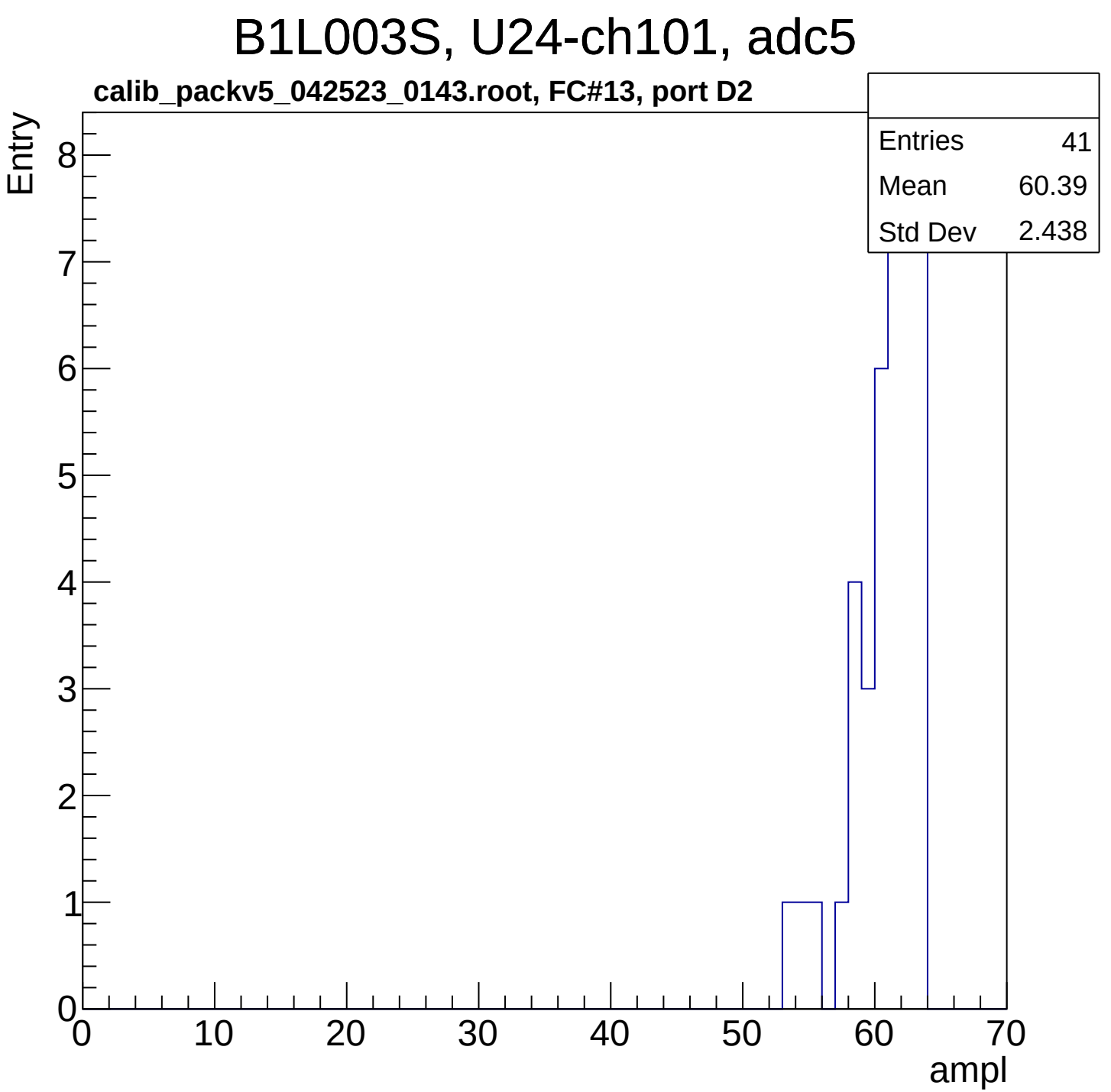
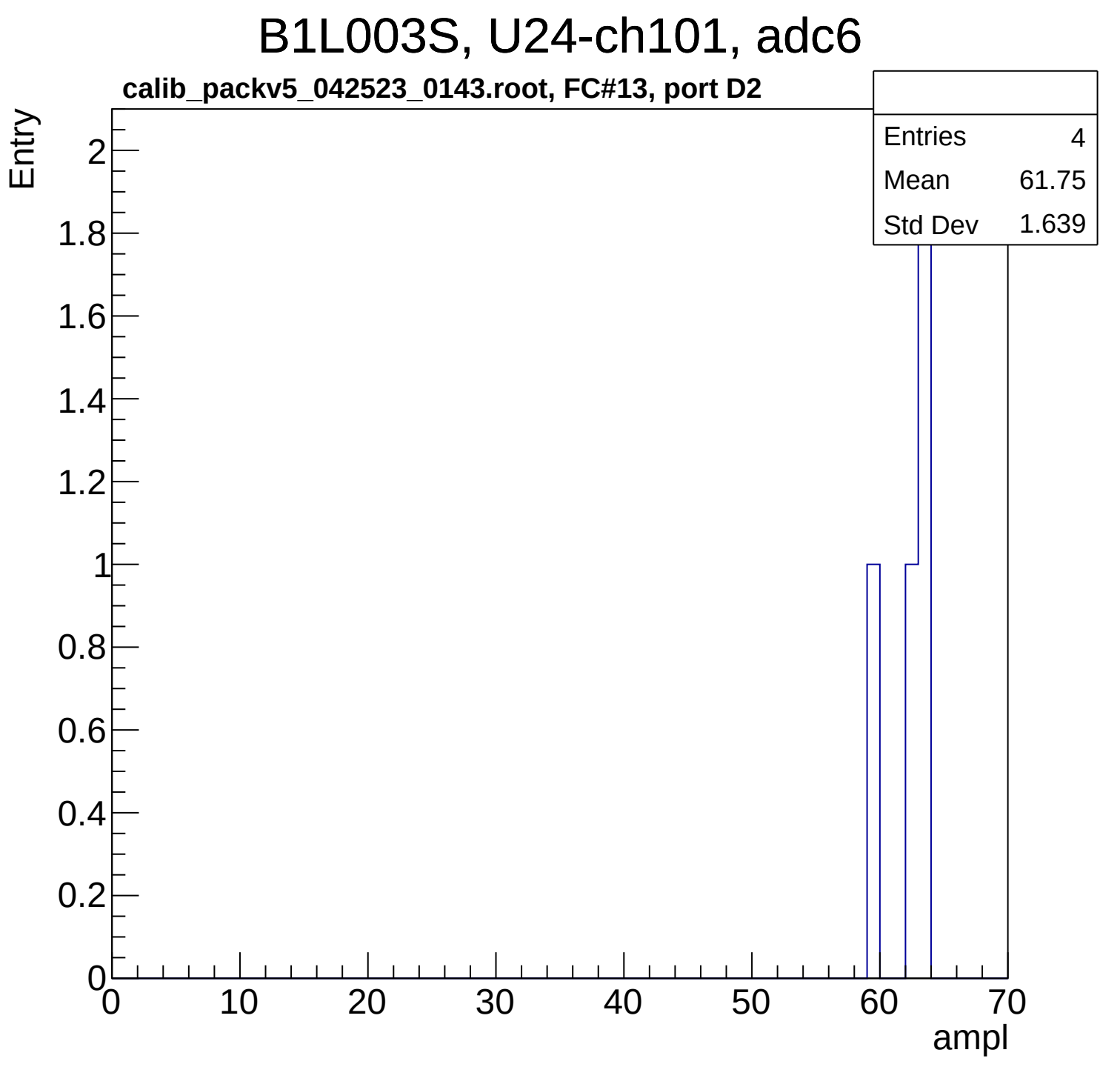
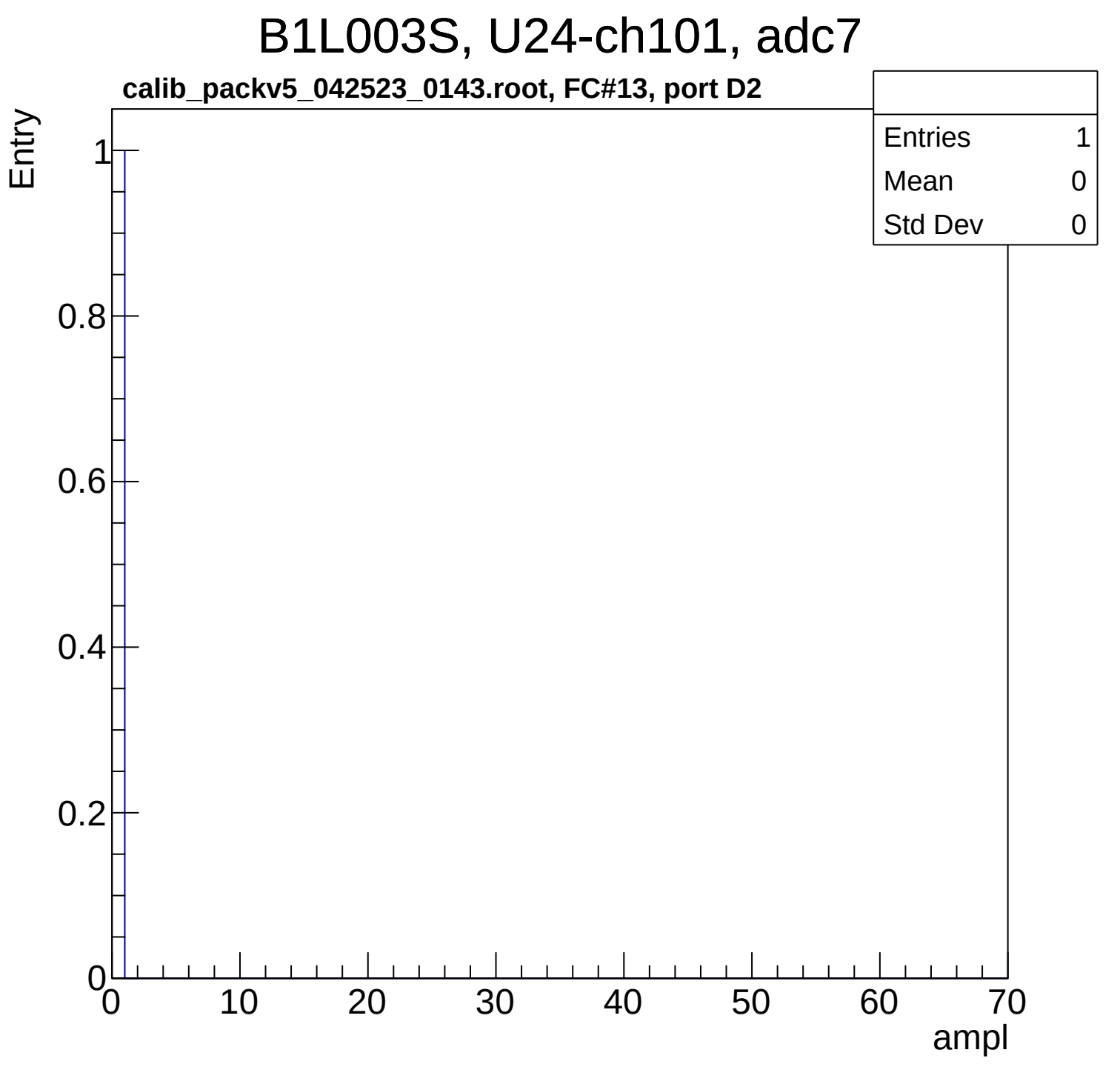
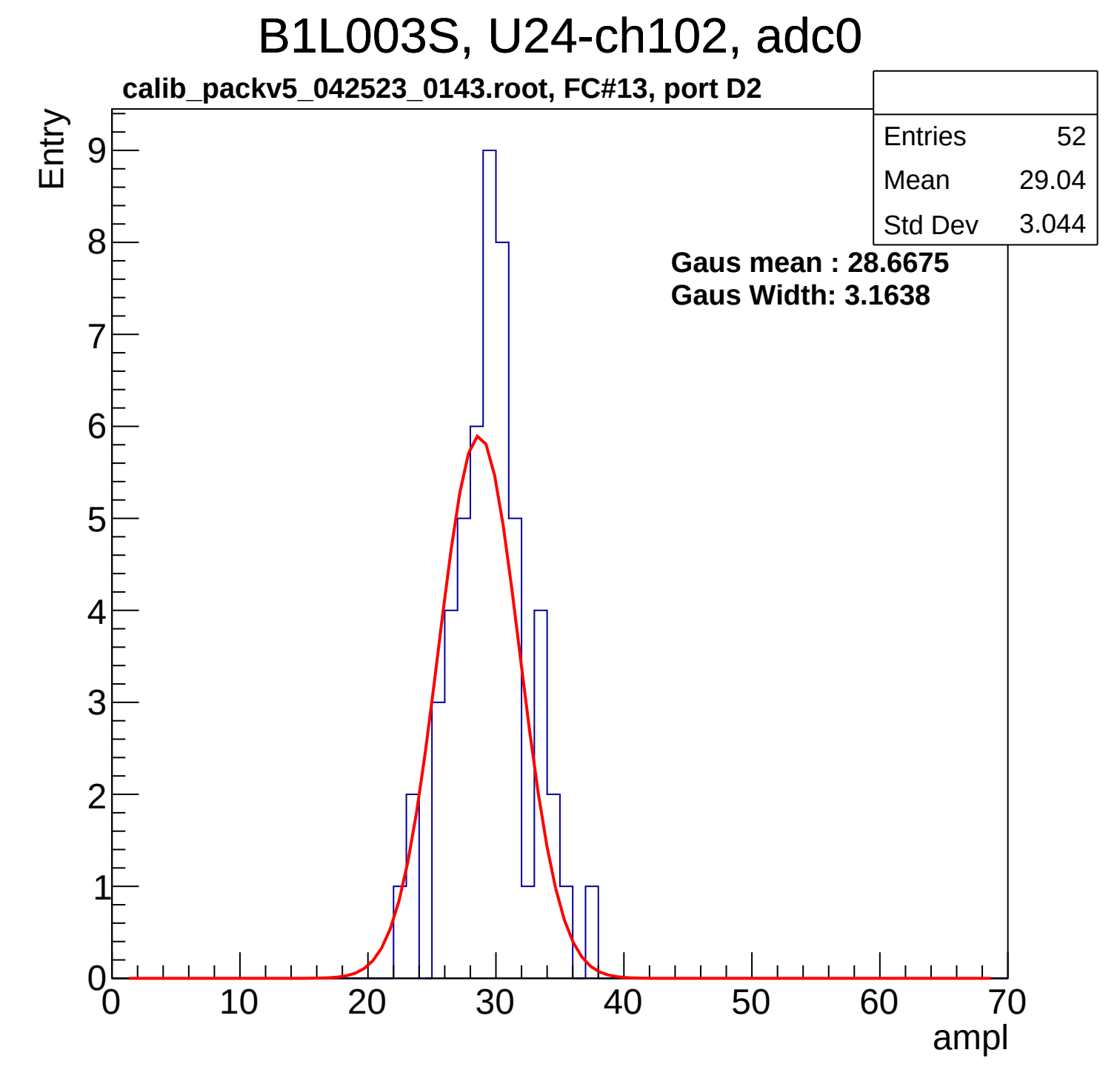

B1L003S, U24-ch101, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 51.2
Std Dev 3.508
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

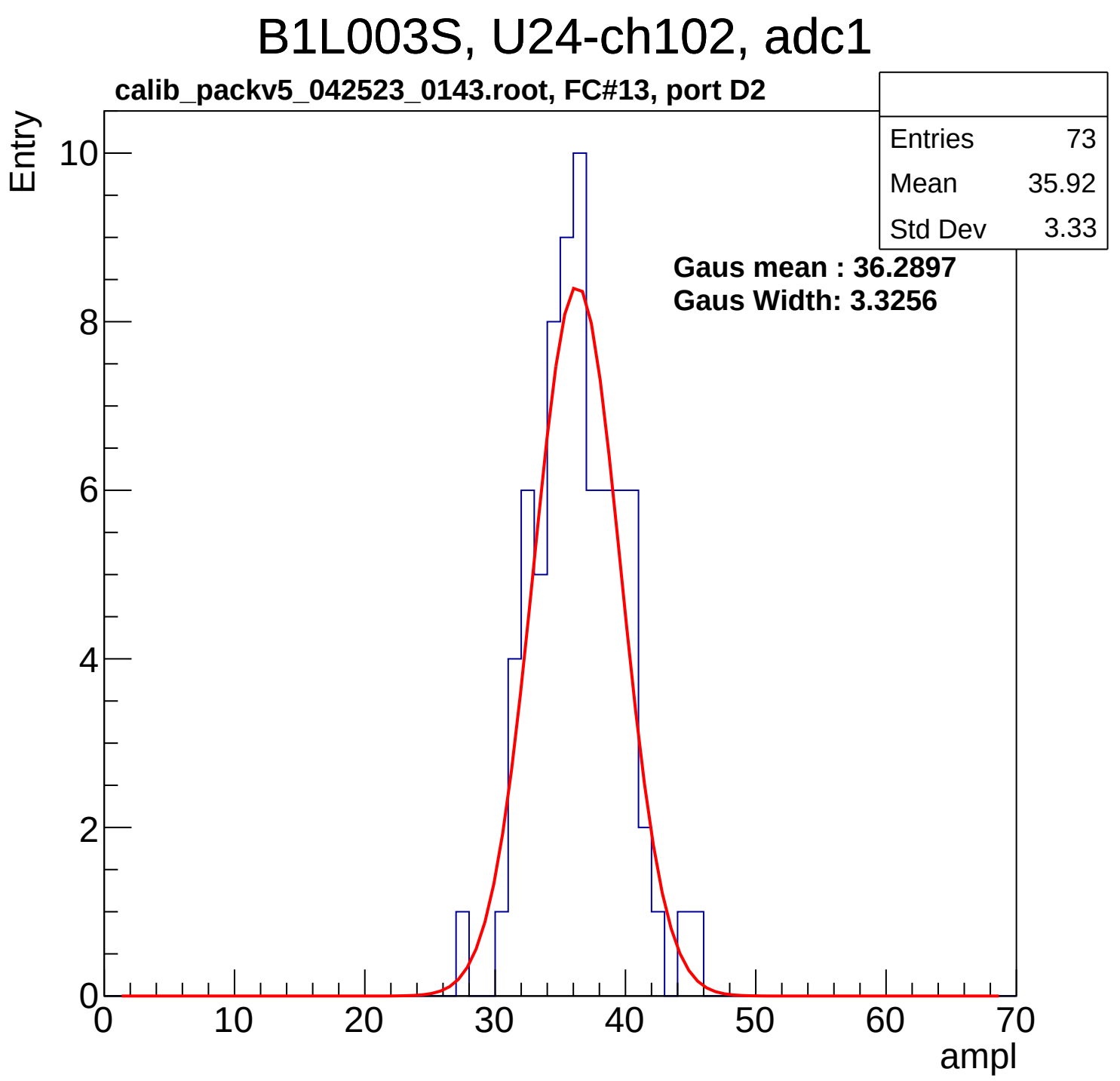

B1L003S, U24-ch101, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 55.15
Std Dev 9.272
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

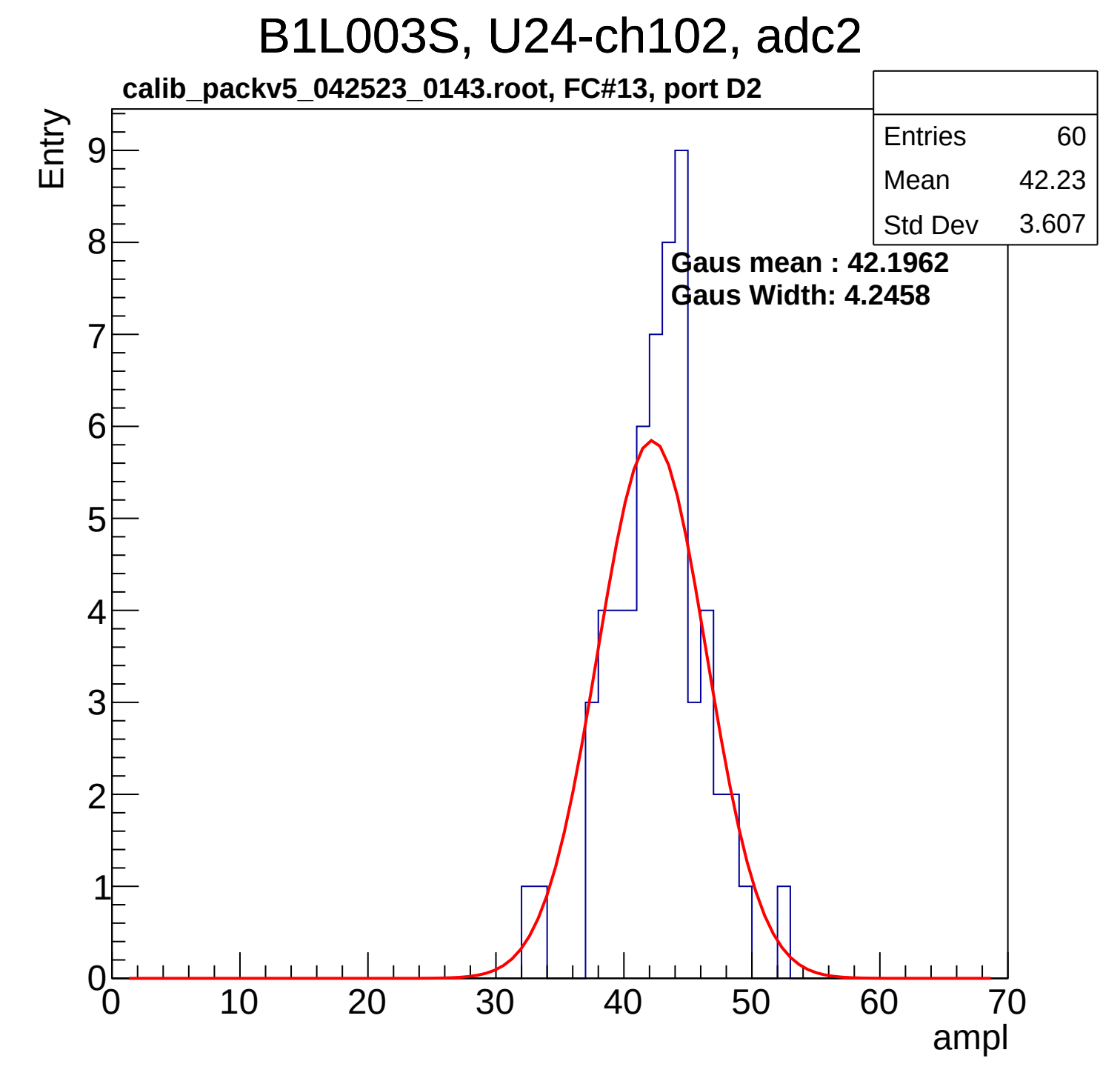

B1L003S, U24-ch101, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 60.39
Std Dev 2.438
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch101, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch101, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch102, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 29.04
Std Dev 3.044
Gaus mean : 28.6675
Gaus Width: 3.1638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch102, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.92
Std Dev 3.33
Gaus mean : 36.2897
Gaus Width: 3.3256
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch102, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 42.23
Std Dev 3.607
Gaus mean : 42.1962
Gaus Width: 4.2458
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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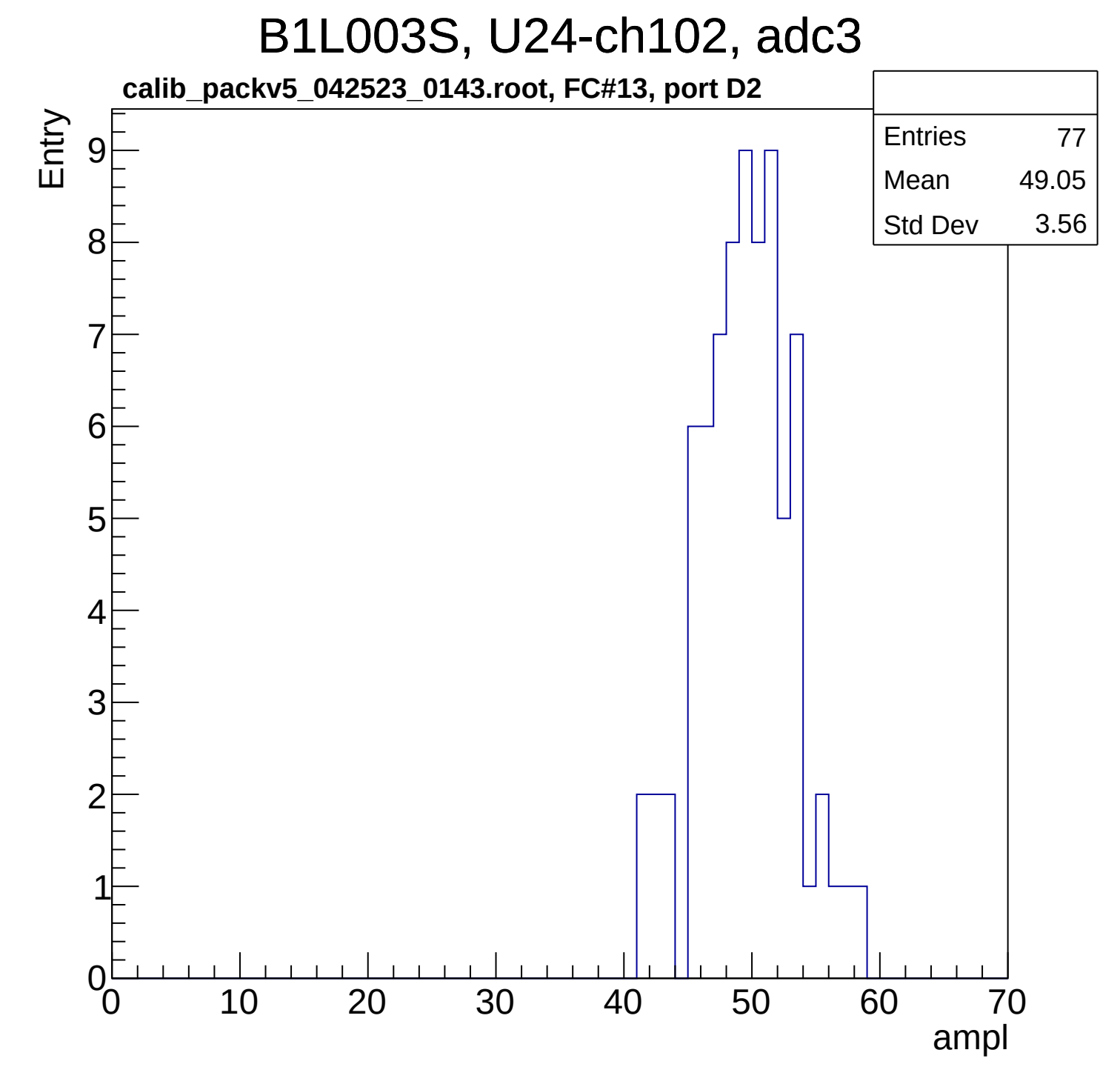
B1L003S, U24-ch102, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 49.05
Std Dev 3.56
Entry
ampl
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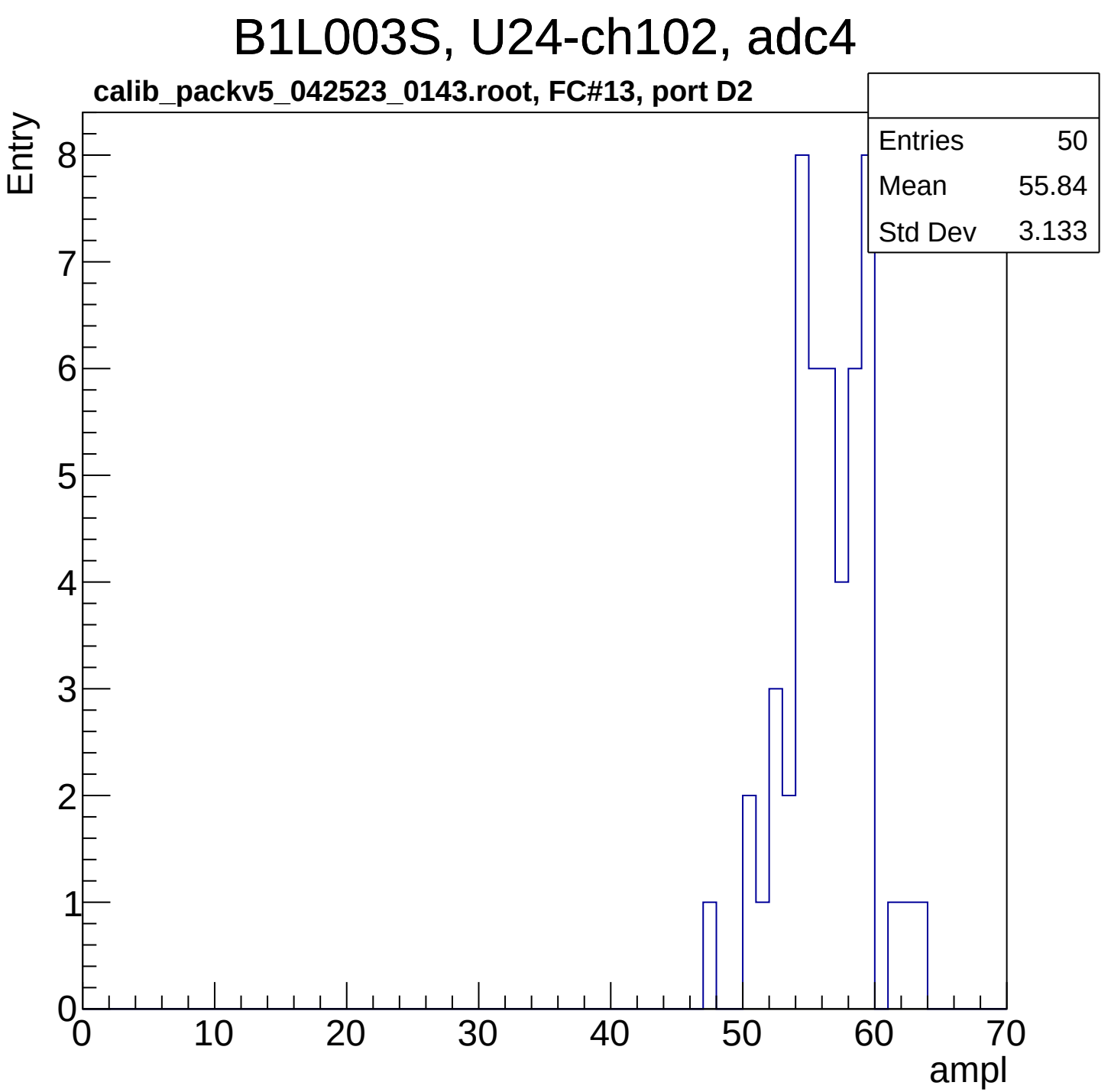
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch102, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 55.84
Std Dev 3.133
Entry
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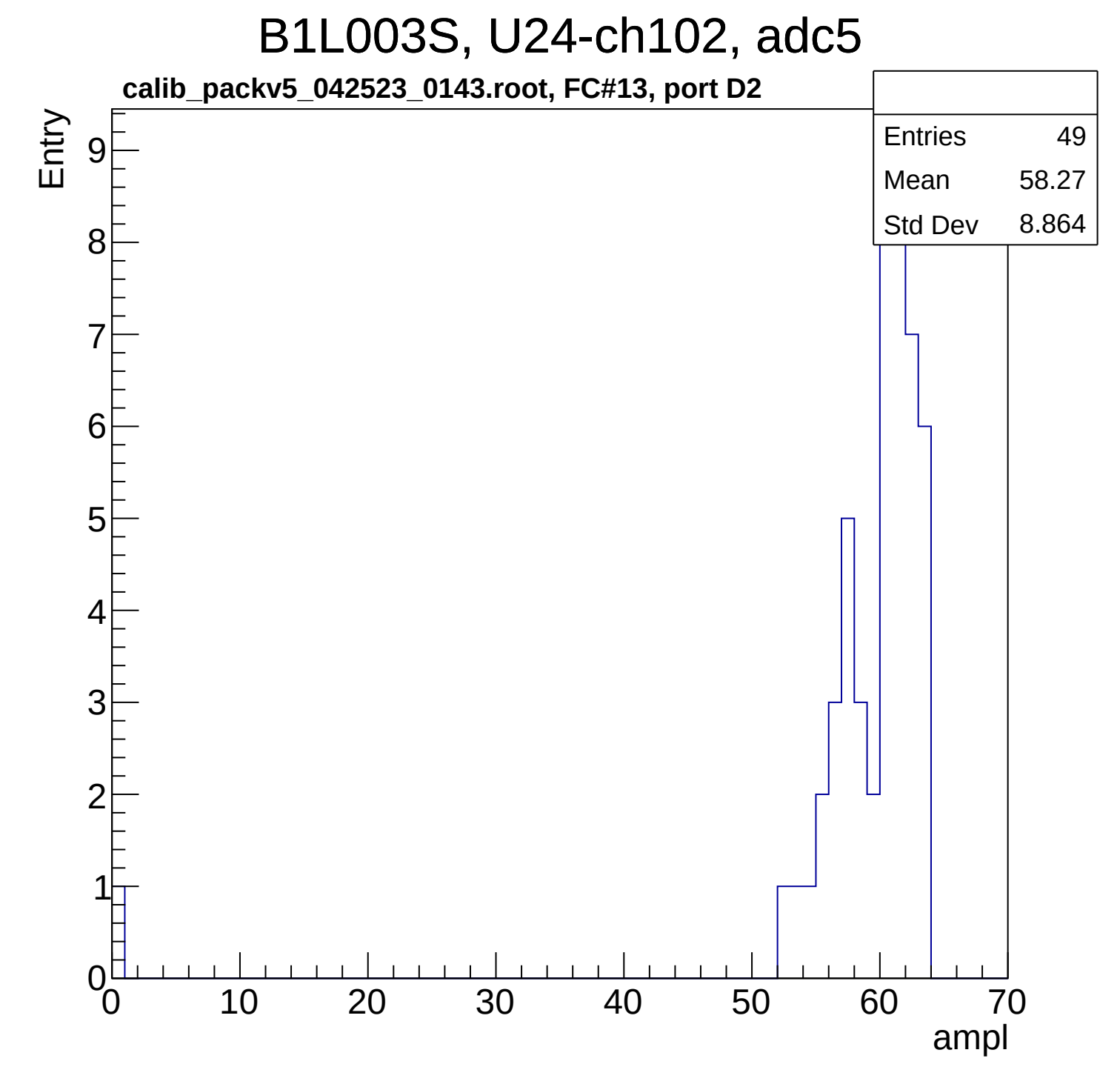
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch102, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.27
Std Dev 8.864
Entry
ampl
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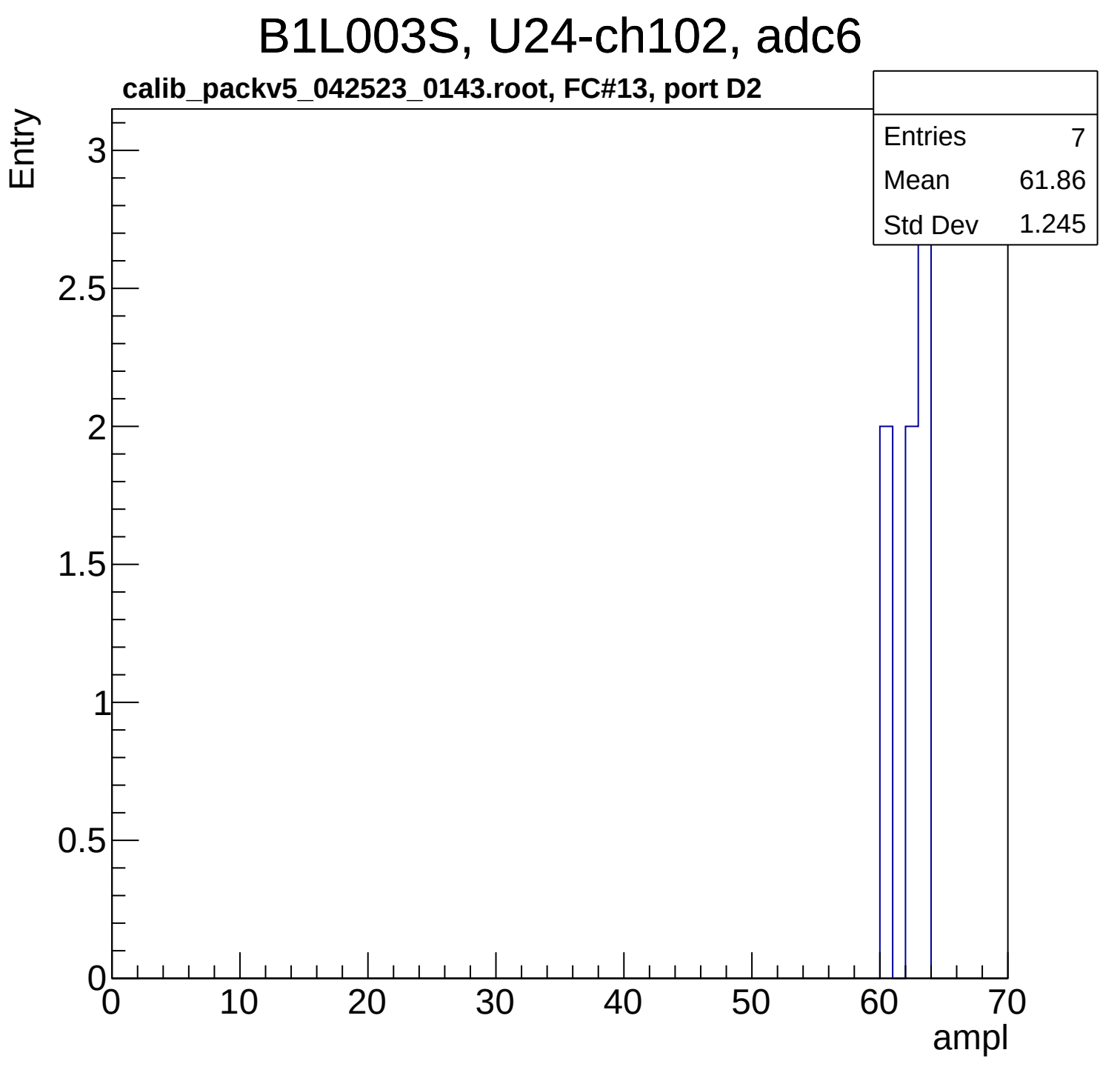
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch102, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.86
Std Dev 1.245
Entry
ampl
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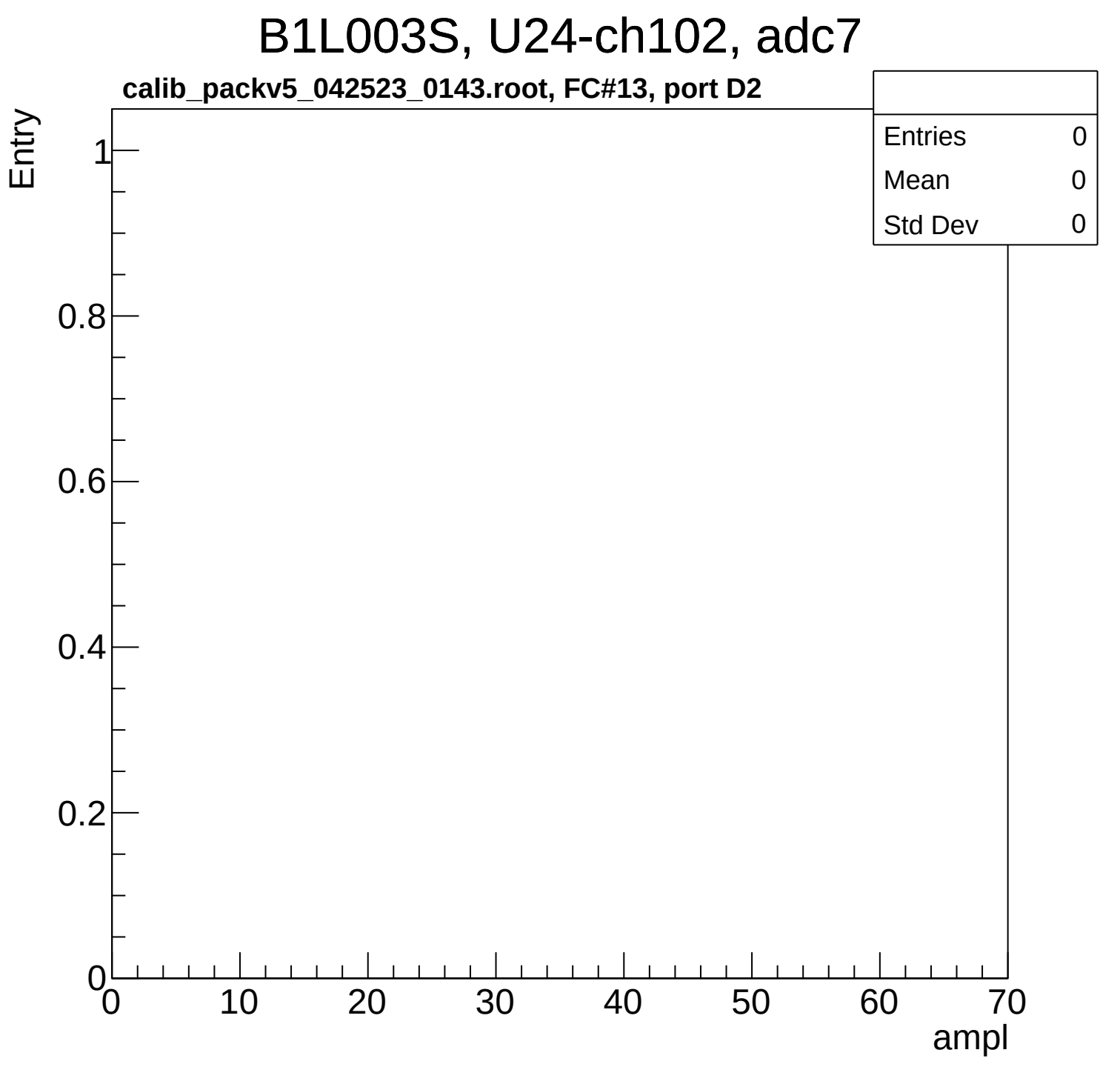
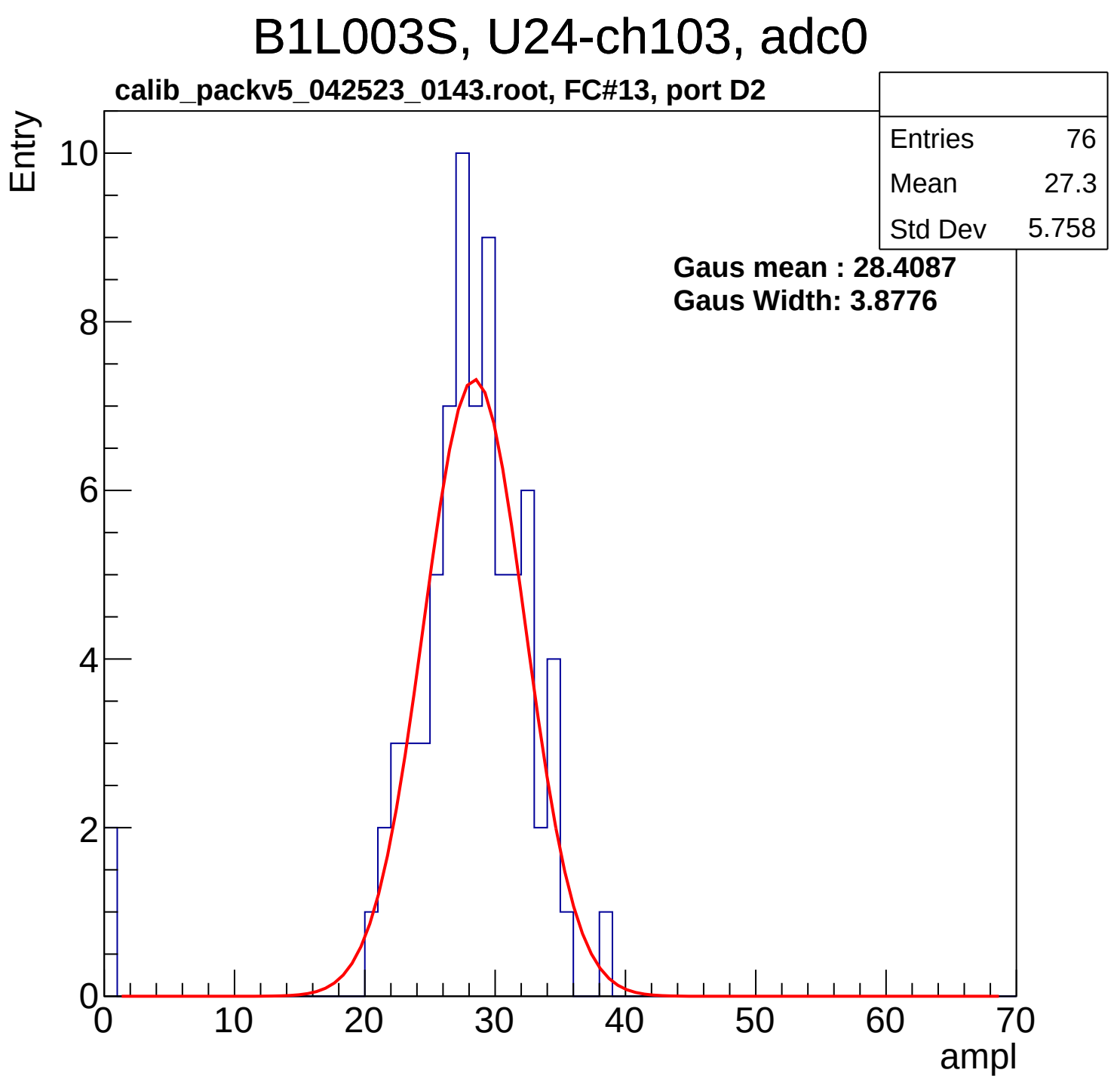
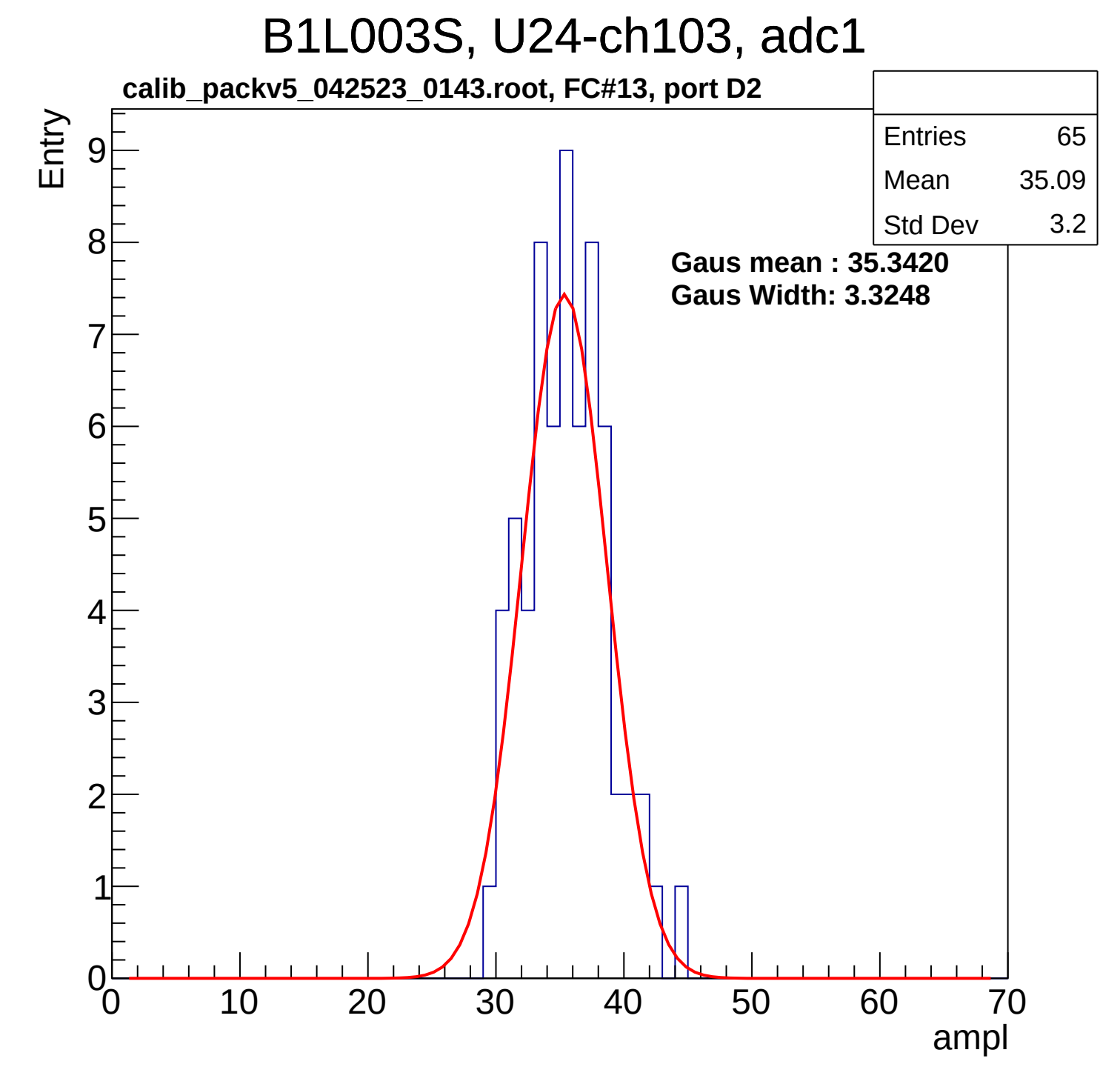
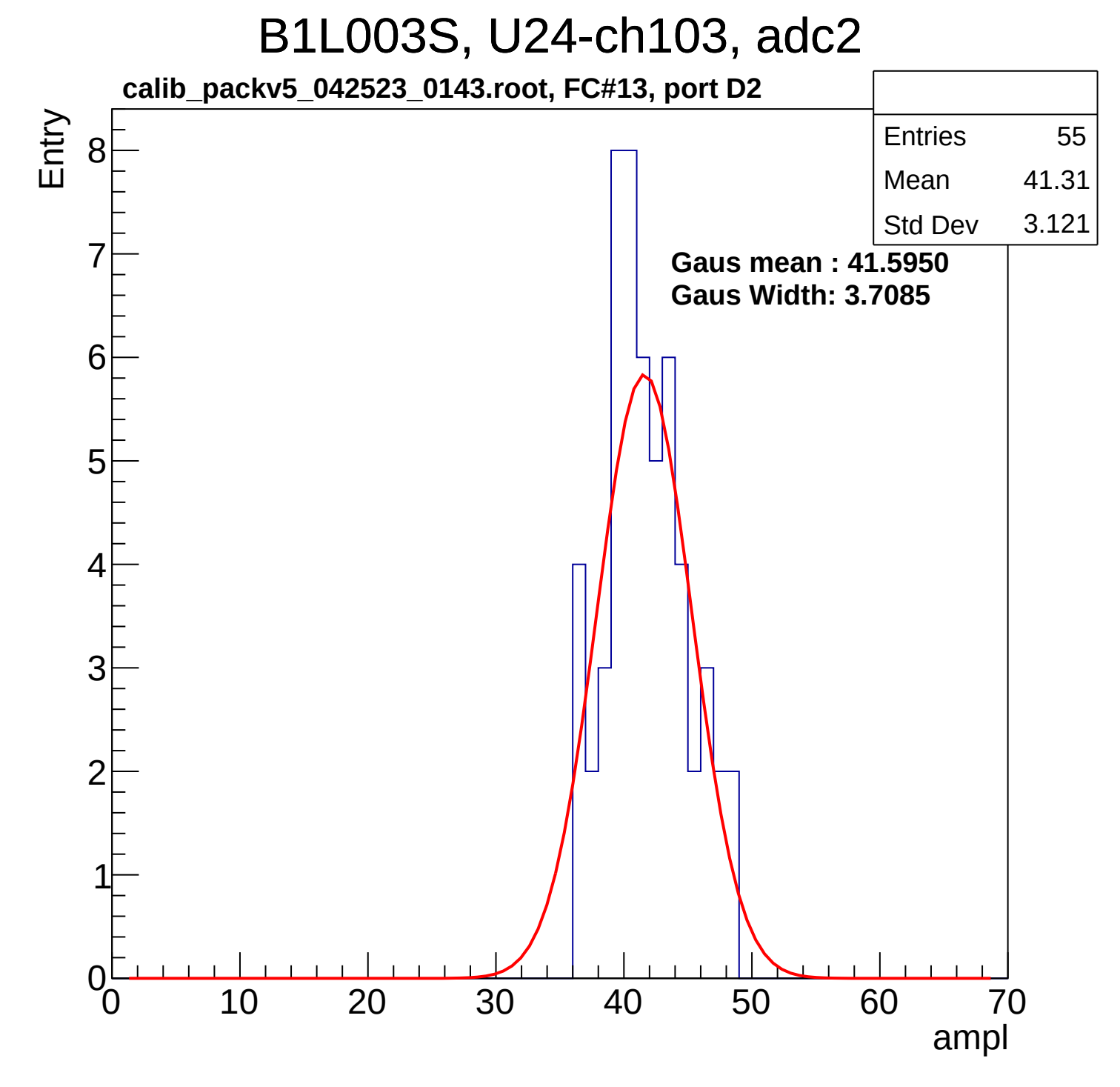
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch102, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch103, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 27.3
Std Dev 5.758
Gaus mean : 28.4087
Gaus Width: 3.8776
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch103, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.09
Std Dev 3.2
Gaus mean : 35.3420
Gaus Width: 3.3248
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch103, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.31
Std Dev 3.121
Gaus mean : 41.5950
Gaus Width: 3.7085
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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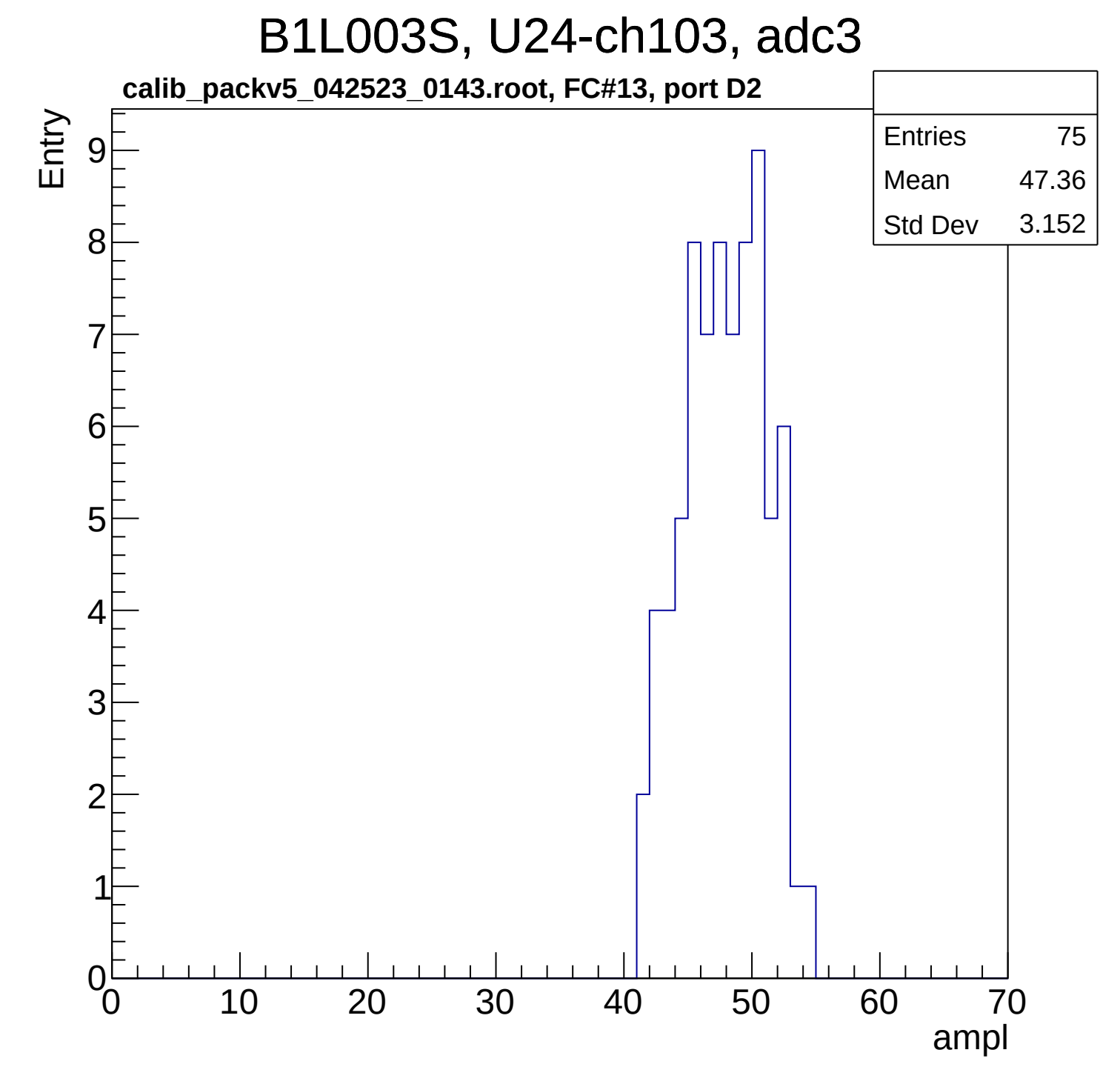
B1L003S, U24-ch103, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 47.36
Std Dev 3.152
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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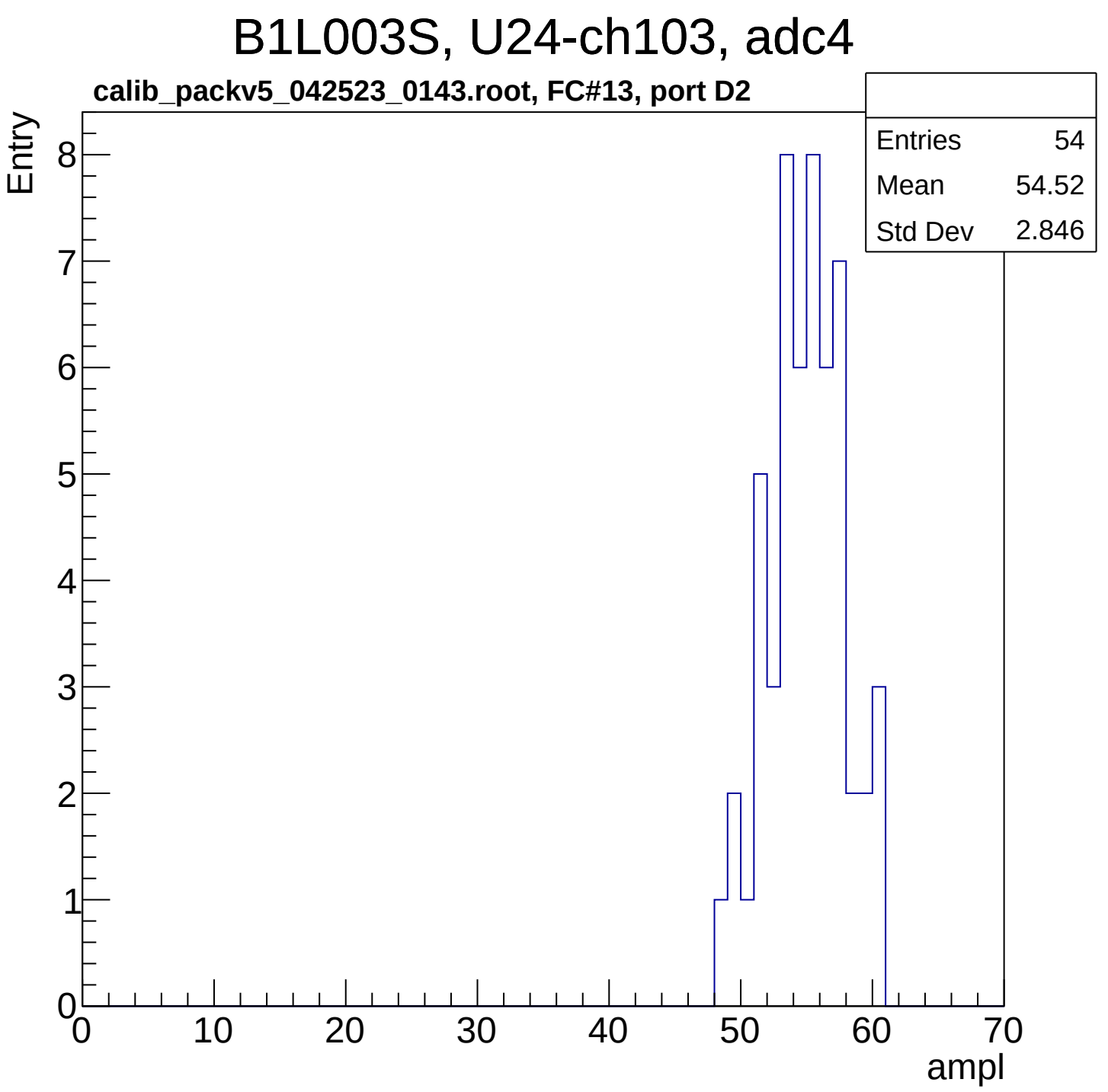
B1L003S, U24-ch103, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.52
Std Dev 2.846
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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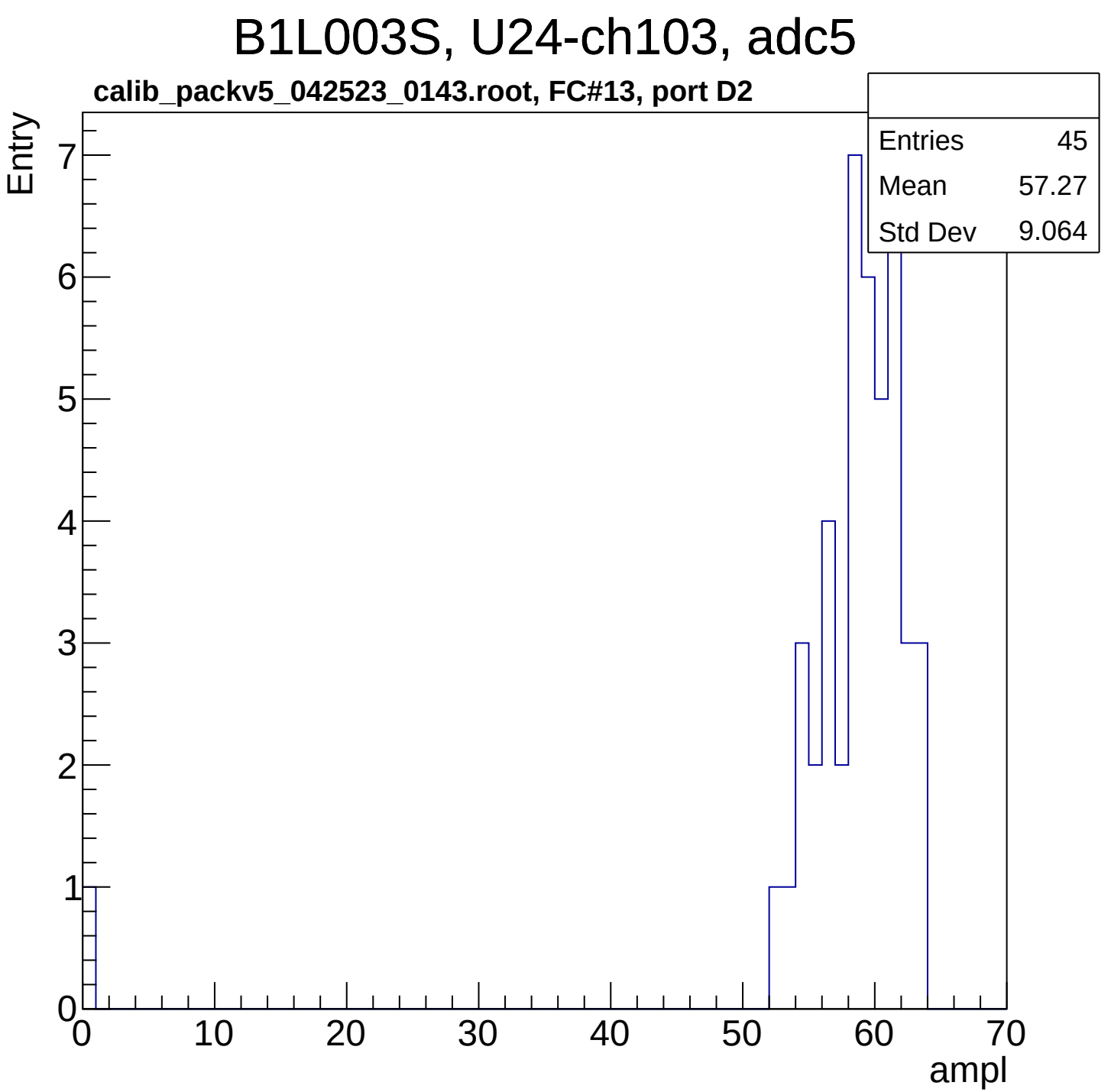
B1L003S, U24-ch103, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 57.27
Std Dev 9.064
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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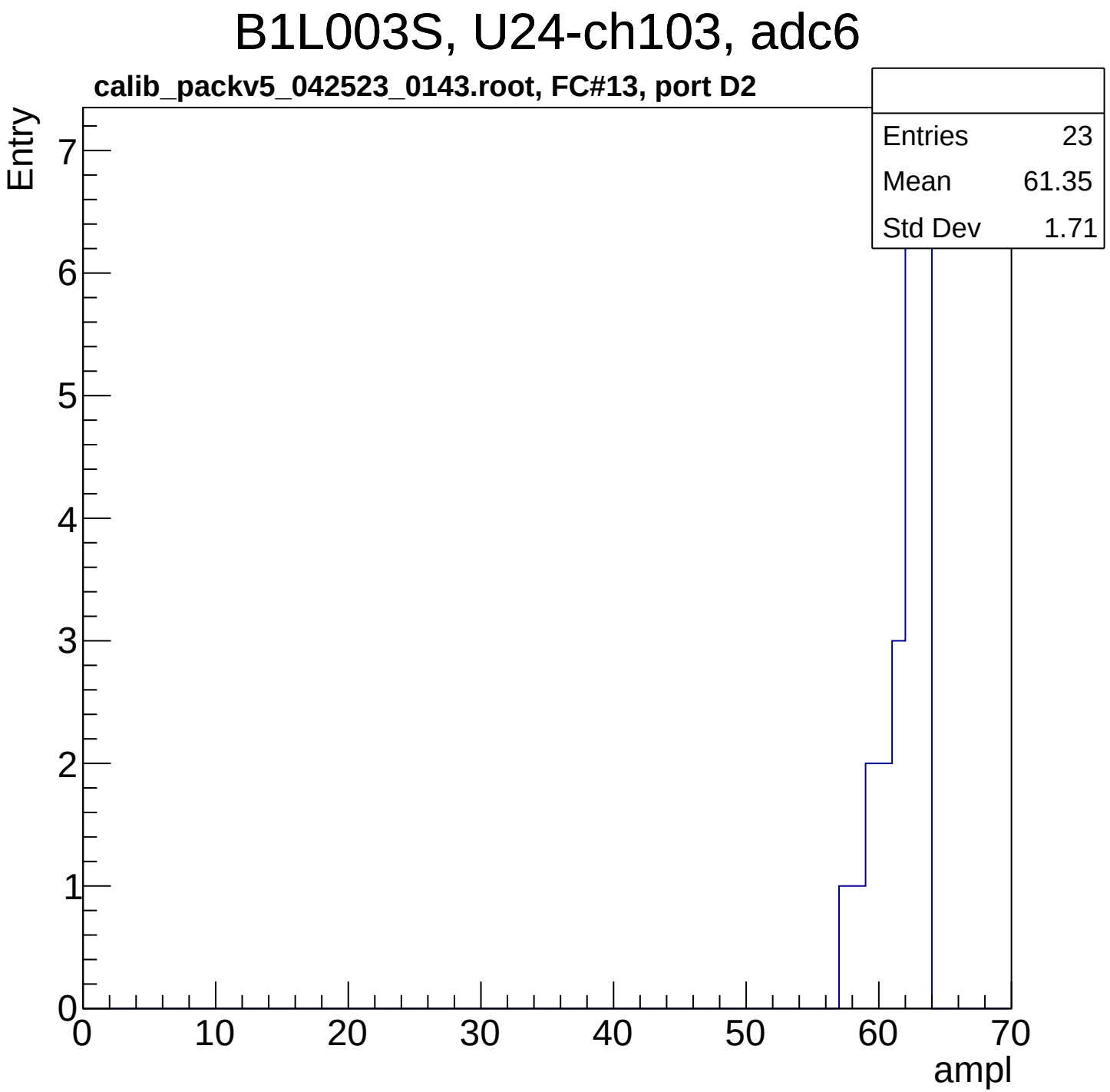
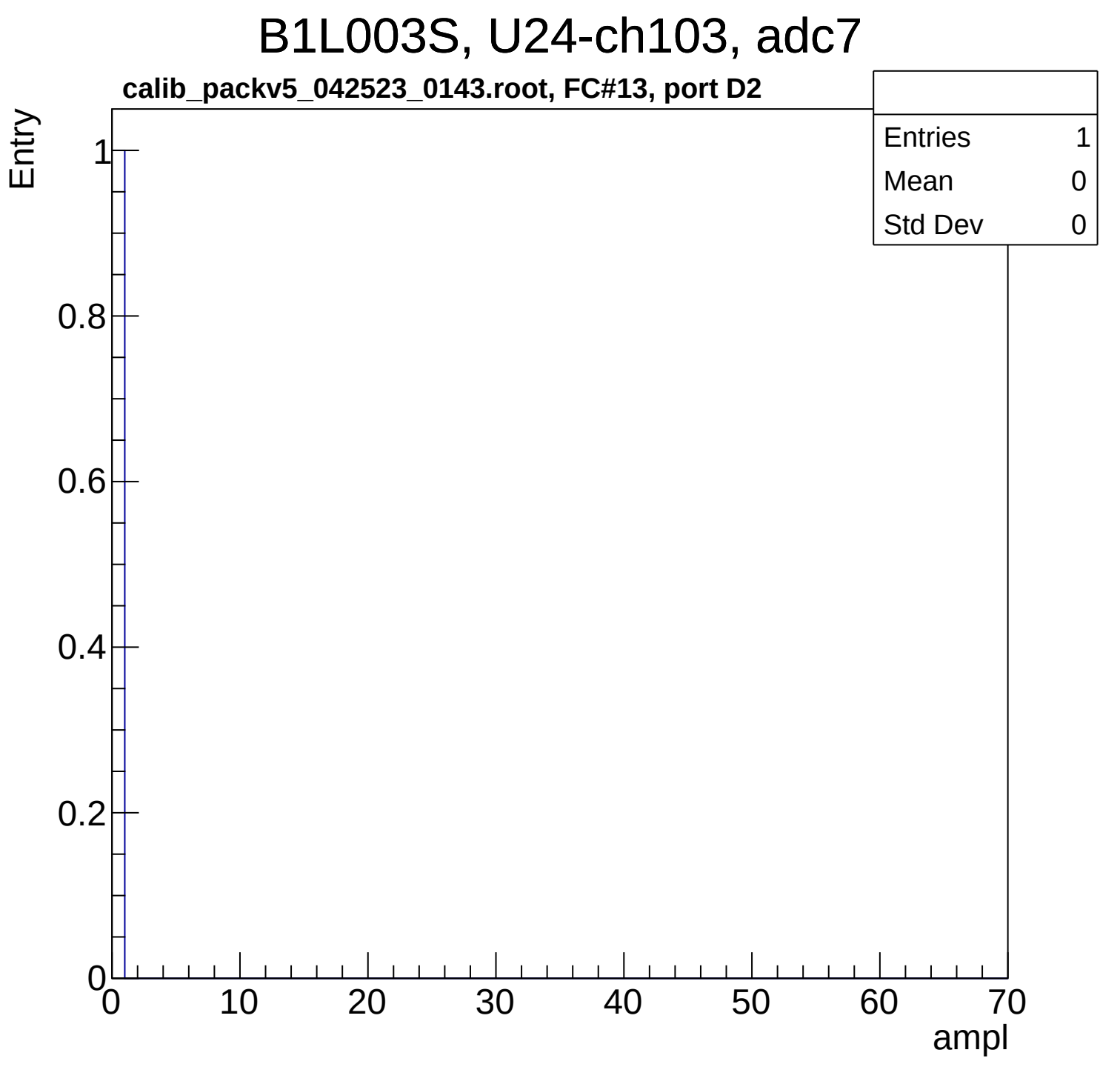

B1L003S, U24-ch103, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 23
Mean 61.35
Std Dev 1.71
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch103, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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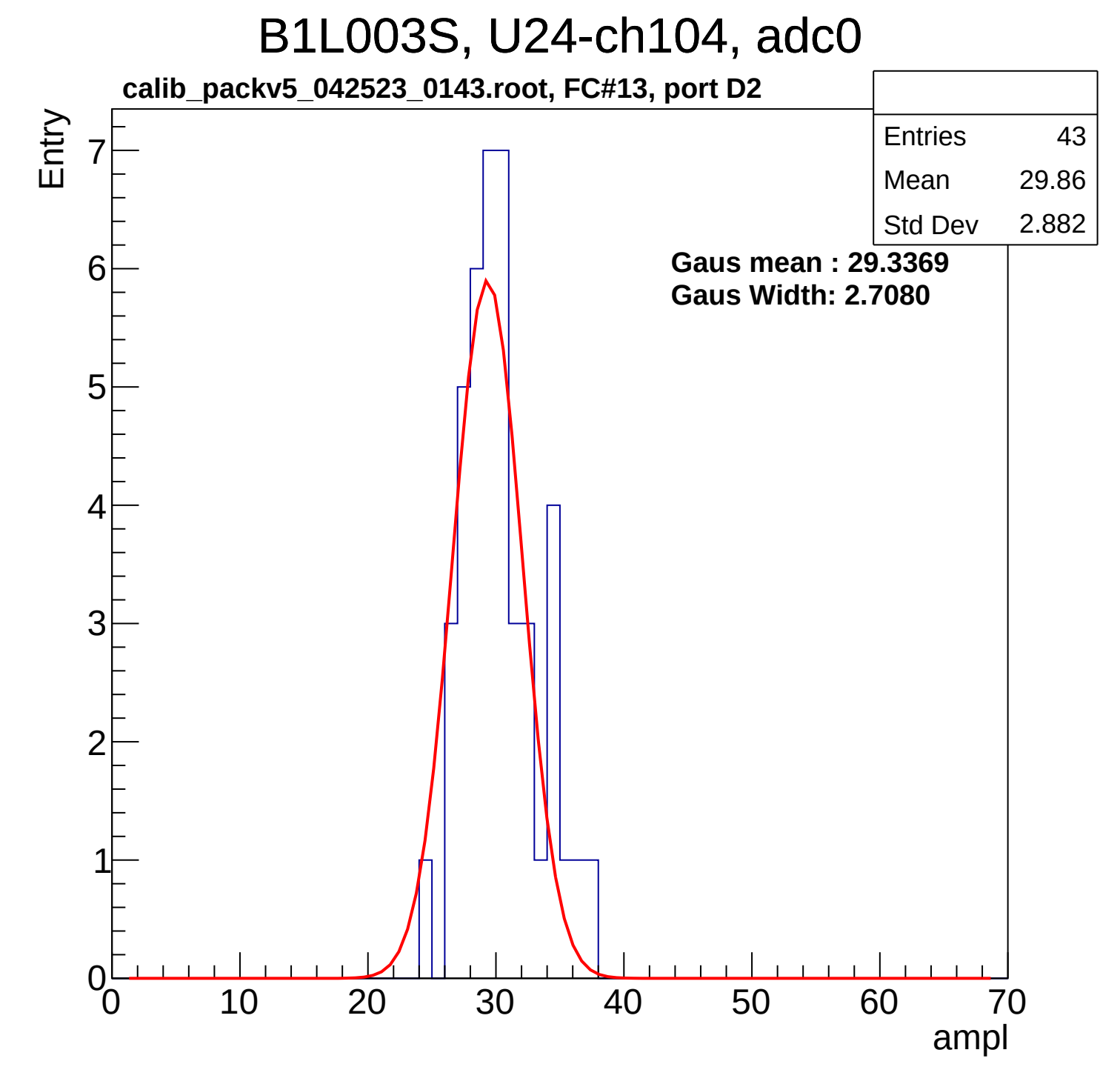
40
50
60
70
ampl


B1L003S, U24-ch104, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 29.86
Std Dev 2.882
Gaus mean : 29.3369
Gaus Width: 2.7080
Entry
ampl
0
1
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3
4
5
6
7
0
10
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60
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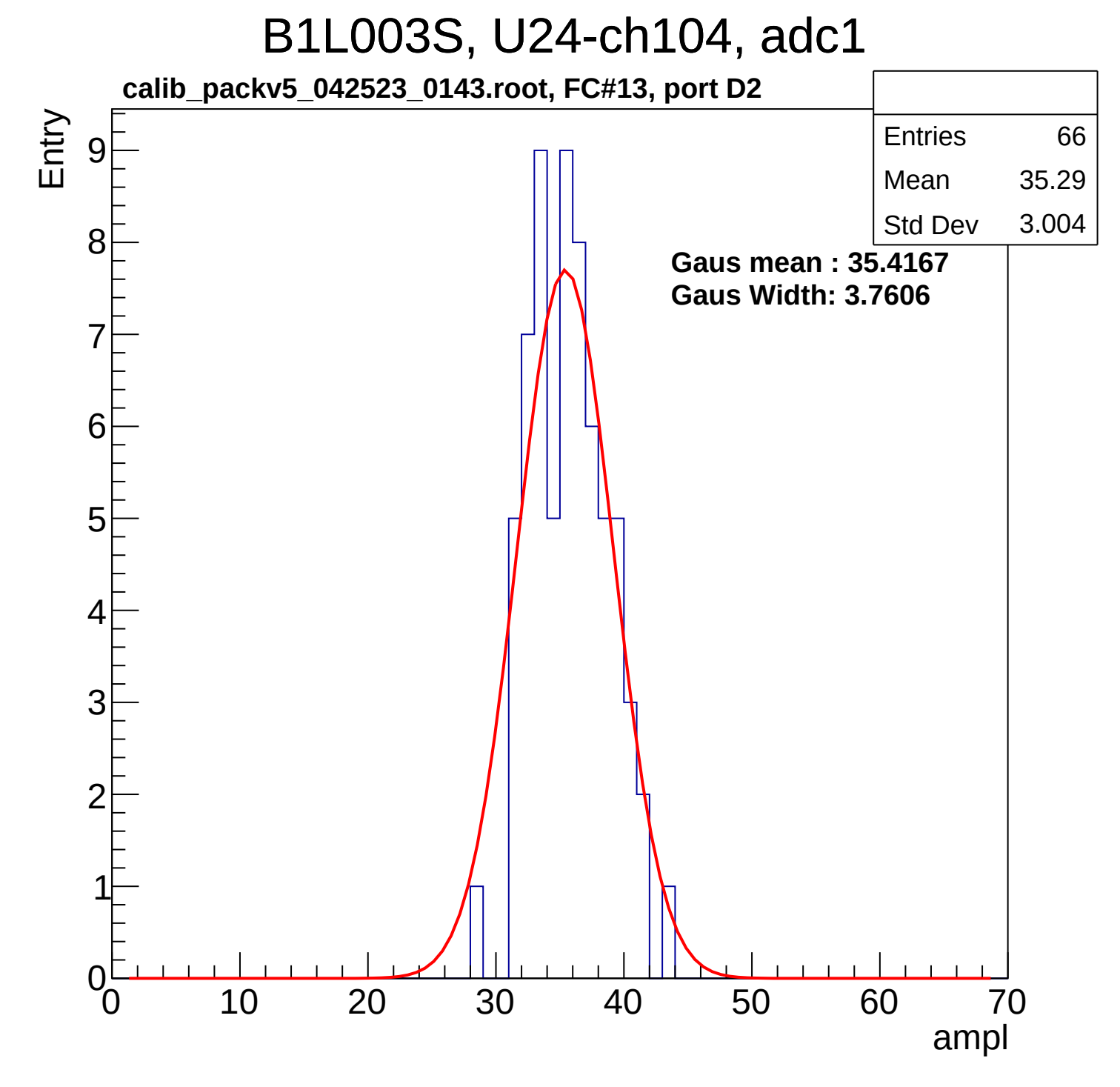
B1L003S, U24-ch104, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.29
Std Dev 3.004
Gaus mean : 35.4167
Gaus Width: 3.7606
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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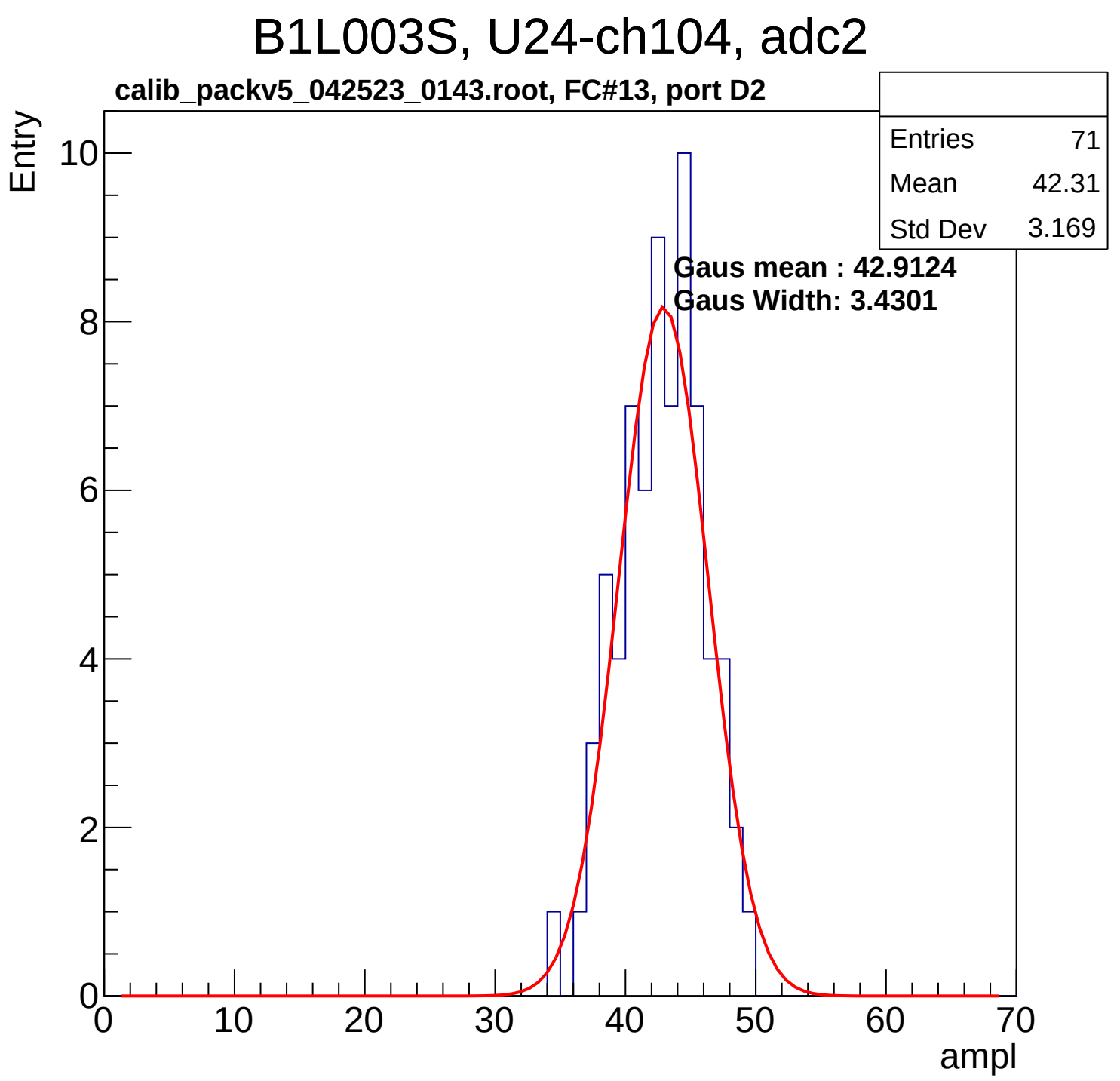
B1L003S, U24-ch104, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 42.31
Std Dev 3.169
Gaus mean : 42.9124
Gaus Width: 3.4301
Entry
ampl
0
2
4
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8
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0
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60
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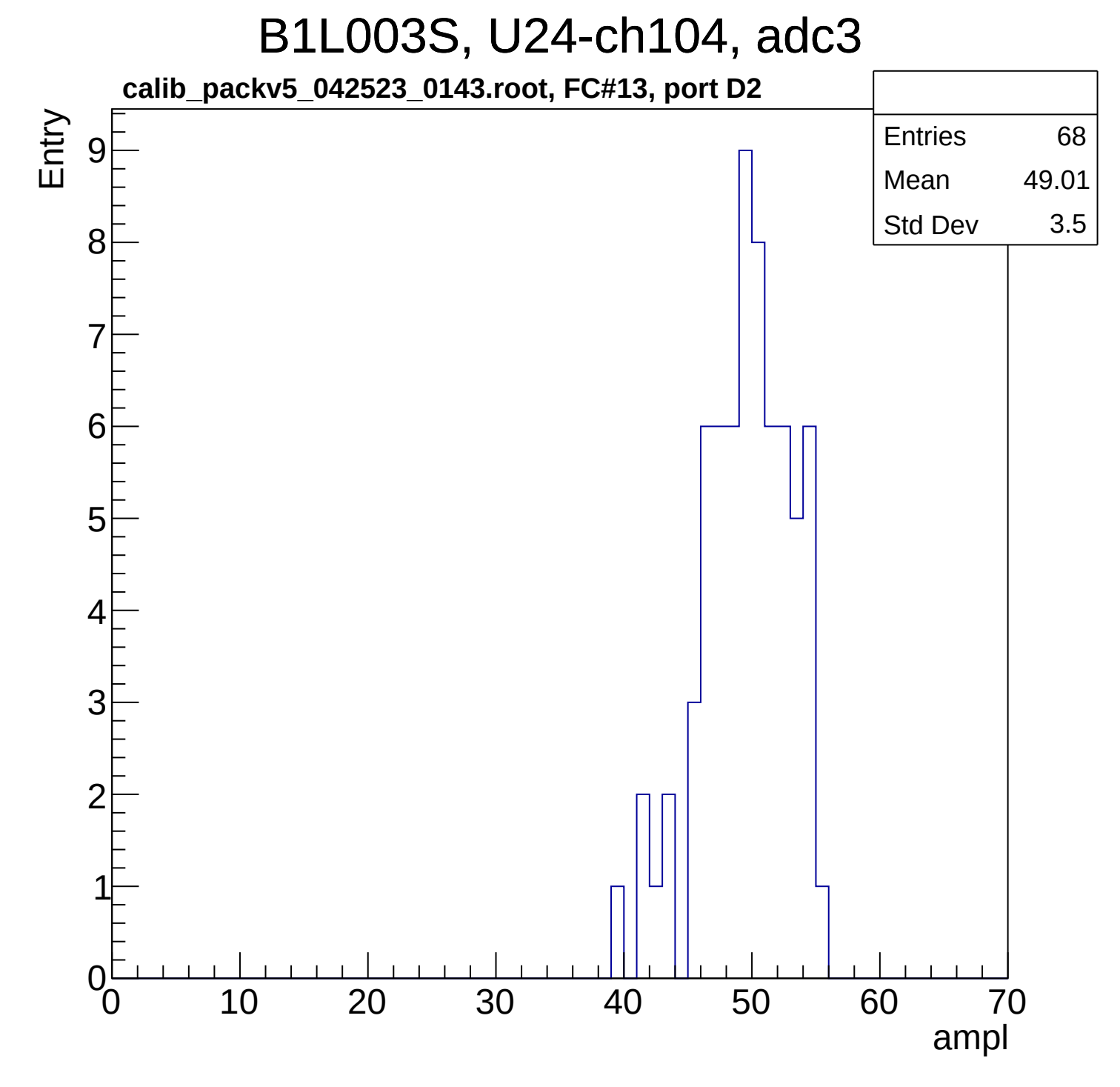
B1L003S, U24-ch104, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.01
Std Dev 3.5
Entry
ampl
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3
4
5
6
7
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0
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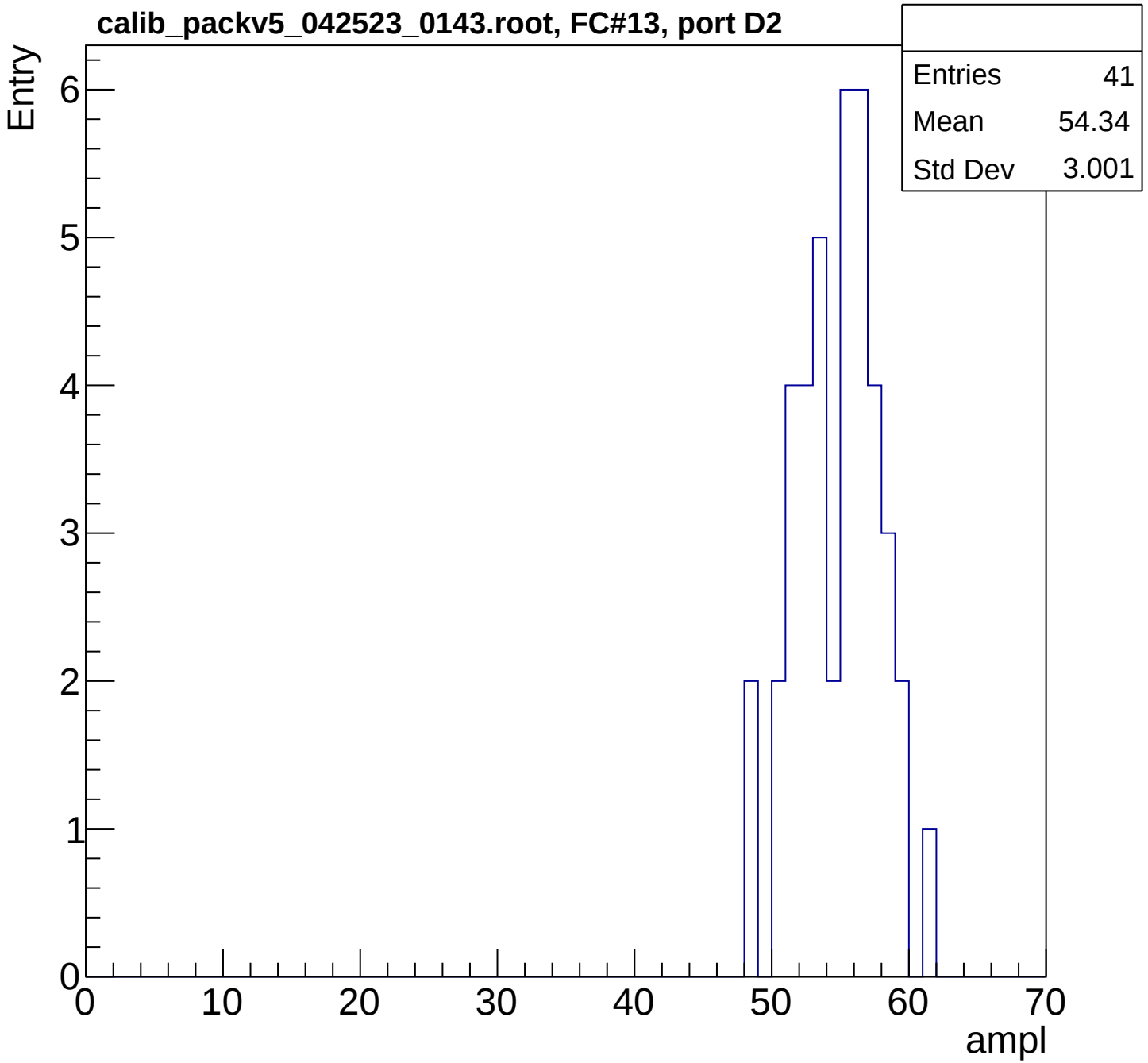
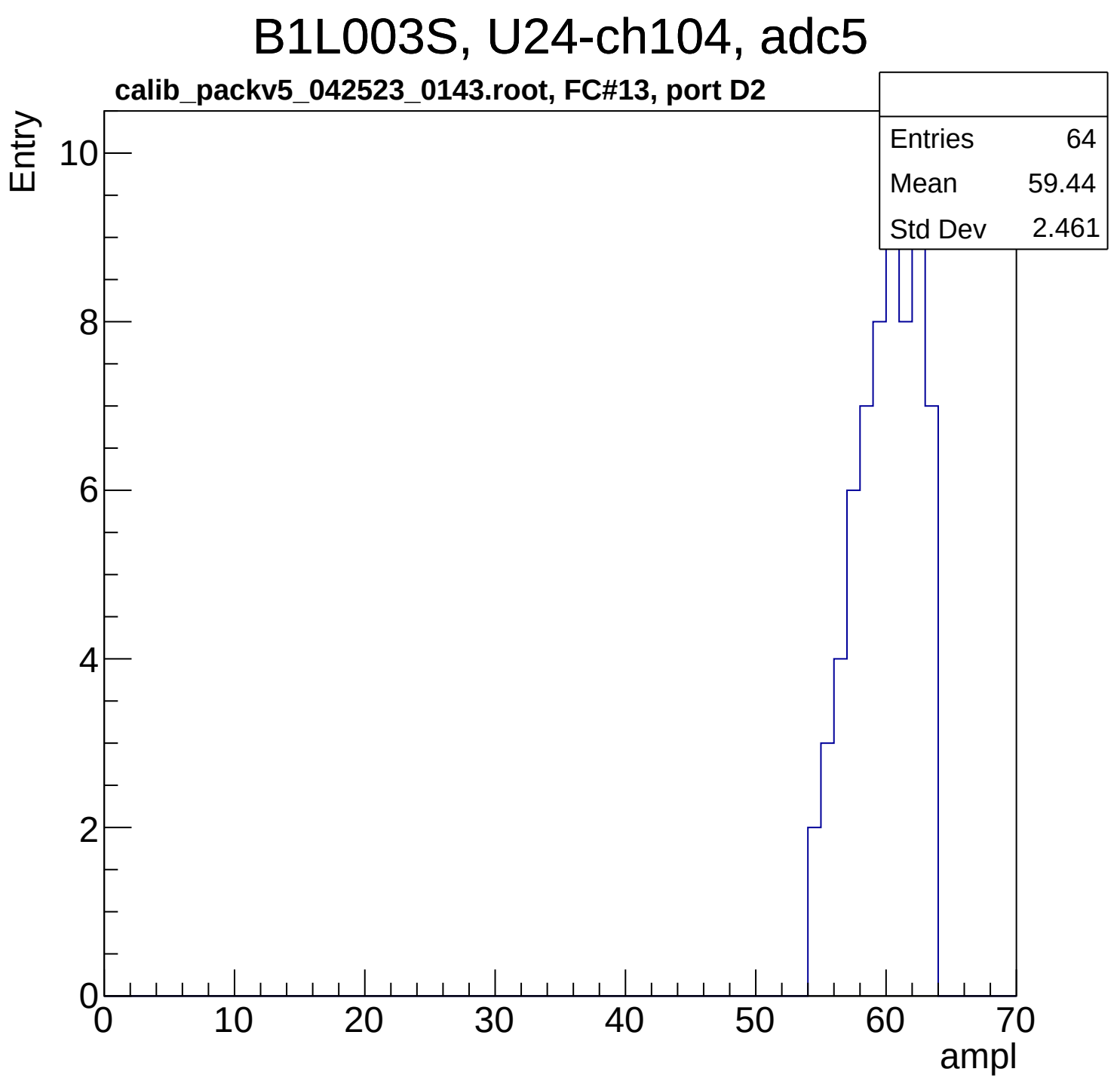
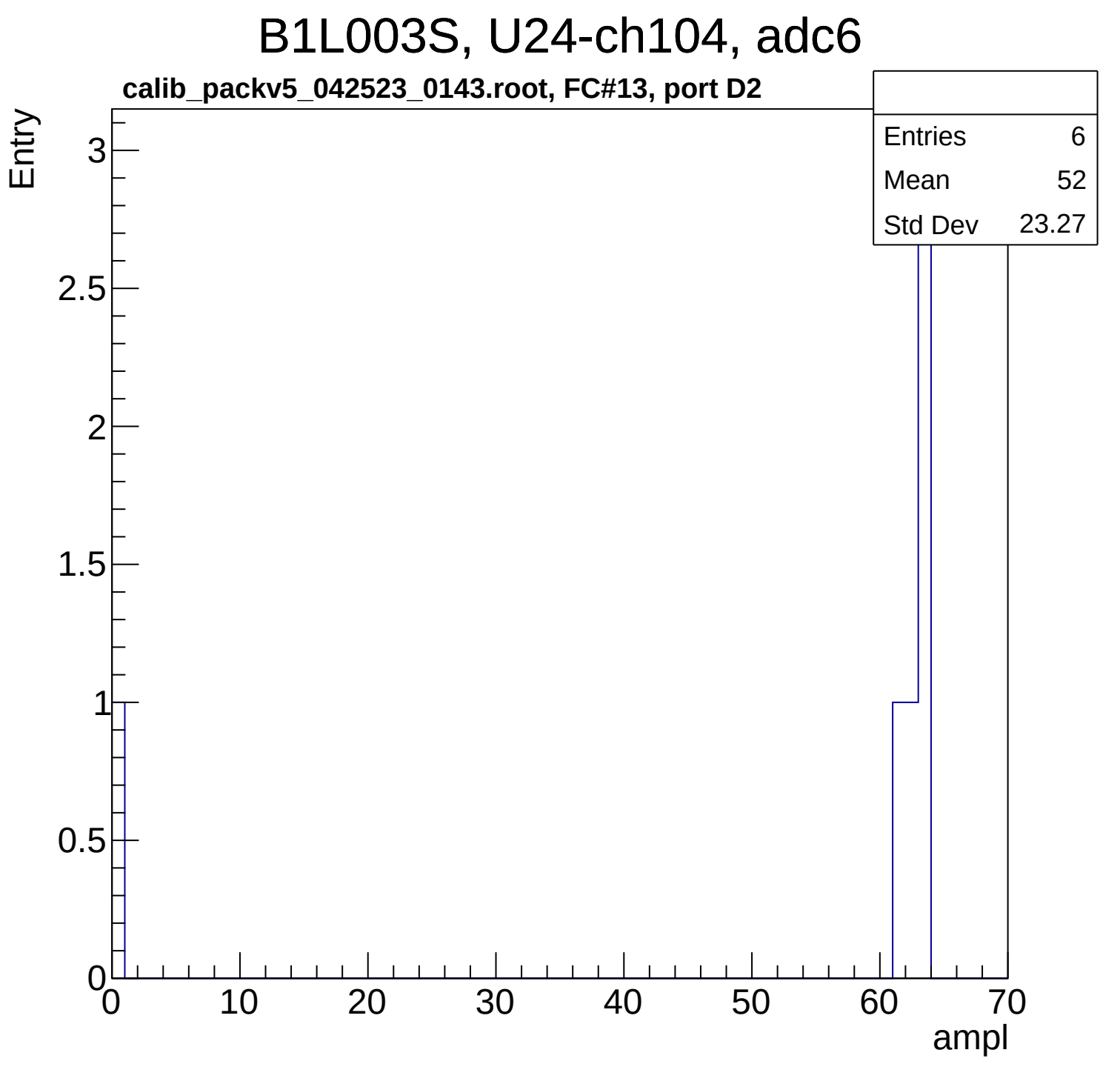

B1L003S, U24-ch104, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 54.34
Std Dev 3.001
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch104, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 59.44
Std Dev 2.461
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch104, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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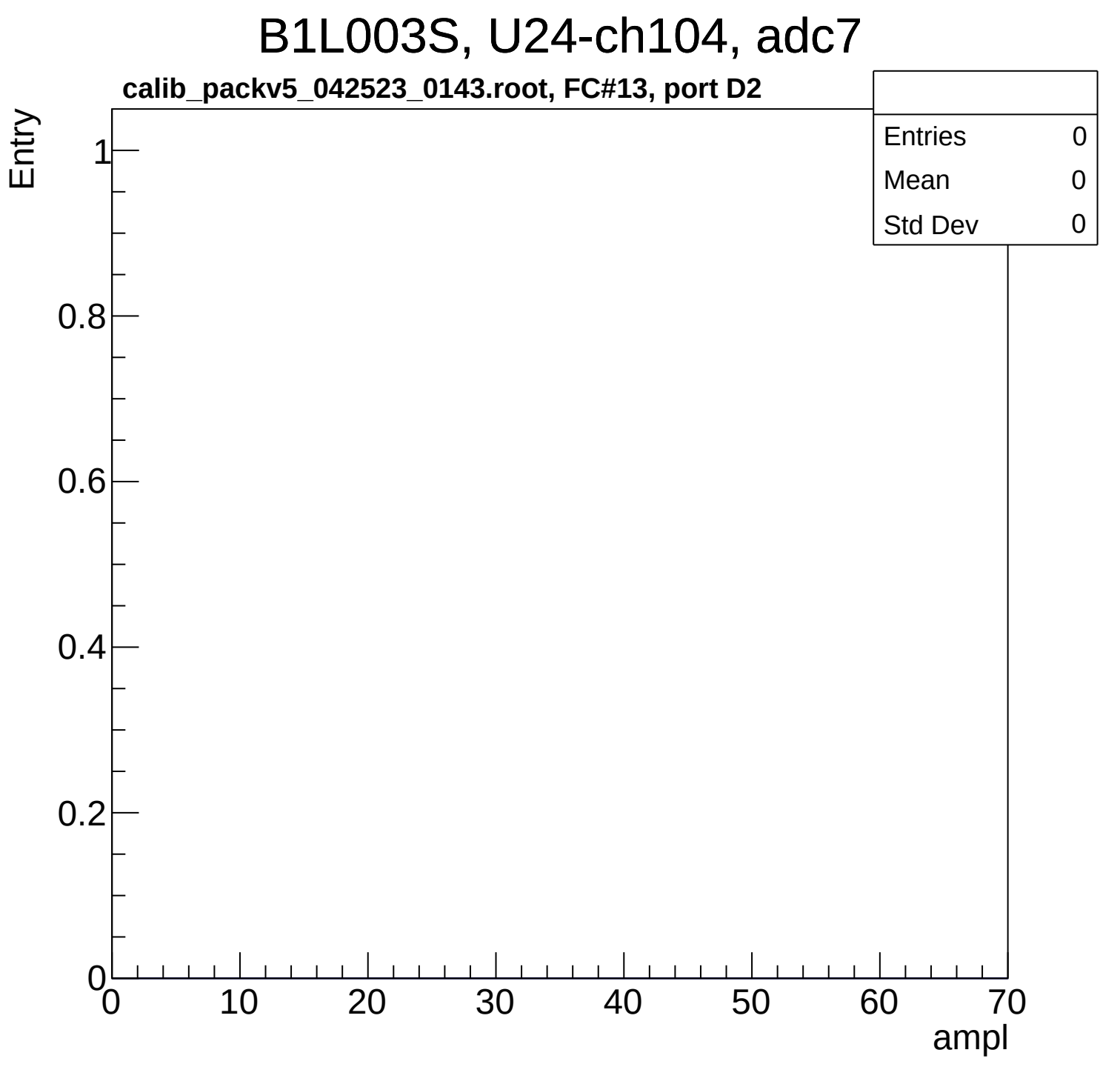
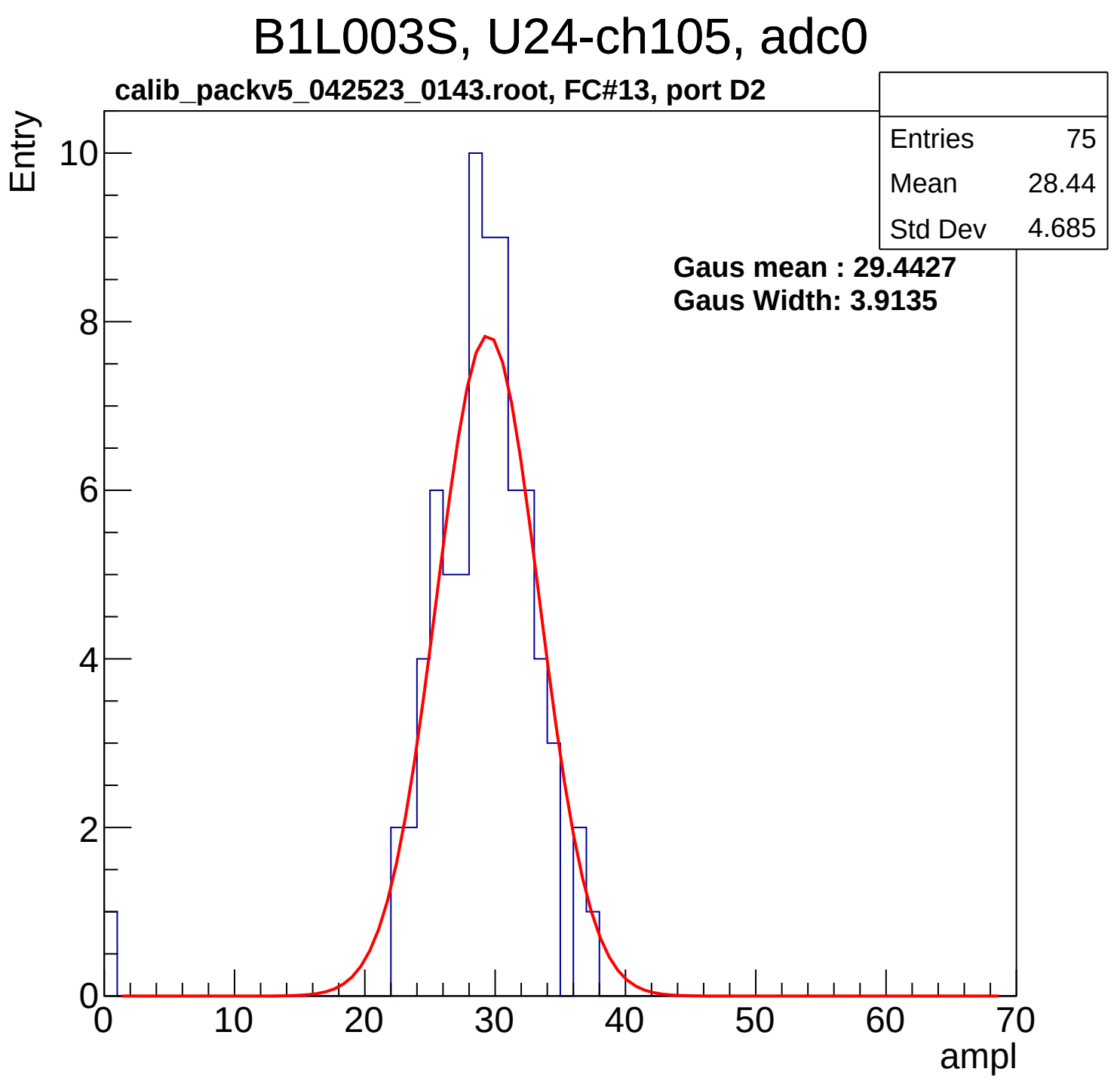
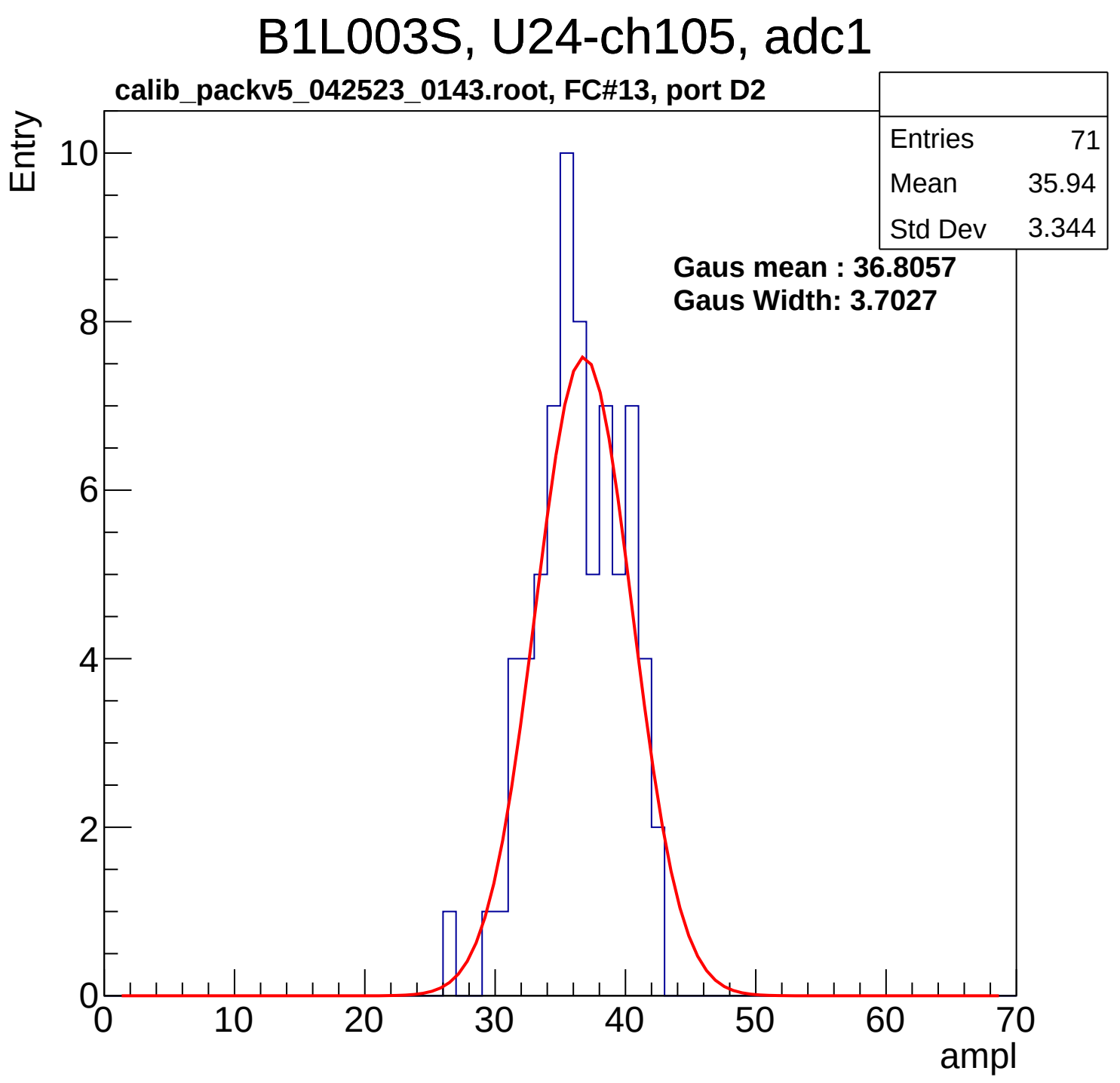

B1L003S, U24-ch104, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch105, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.44
Std Dev 4.685
Gaus mean : 29.4427
Gaus Width: 3.9135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch105, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.94
Std Dev 3.344
Gaus mean : 36.8057
Gaus Width: 3.7027
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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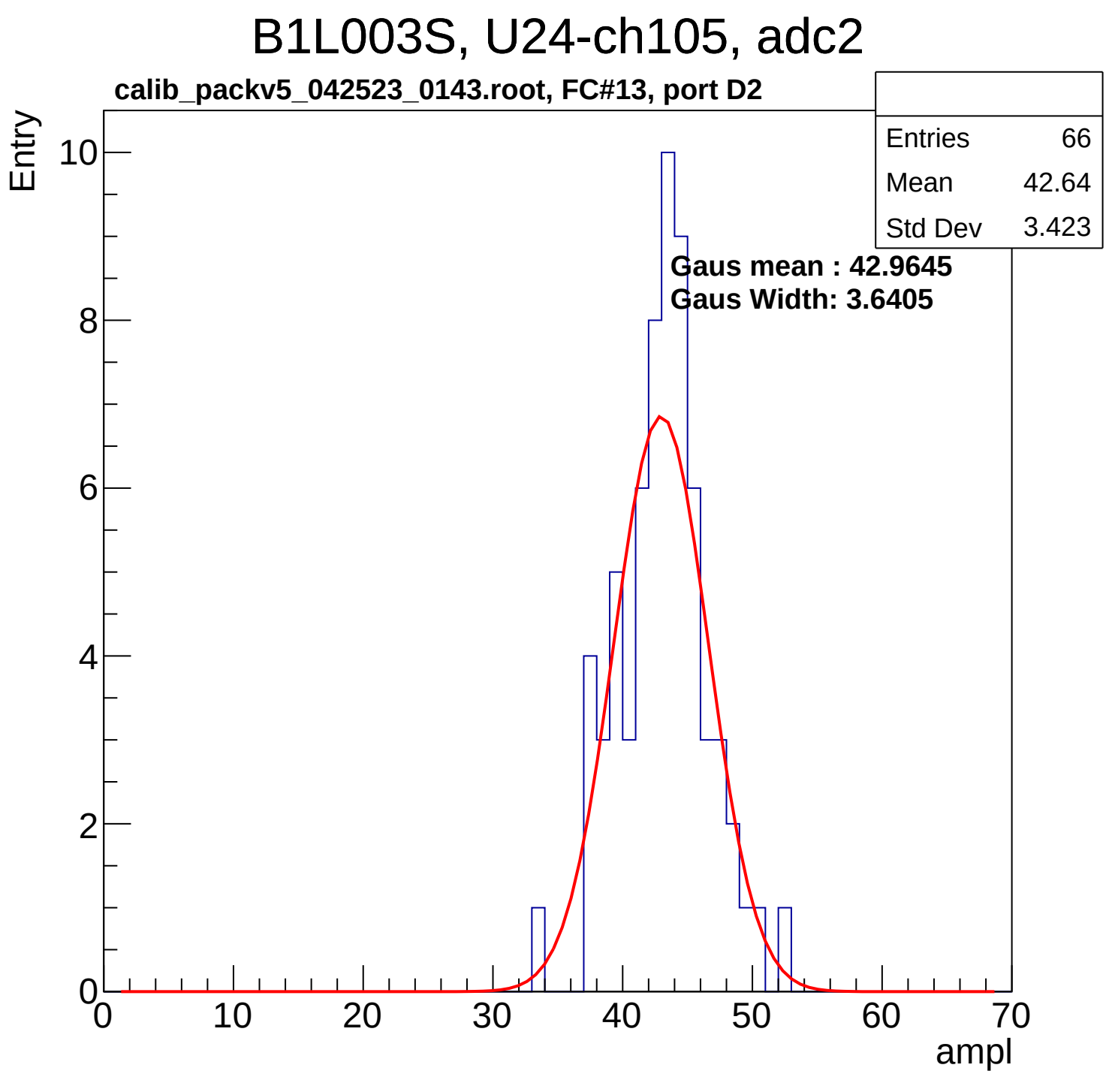
B1L003S, U24-ch105, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.64
Std Dev 3.423
Gaus mean : 42.9645
Gaus Width: 3.6405
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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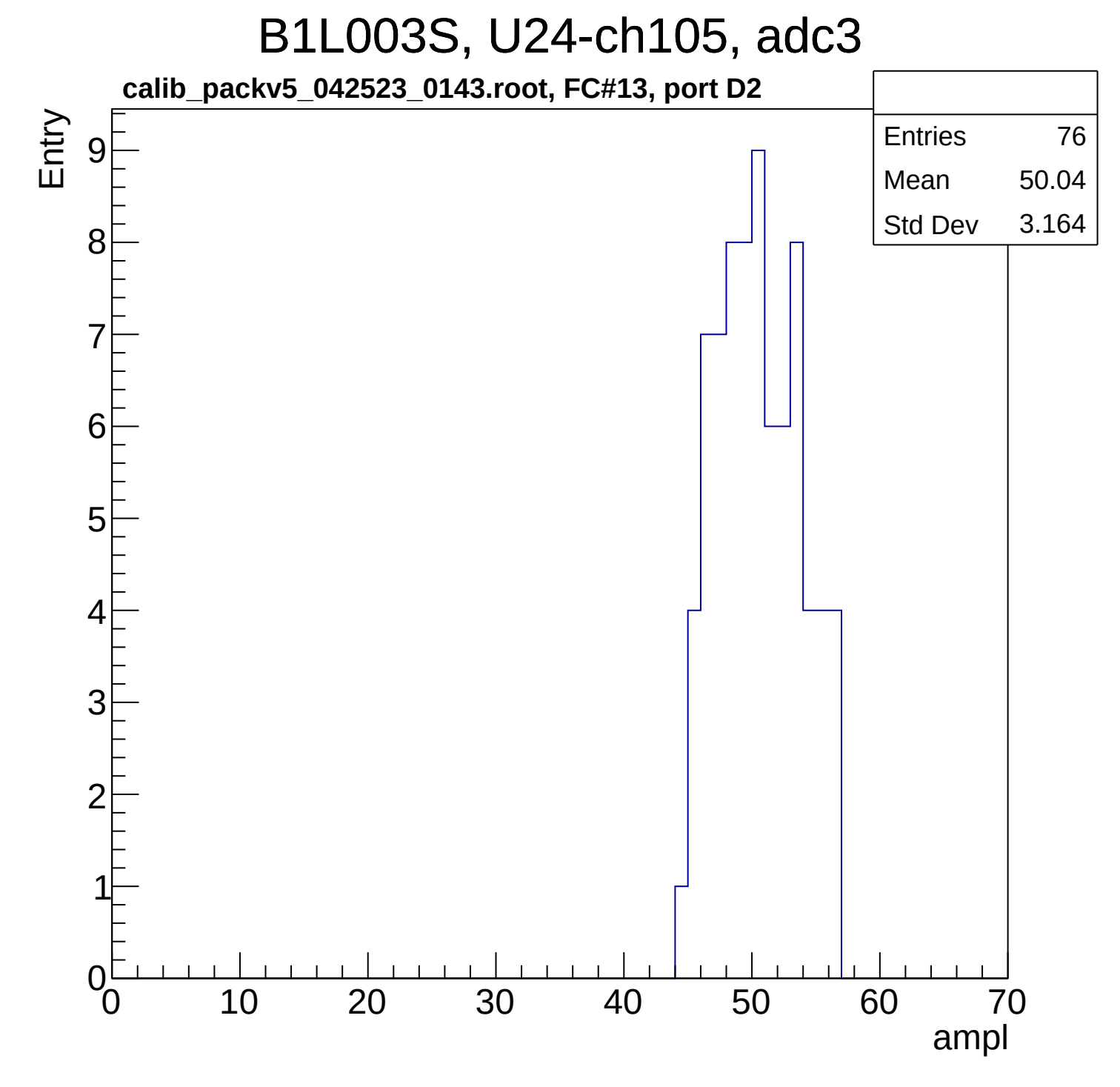
B1L003S, U24-ch105, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 50.04
Std Dev 3.164
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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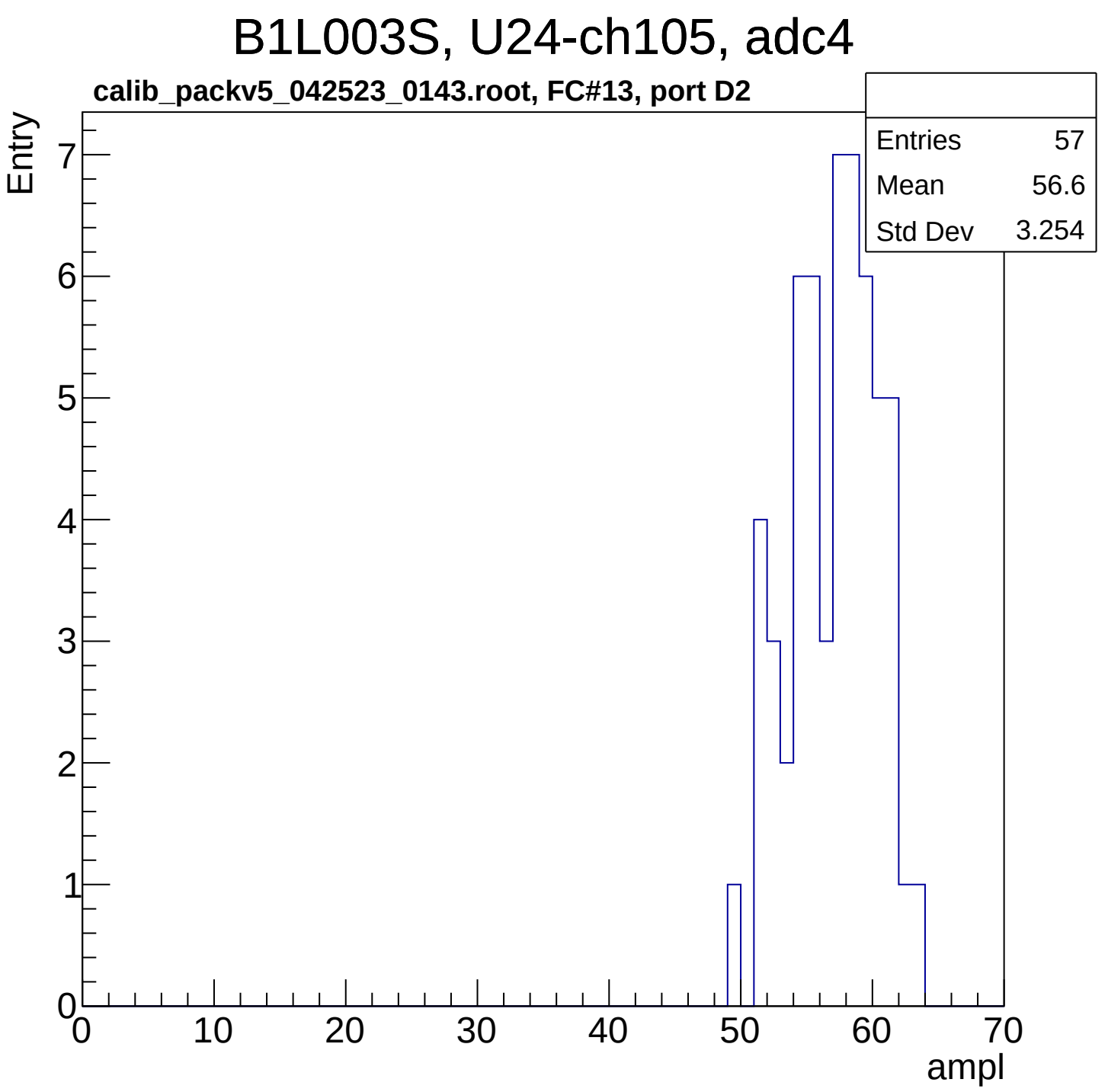
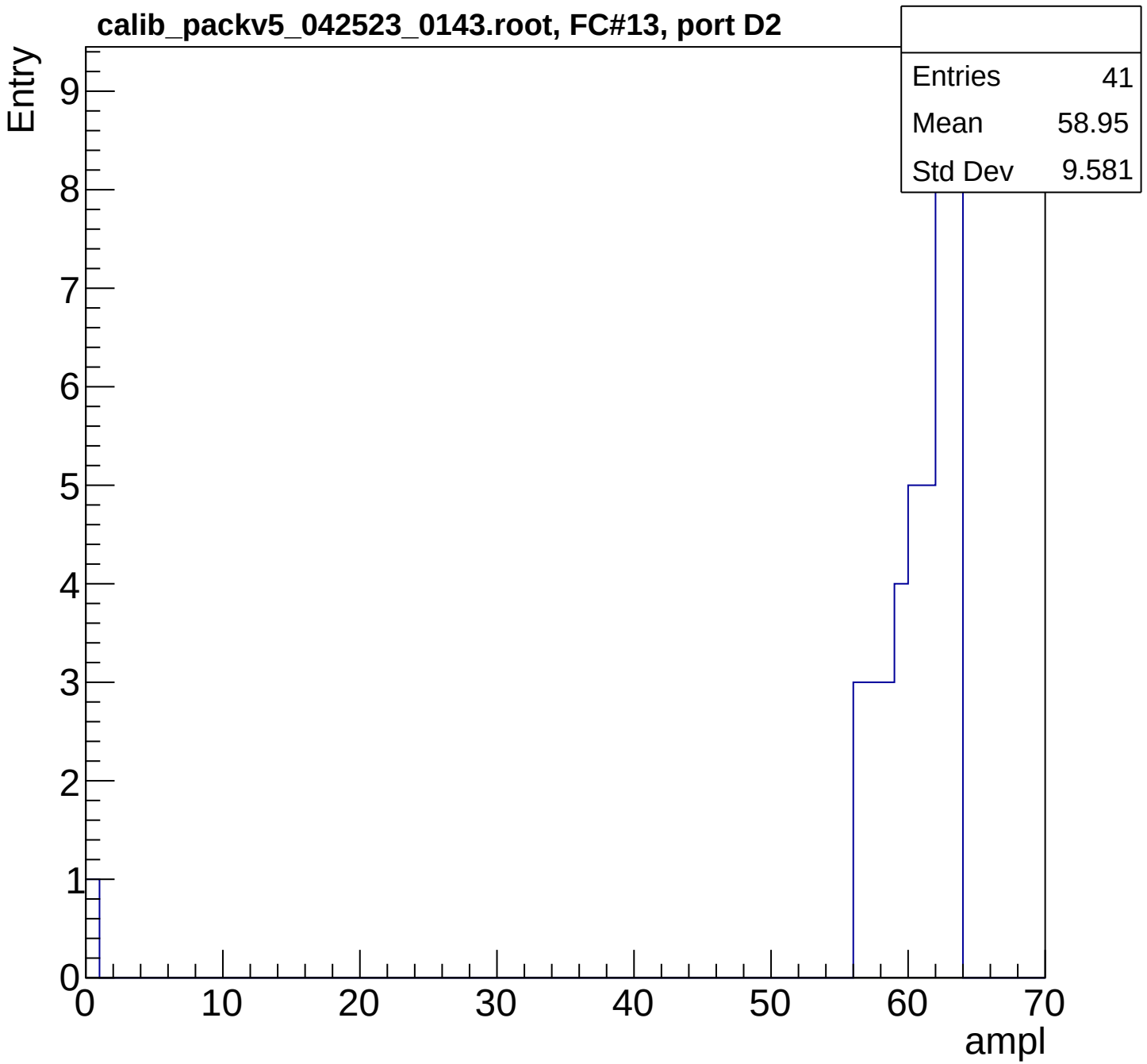

B1L003S, U24-ch105, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 56.6
Std Dev 3.254
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch105, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.95
Std Dev 9.581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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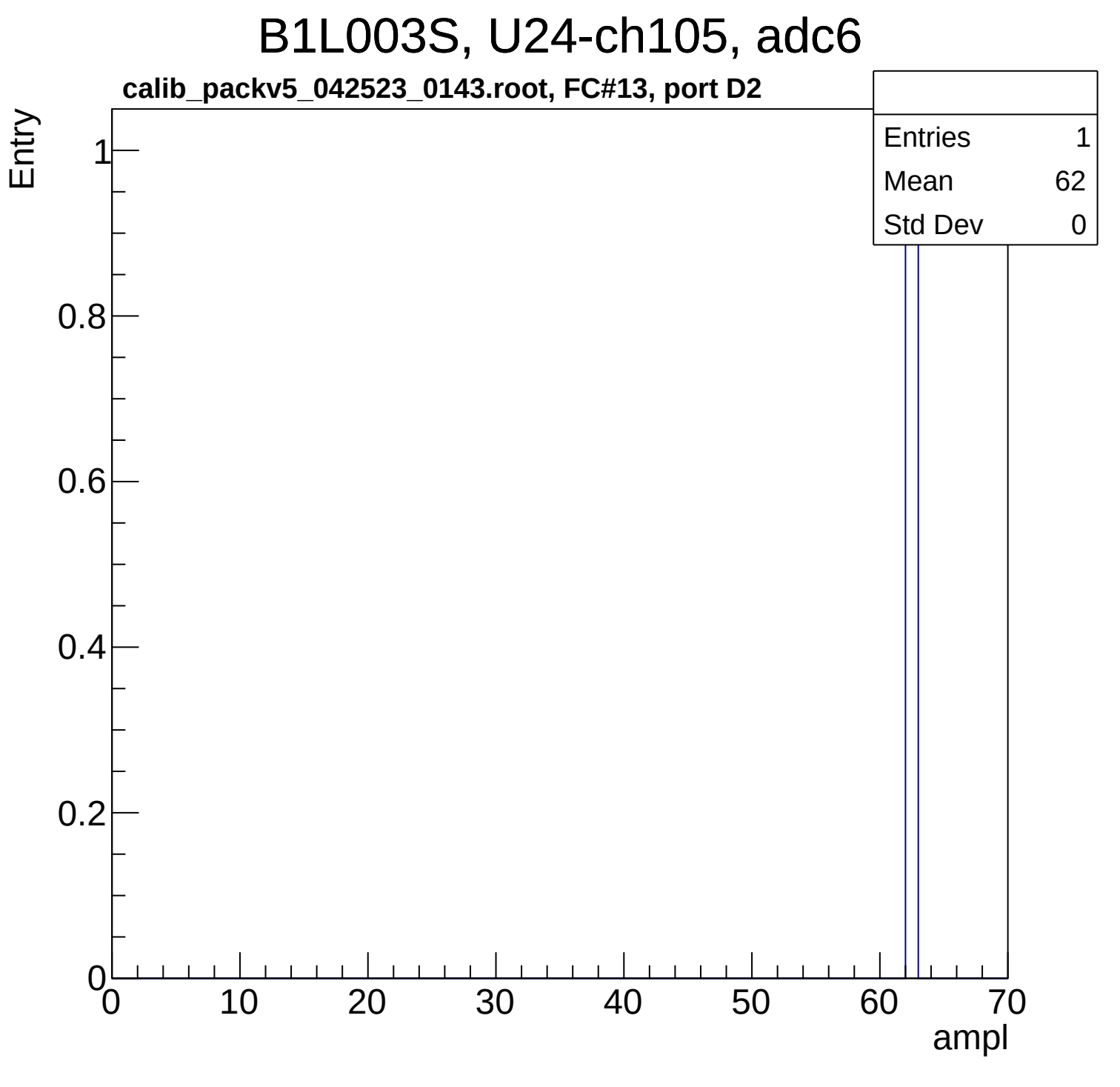
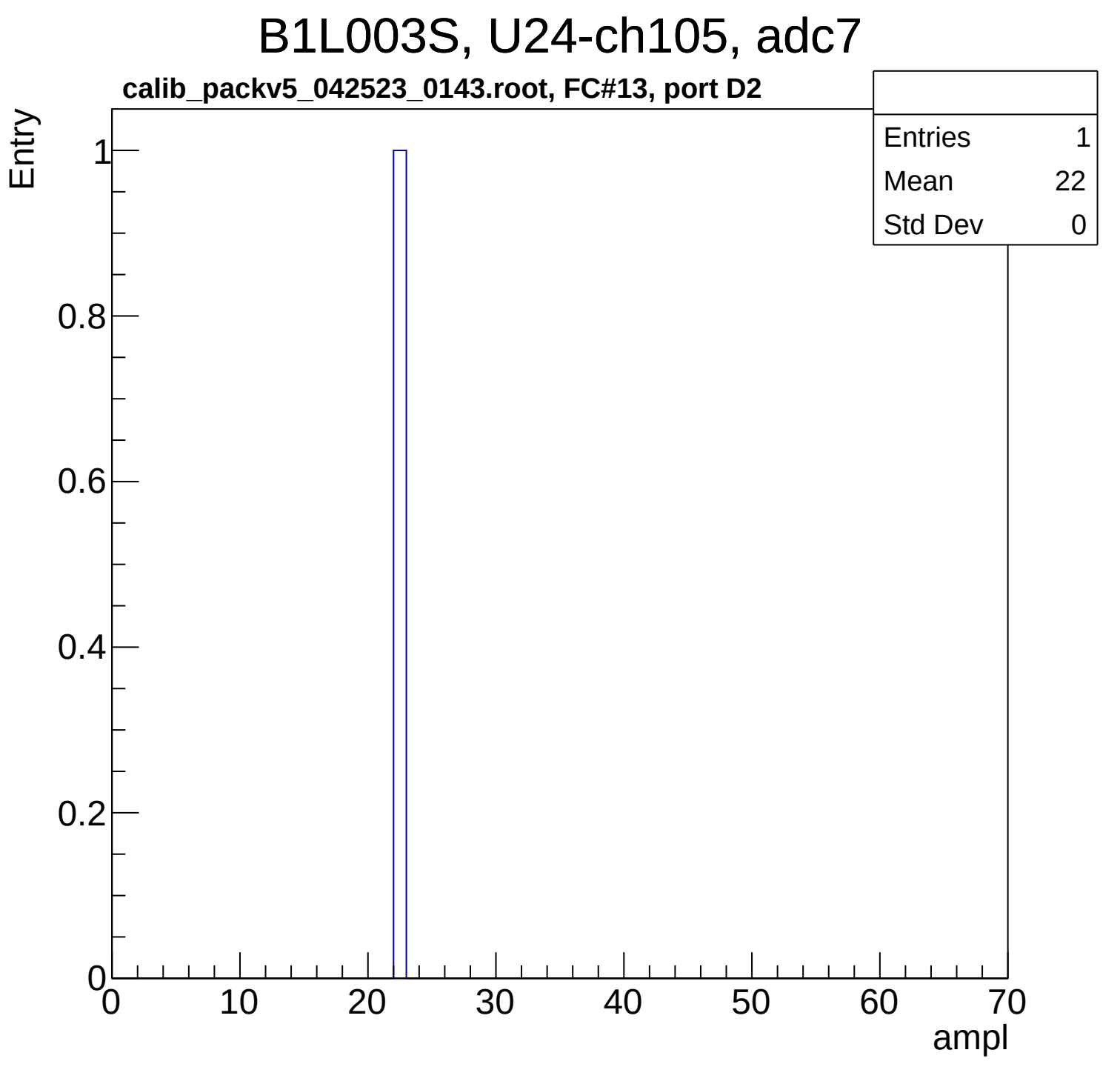

B1L003S, U24-ch105, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch105, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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30
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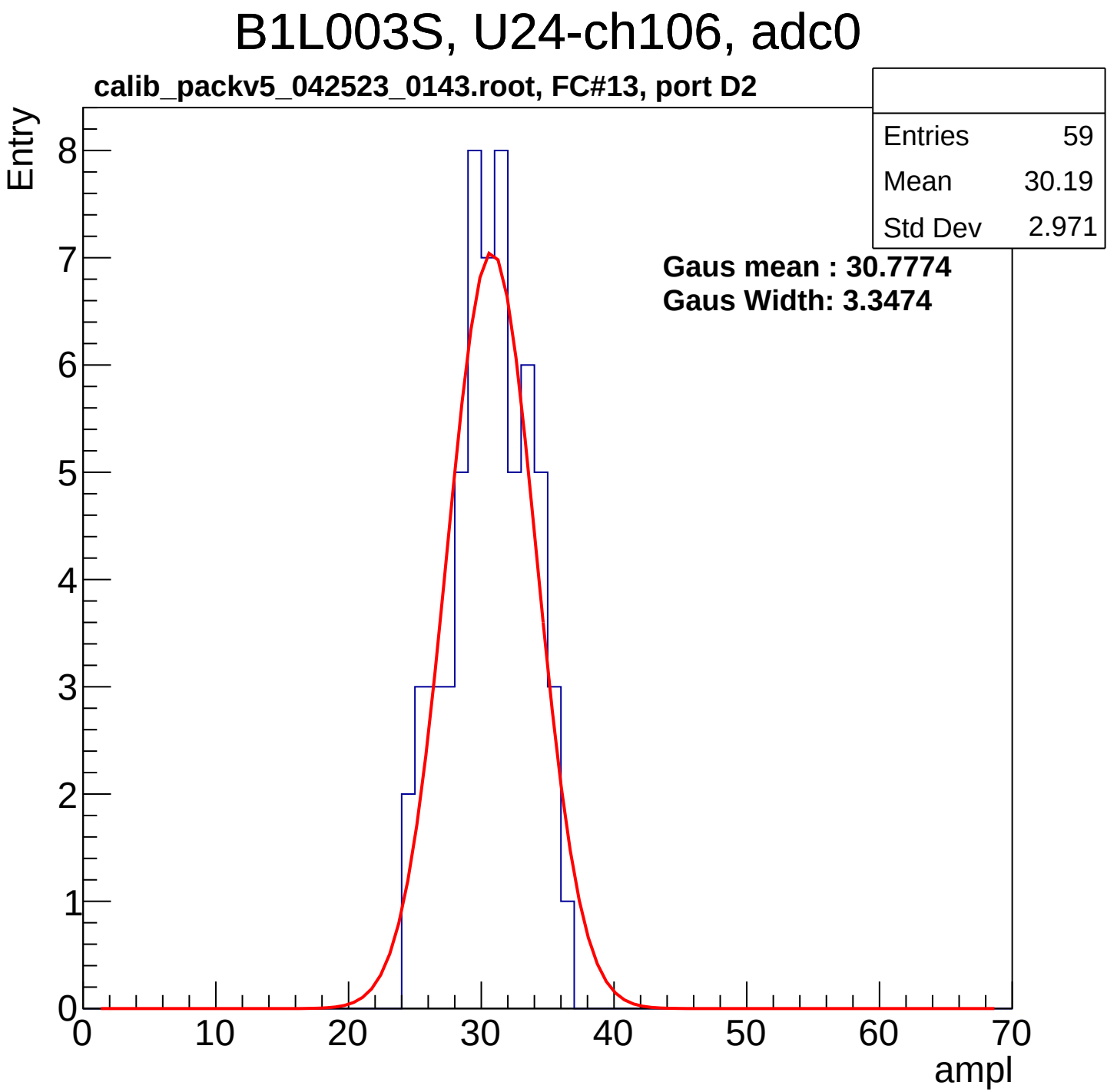
50
60
70
ampl


B1L003S, U24-ch106, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 30.19
Std Dev 2.971
Gaus mean : 30.7774
Gaus Width: 3.3474
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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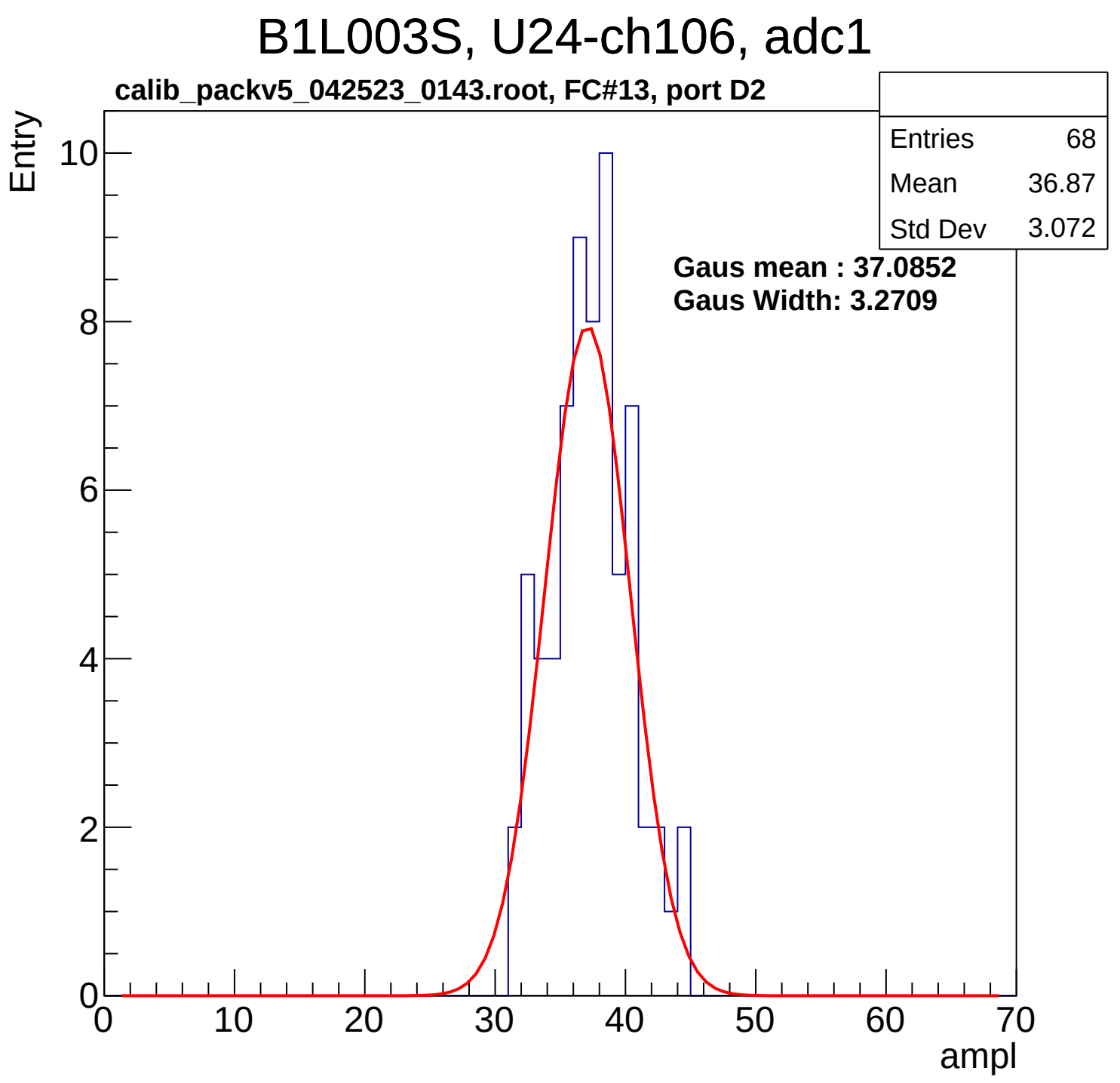
B1L003S, U24-ch106, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.87
Std Dev 3.072
Gaus mean : 37.0852
Gaus Width: 3.2709
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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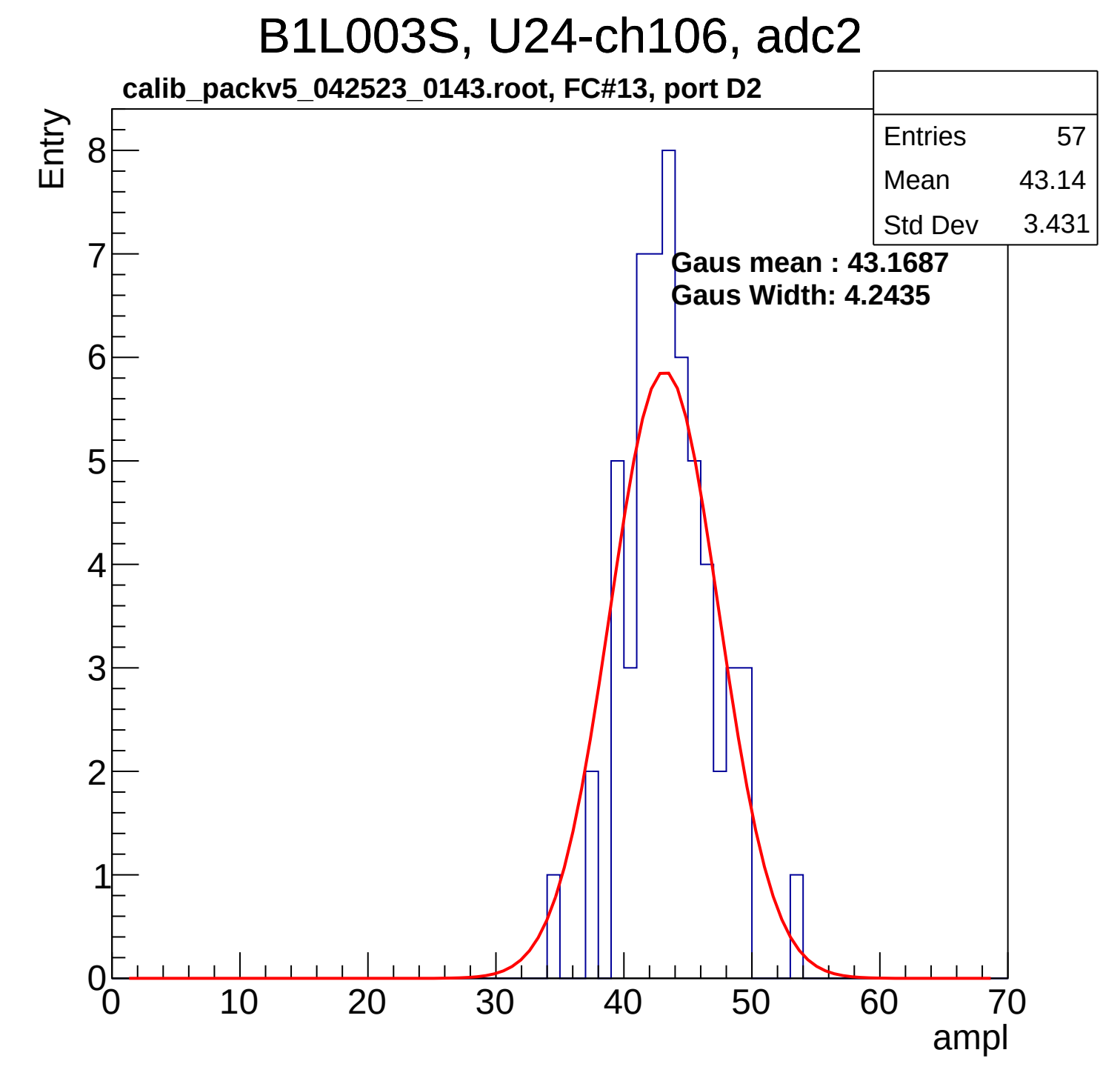
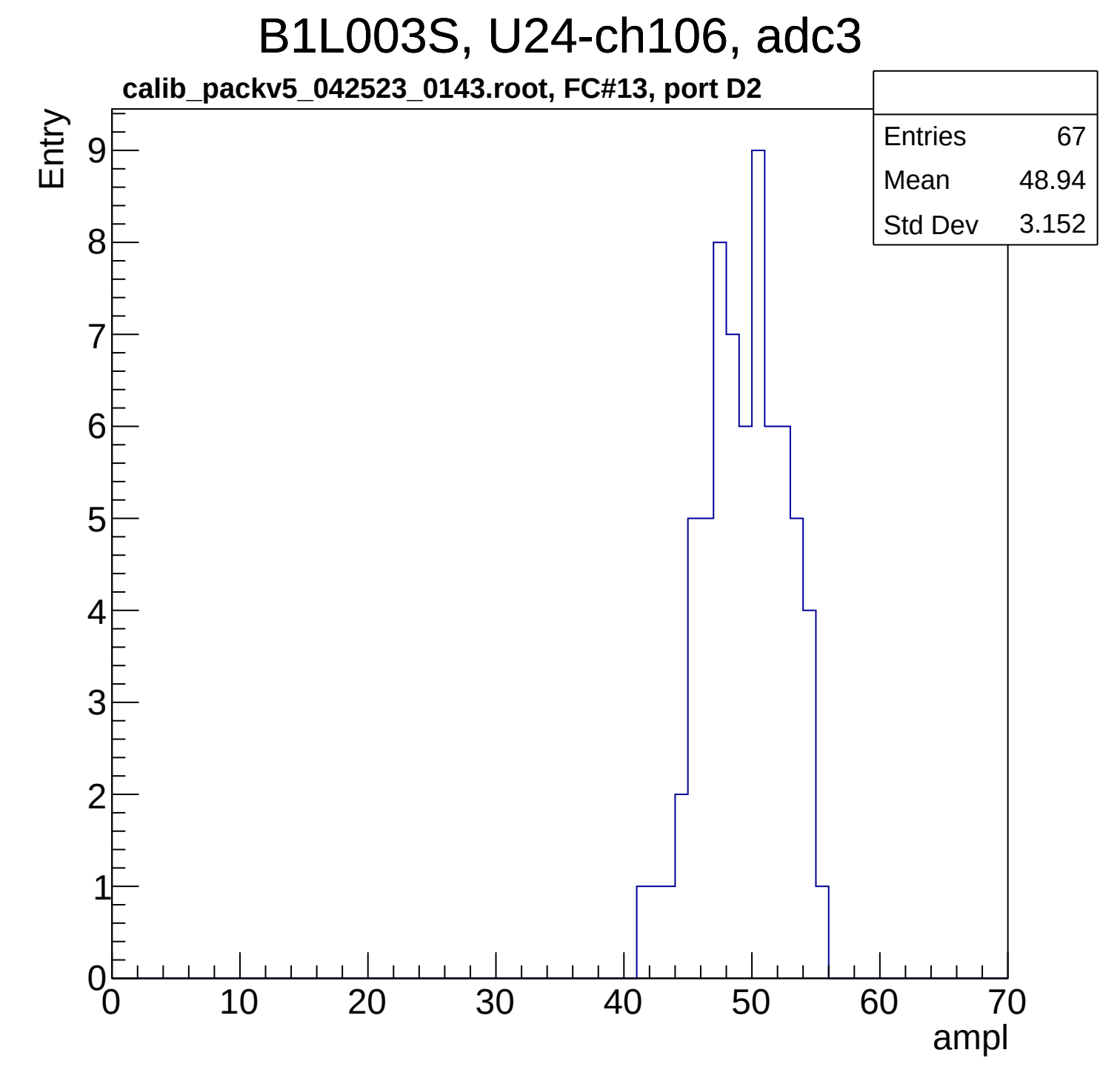

B1L003S, U24-ch106, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 43.14
Std Dev 3.431
Gaus mean : 43.1687
Gaus Width: 4.2435
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch106, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.94
Std Dev 3.152
Entry
ampl
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2
3
4
5
6
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0
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60
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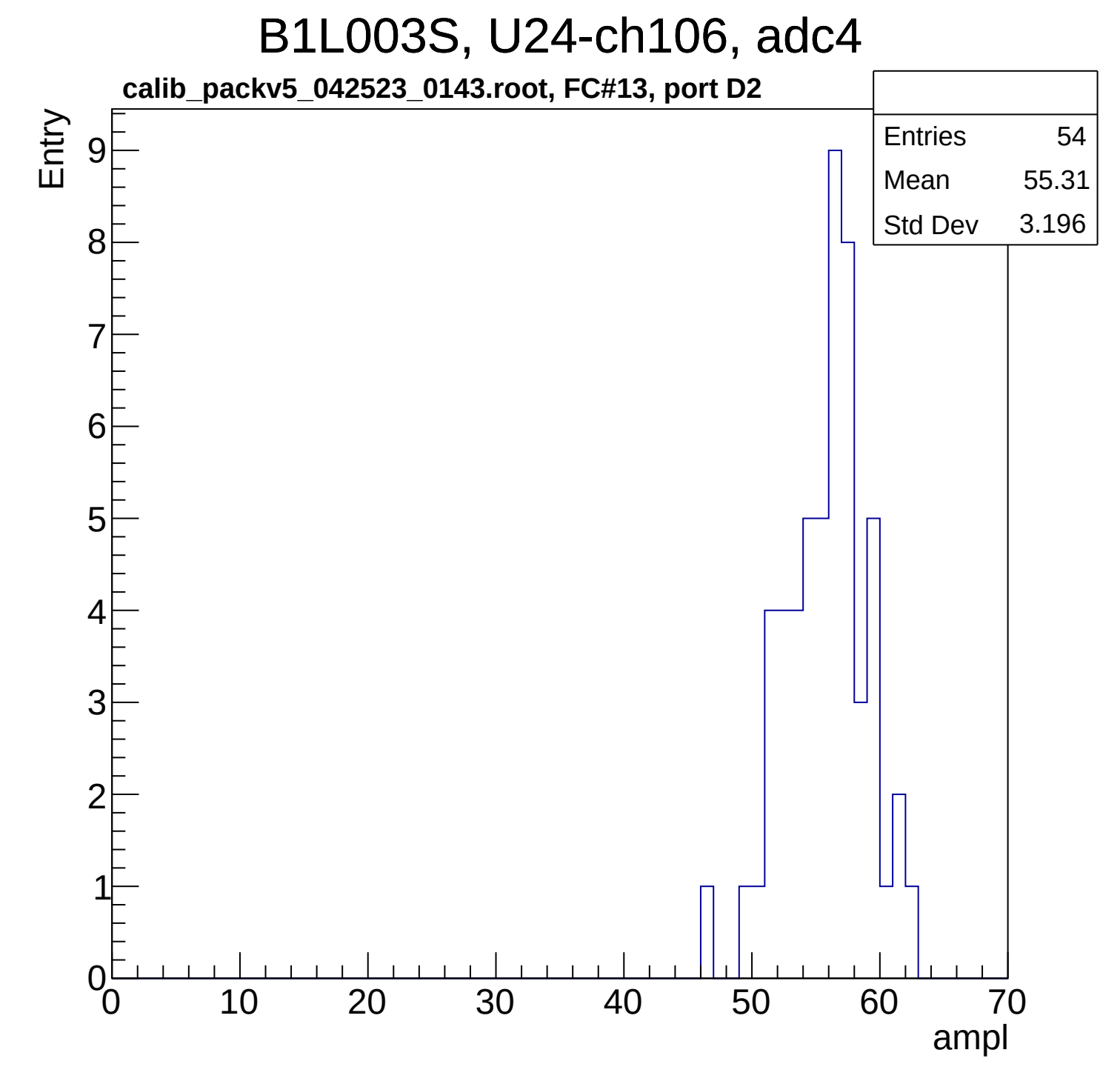
B1L003S, U24-ch106, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.31
Std Dev 3.196
Entry
ampl
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3
4
5
6
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0
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40
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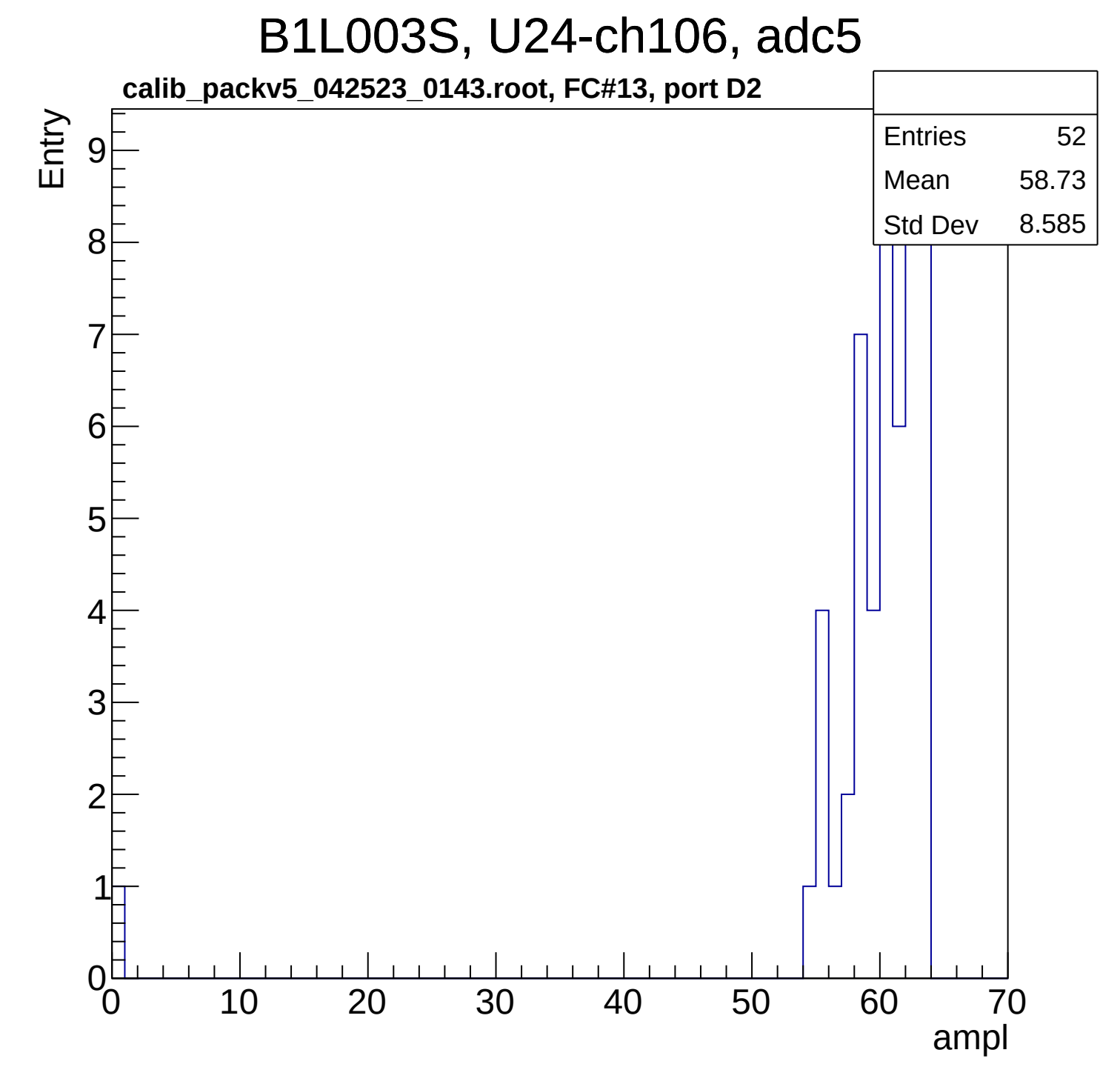
B1L003S, U24-ch106, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.73
Std Dev 8.585
Entry
ampl
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0
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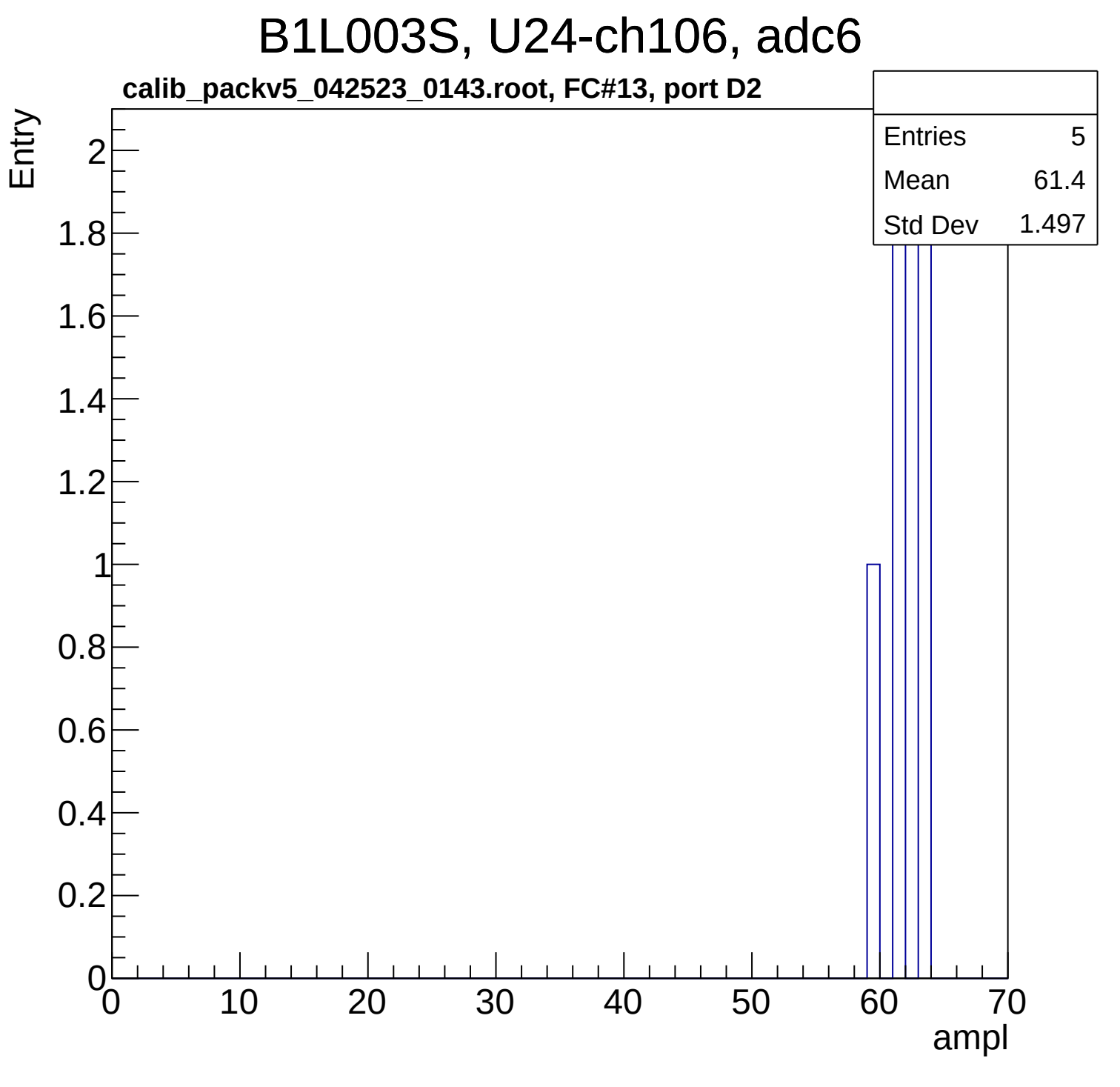
B1L003S, U24-ch106, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.4
Std Dev 1.497
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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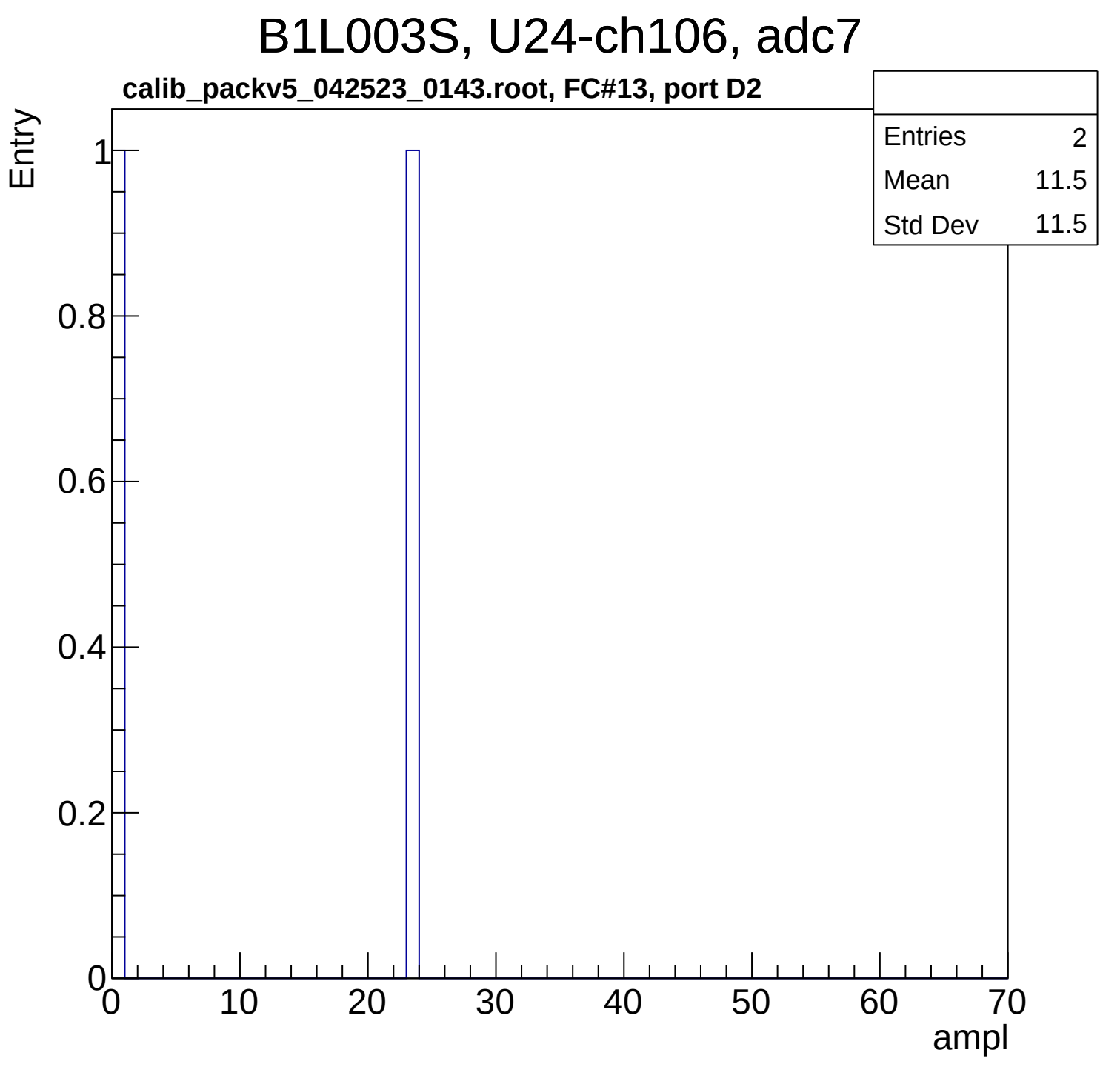
B1L003S, U24-ch106, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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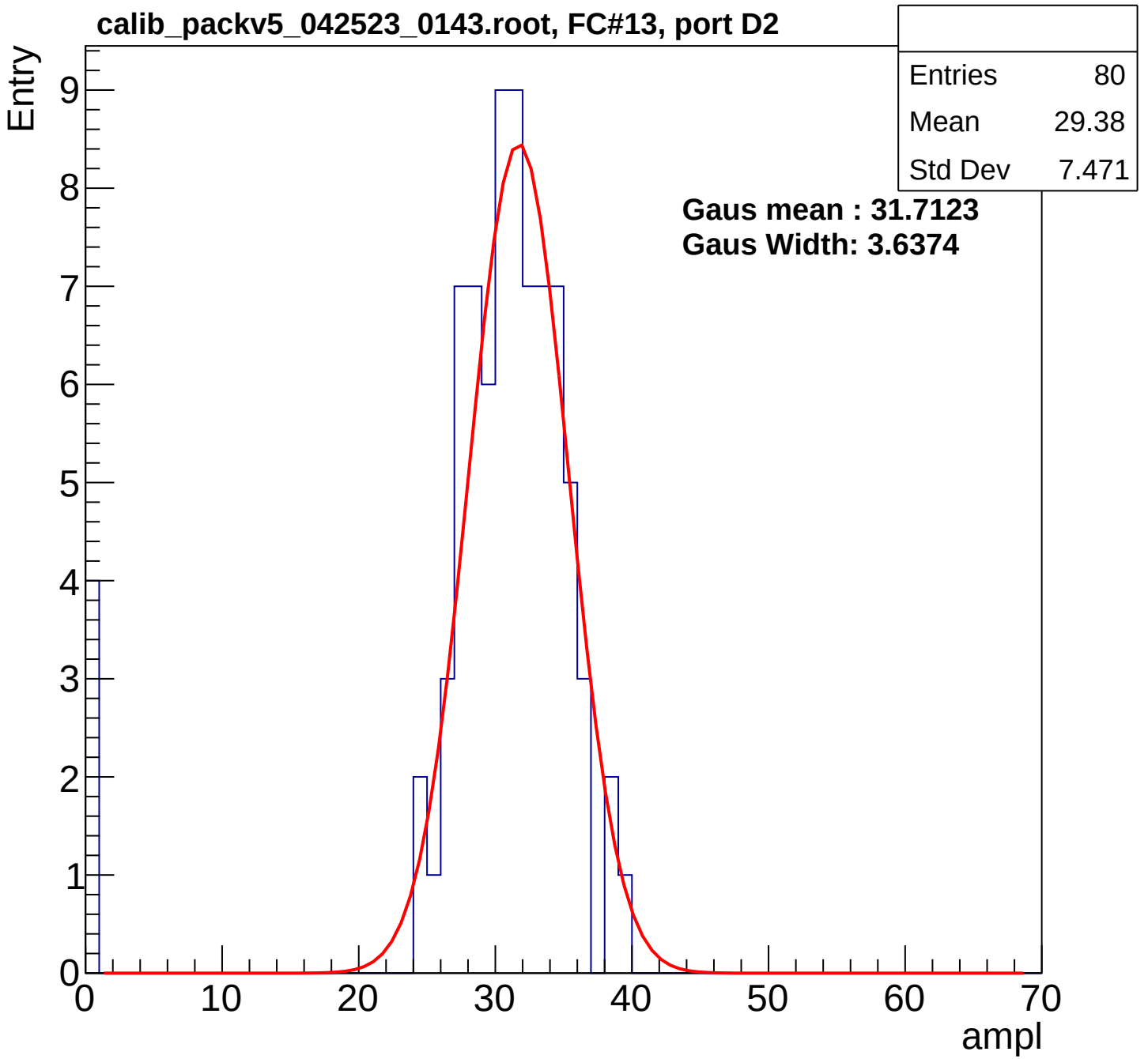
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch107, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 29.38
Std Dev 7.471
Gaus mean : 31.7123
Gaus Width: 3.6374
Entry
ampl
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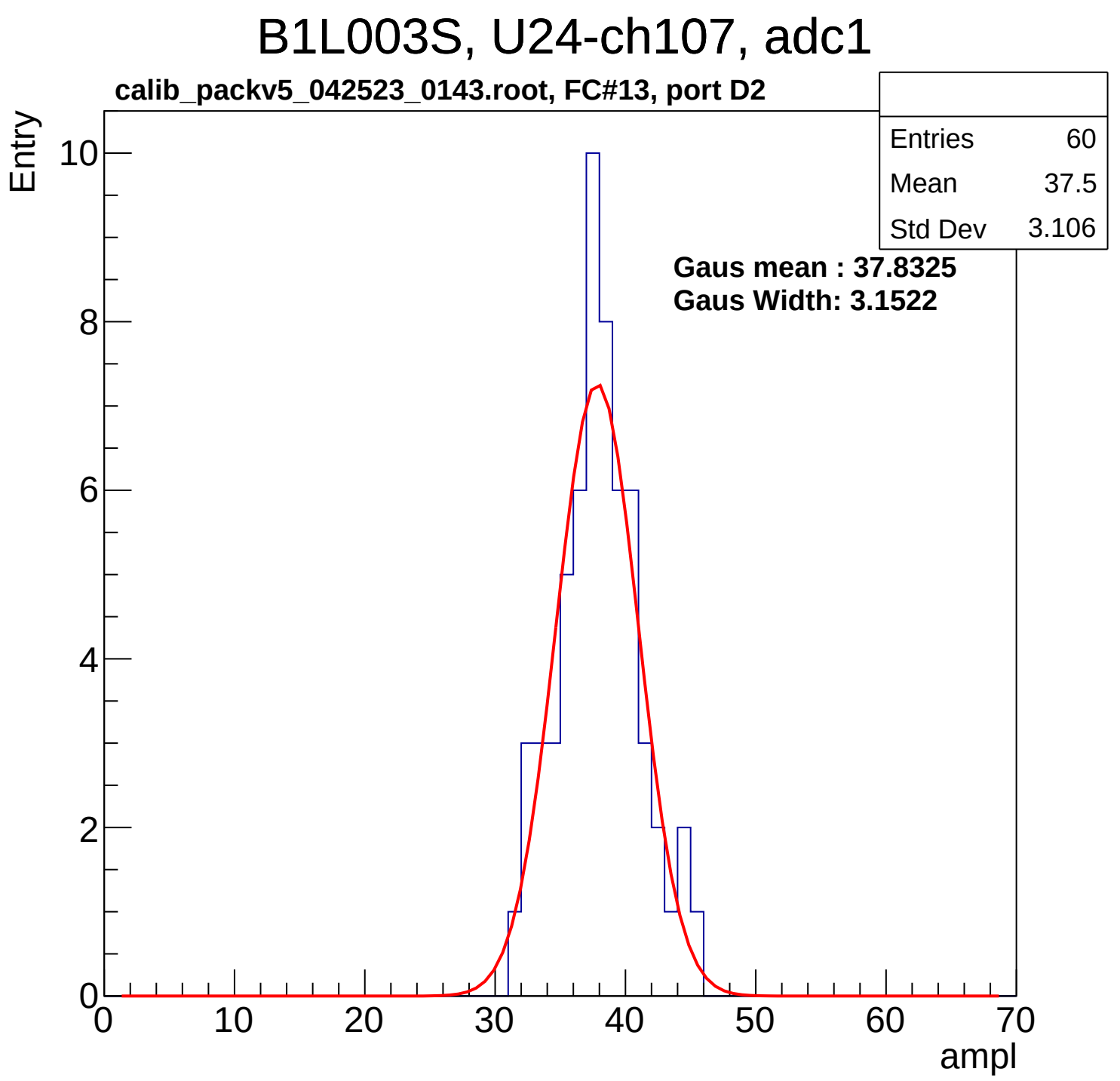
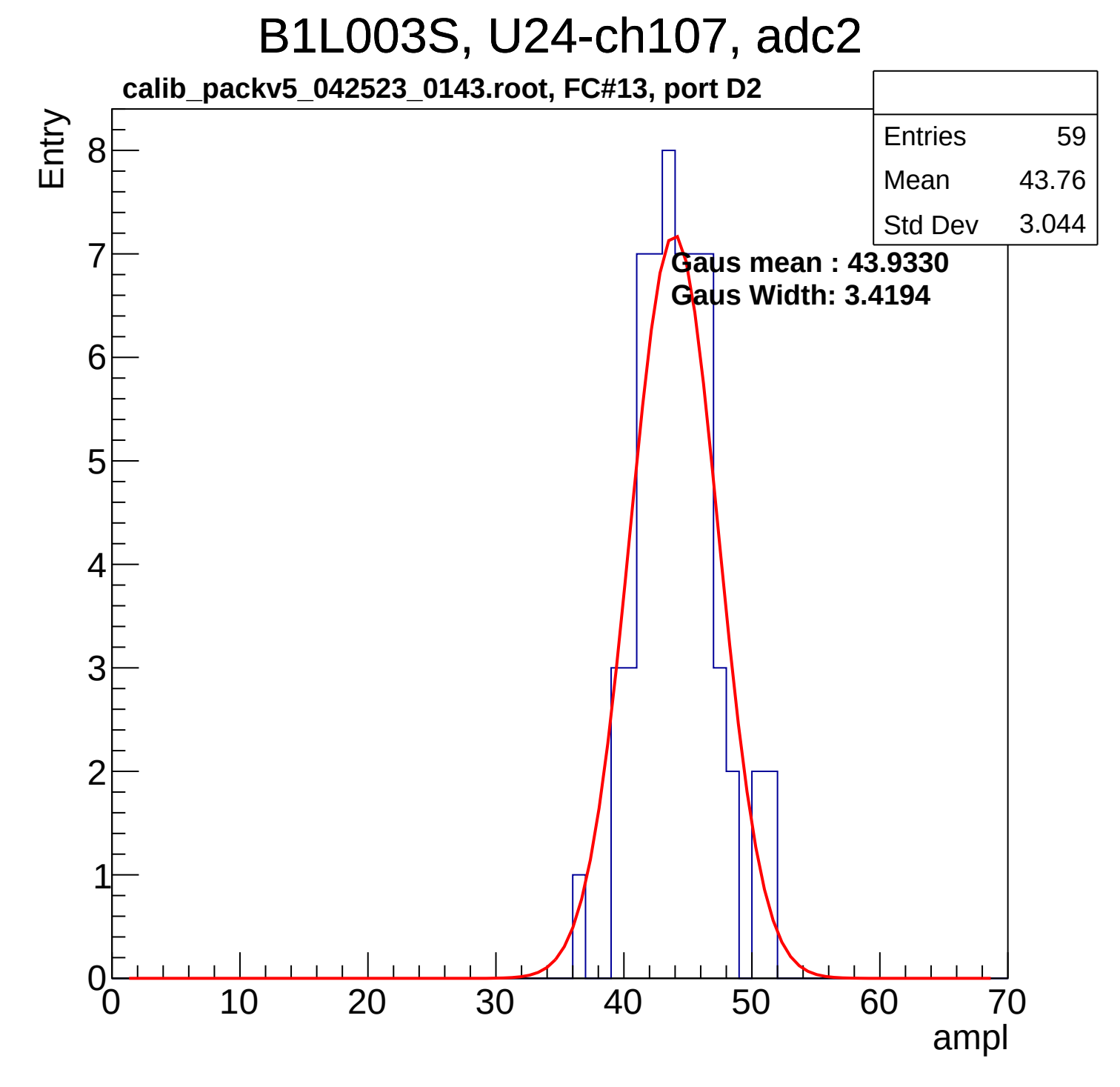
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch107, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 37.5
Std Dev 3.106
Gaus mean : 37.8325
Gaus Width: 3.1522
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch107, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.76
Std Dev 3.044
Gaus mean : 43.9330
Gaus Width: 3.4194
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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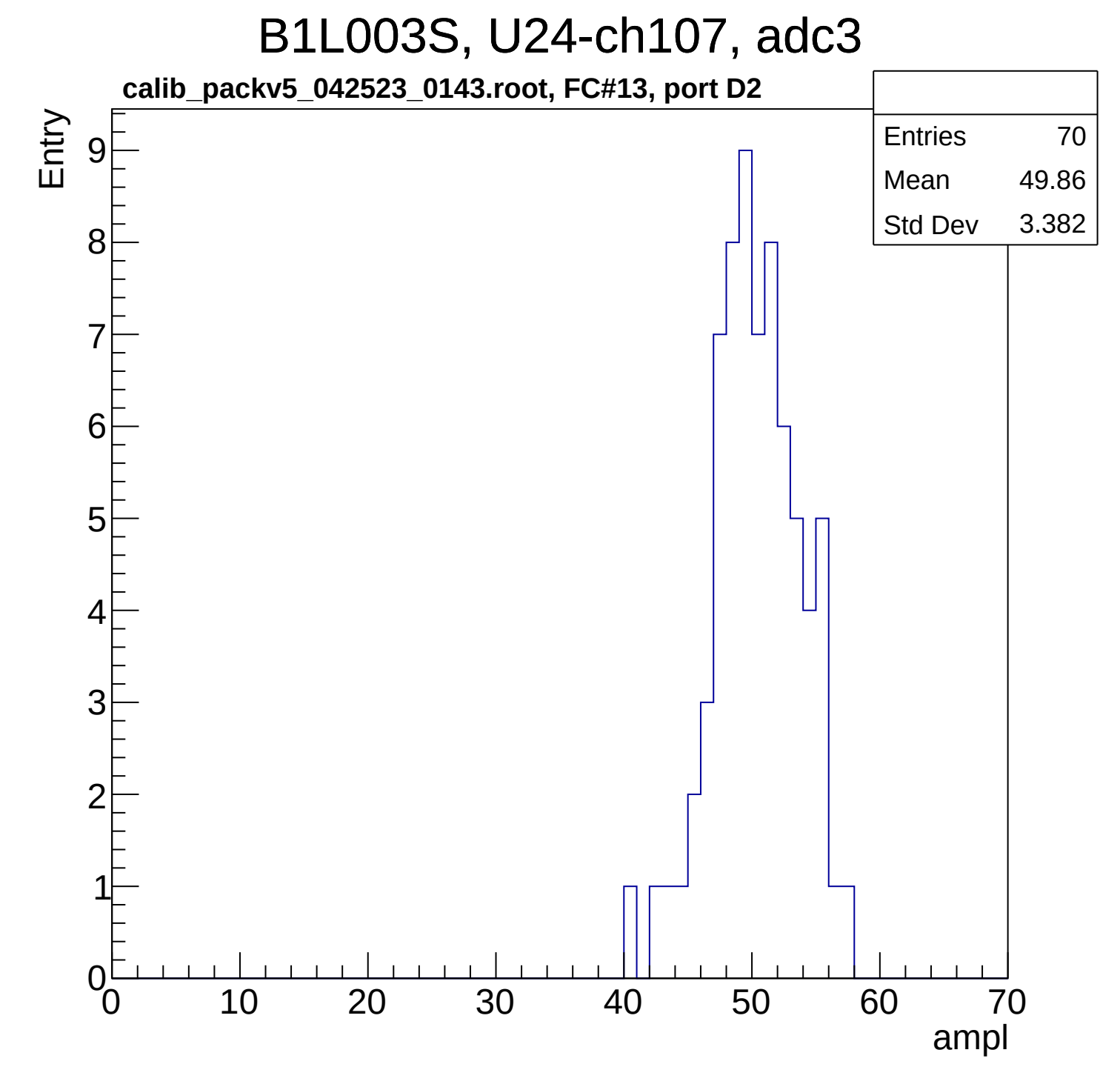
B1L003S, U24-ch107, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 49.86
Std Dev 3.382
Entry
ampl
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2
3
4
5
6
7
8
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0
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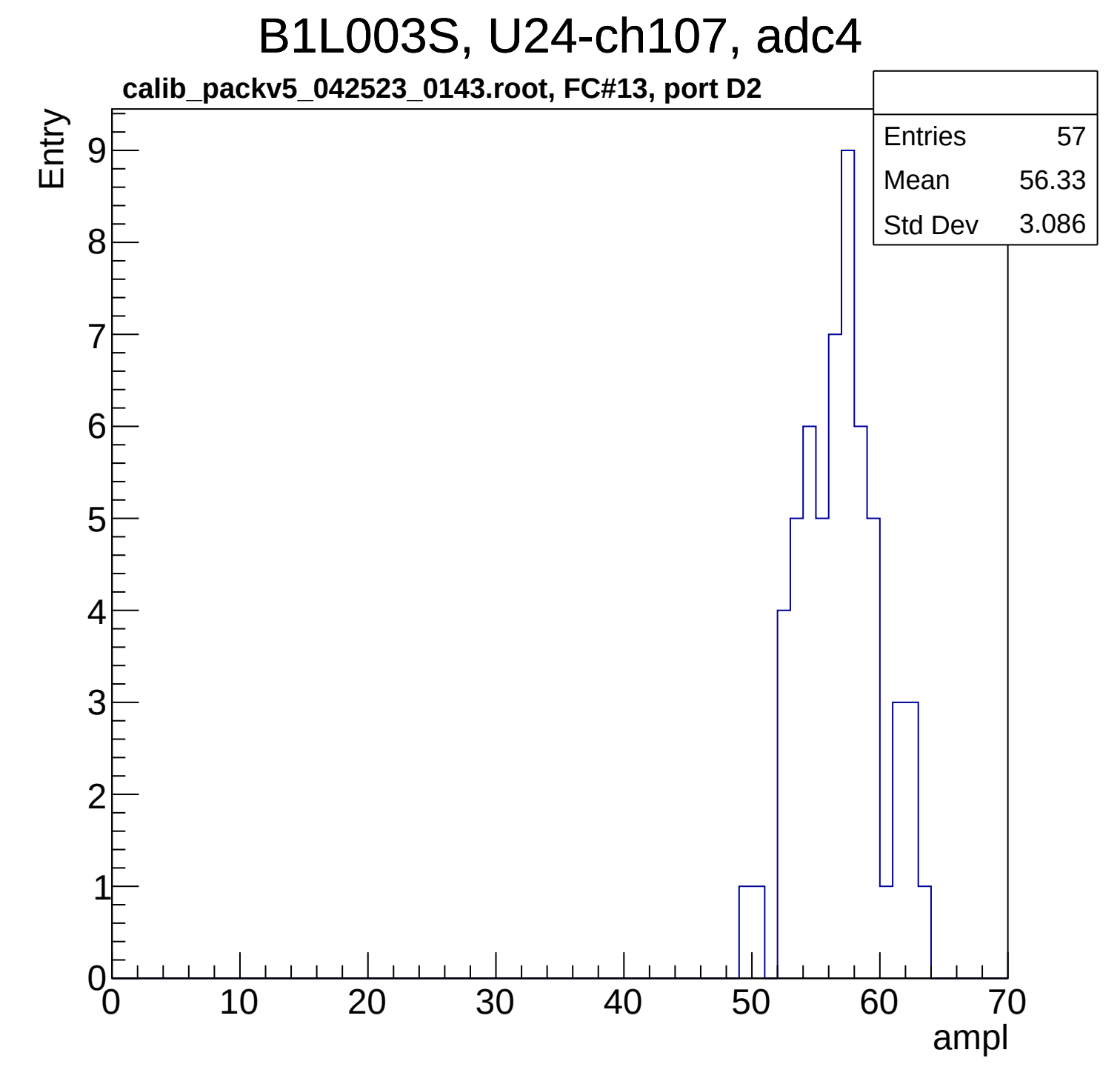
B1L003S, U24-ch107, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 56.33
Std Dev 3.086
Entry
ampl
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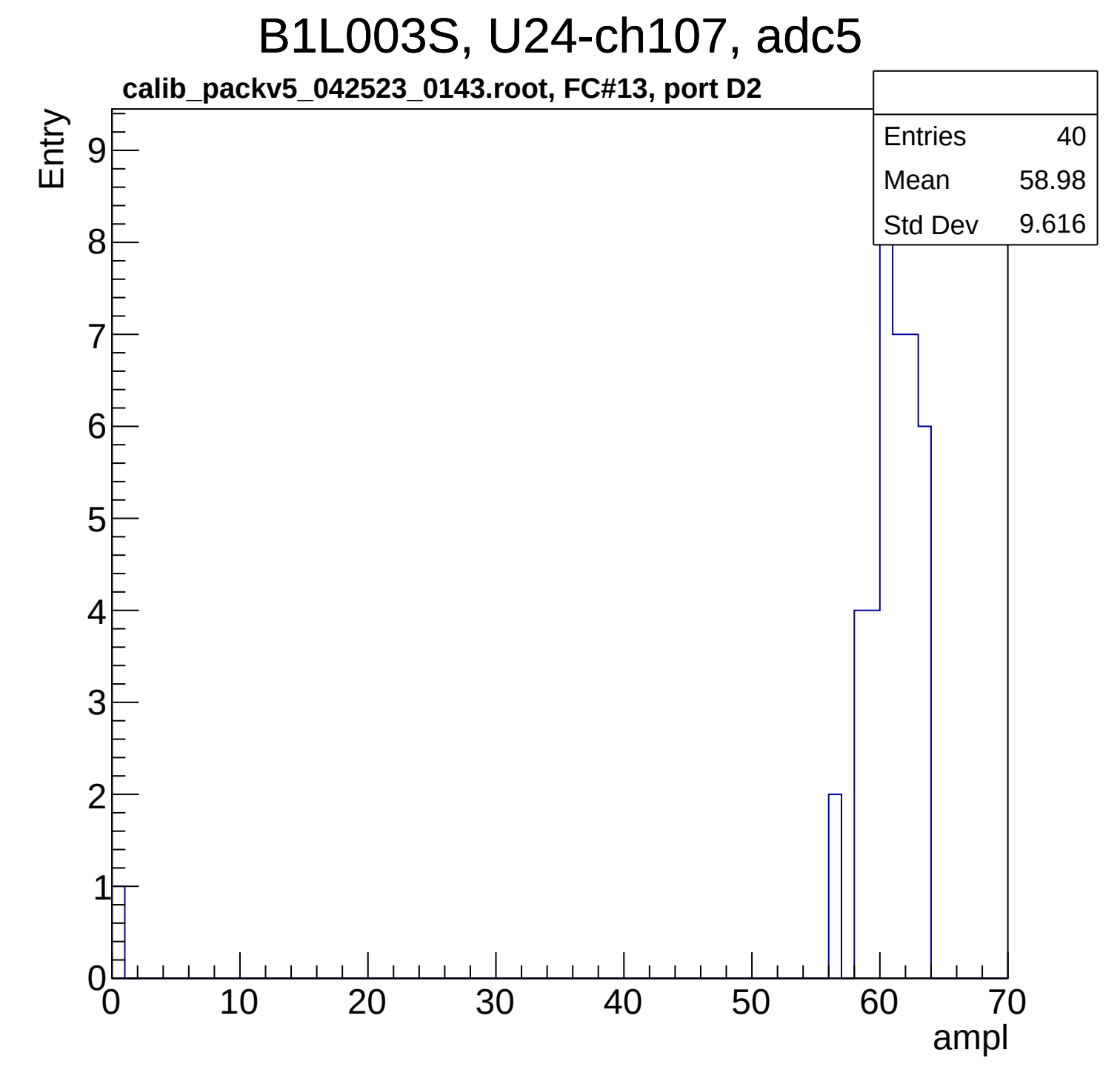
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch107, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.98
Std Dev 9.616
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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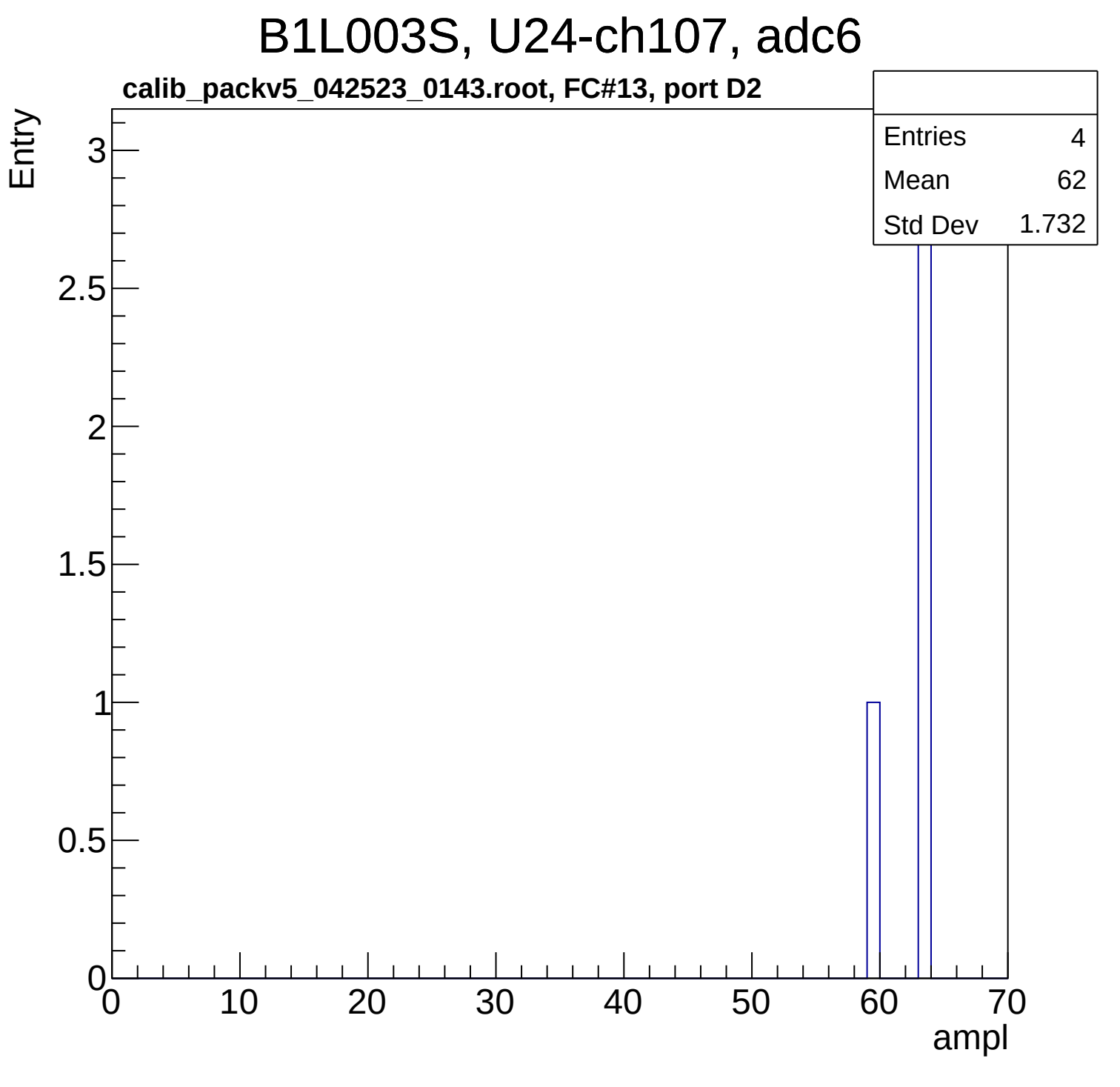
B1L003S, U24-ch107, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 1.732
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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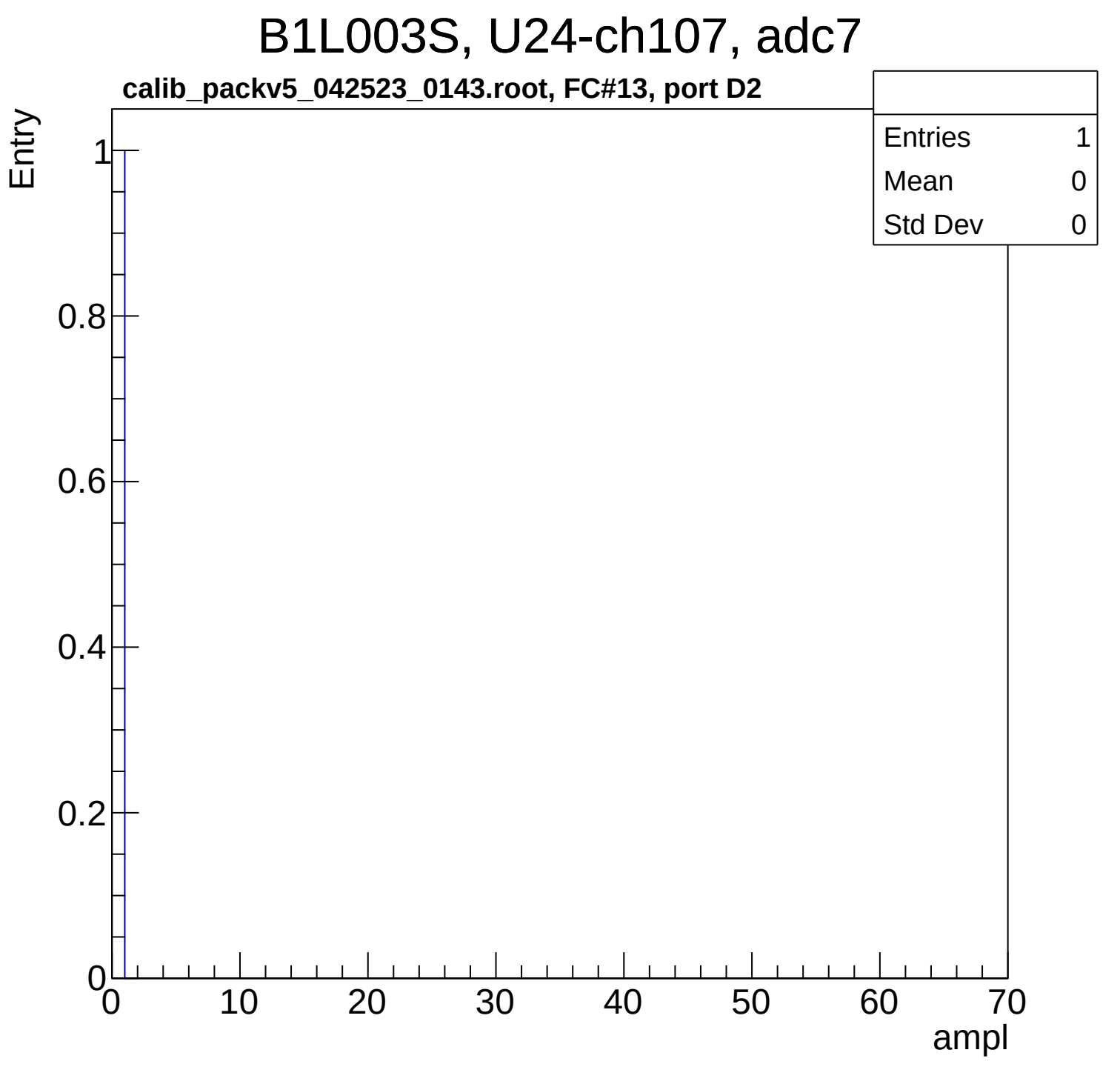
B1L003S, U24-ch107, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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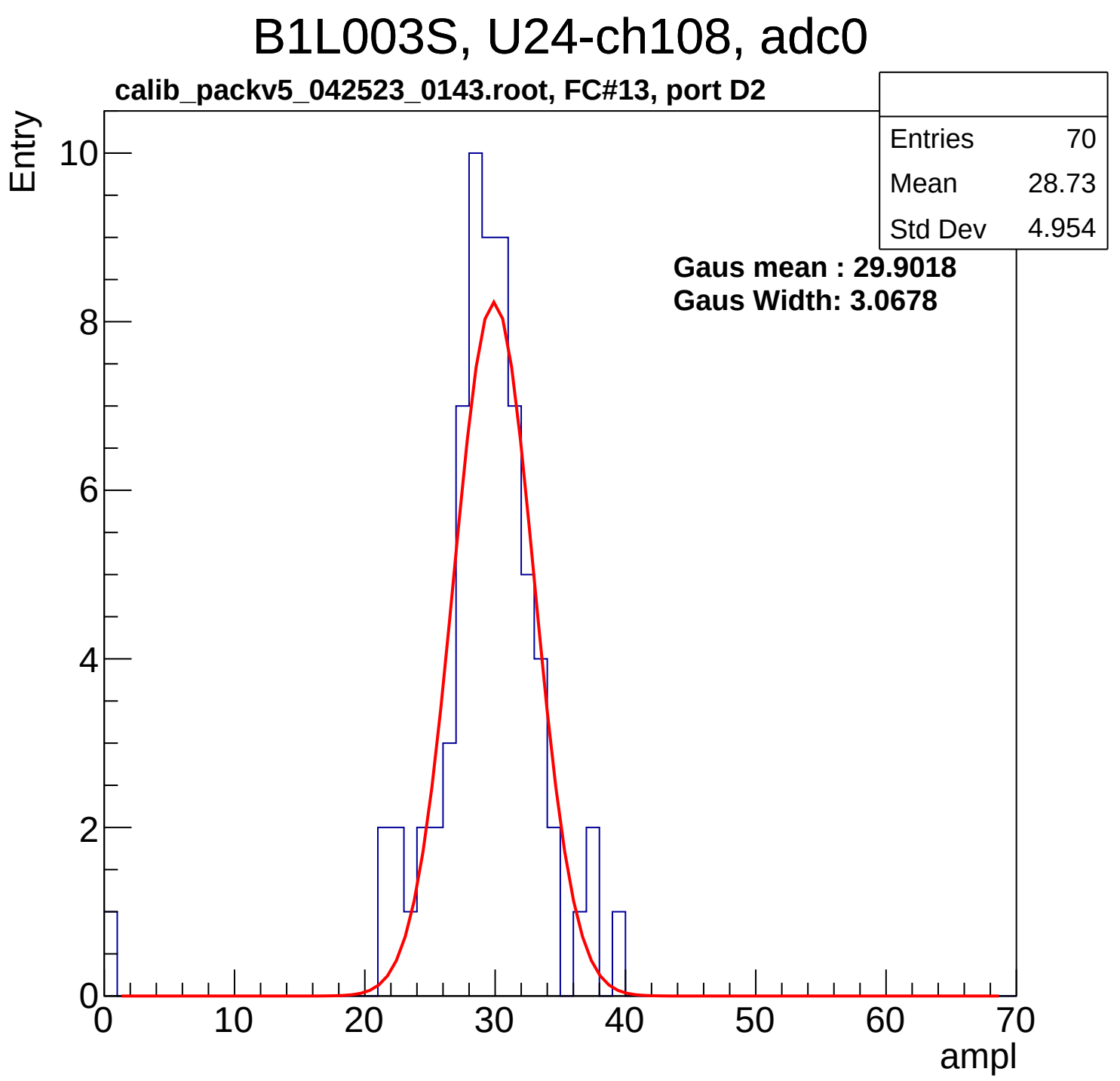
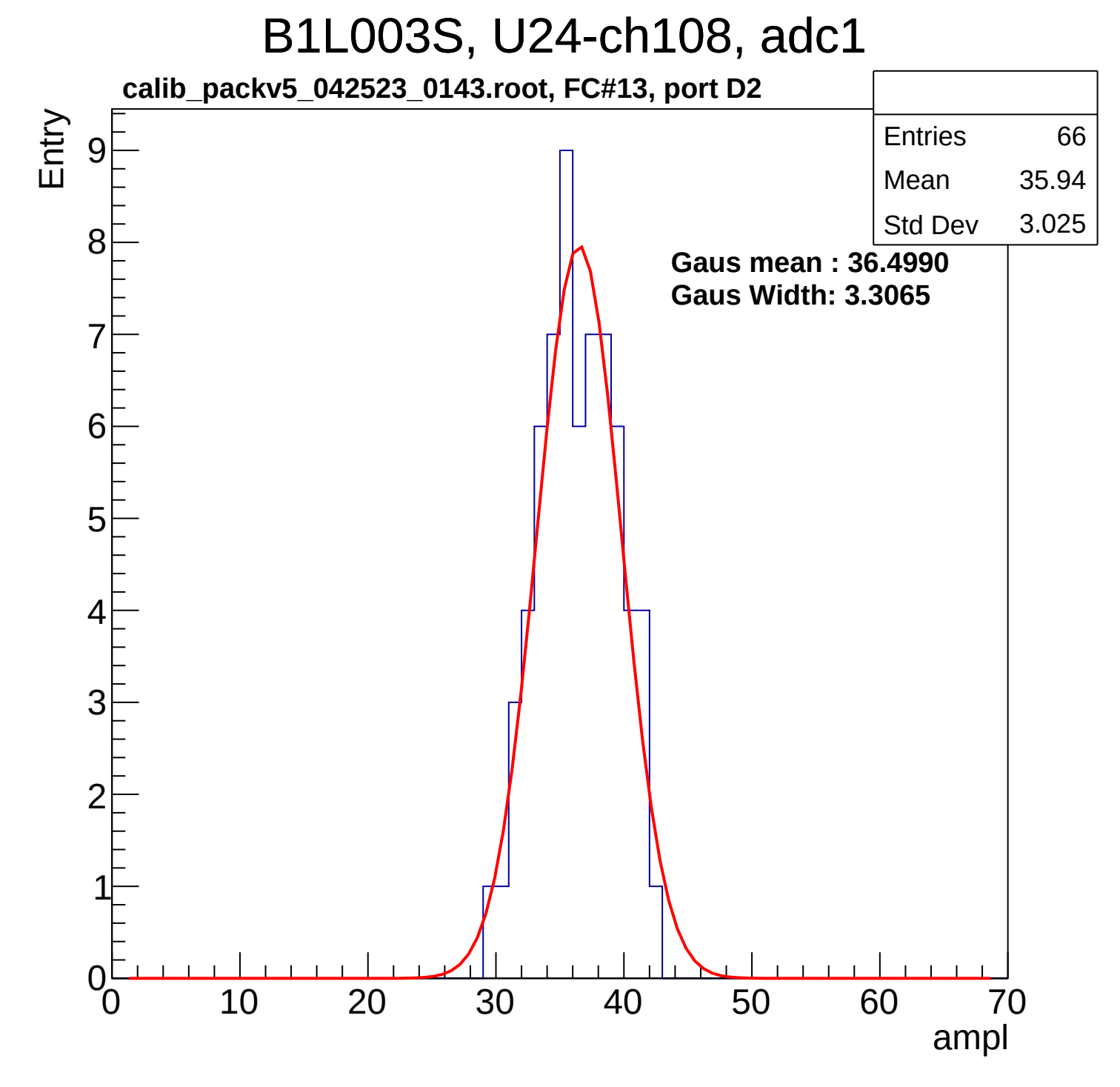
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch108, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 28.73
Std Dev 4.954
Gaus mean : 29.9018
Gaus Width: 3.0678
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch108, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.94
Std Dev 3.025
Gaus mean : 36.4990
Gaus Width: 3.3065
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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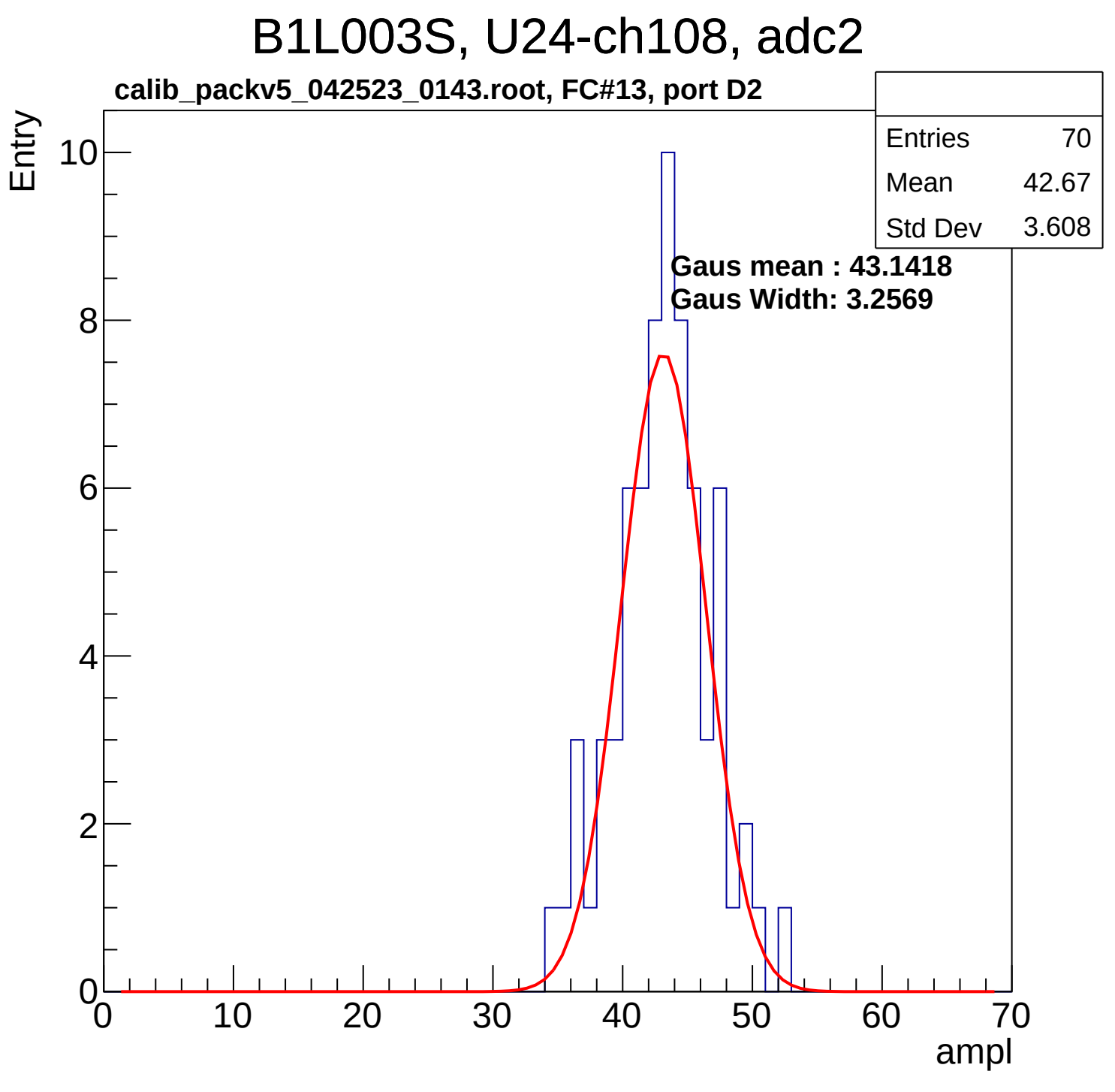
B1L003S, U24-ch108, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.67
Std Dev 3.608
Gaus mean : 43.1418
Gaus Width: 3.2569
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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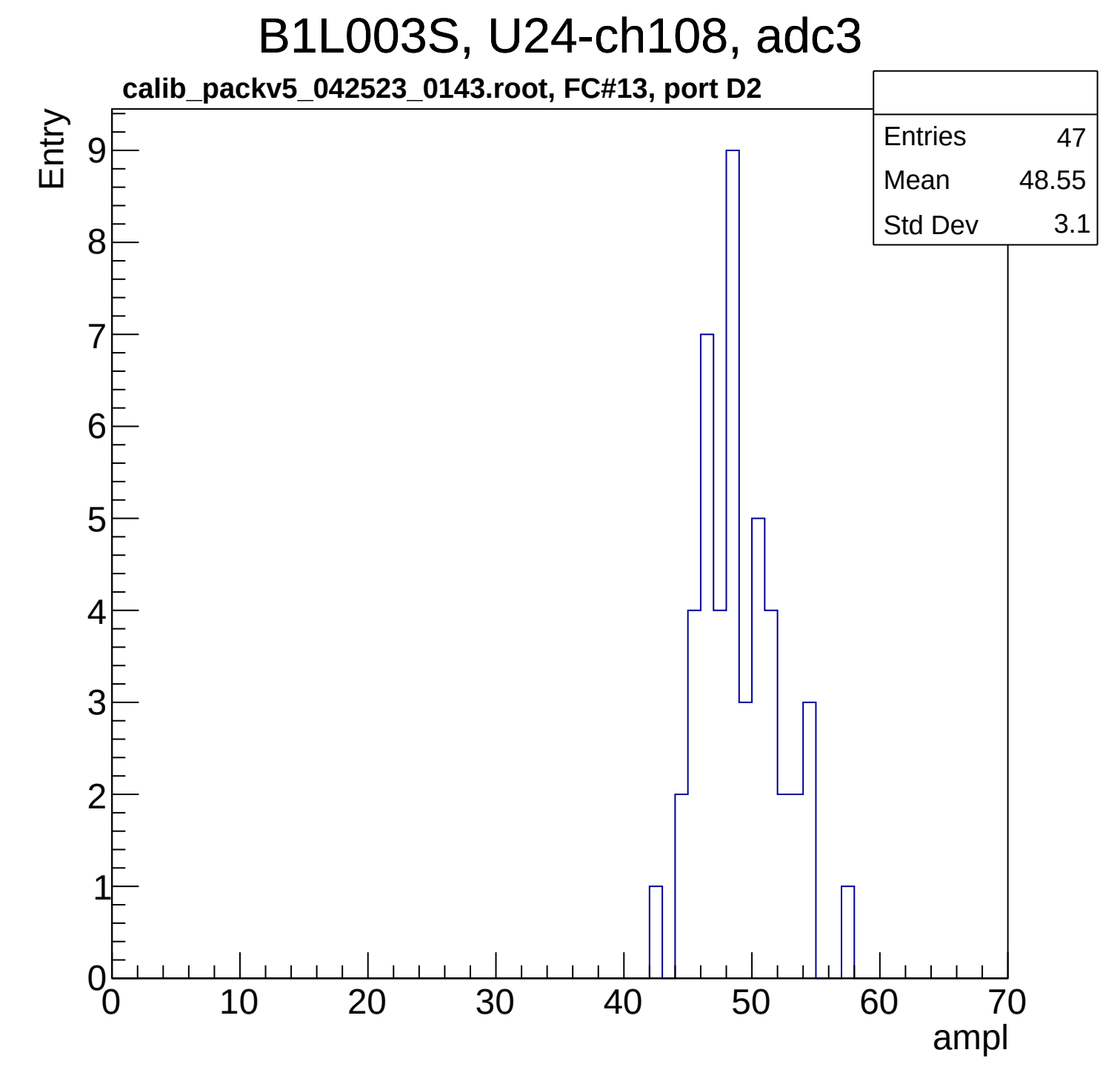
B1L003S, U24-ch108, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 48.55
Std Dev 3.1
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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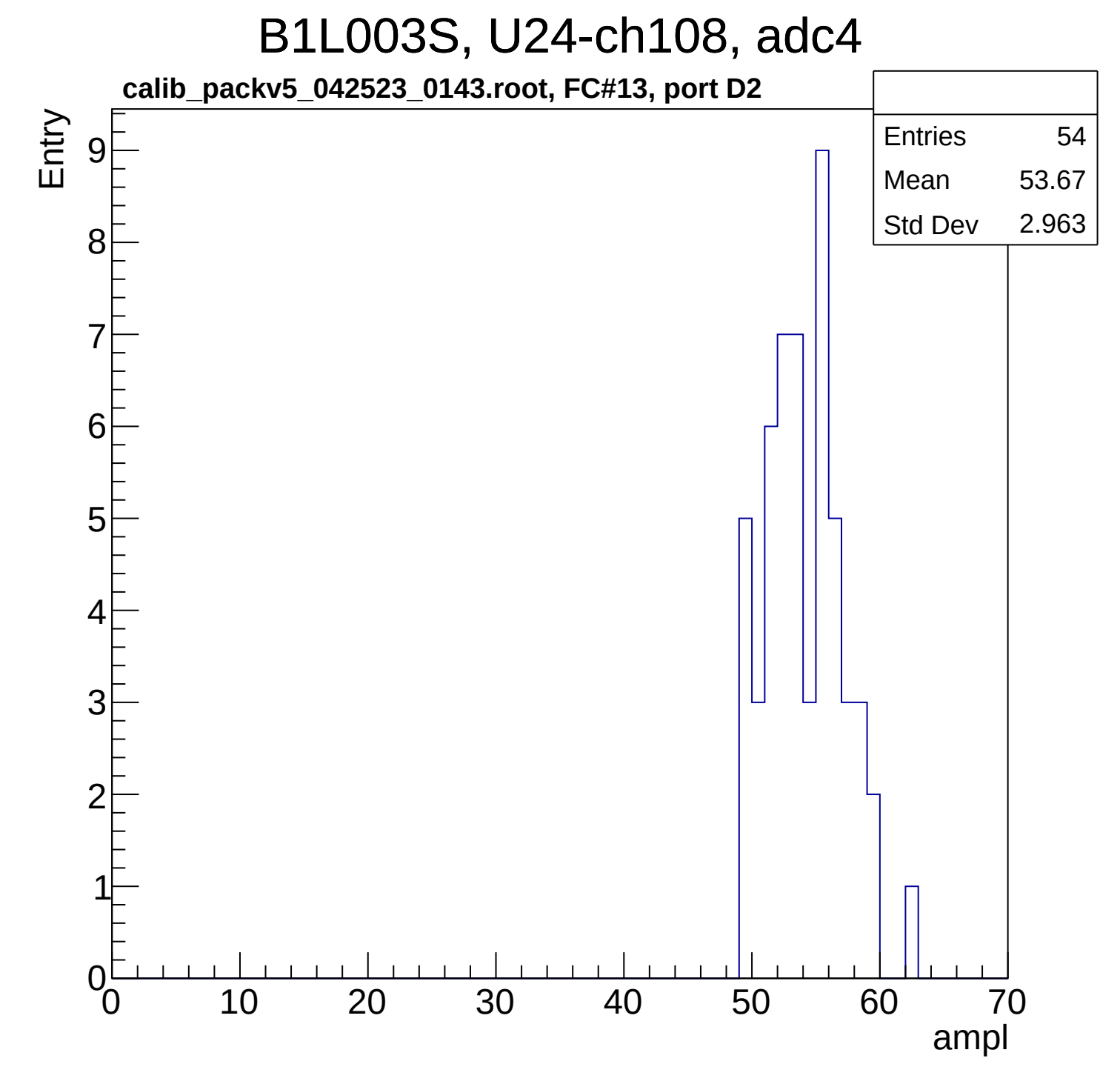
B1L003S, U24-ch108, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.67
Std Dev 2.963
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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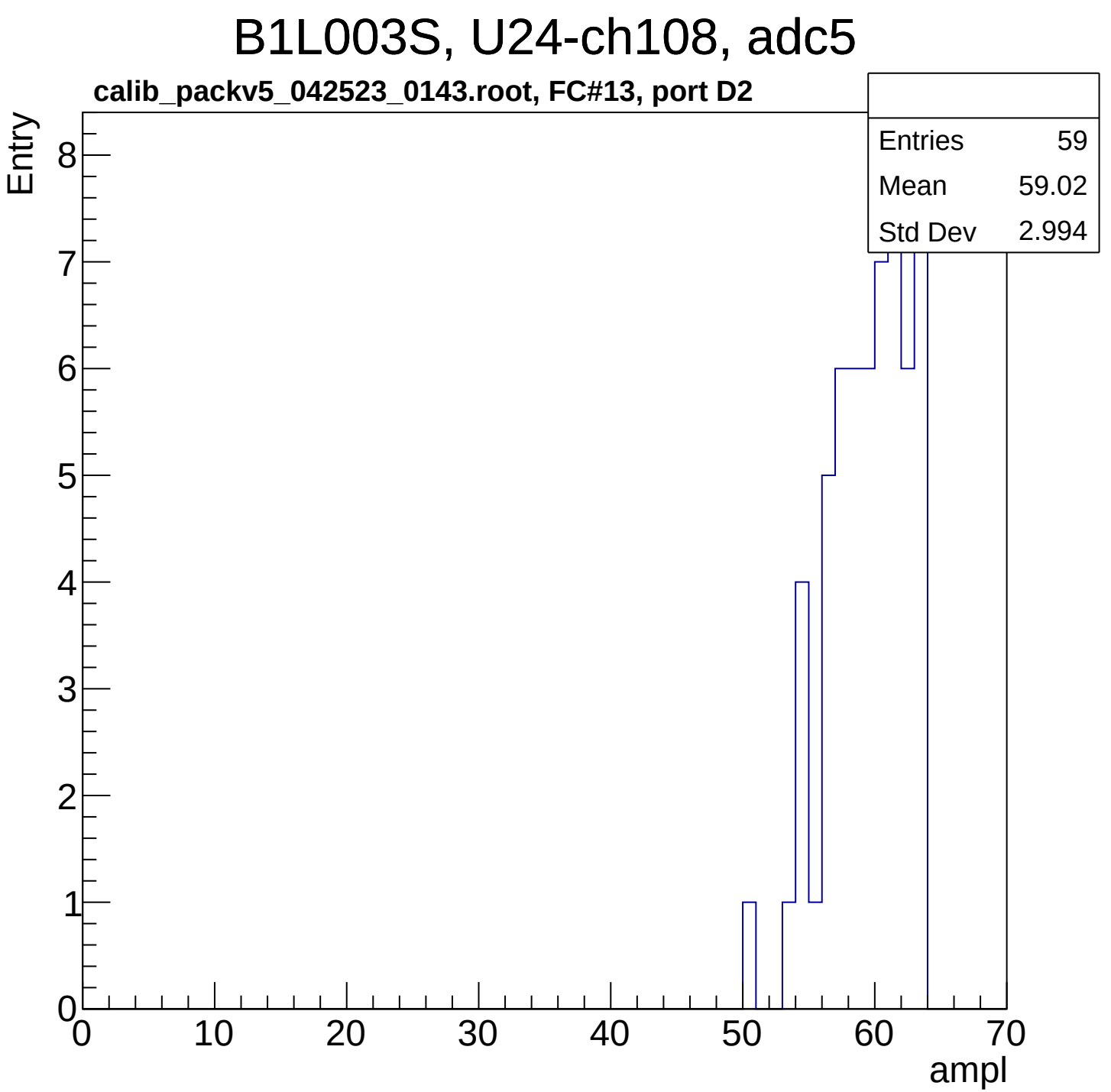
B1L003S, U24-ch108, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 59.02
Std Dev 2.994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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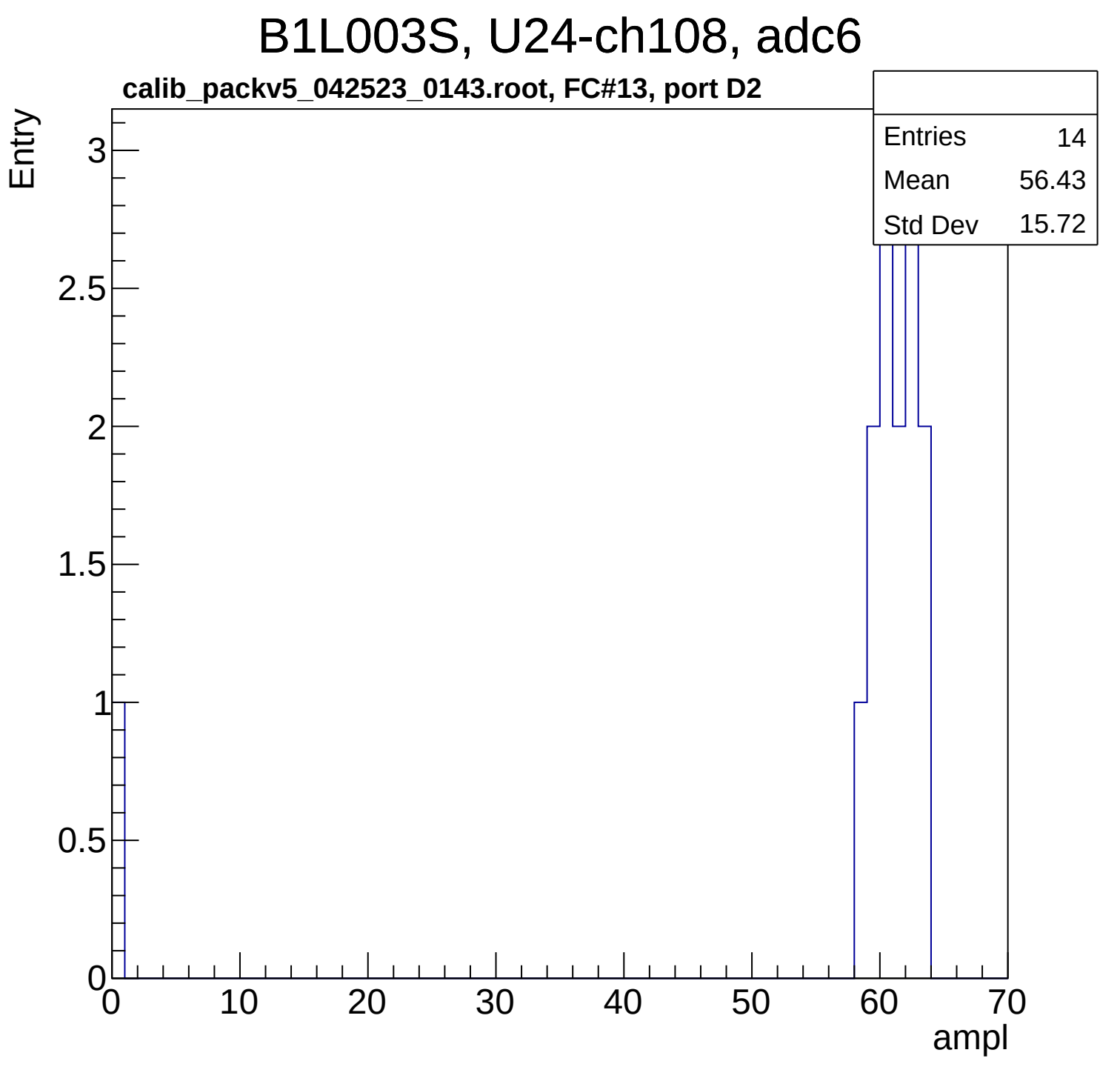
B1L003S, U24-ch108, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 56.43
Std Dev 15.72
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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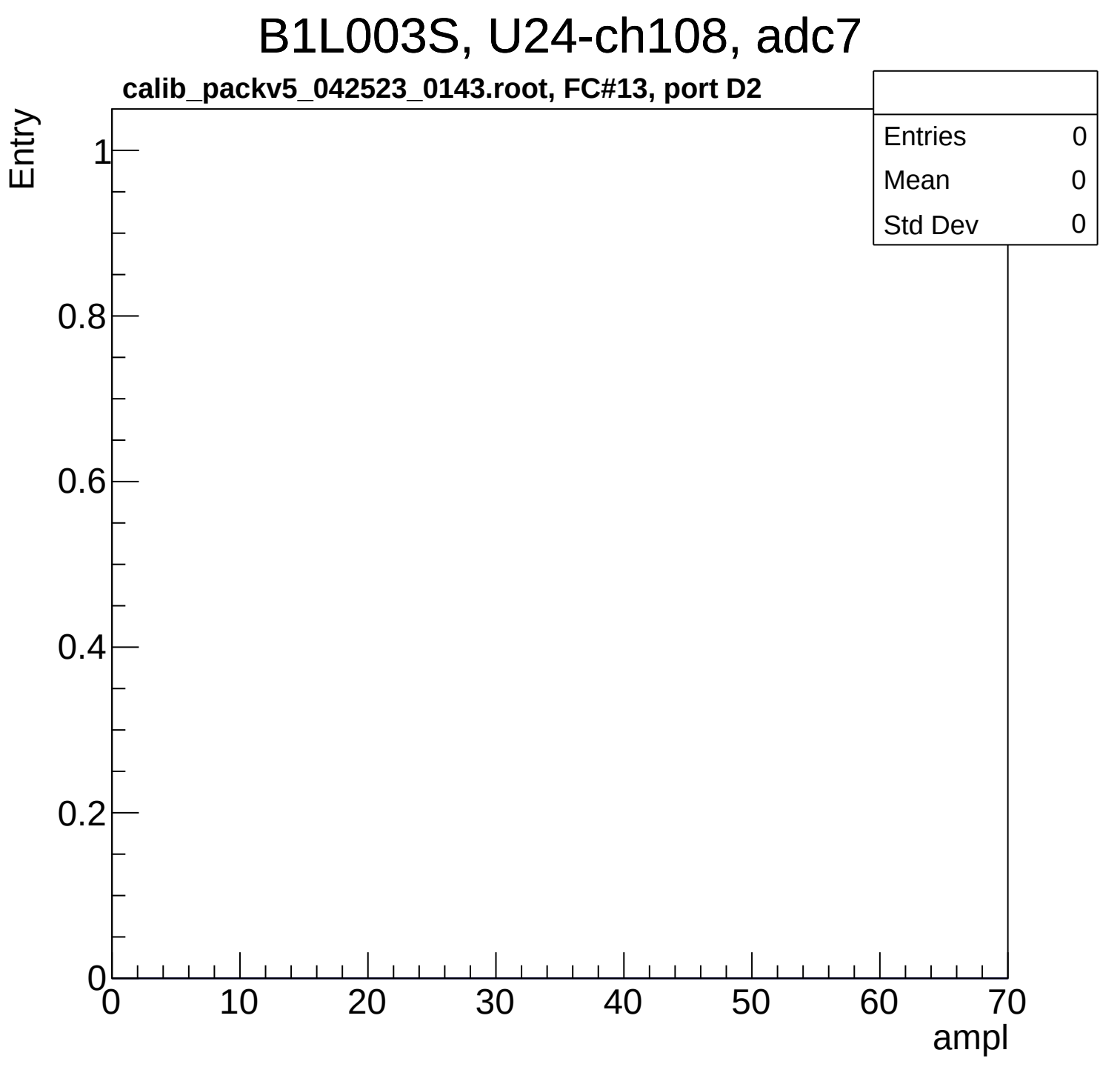
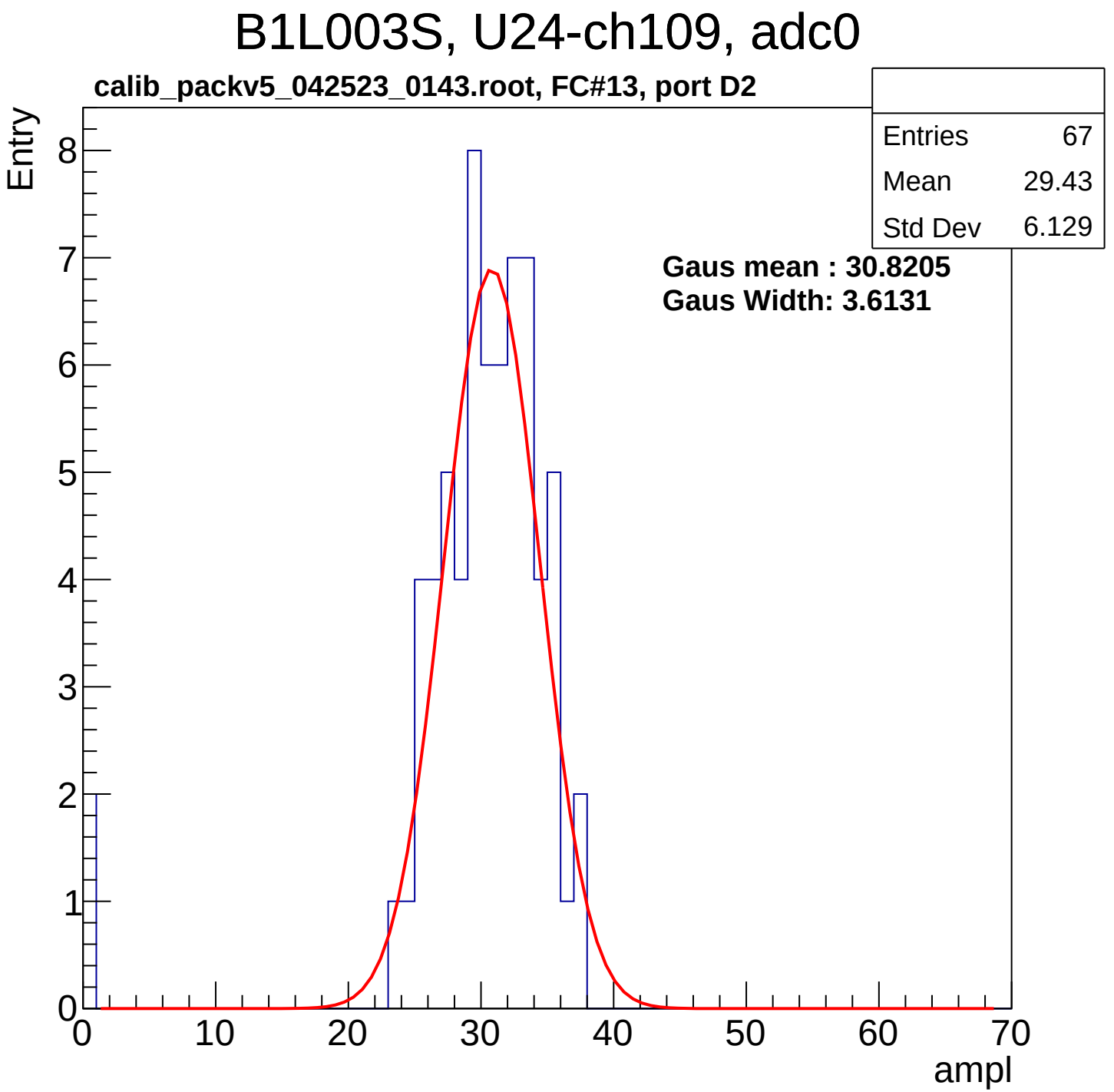
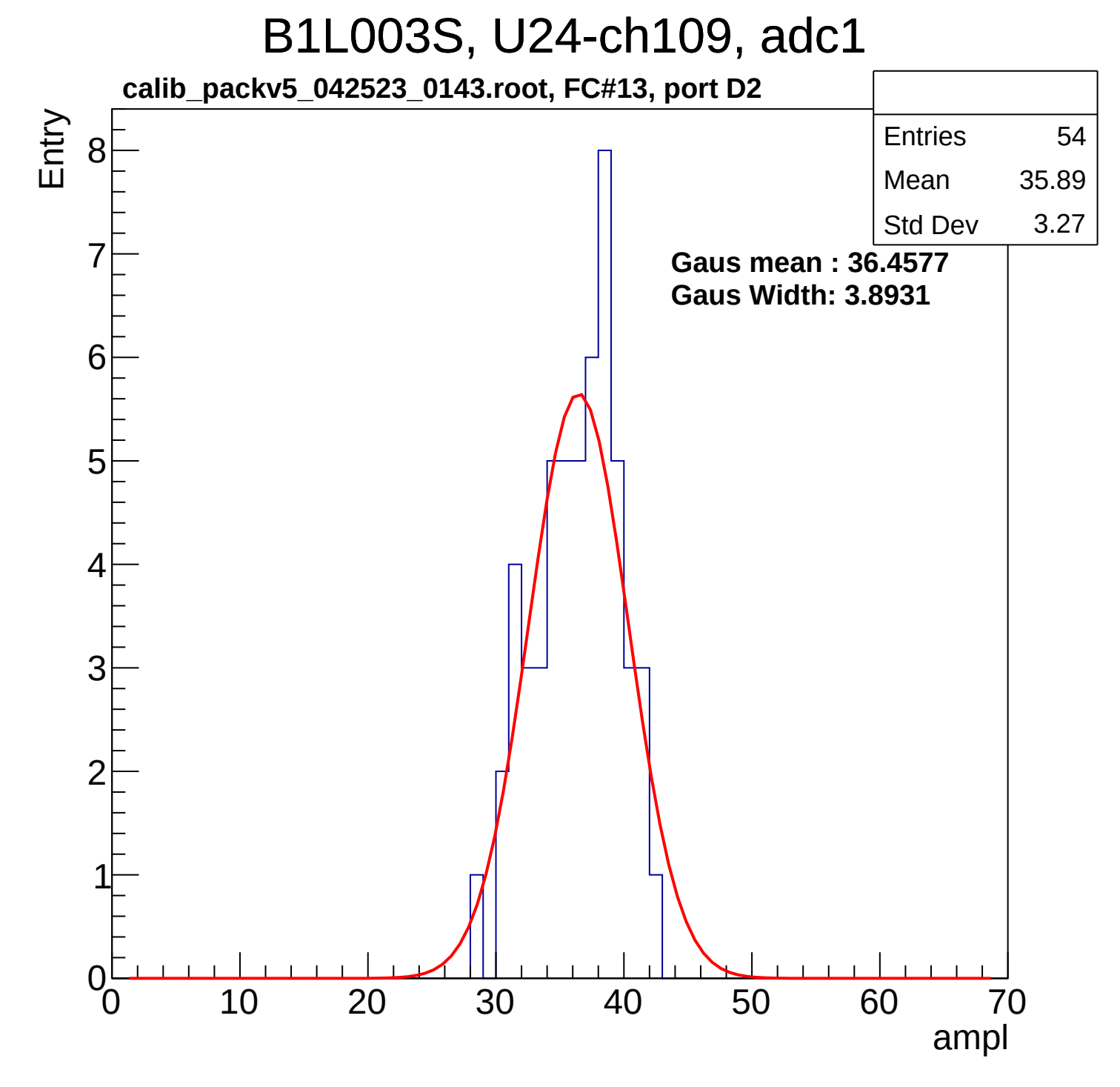
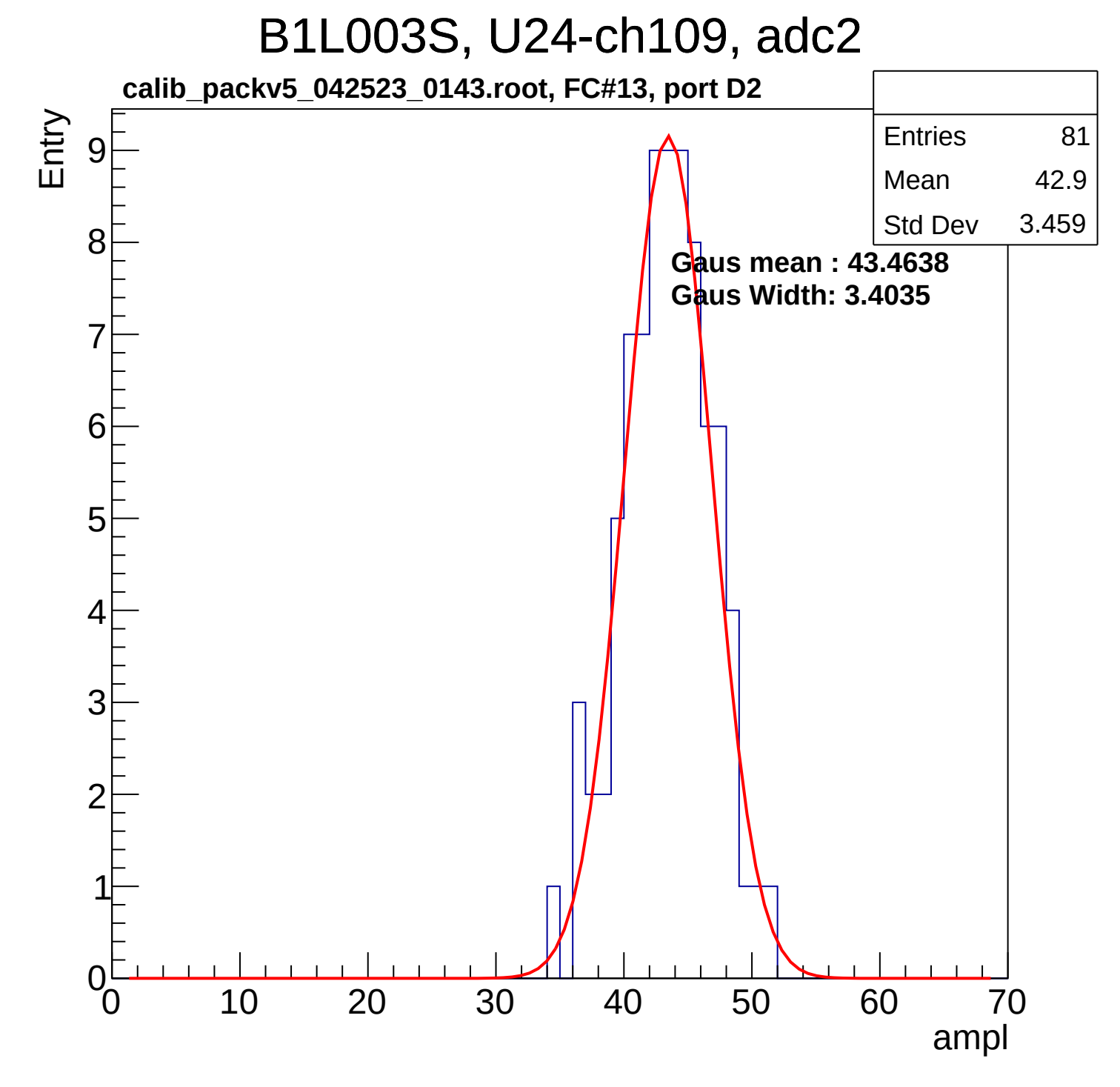
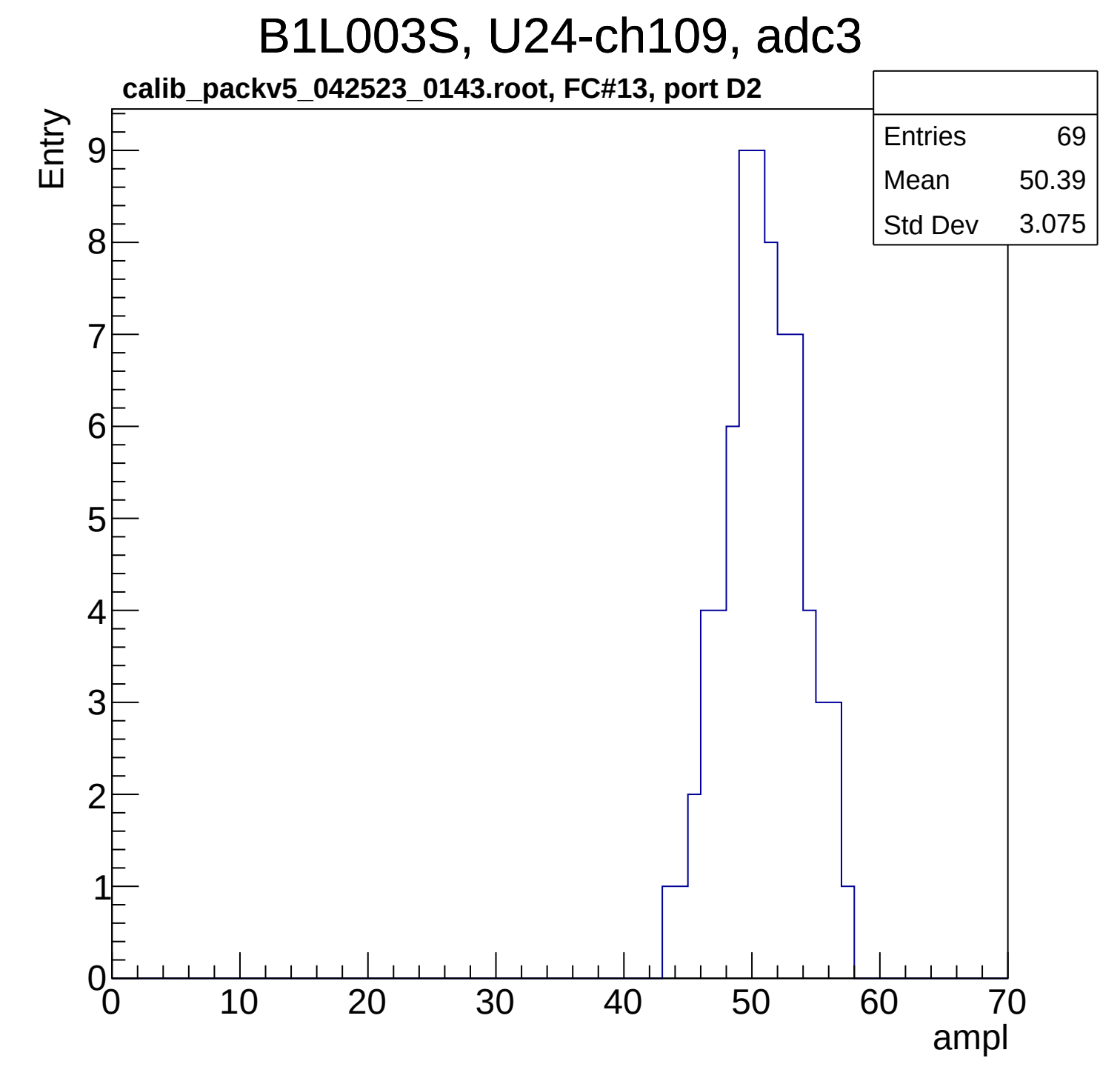

B1L003S, U24-ch108, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch109, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.43
Std Dev 6.129
Gaus mean : 30.8205
Gaus Width: 3.6131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch109, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 35.89
Std Dev 3.27
Gaus mean : 36.4577
Gaus Width: 3.8931
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch109, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 42.9
Std Dev 3.459
Gaus mean : 43.4638
Gaus Width: 3.4035
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch109, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.39
Std Dev 3.075
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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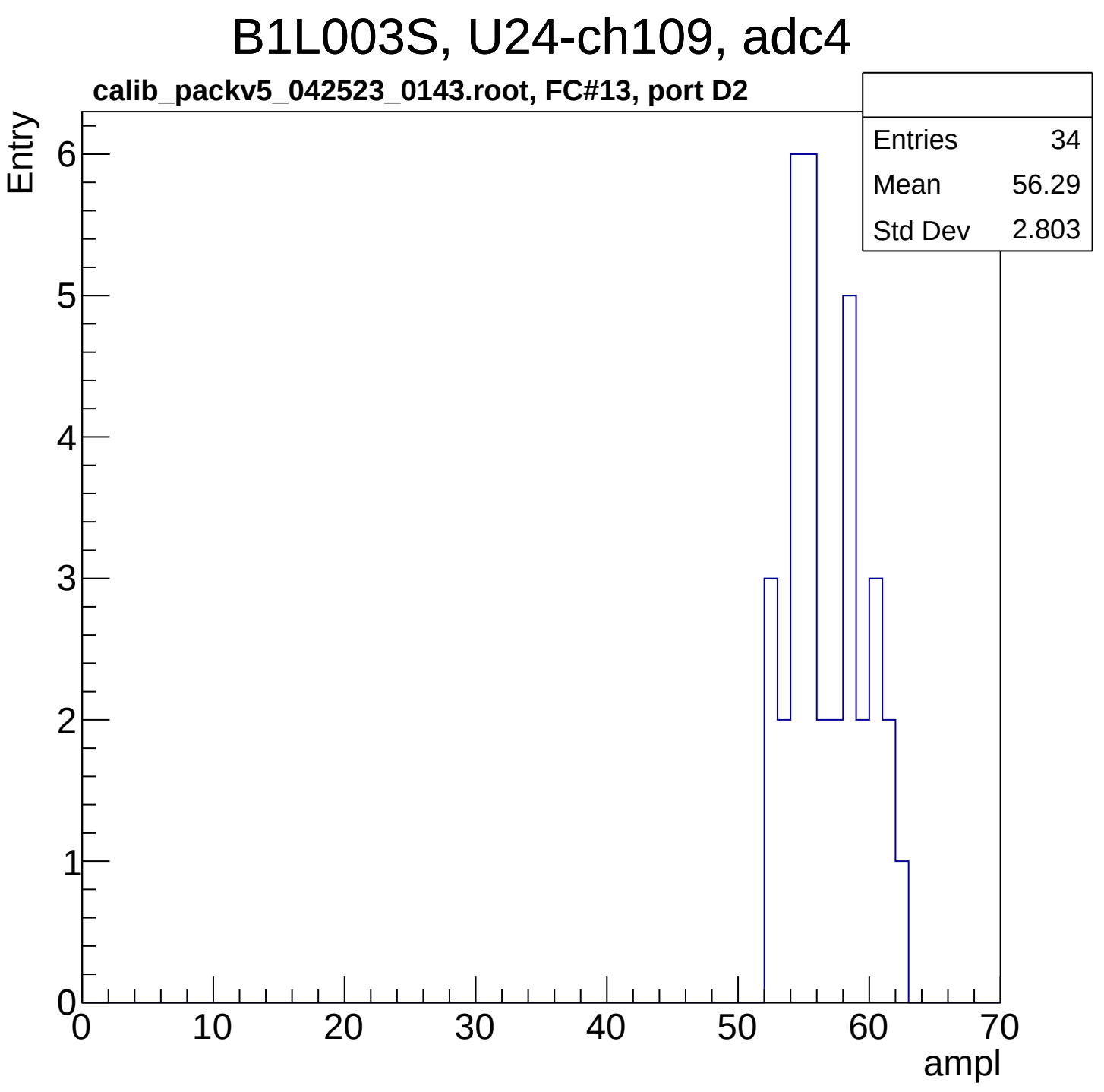
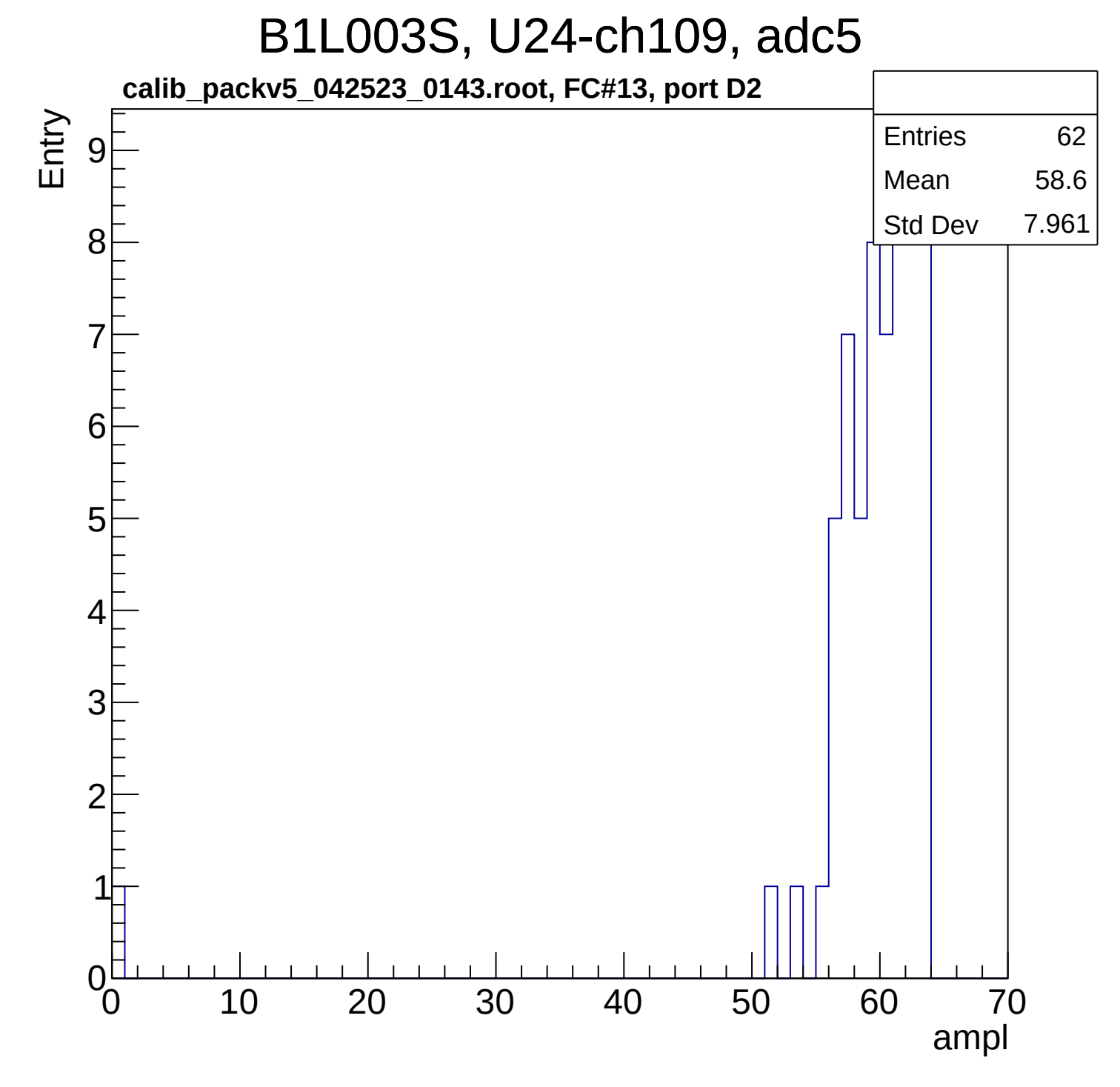
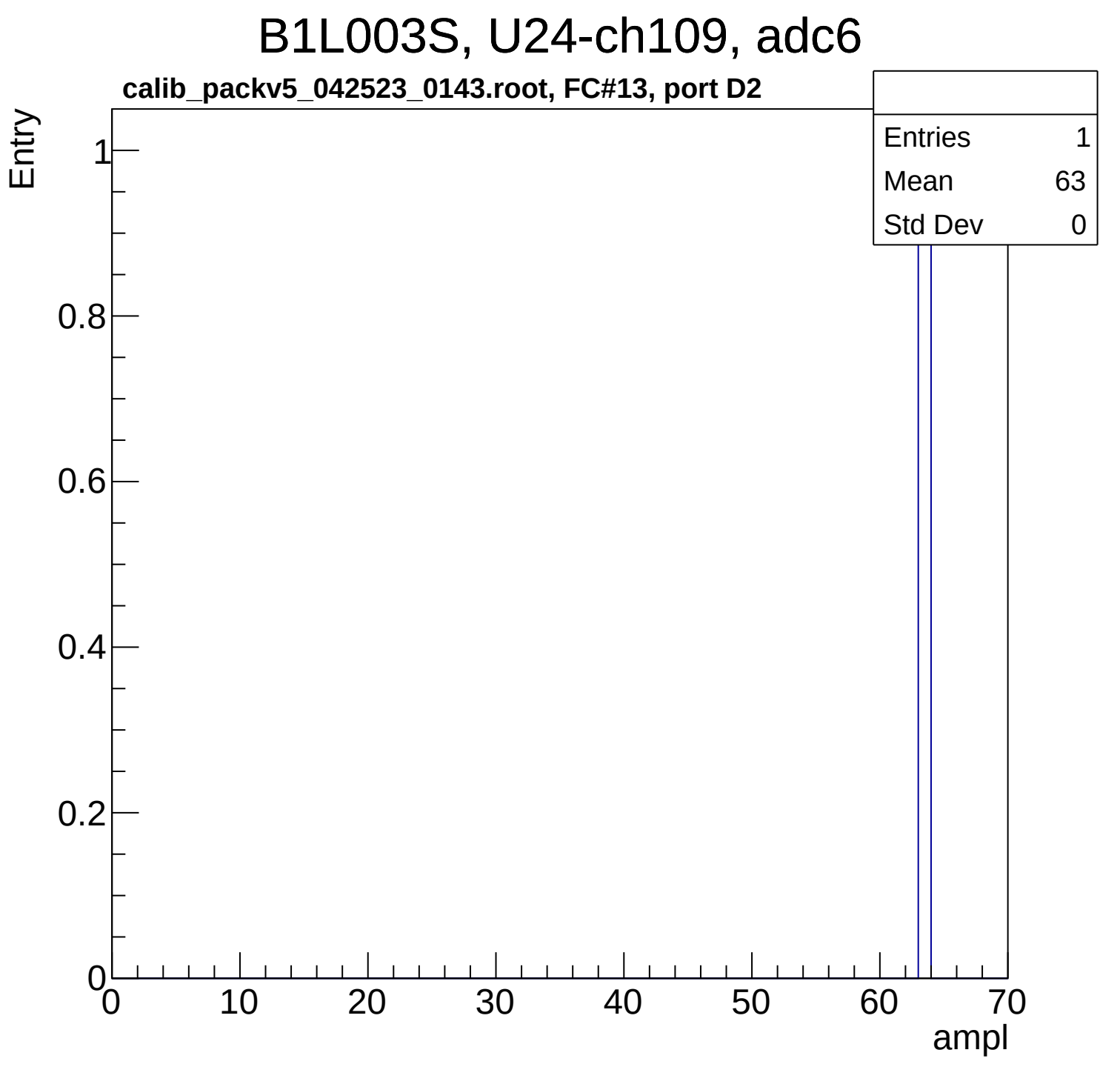
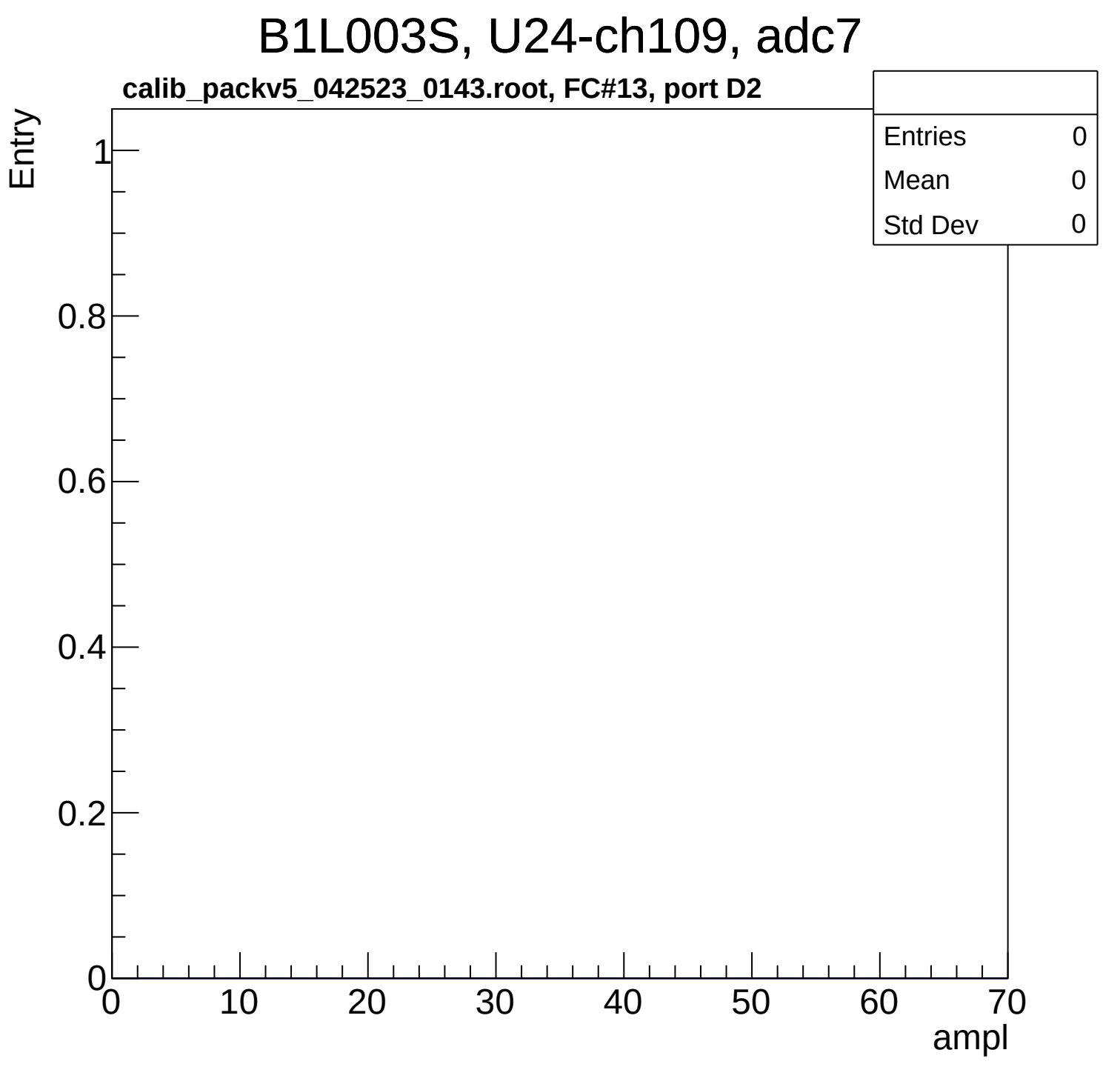
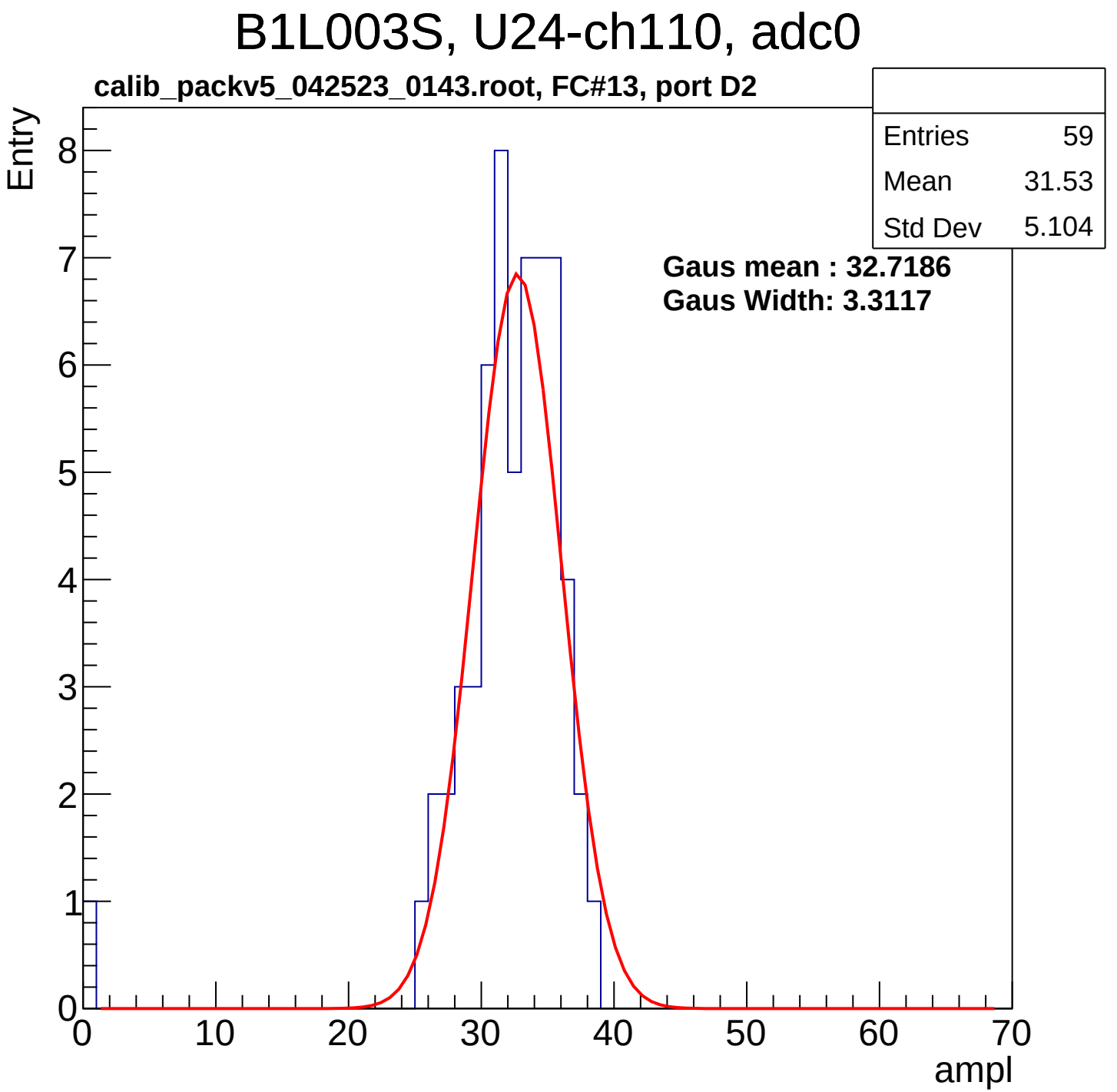
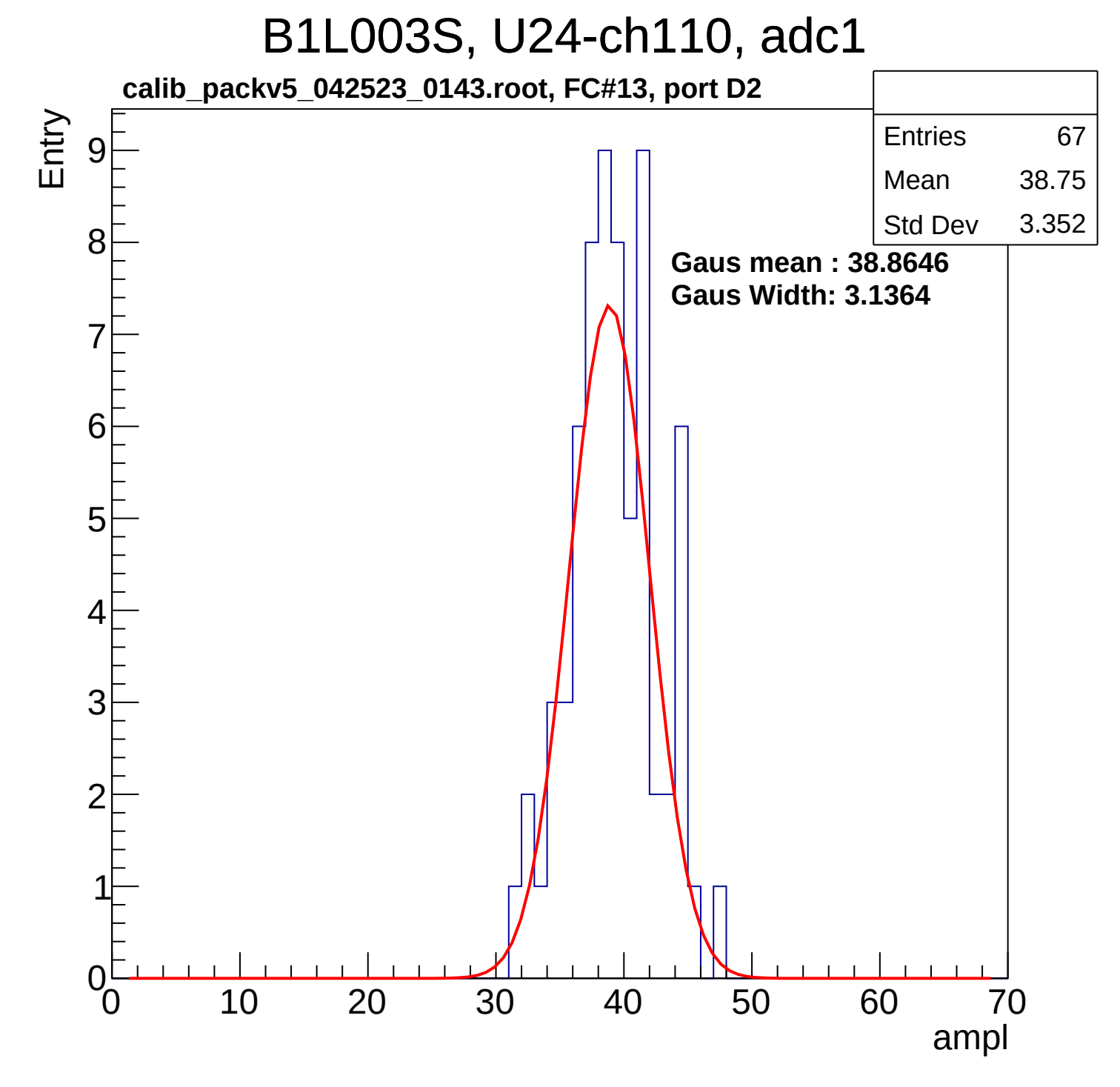

B1L003S, U24-ch109, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 34
Mean 56.29
Std Dev 2.803
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

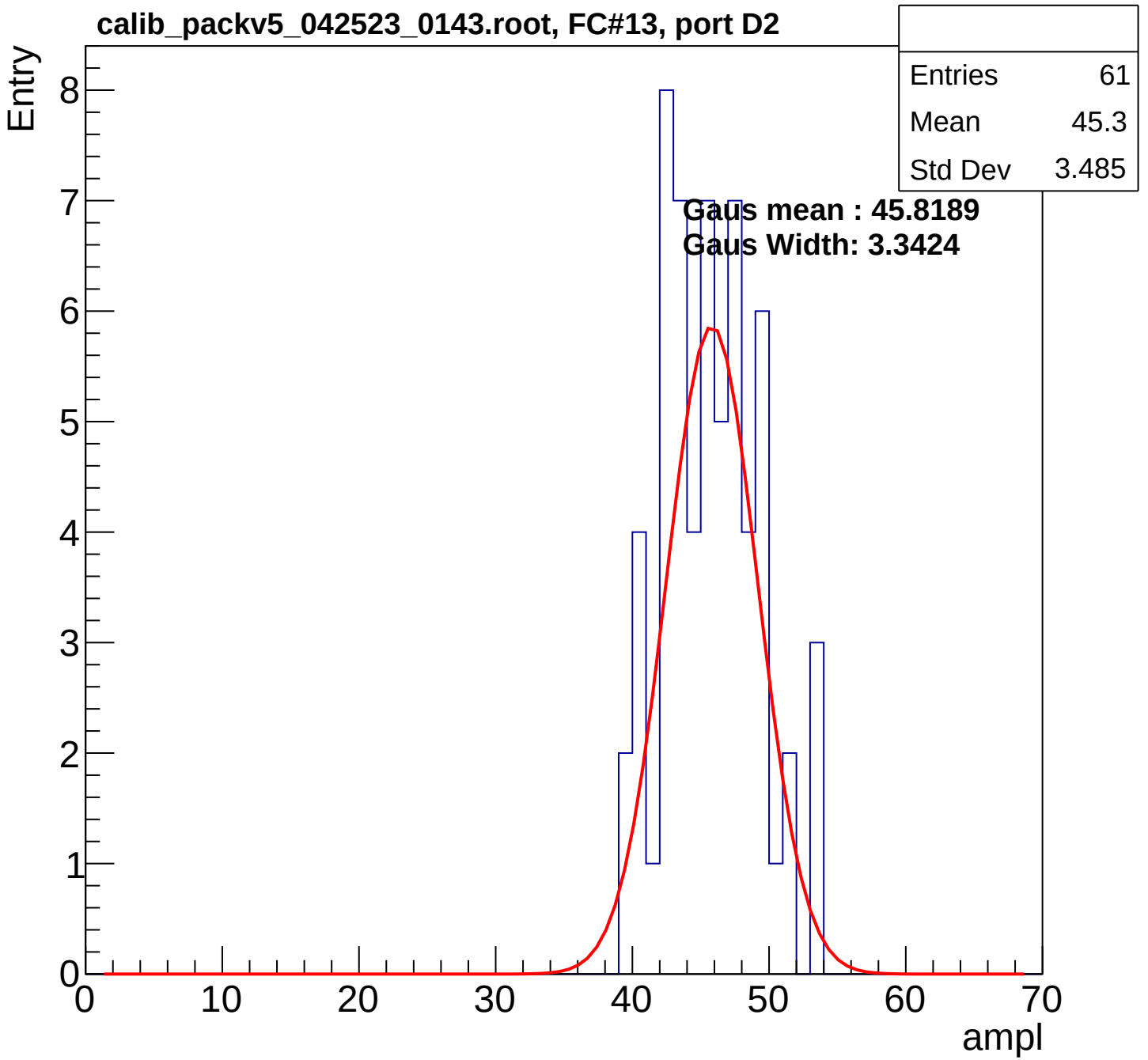

B1L003S, U24-ch109, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.6
Std Dev 7.961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

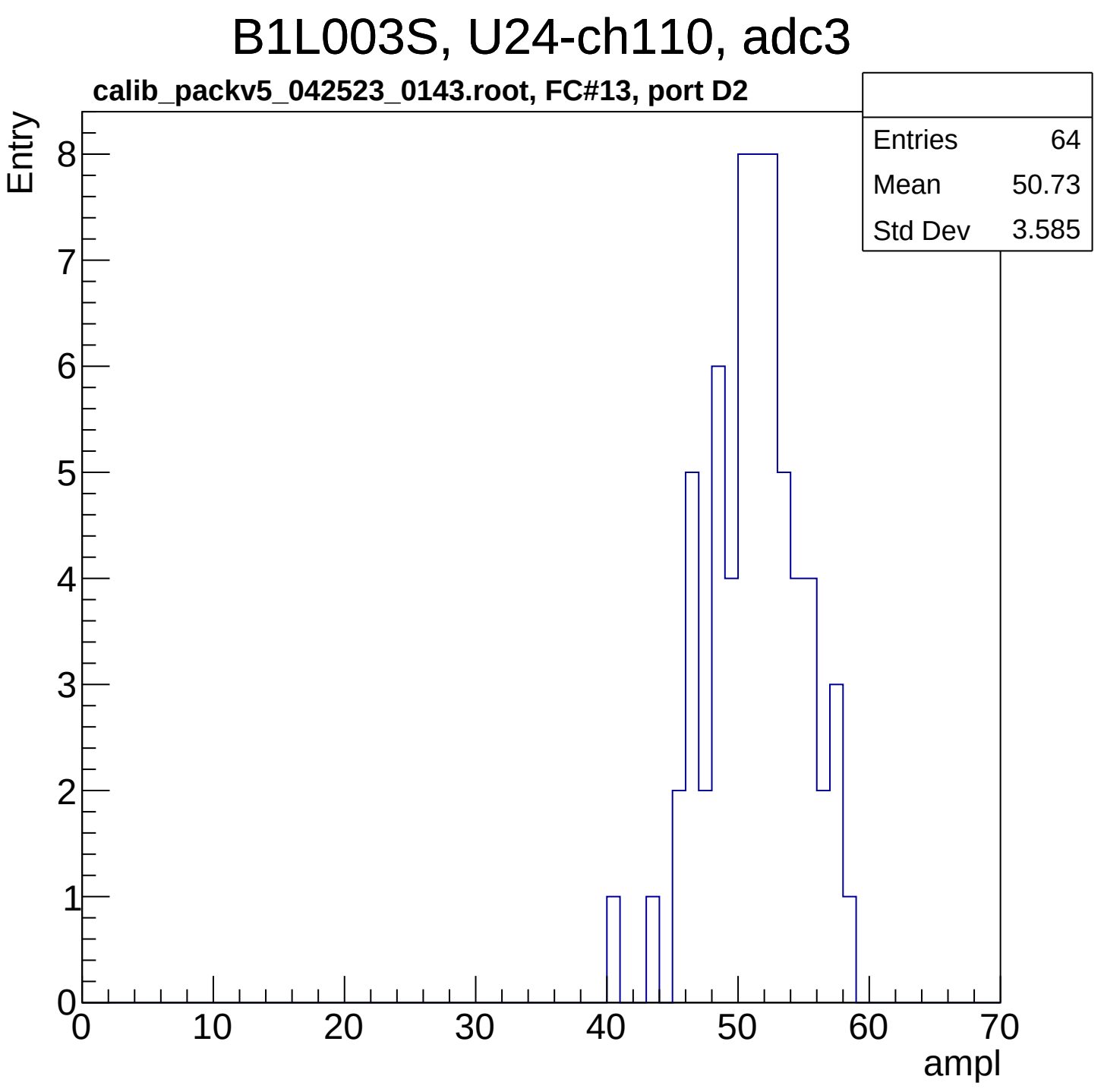

B1L003S, U24-ch109, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch109, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch110, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 31.53
Std Dev 5.104
Gaus mean : 32.7186
Gaus Width: 3.3117
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch110, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 38.75
Std Dev 3.352
Gaus mean : 38.8646
Gaus Width: 3.1364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch110, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 45.3
Std Dev 3.485
Gaus mean : 45.8189
Gaus Width: 3.3424
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch110, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 50.73
Std Dev 3.585
Entry
ampl
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1
2
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4
5
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7
8
0
10
20
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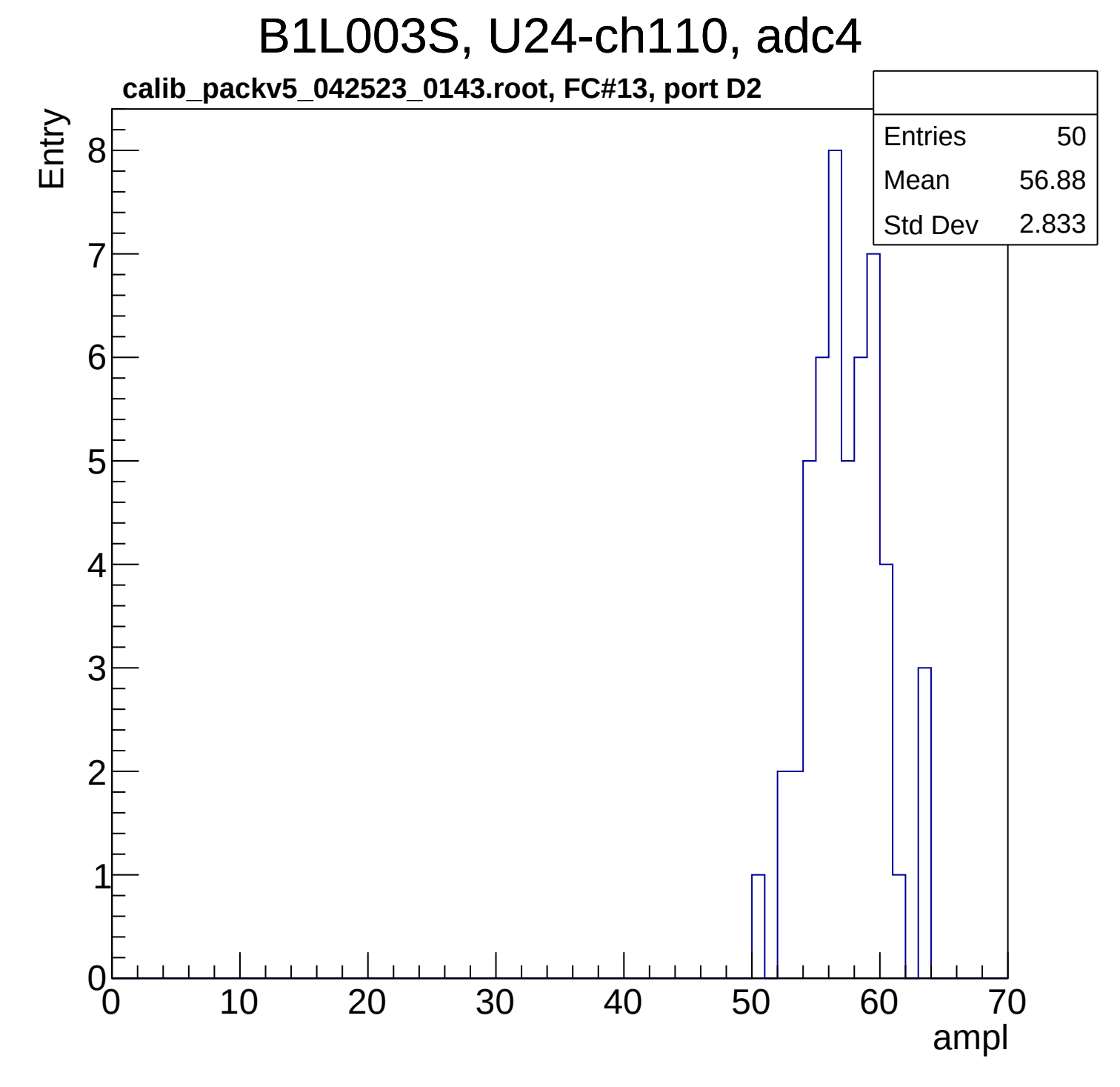
40
50
60
70


B1L003S, U24-ch110, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 56.88
Std Dev 2.833
Entry
ampl
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1
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4
5
6
7
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0
10
20
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50
60
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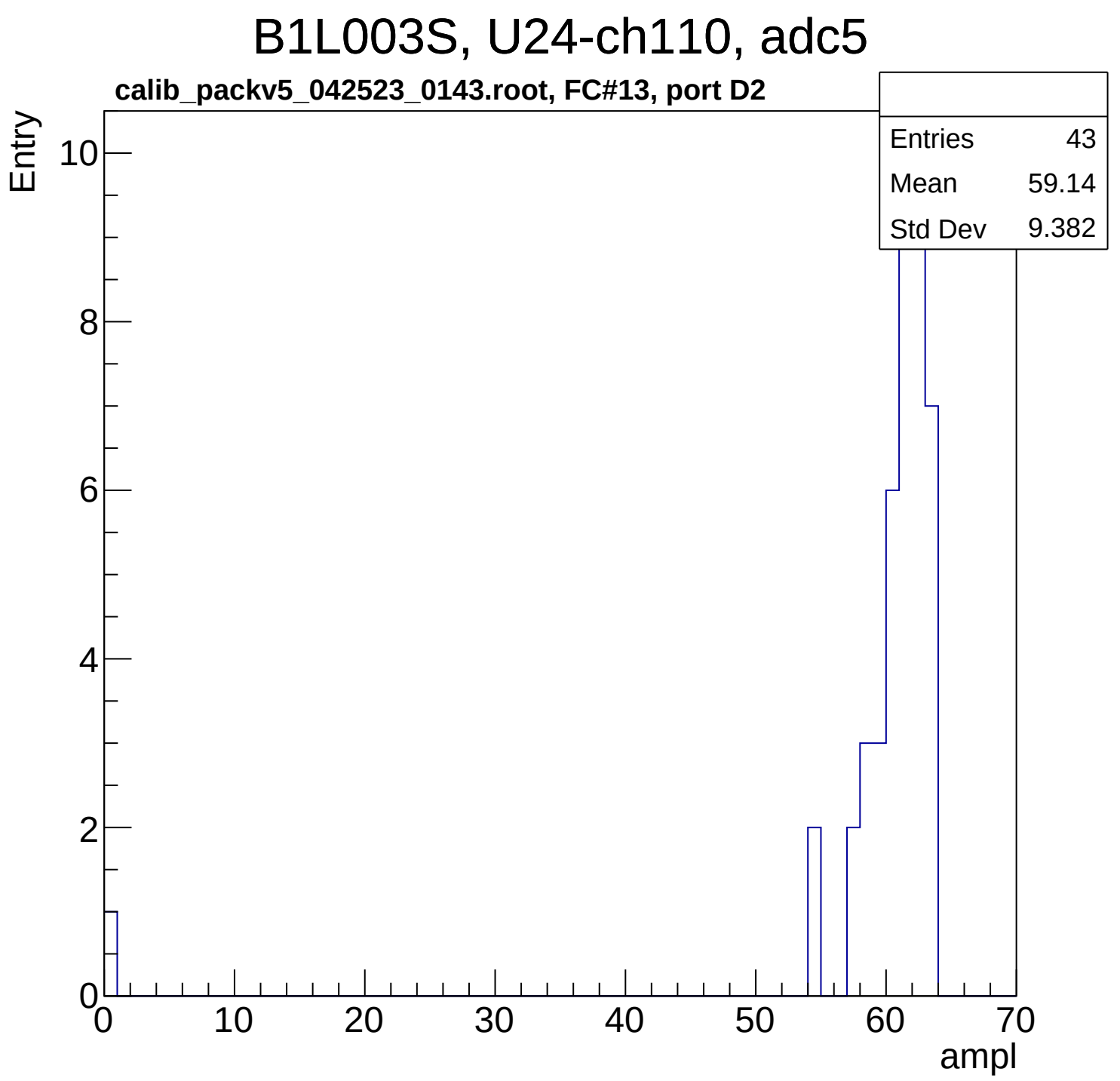
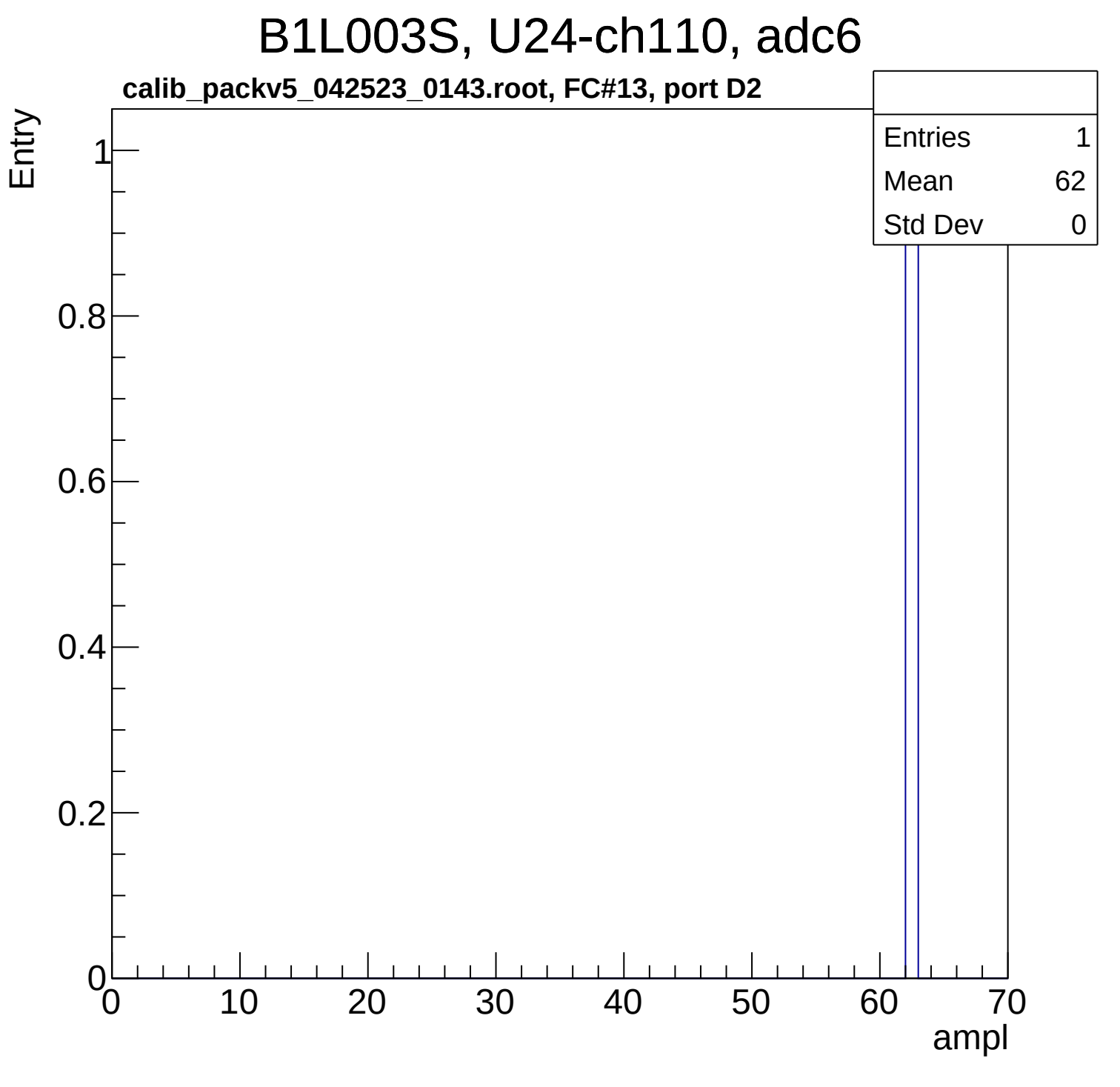

B1L003S, U24-ch110, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.14
Std Dev 9.382
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch110, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
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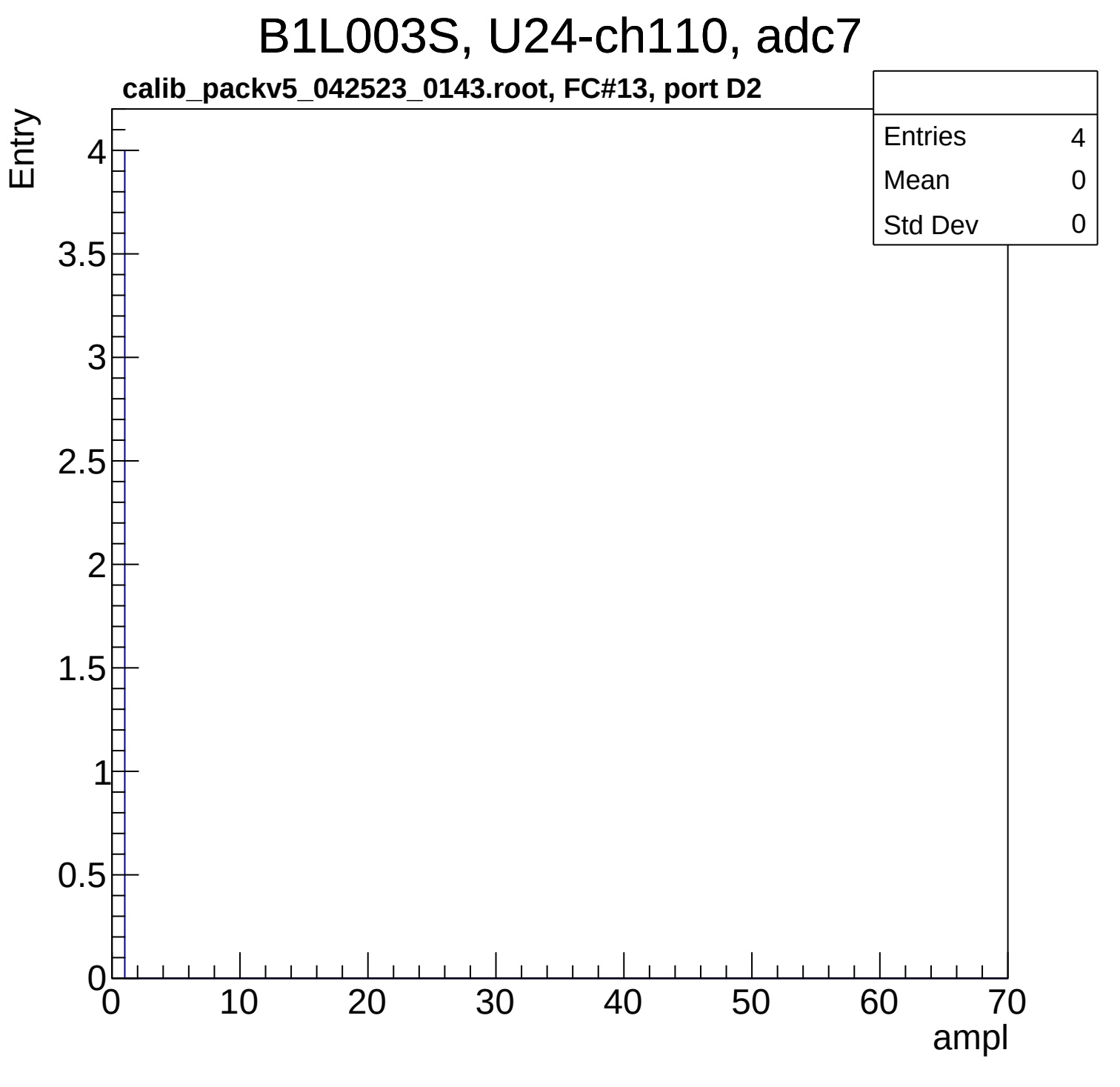
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch110, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 0
Std Dev 0
Entry
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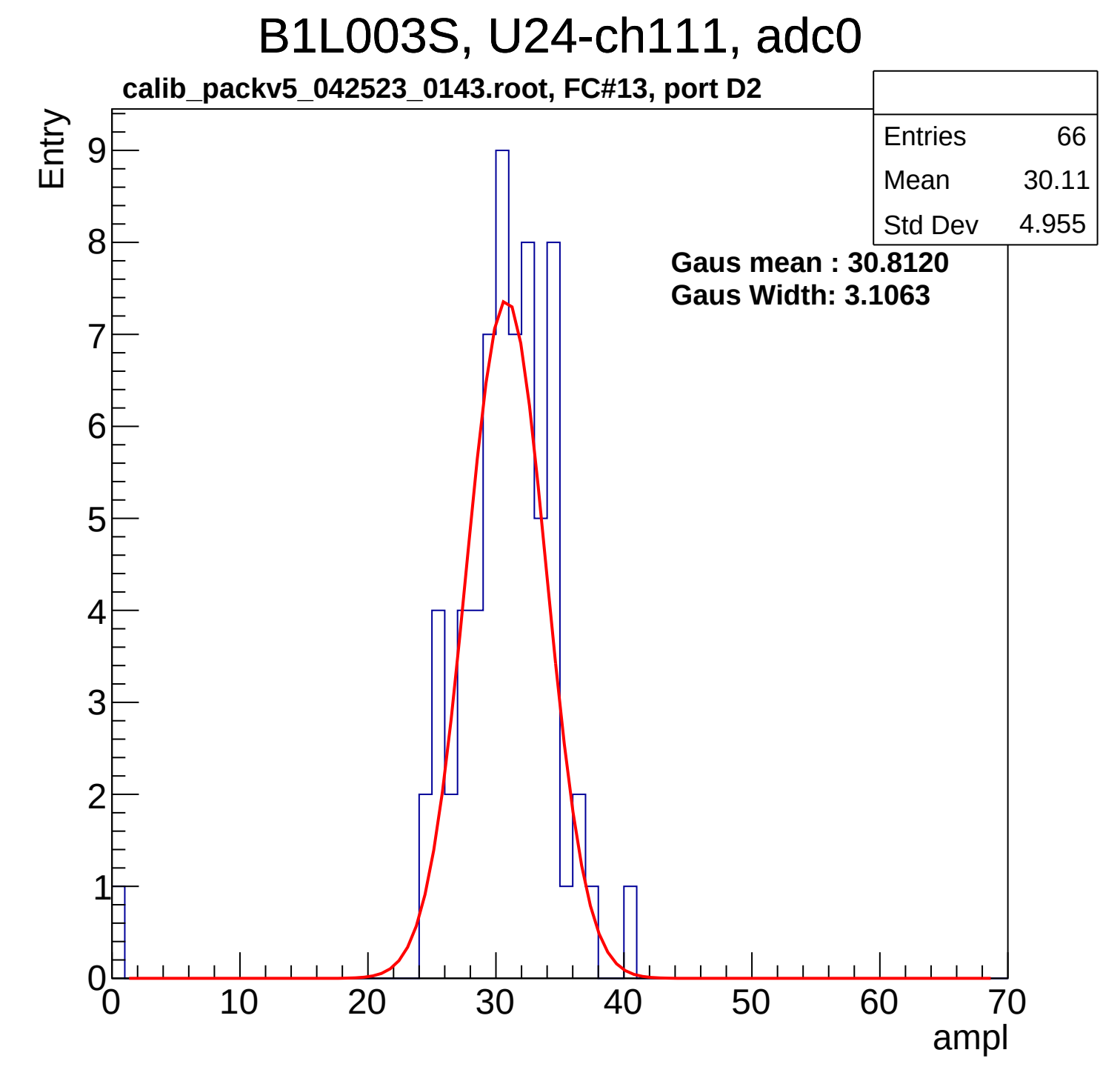
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch111, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.11
Std Dev 4.955
Gaus mean : 30.8120
Gaus Width: 3.1063
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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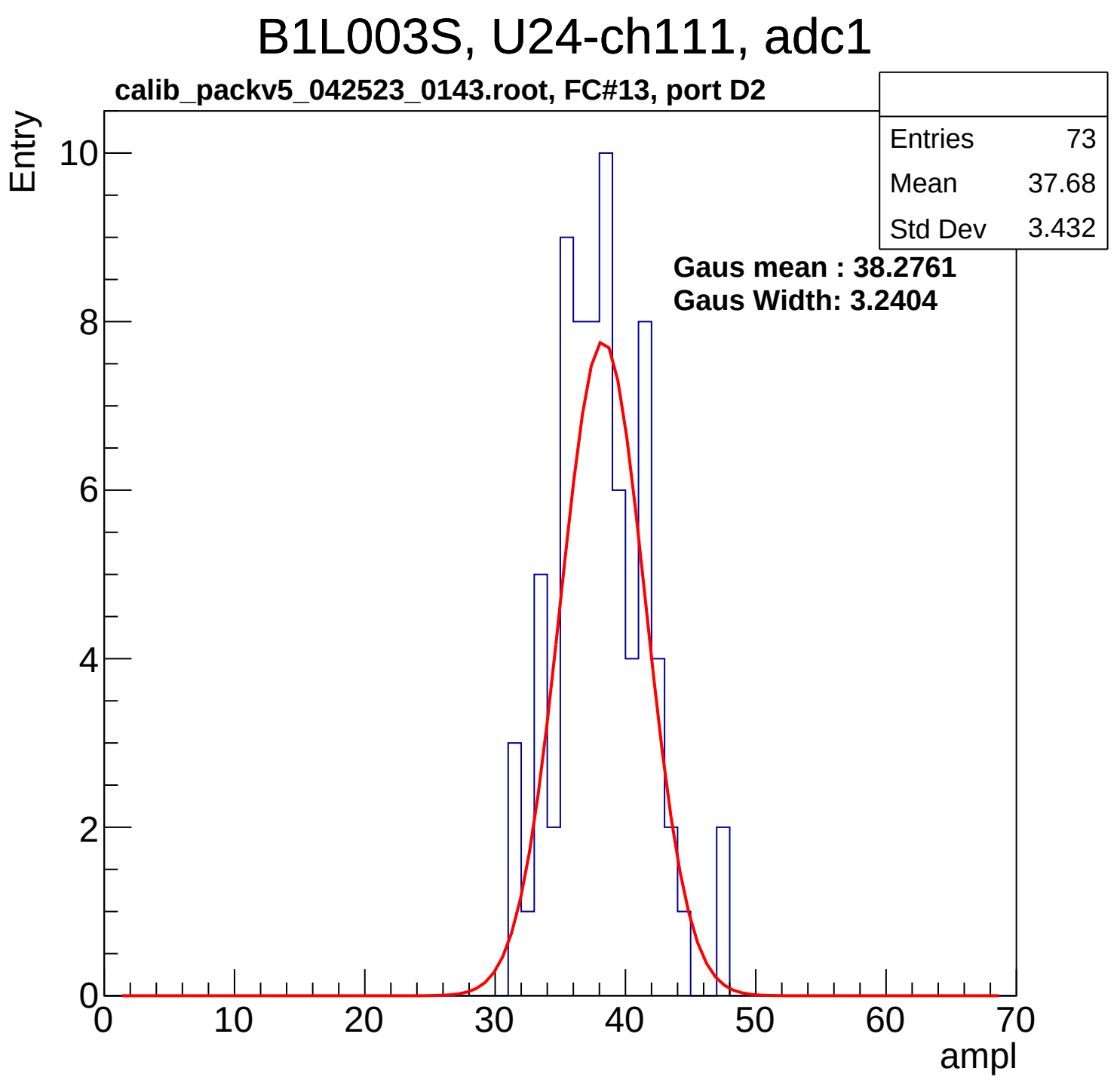
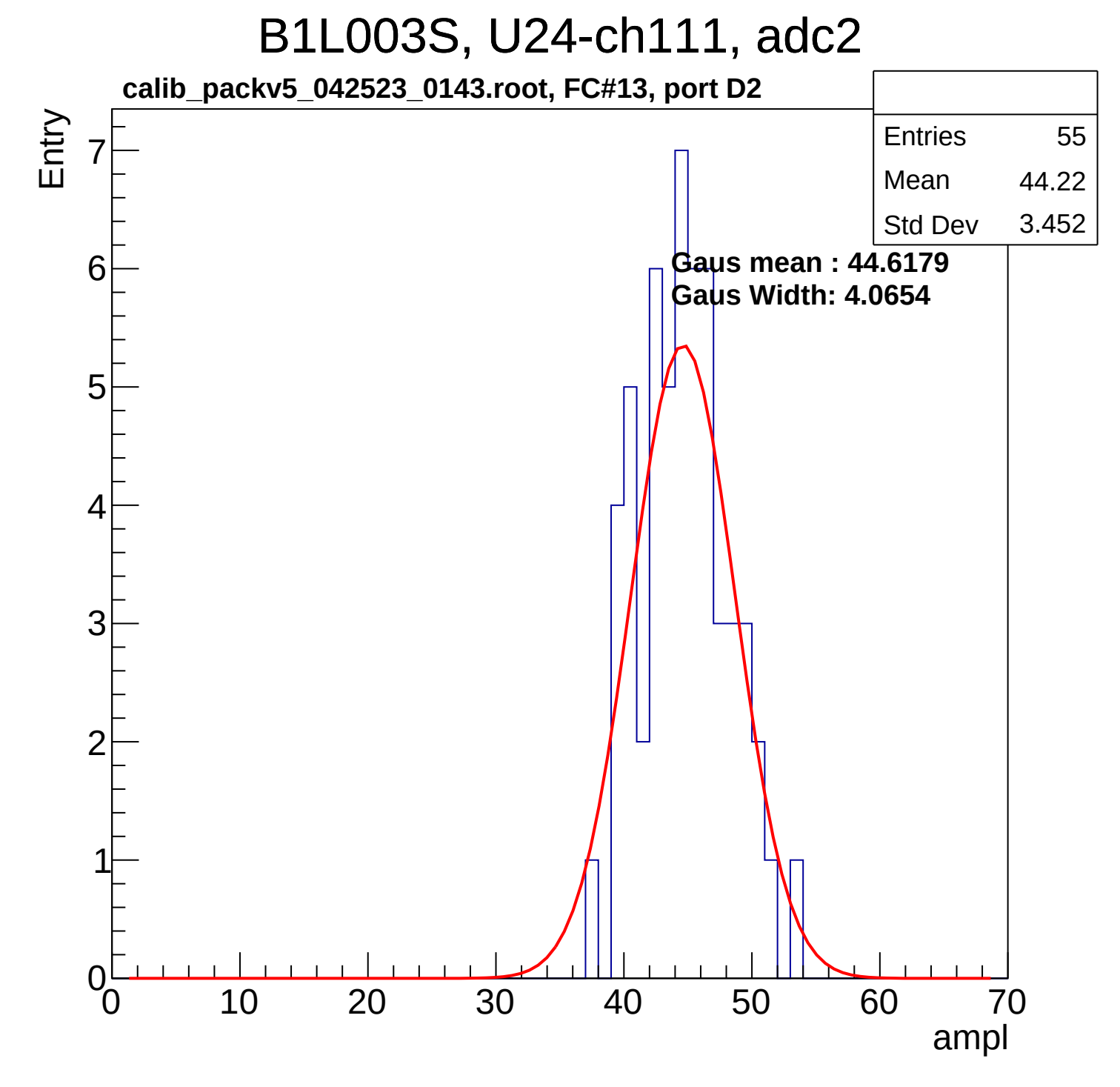
40
50
60
70


B1L003S, U24-ch111, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 37.68
Std Dev 3.432
Gaus mean : 38.2761
Gaus Width: 3.2404
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch111, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 44.22
Std Dev 3.452
Gaus mean : 44.6179
Gaus Width: 4.0654
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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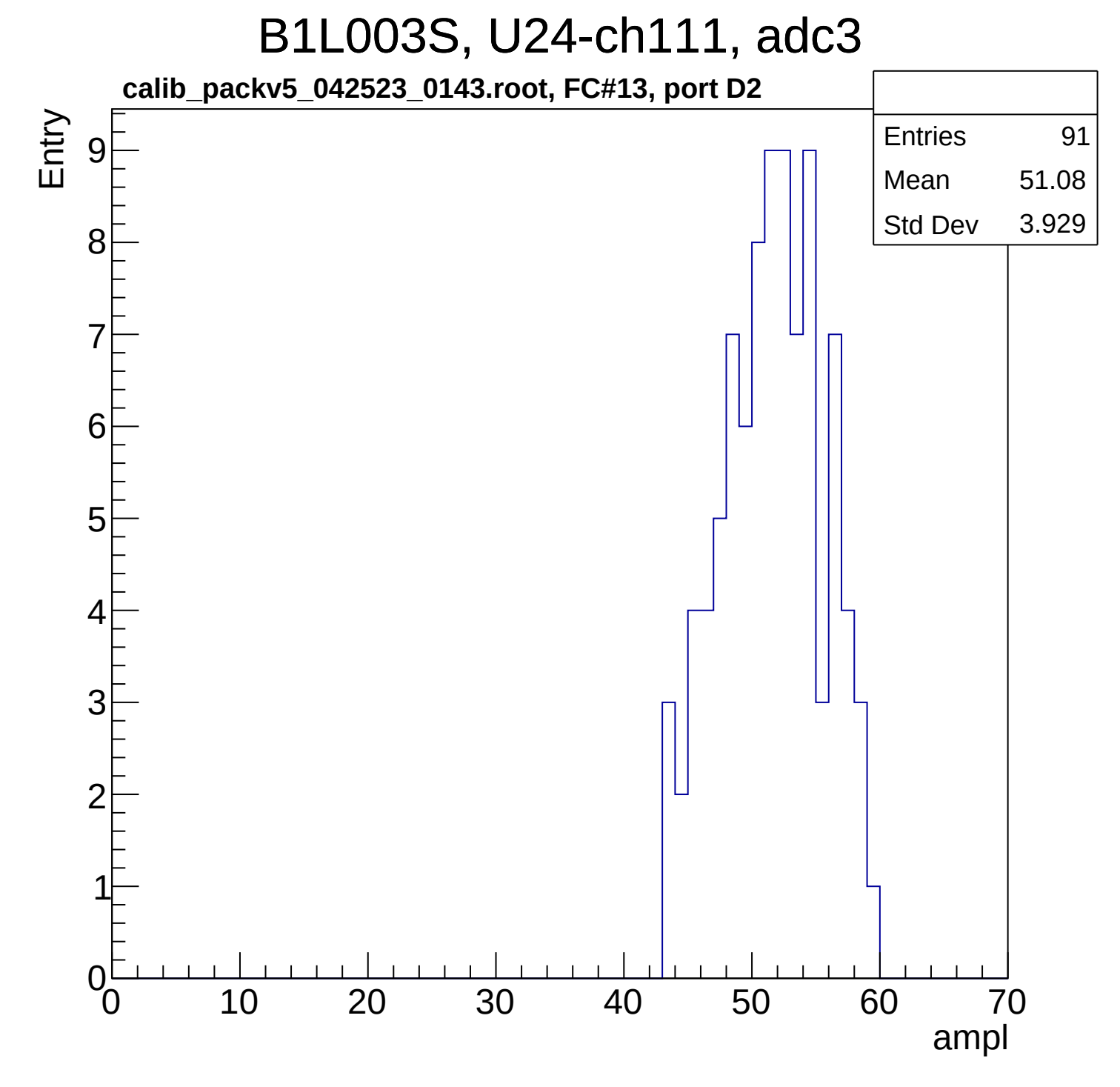
B1L003S, U24-ch111, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 91
Mean 51.08
Std Dev 3.929
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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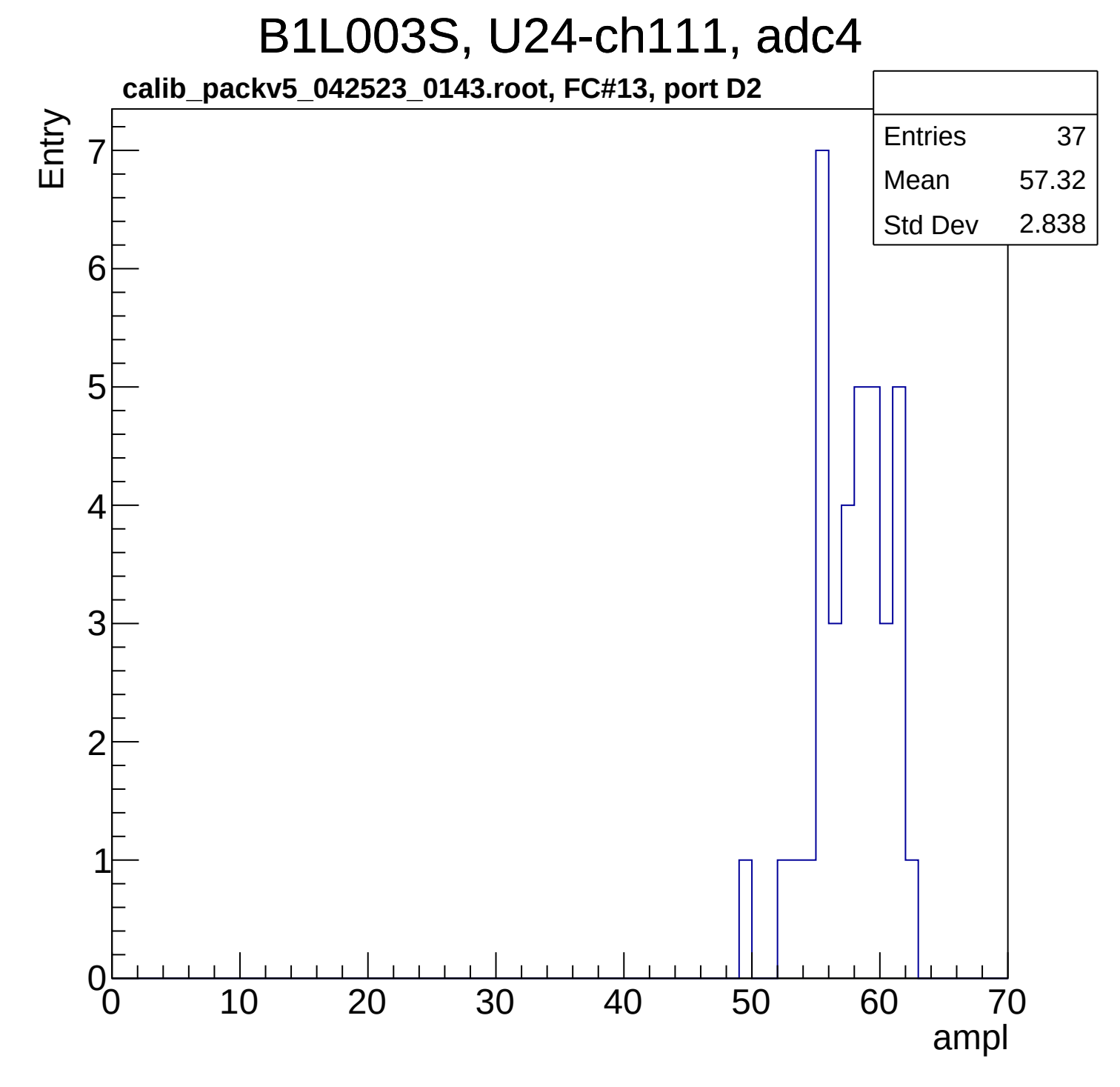
B1L003S, U24-ch111, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 57.32
Std Dev 2.838
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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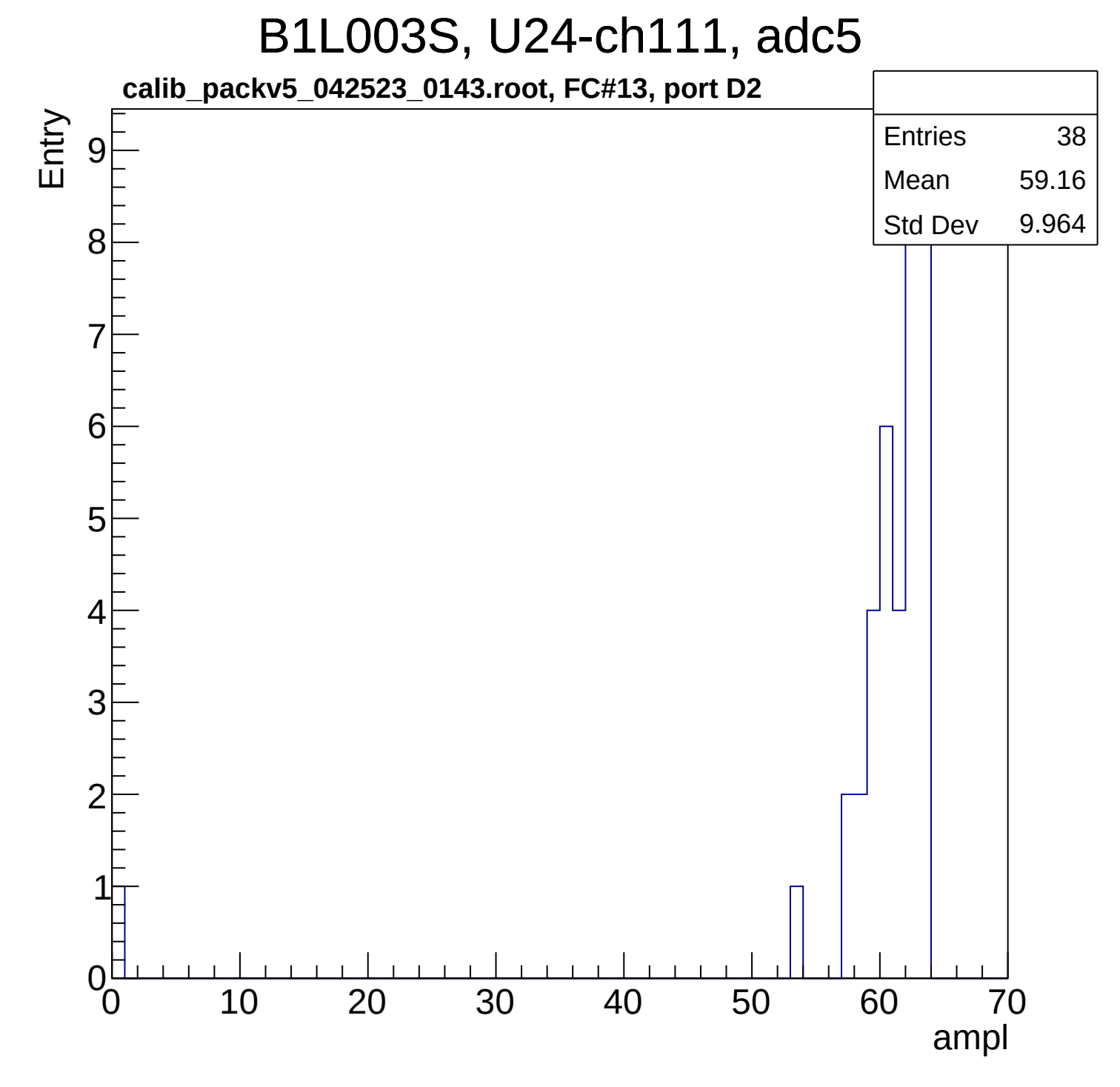
B1L003S, U24-ch111, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 59.16
Std Dev 9.964
Entry
ampl
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2
3
4
5
6
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0
10
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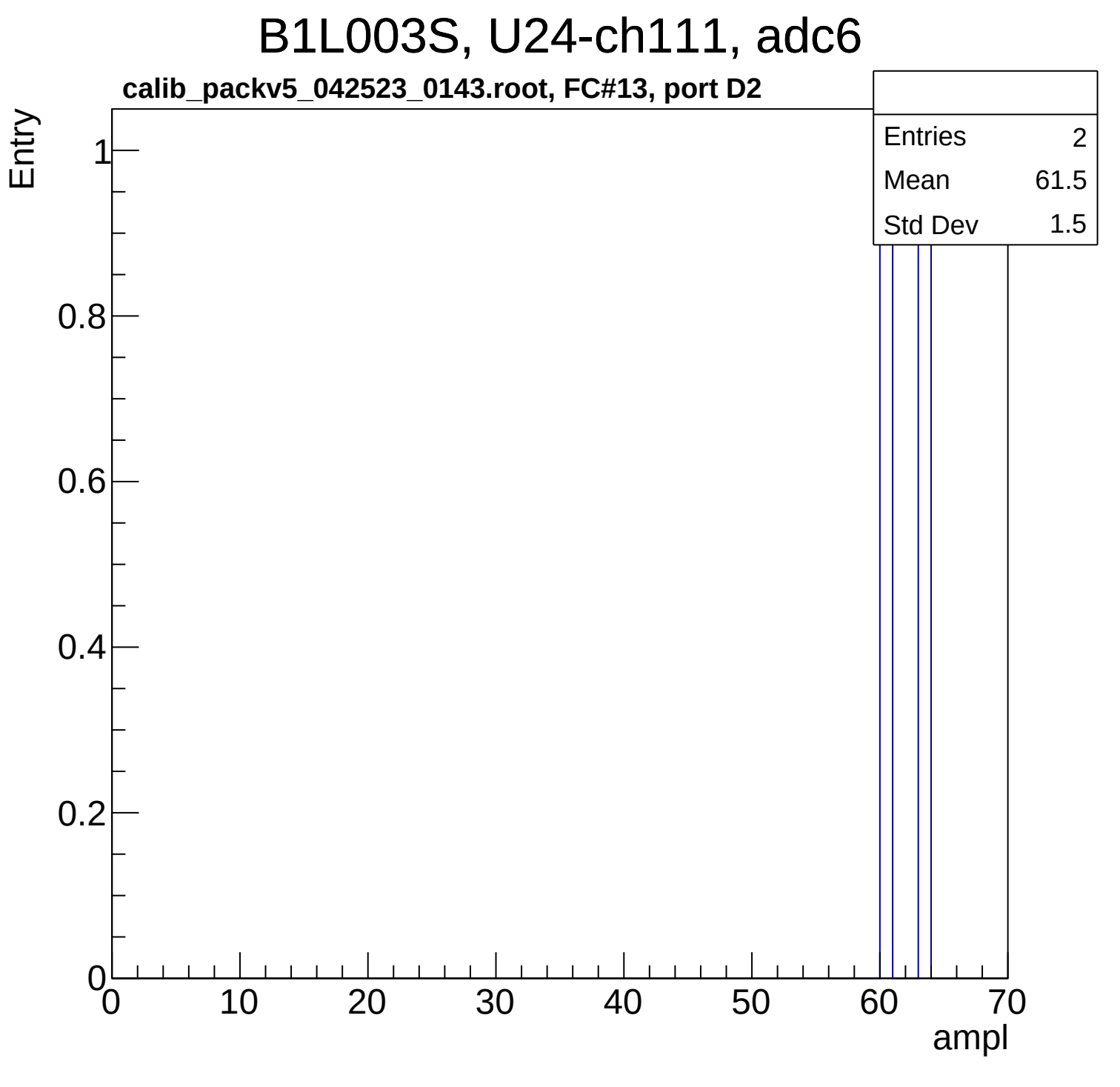
B1L003S, U24-ch111, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
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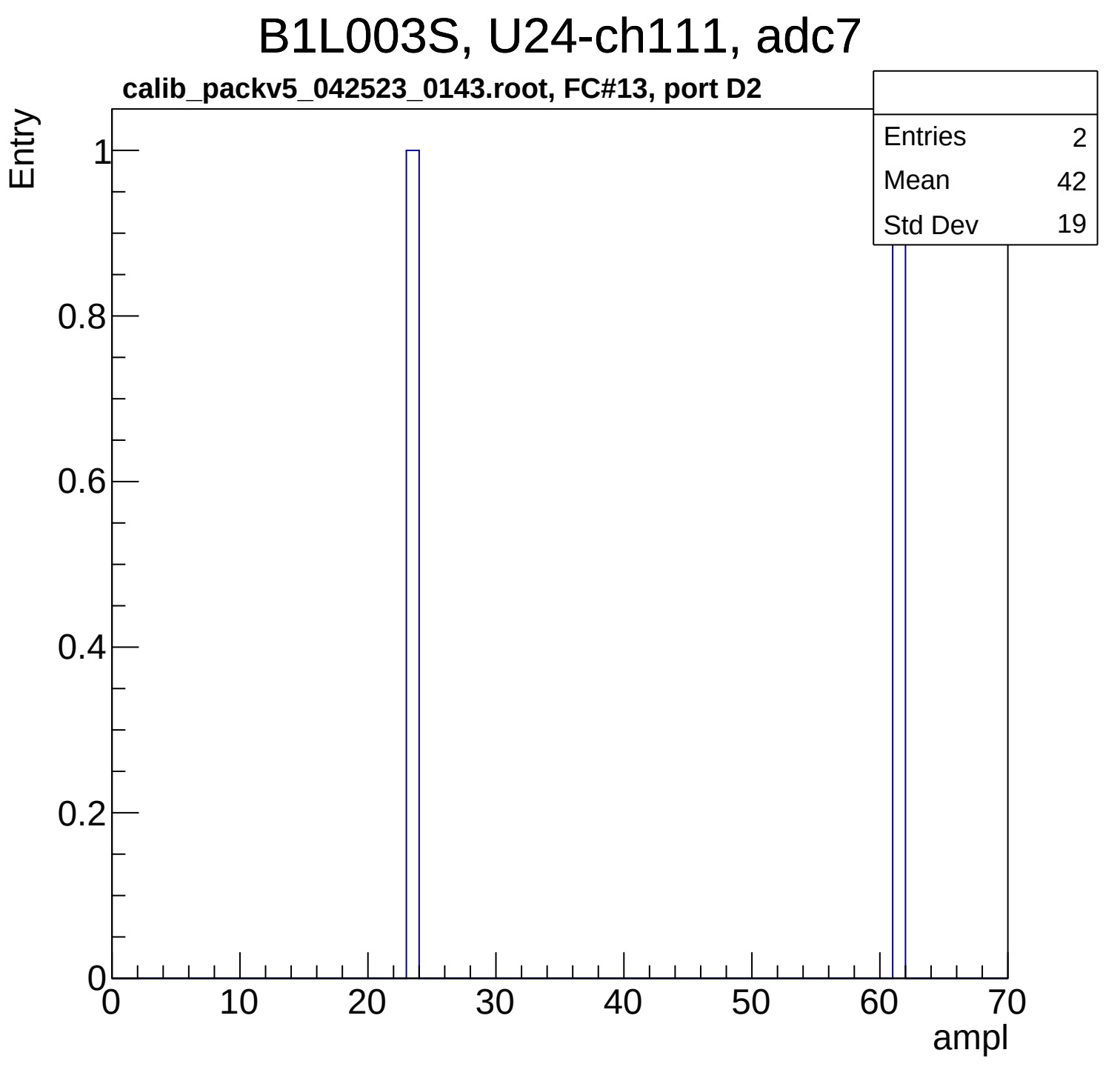
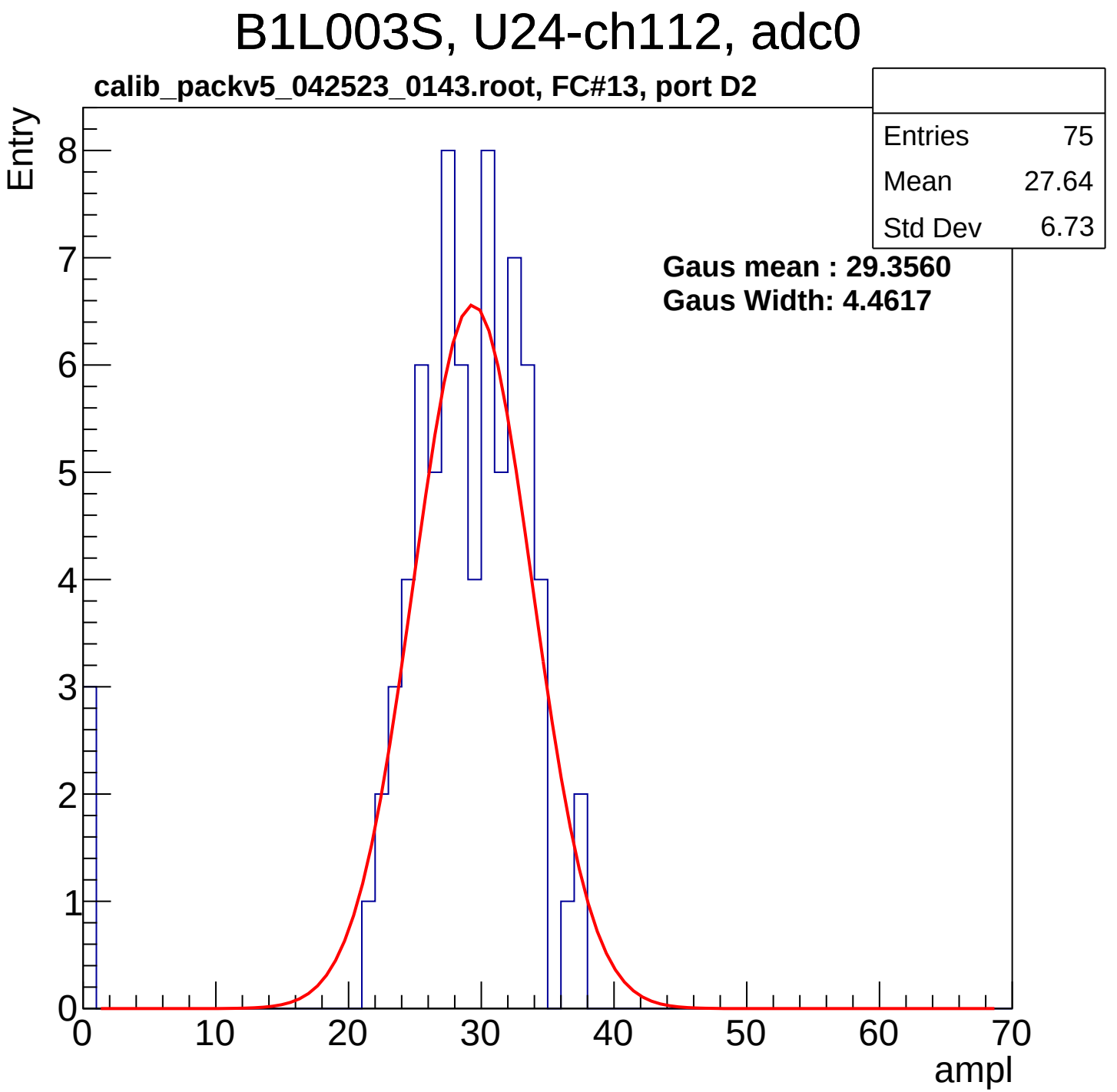
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch111, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 42
Std Dev 19
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch112, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 27.64
Std Dev 6.73
Gaus mean : 29.3560
Gaus Width: 4.4617
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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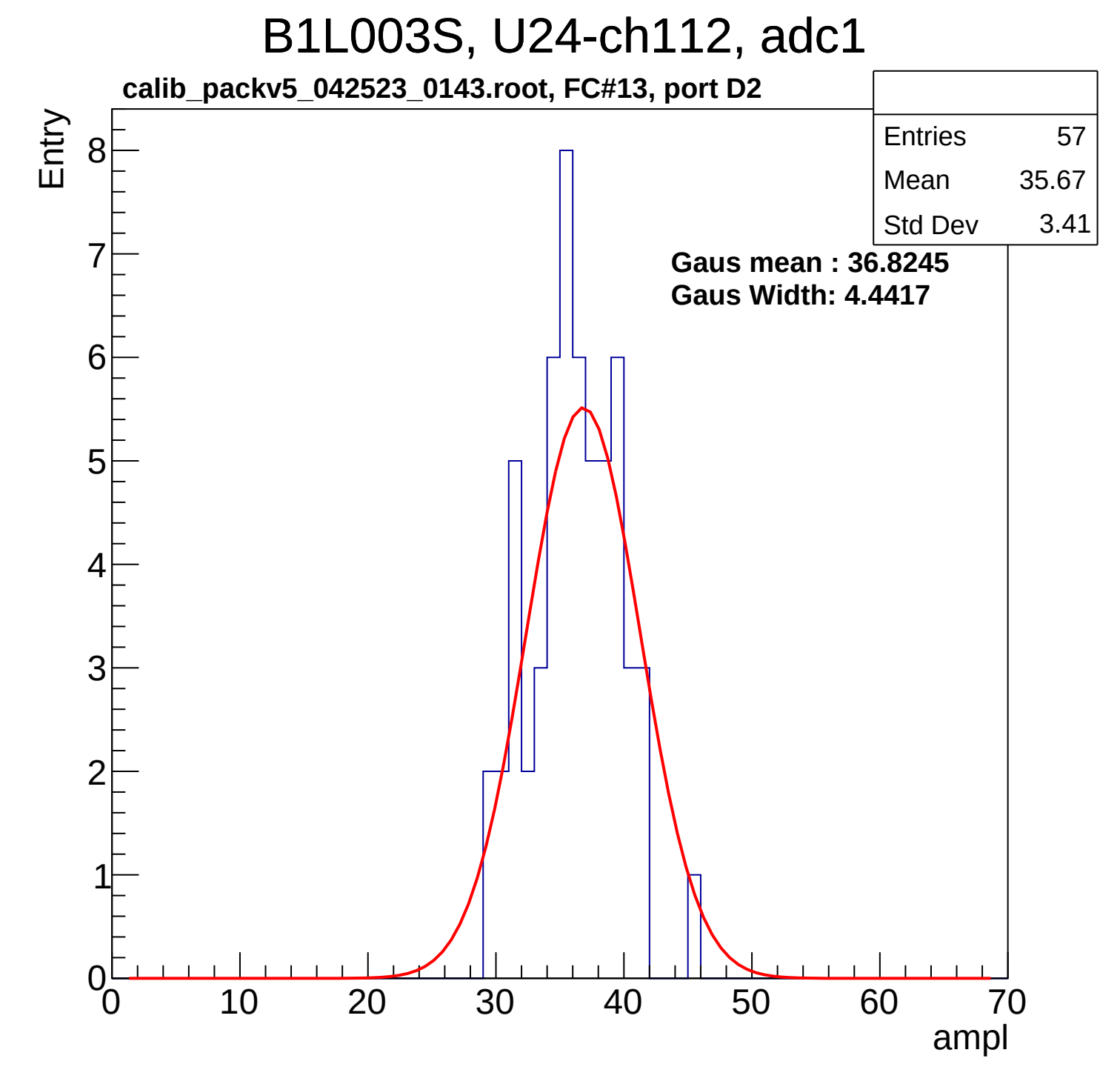
B1L003S, U24-ch112, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.67
Std Dev 3.41
Gaus mean : 36.8245
Gaus Width: 4.4417
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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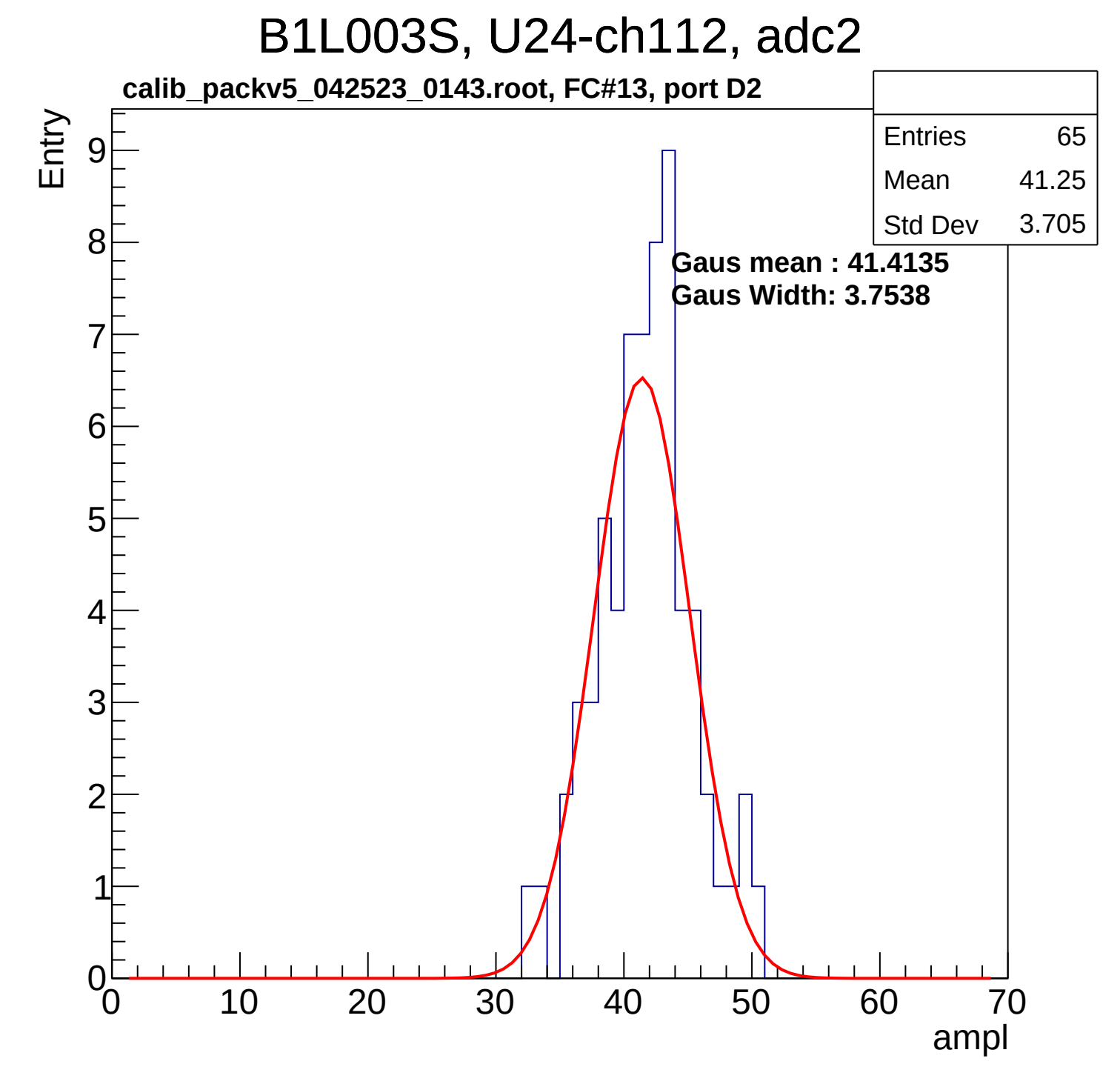
B1L003S, U24-ch112, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 41.25
Std Dev 3.705
Gaus mean : 41.4135
Gaus Width: 3.7538
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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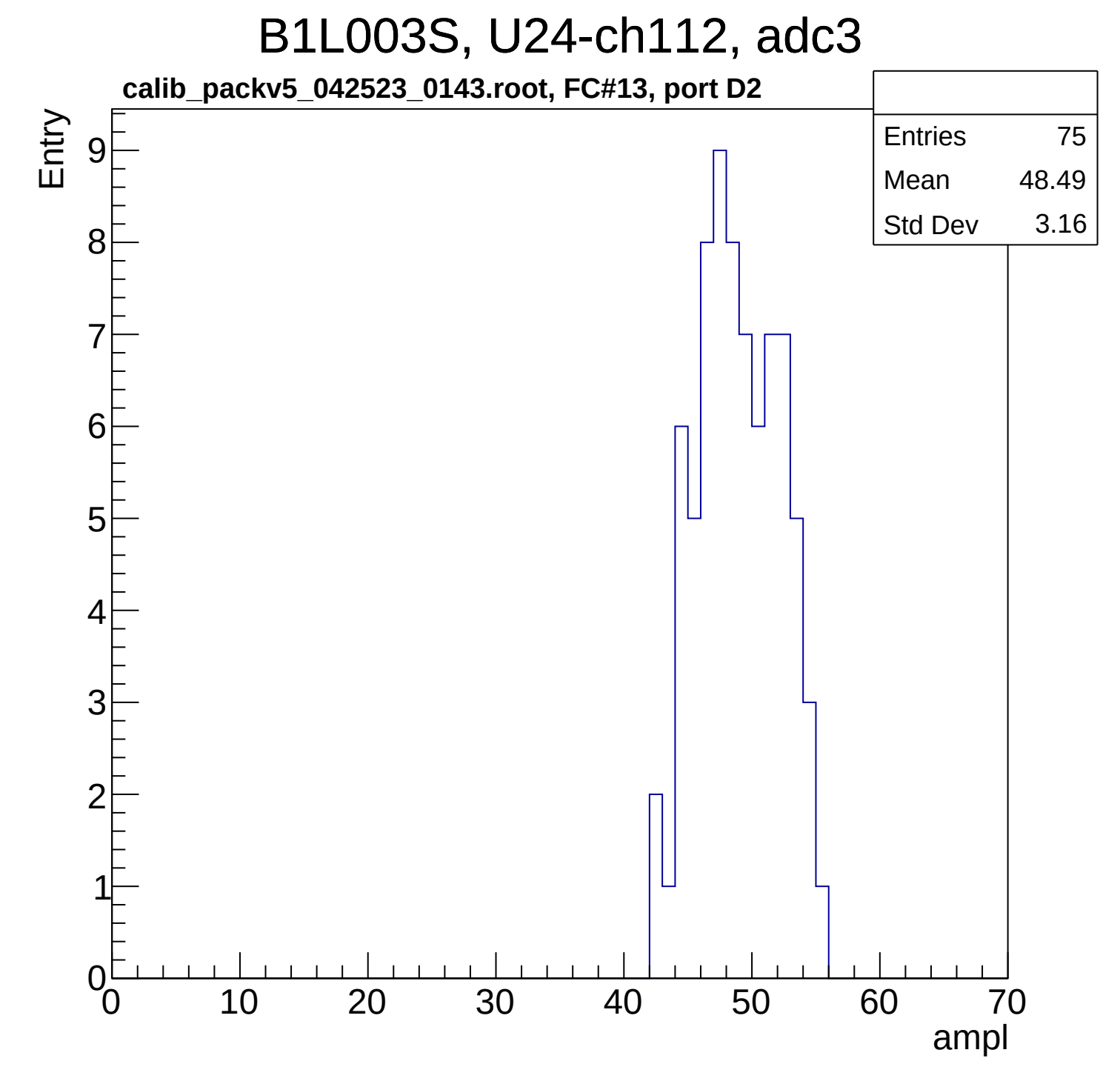
B1L003S, U24-ch112, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 48.49
Std Dev 3.16
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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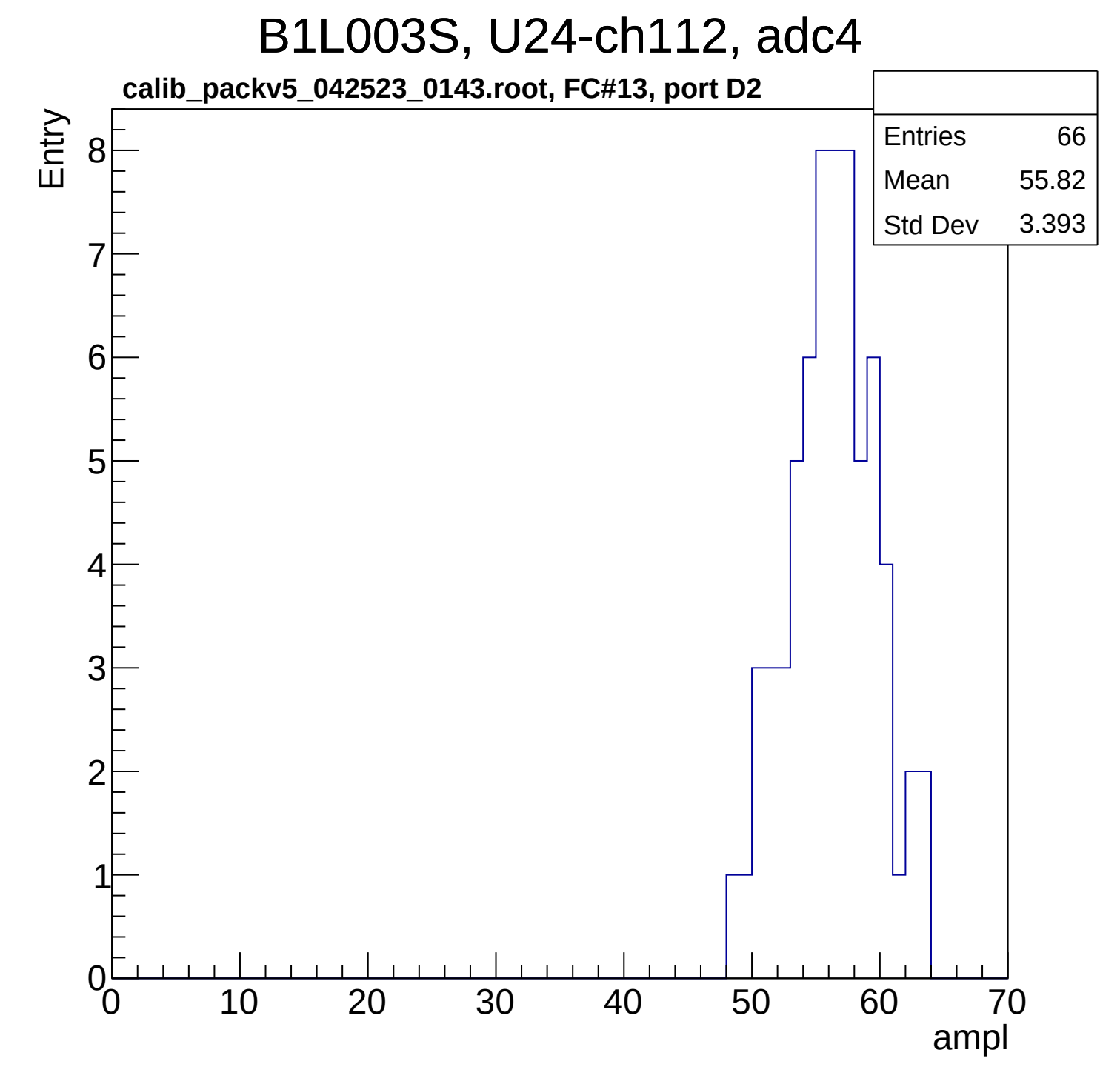
B1L003S, U24-ch112, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 55.82
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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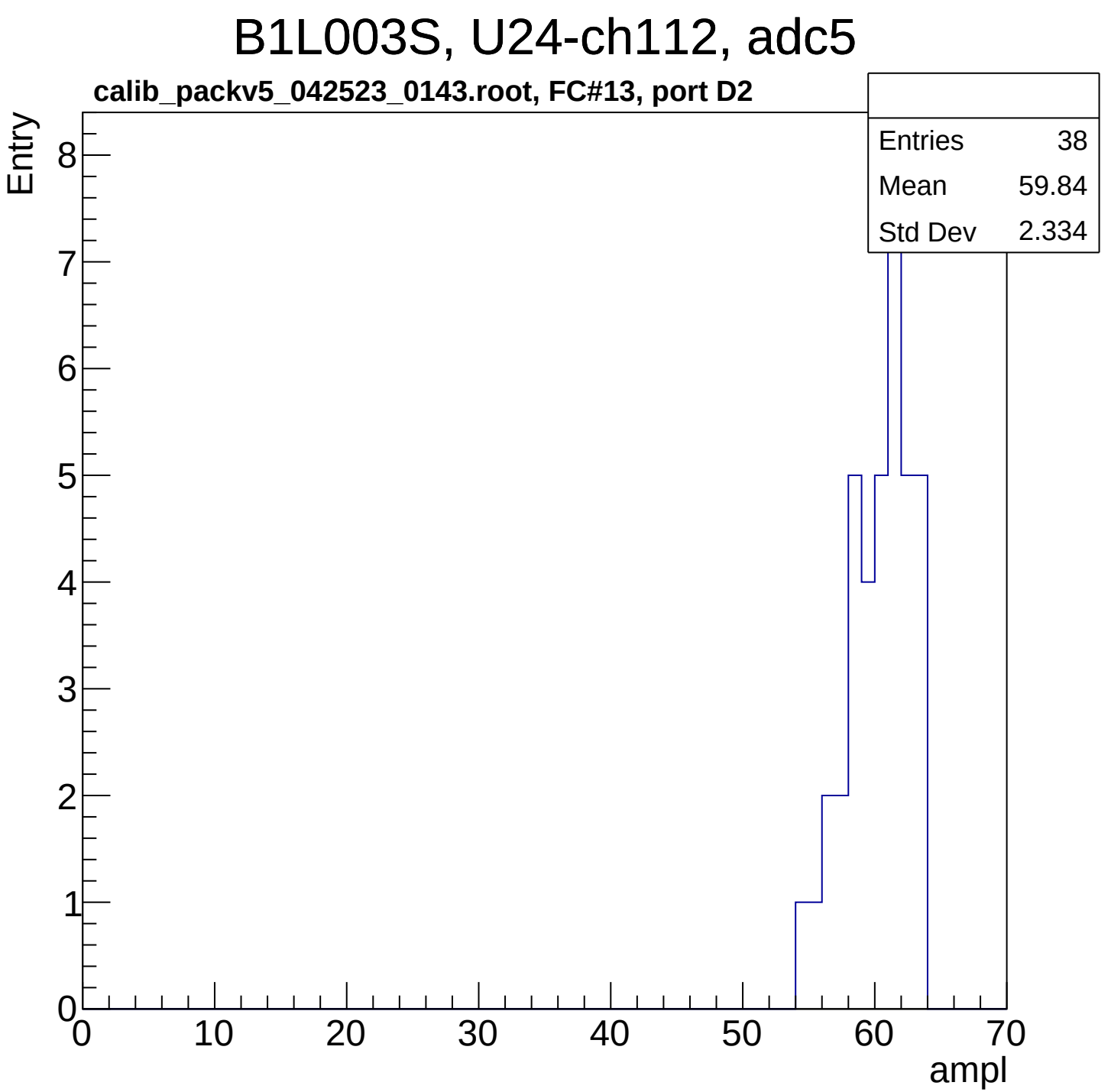
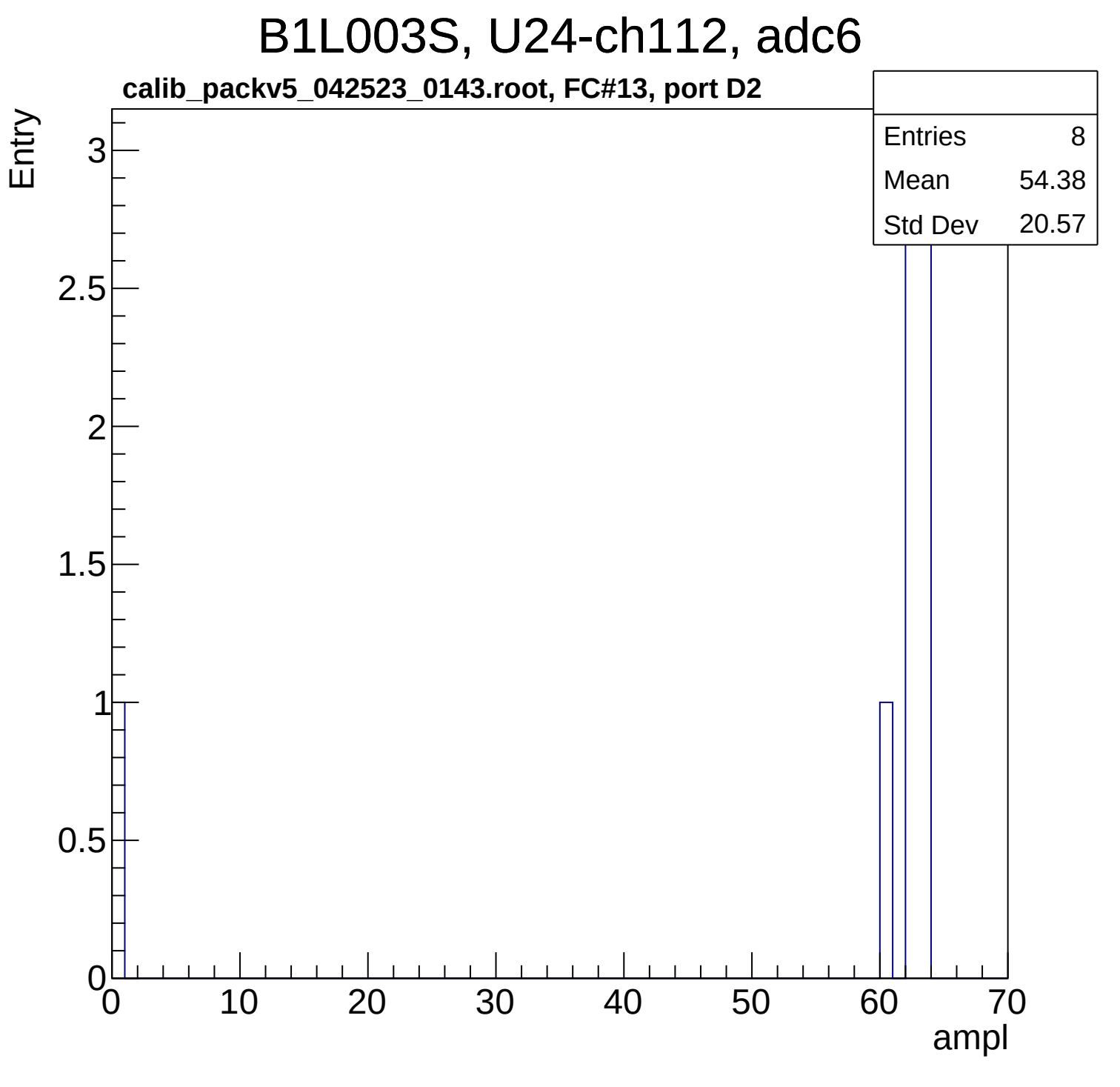

B1L003S, U24-ch112, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 59.84
Std Dev 2.334
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch112, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.38
Std Dev 20.57
Entry
ampl
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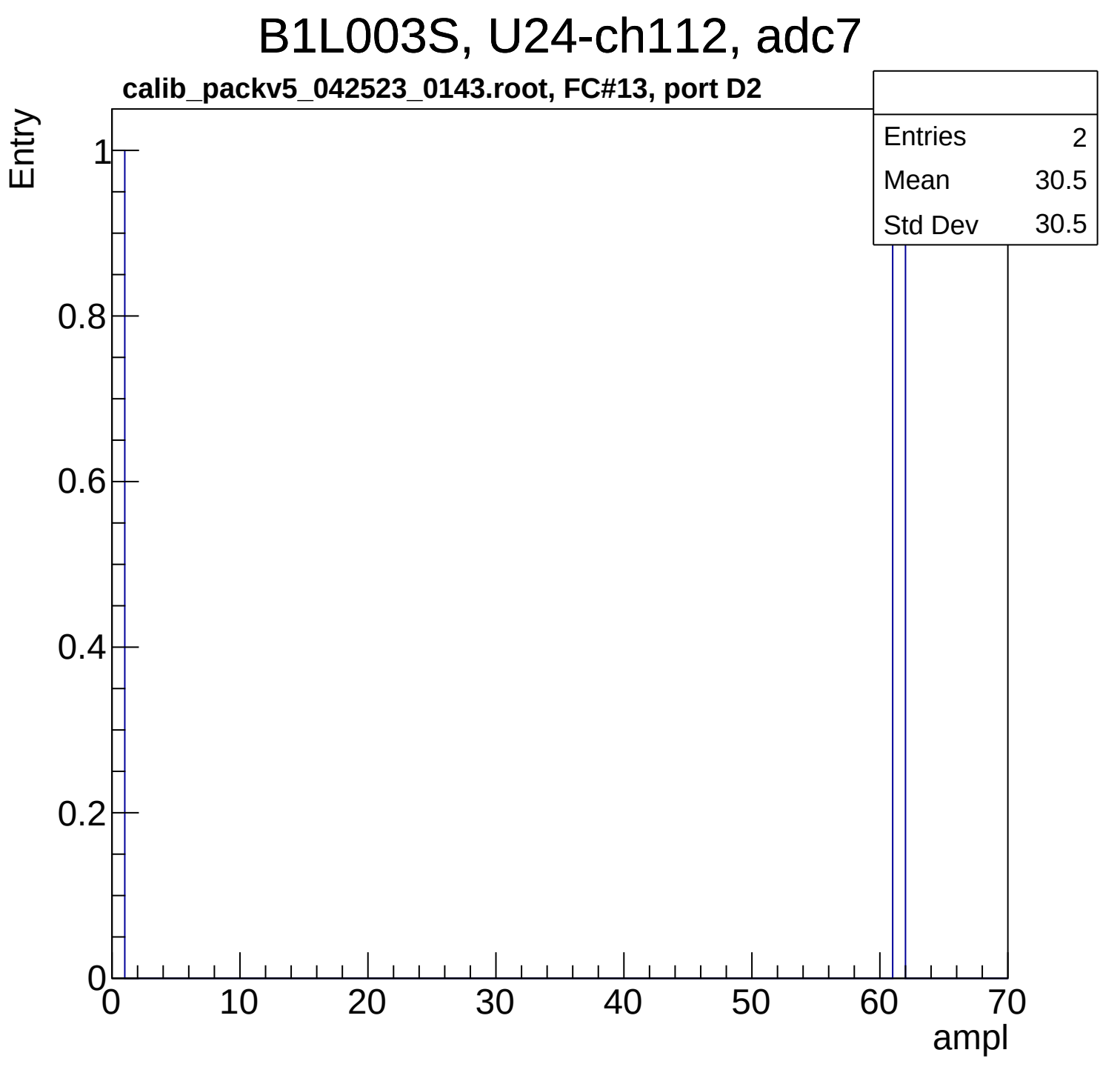
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch112, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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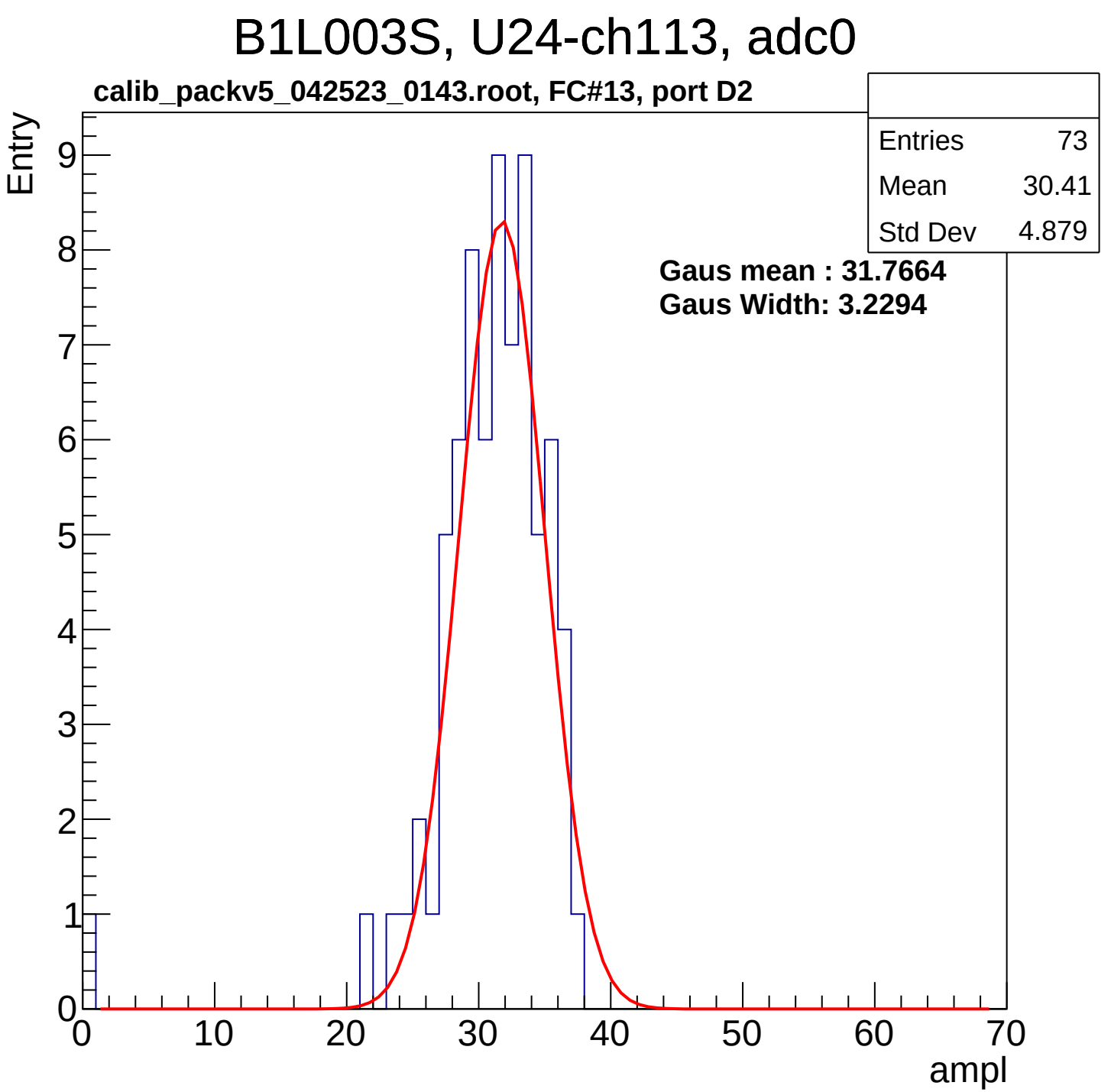
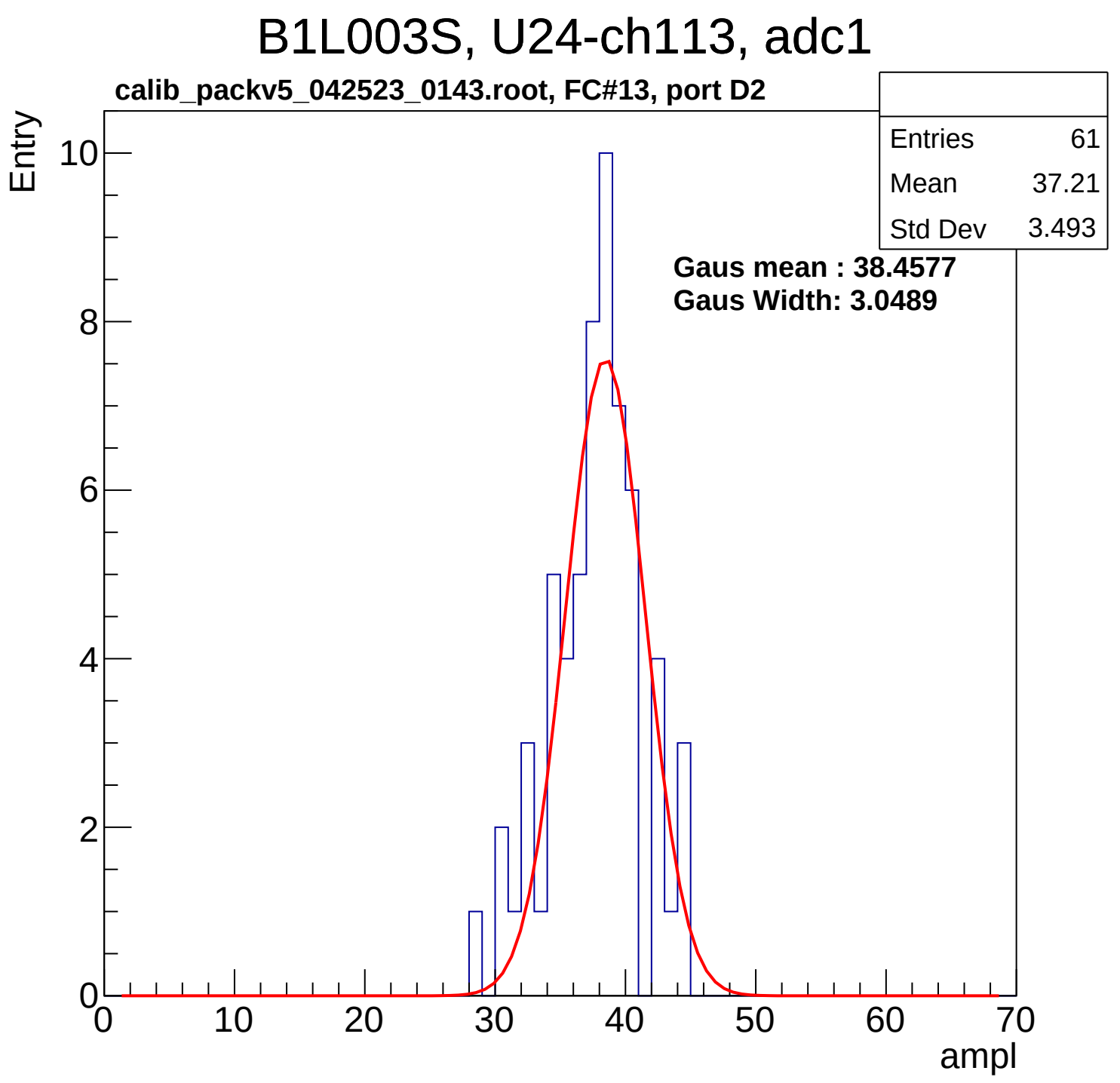
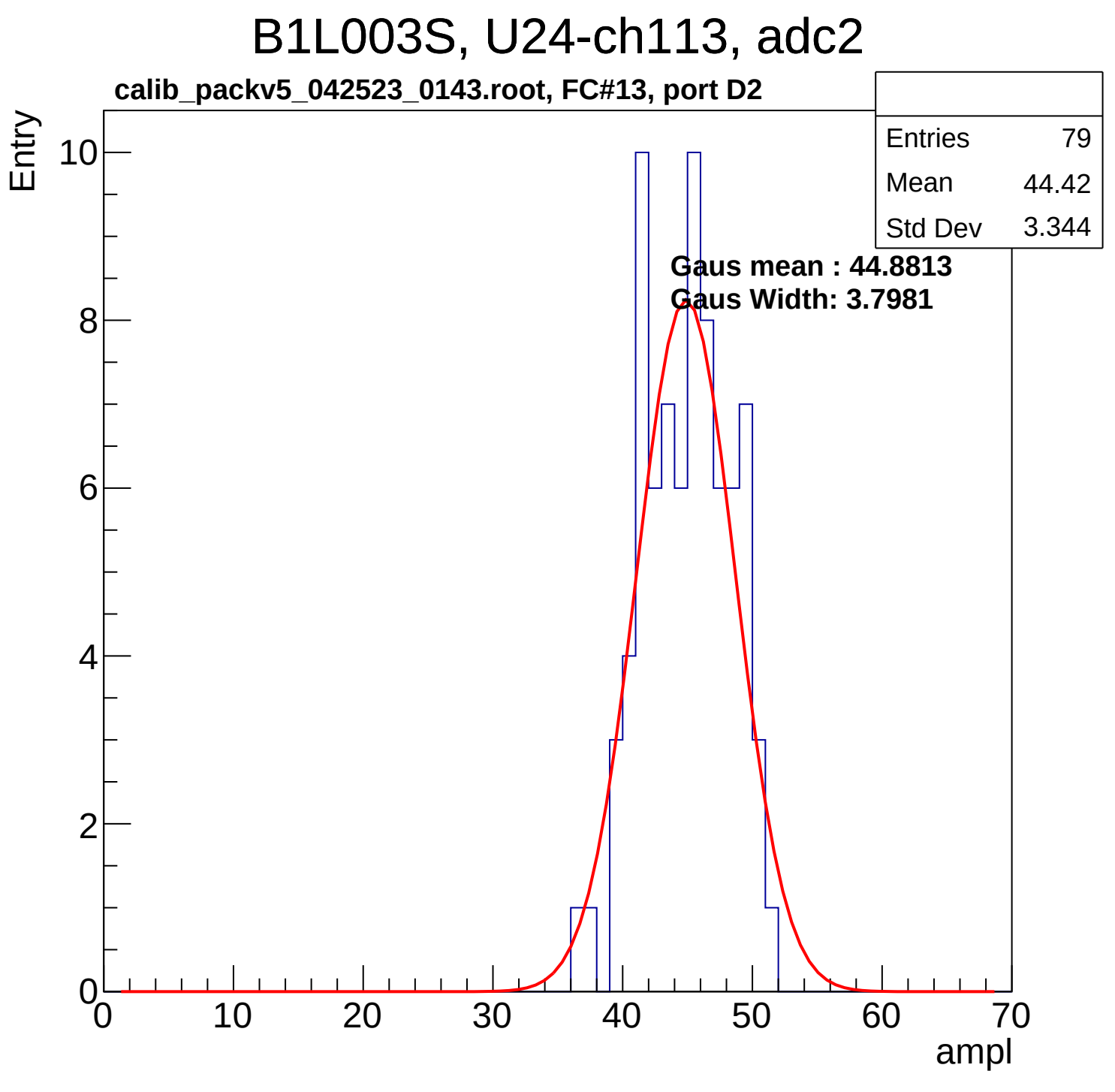
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch113, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 30.41
Std Dev 4.879
Gaus mean : 31.7664
Gaus Width: 3.2294
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch113, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 37.21
Std Dev 3.493
Gaus mean : 38.4577
Gaus Width: 3.0489
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch113, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 44.42
Std Dev 3.344
Gaus mean : 44.8813
Gaus Width: 3.7981
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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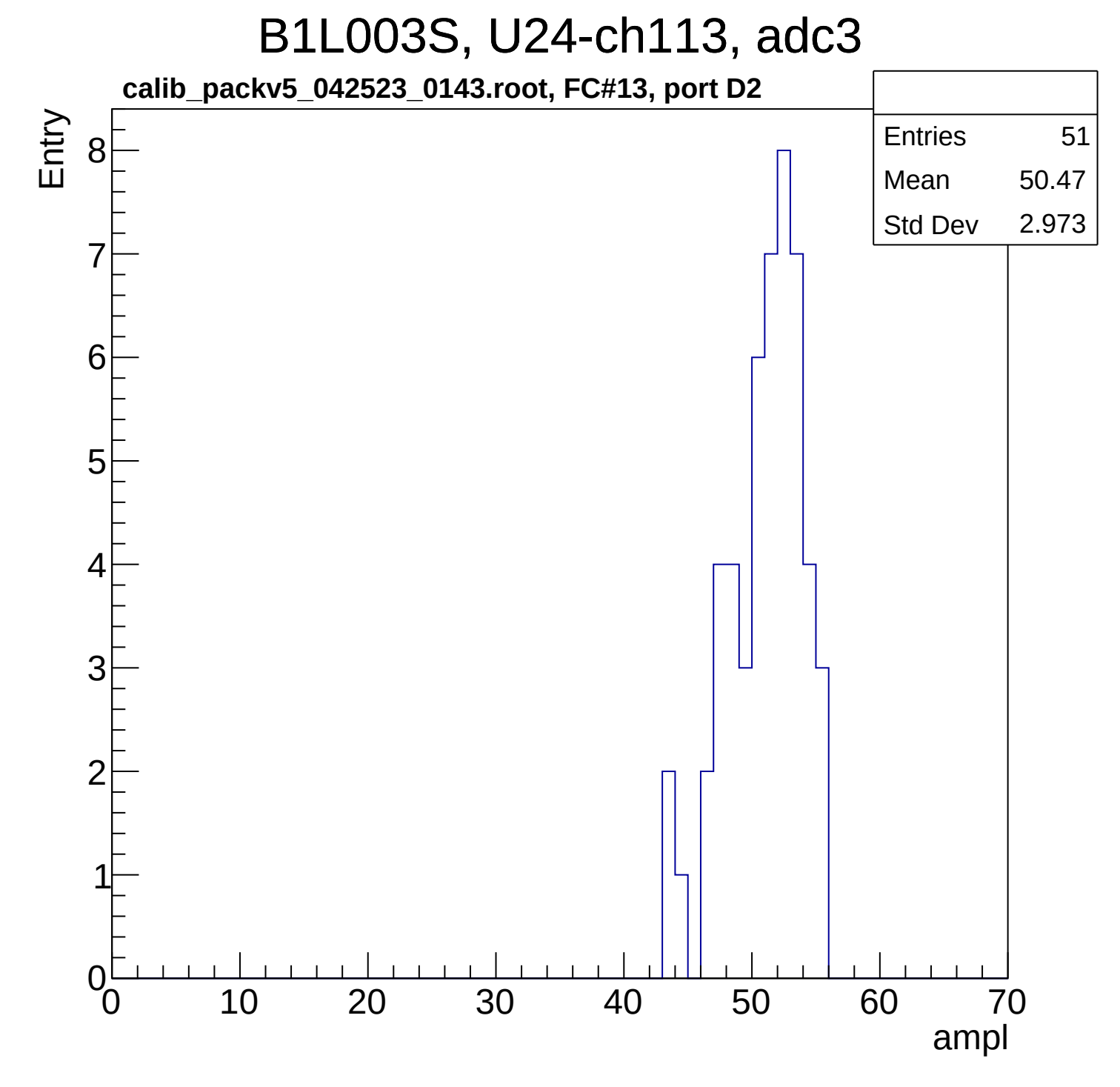
B1L003S, U24-ch113, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 50.47
Std Dev 2.973
Entry
ampl
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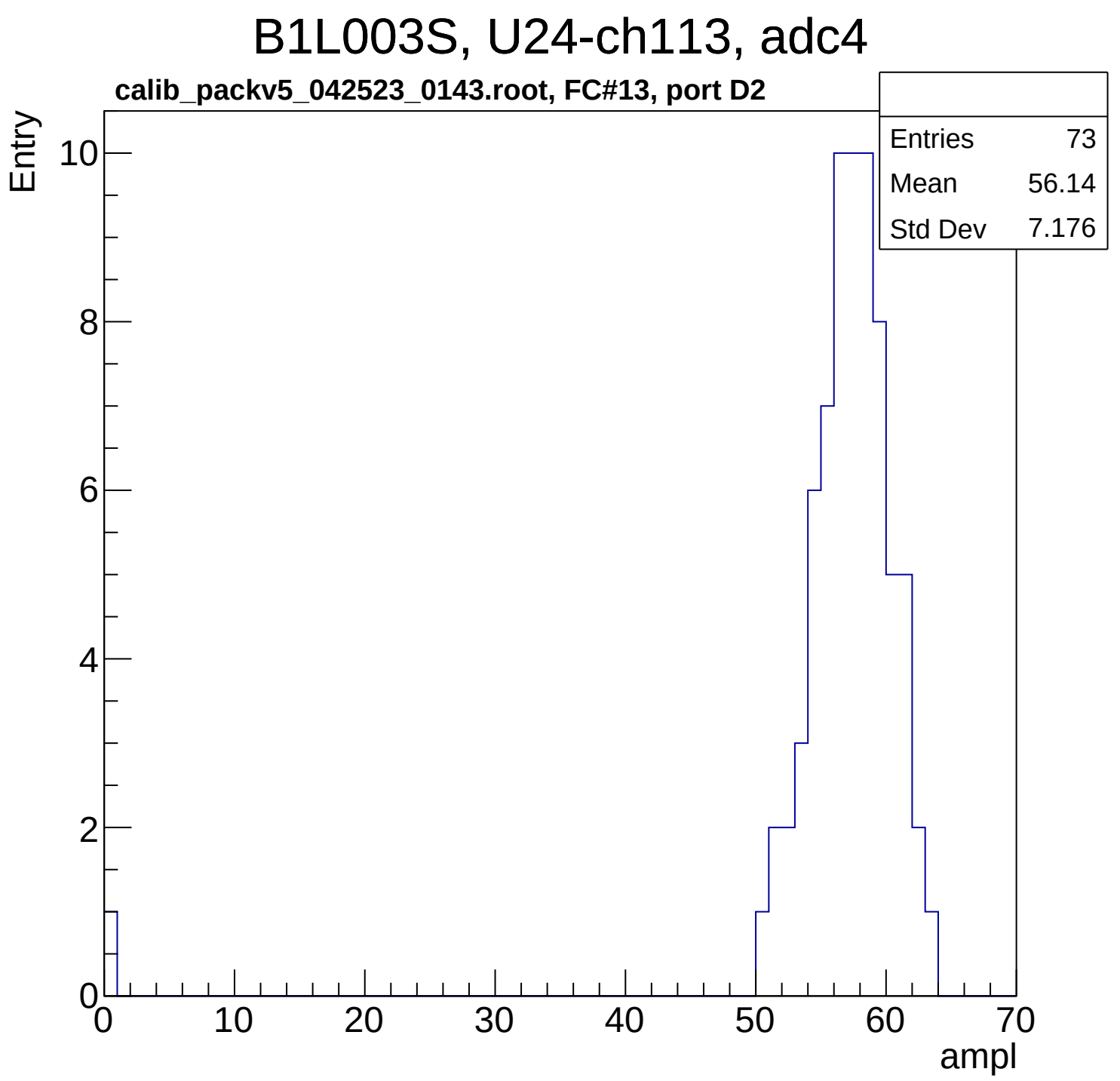
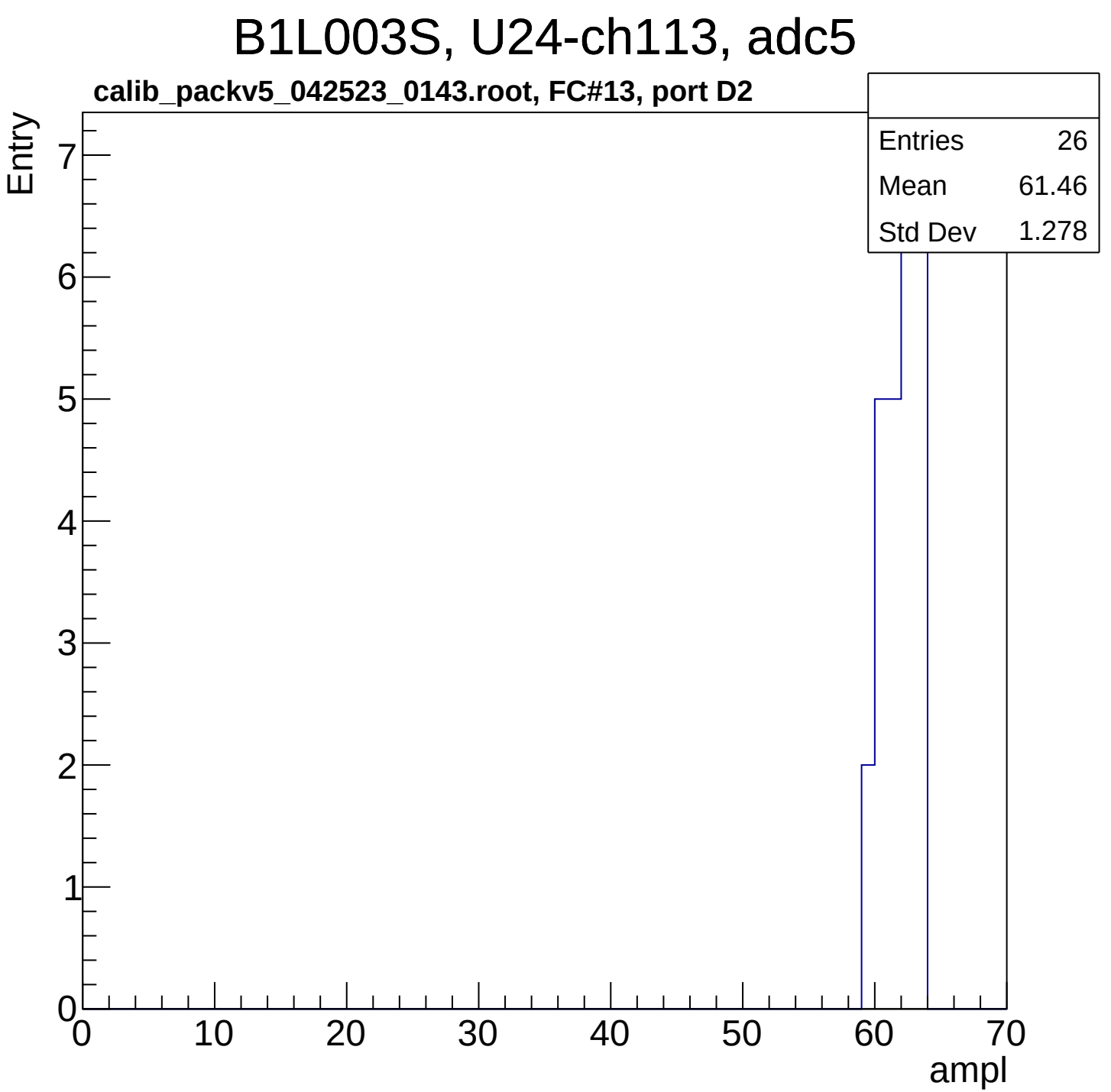
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch113, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 56.14
Std Dev 7.176
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch113, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 26
Mean 61.46
Std Dev 1.278
Entry
ampl
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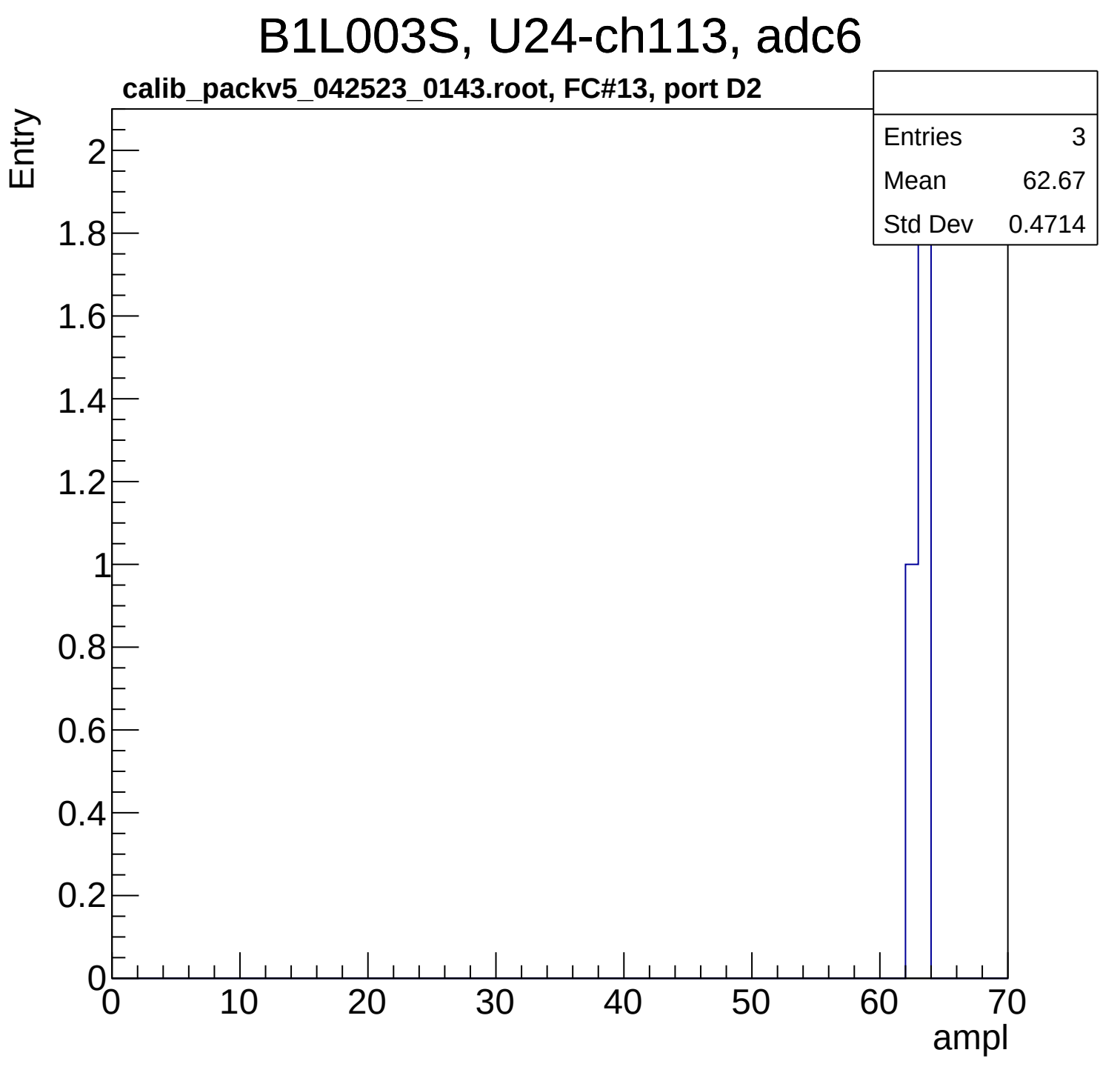
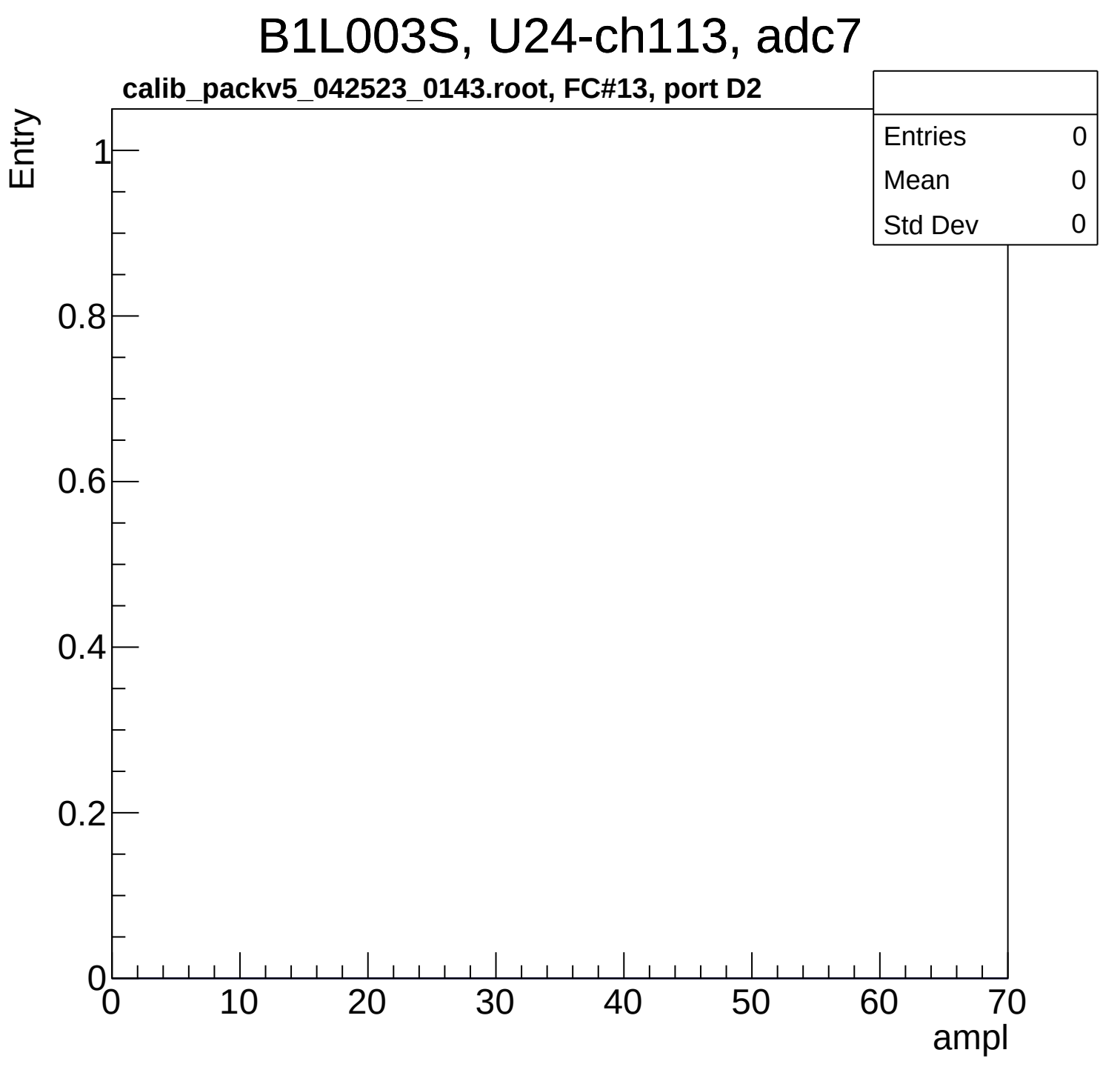
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U24-ch113, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U24-ch113, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
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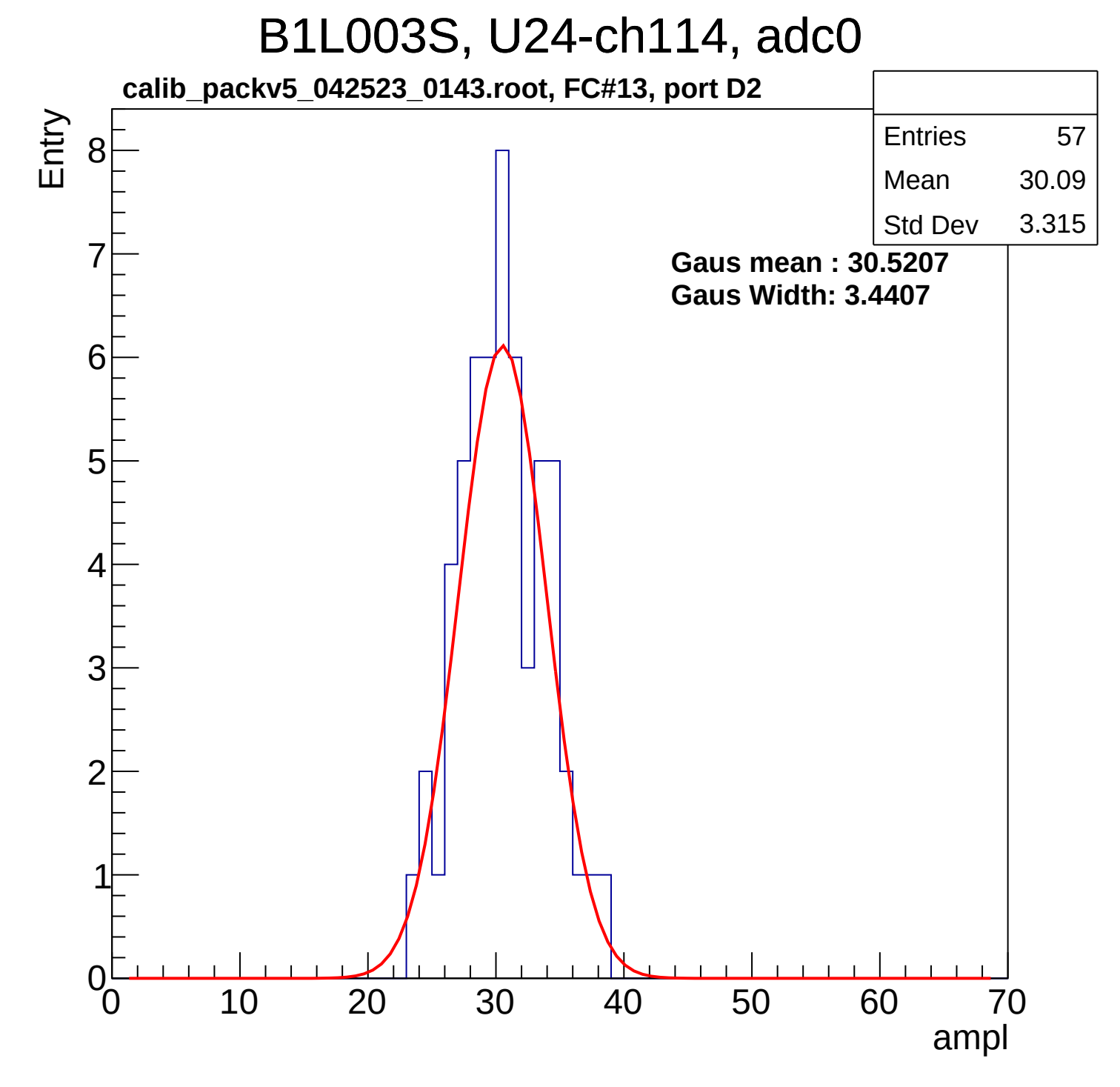
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch114, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 30.09
Std Dev 3.315
Gaus mean : 30.5207
Gaus Width: 3.4407
Entry
ampl
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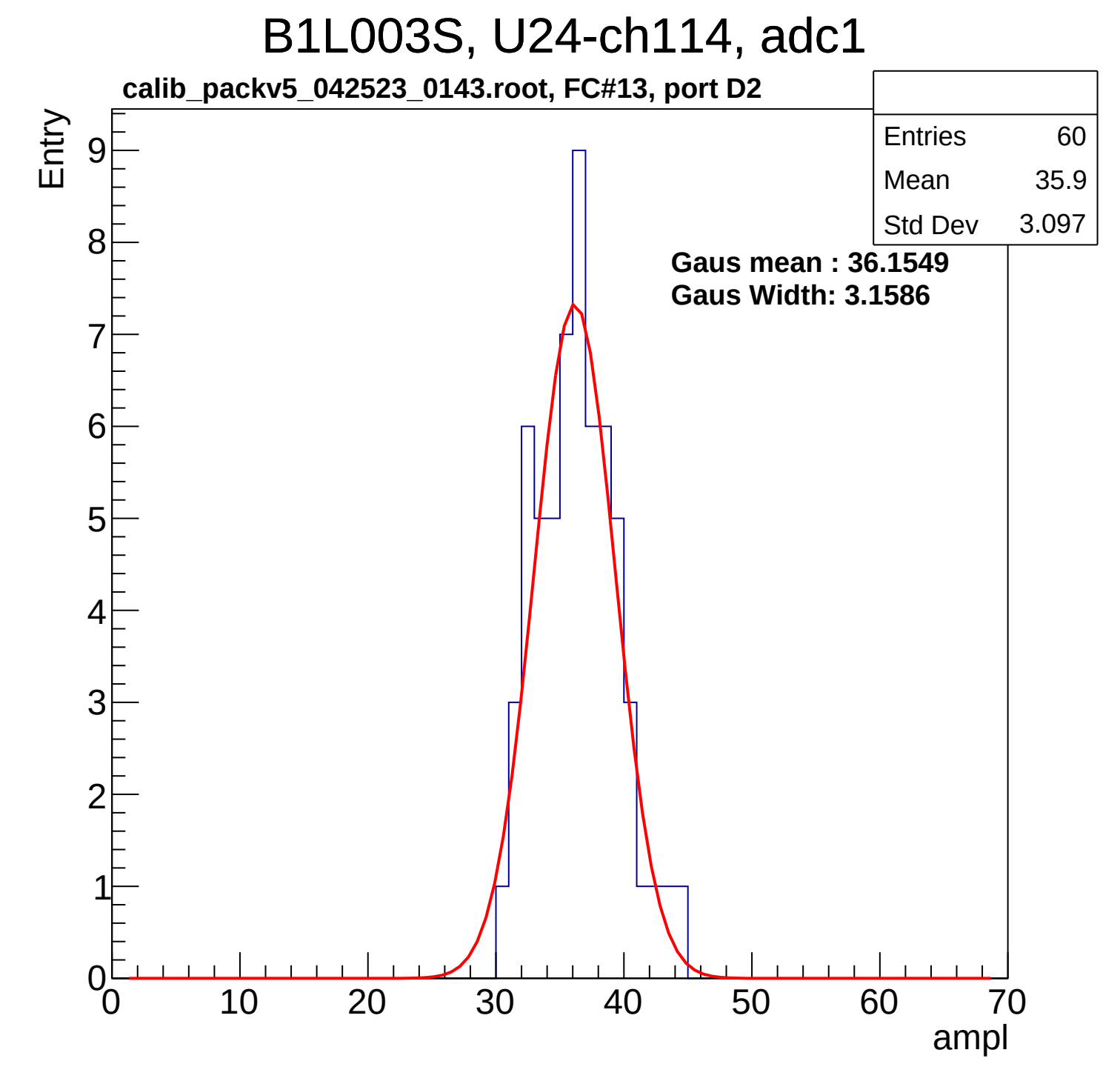
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch114, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 35.9
Std Dev 3.097
Gaus mean : 36.1549
Gaus Width: 3.1586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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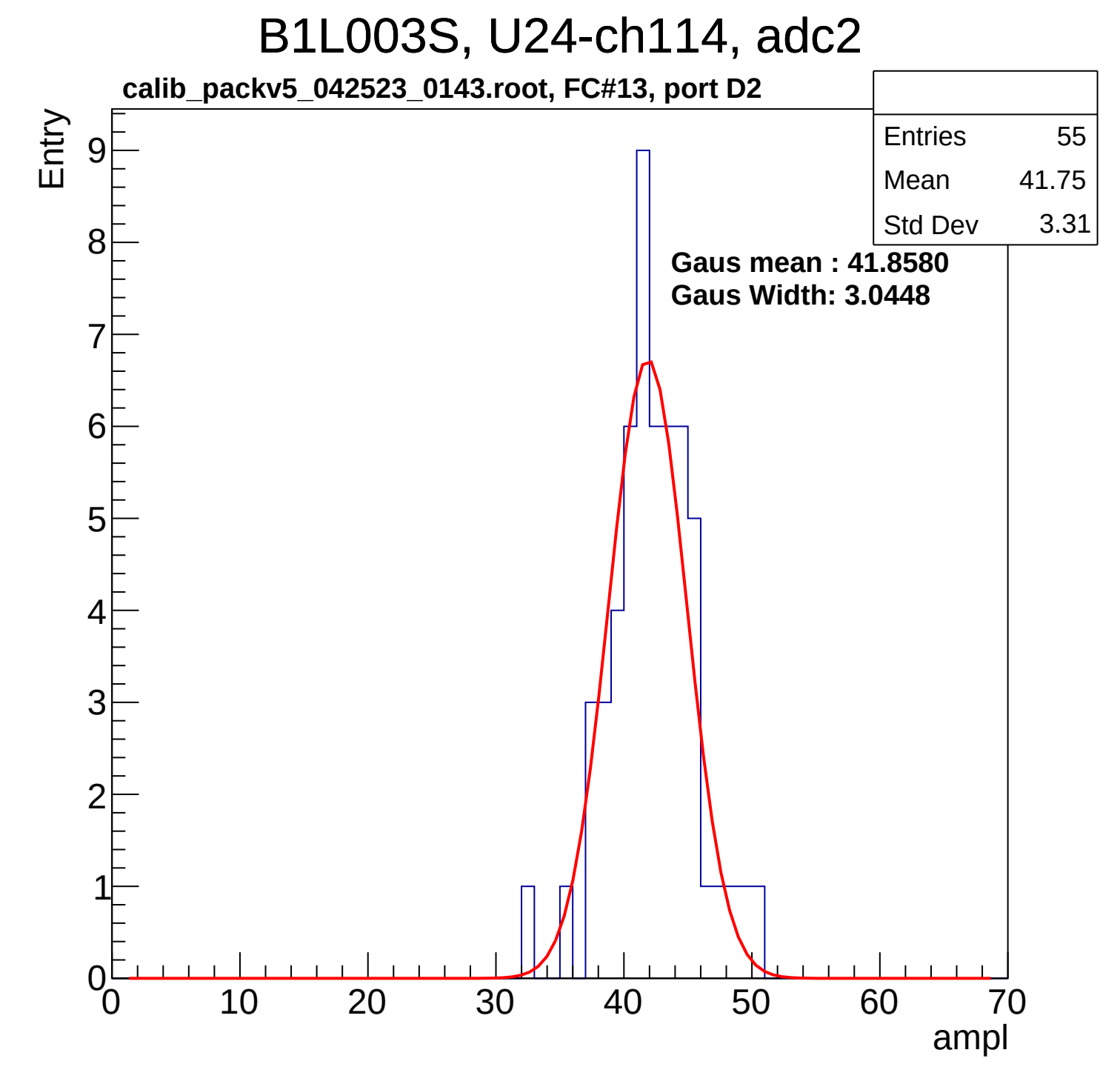
B1L003S, U24-ch114, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.75
Std Dev 3.31
Gaus mean : 41.8580
Gaus Width: 3.0448
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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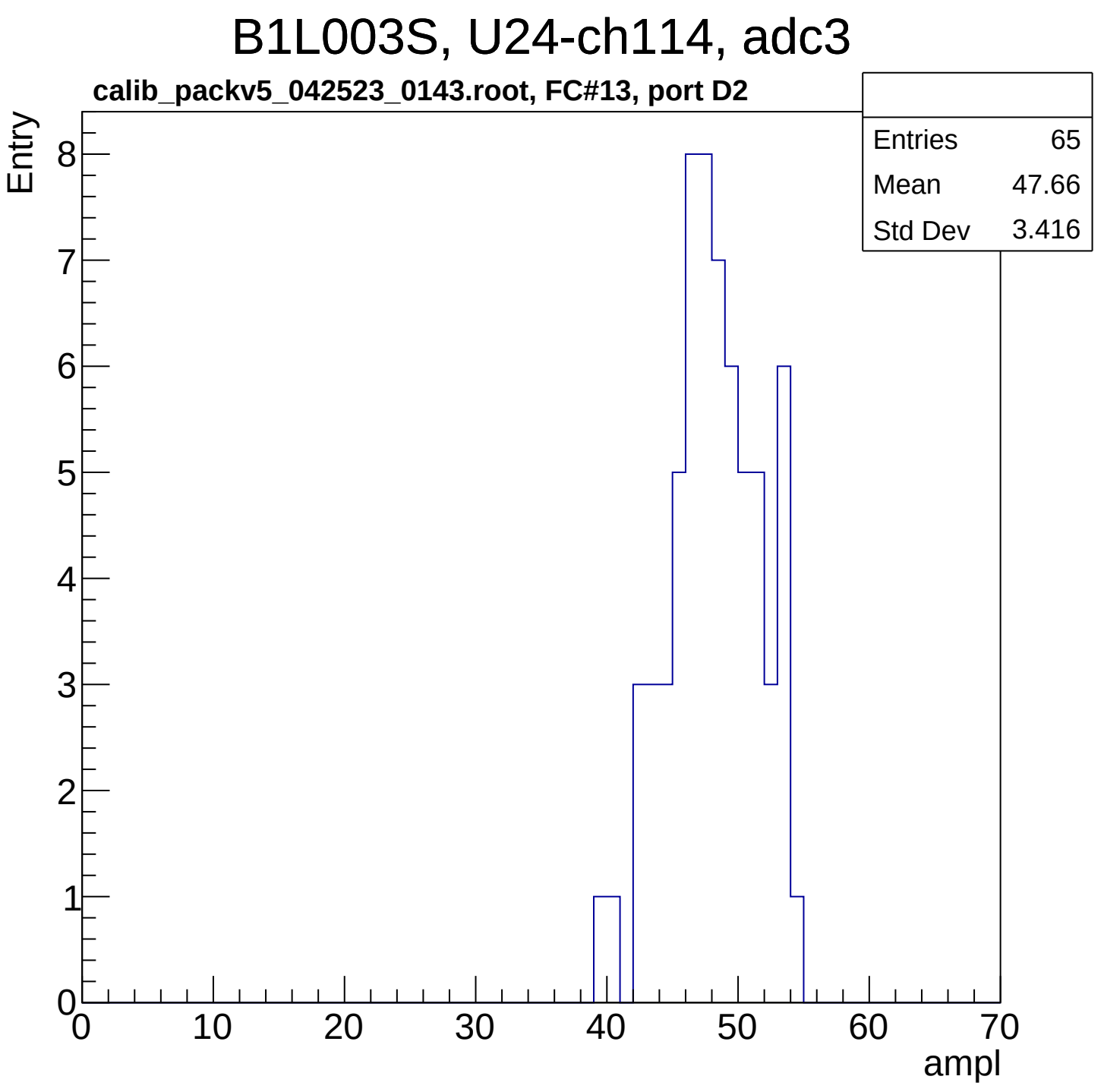
B1L003S, U24-ch114, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.66
Std Dev 3.416
Entry
ampl
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1
2
3
4
5
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7
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0
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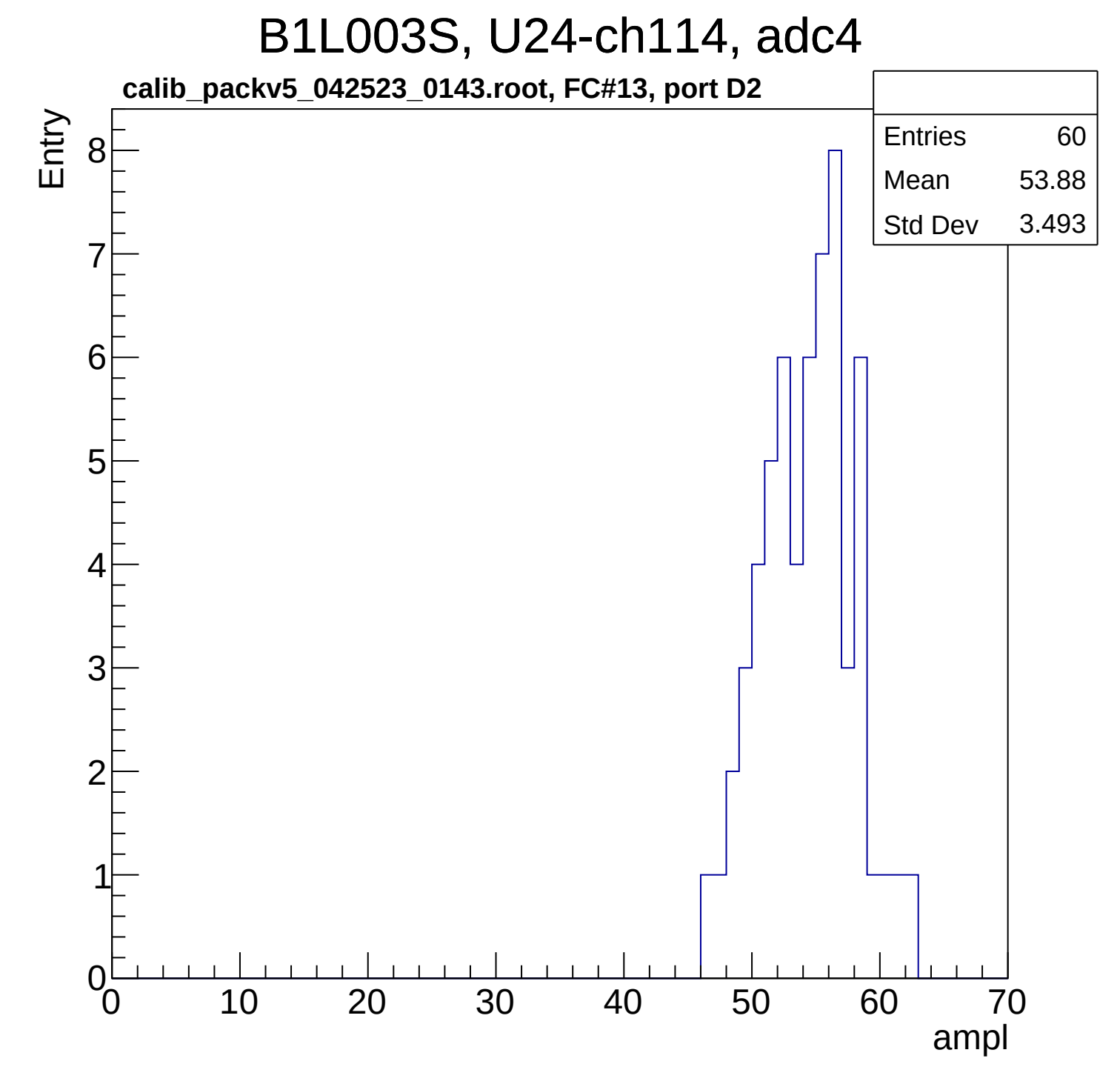
B1L003S, U24-ch114, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 53.88
Std Dev 3.493
Entry
ampl
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1
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4
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60
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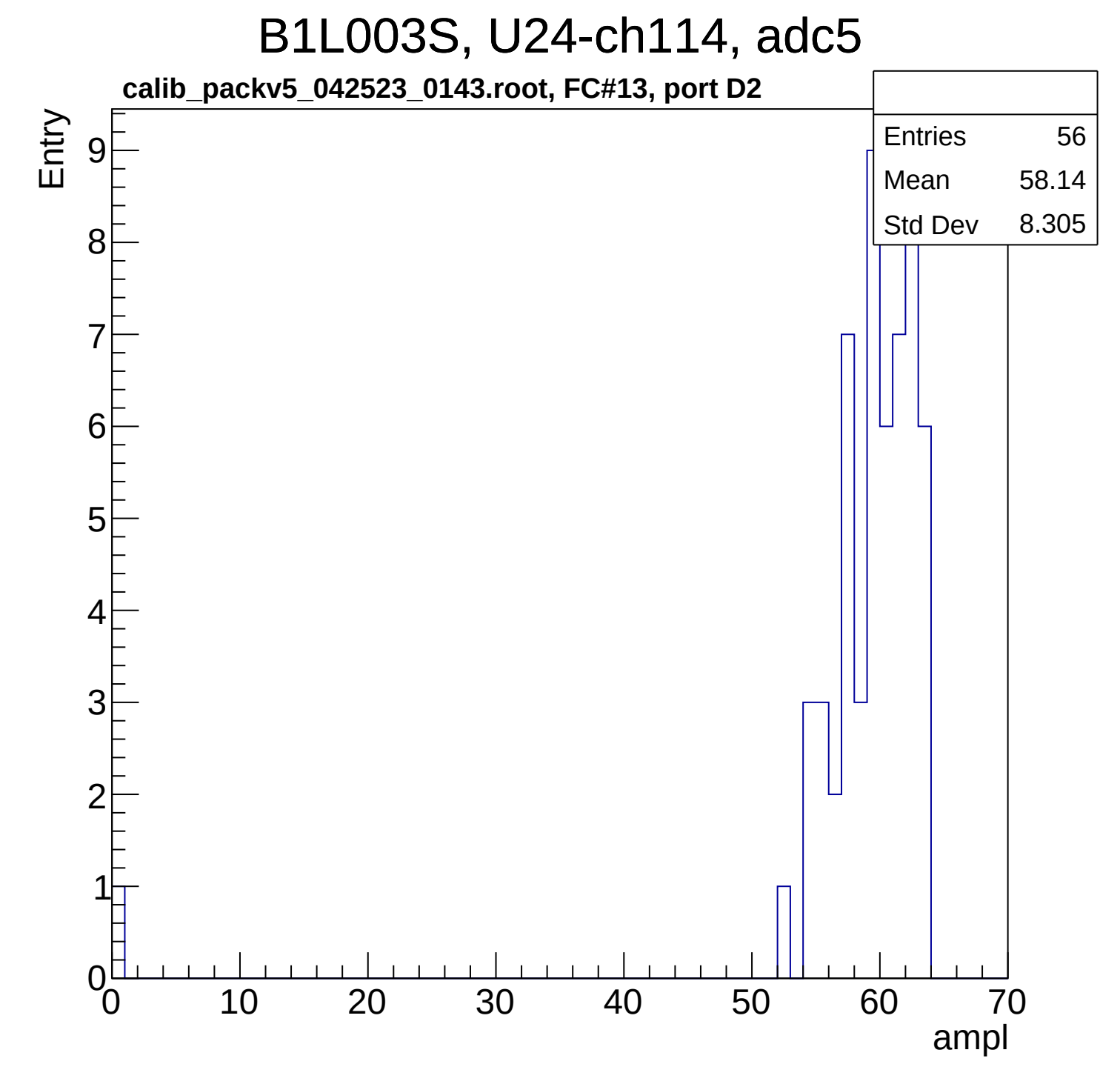
B1L003S, U24-ch114, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.14
Std Dev 8.305
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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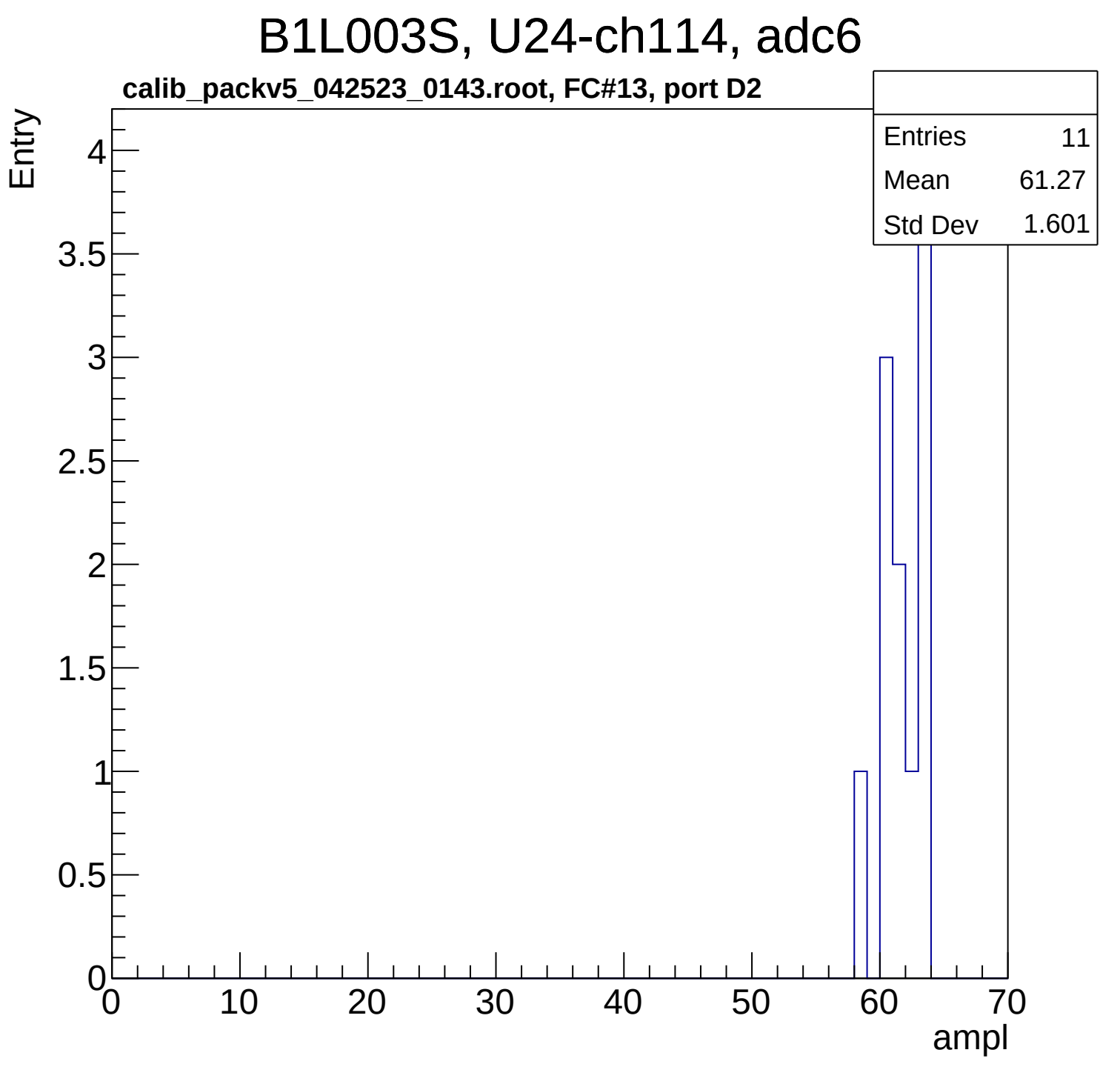
B1L003S, U24-ch114, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.27
Std Dev 1.601
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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60
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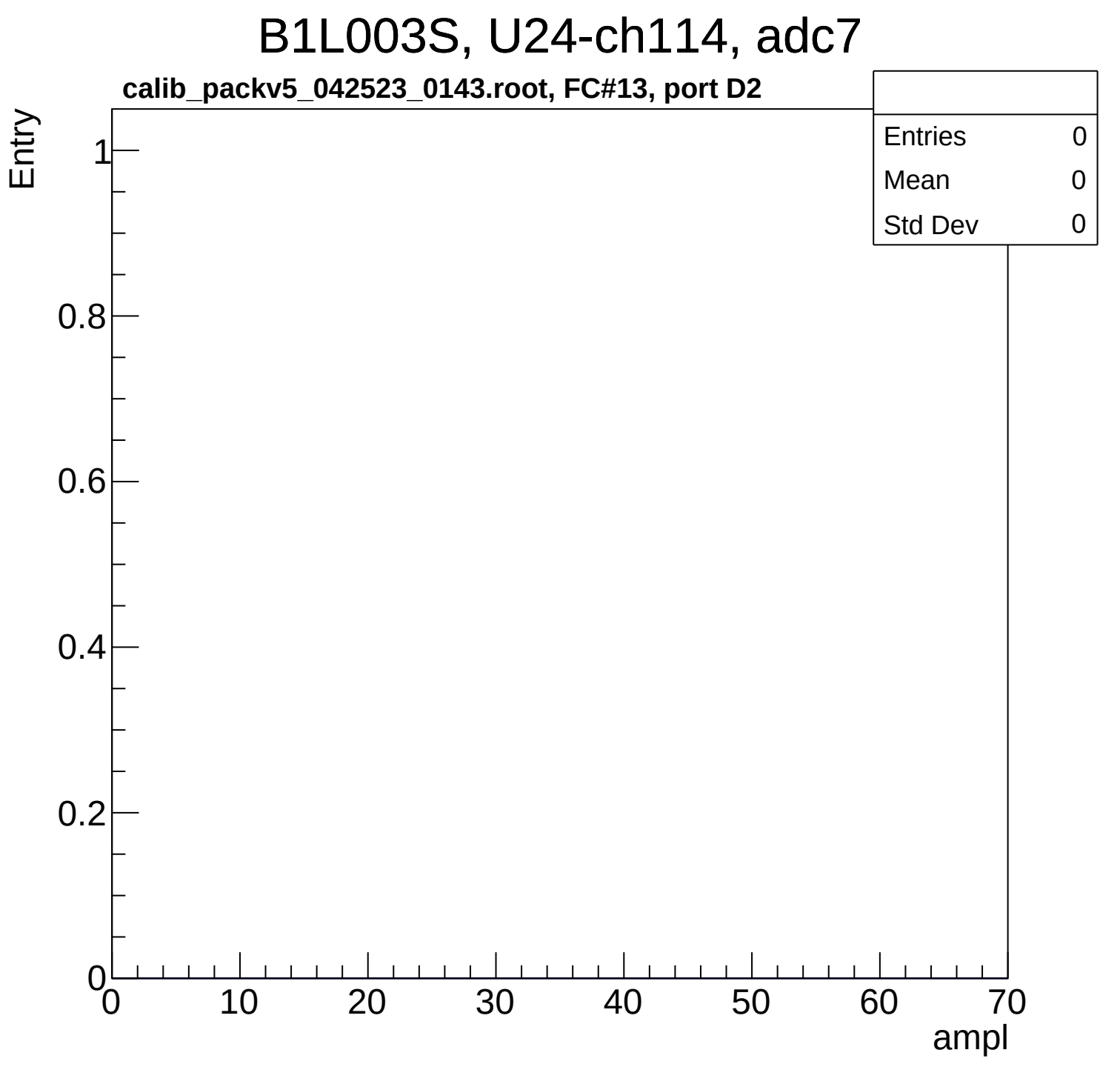
B1L003S, U24-ch114, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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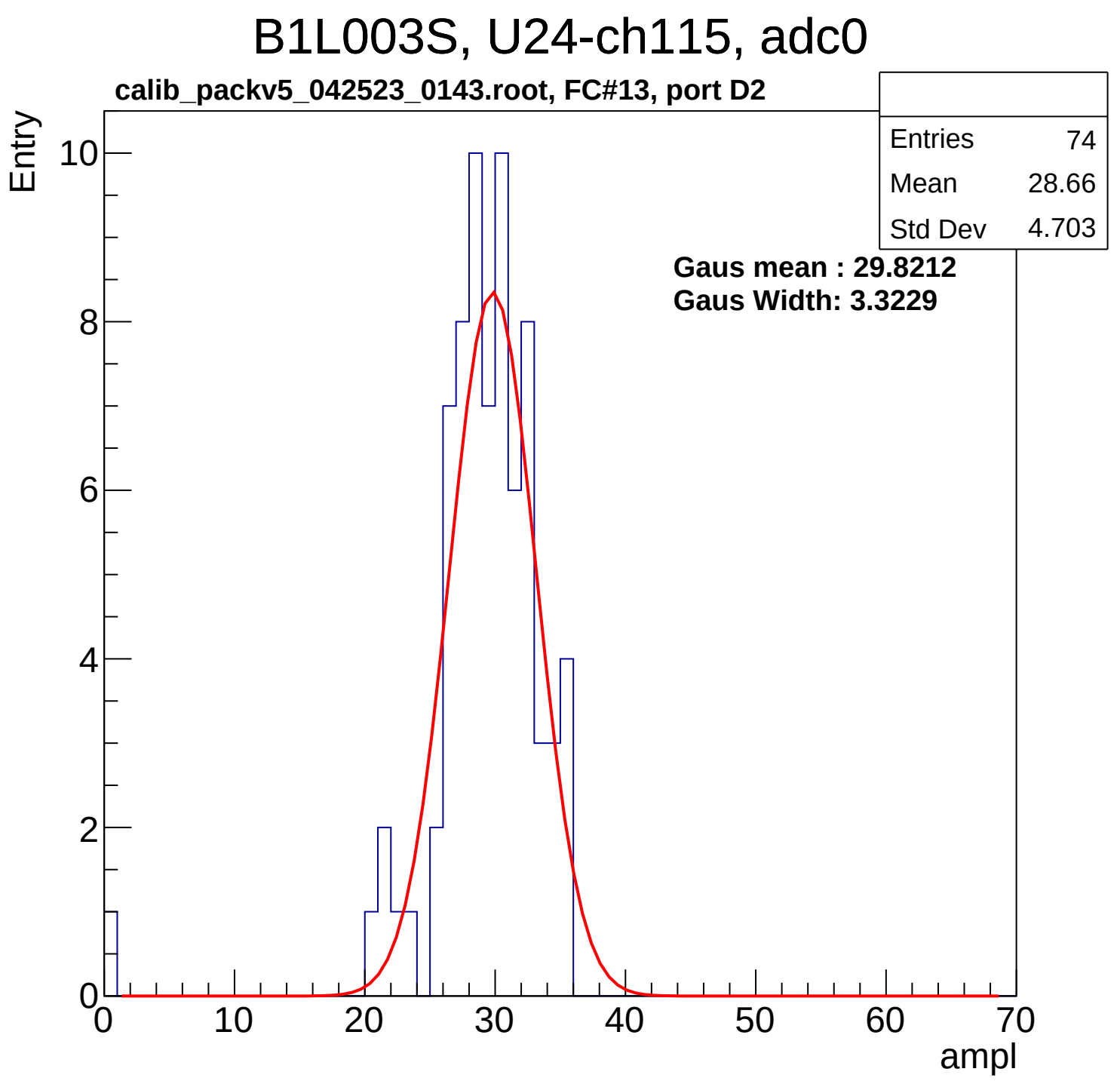
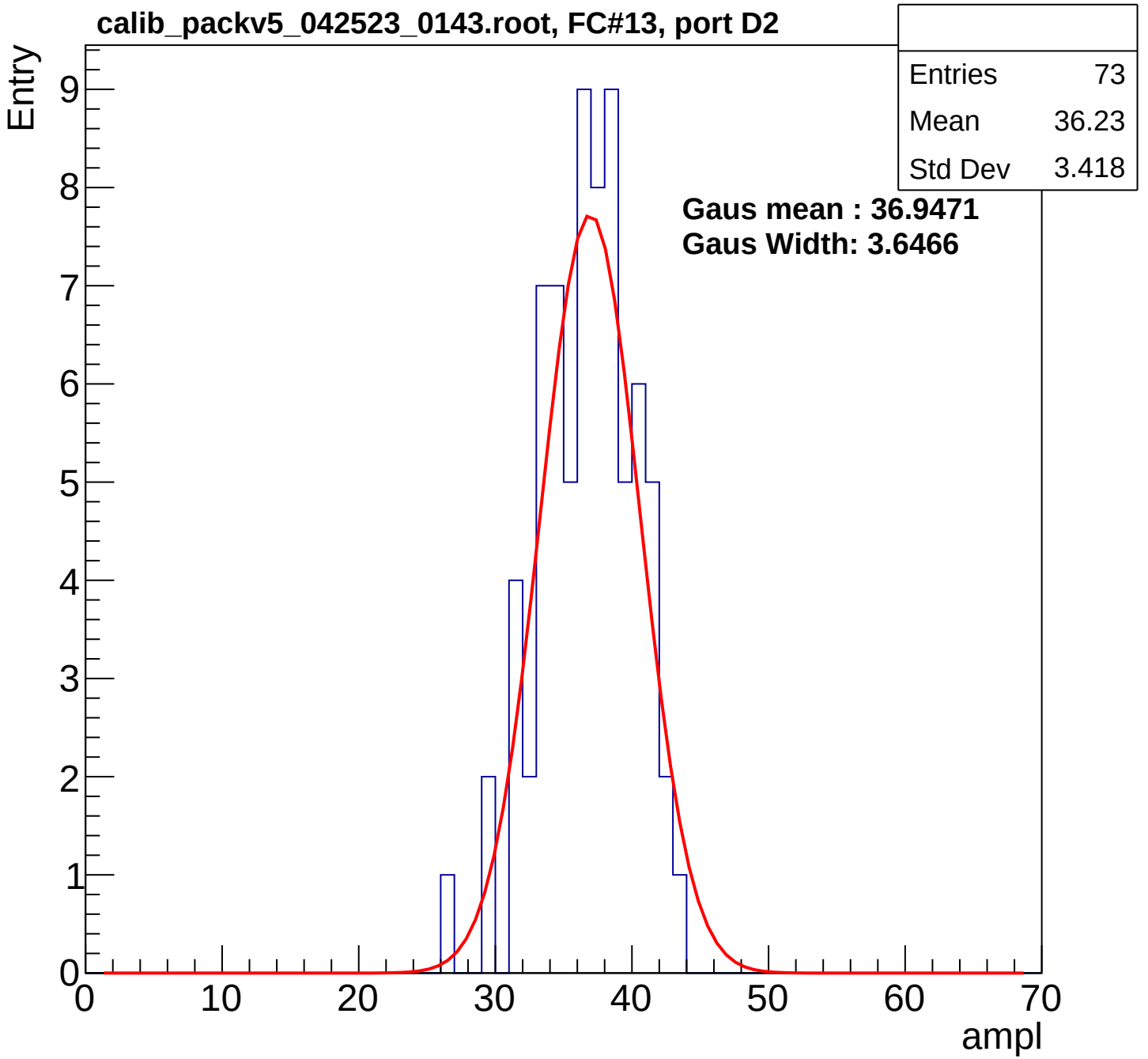
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch115, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 28.66
Std Dev 4.703
Gaus mean : 29.8212
Gaus Width: 3.3229
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch115, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.23
Std Dev 3.418
Gaus mean : 36.9471
Gaus Width: 3.6466
Entry
ampl
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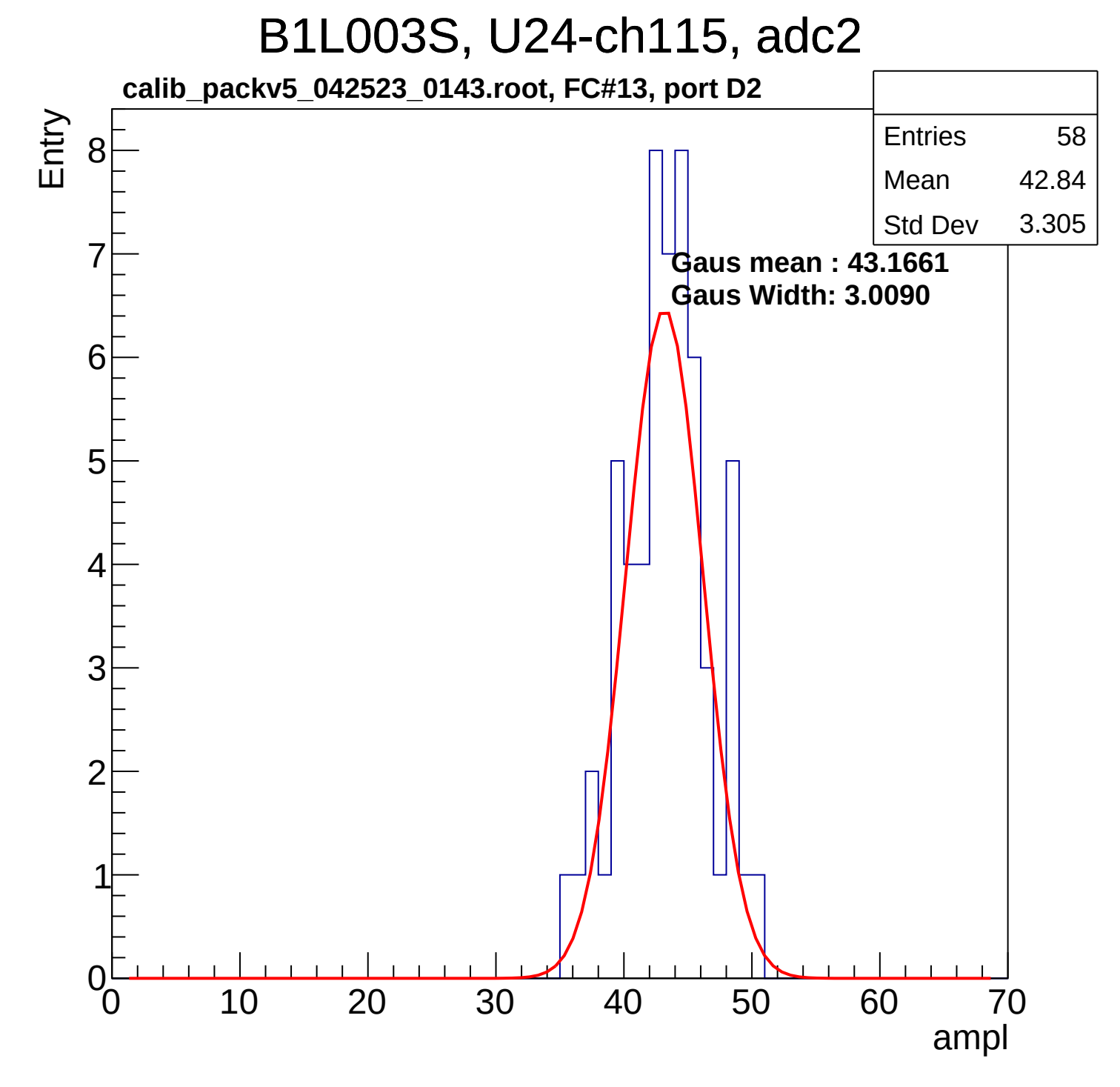
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U24-ch115, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.84
Std Dev 3.305
Gaus mean : 43.1661
Gaus Width: 3.0090
Entry
ampl
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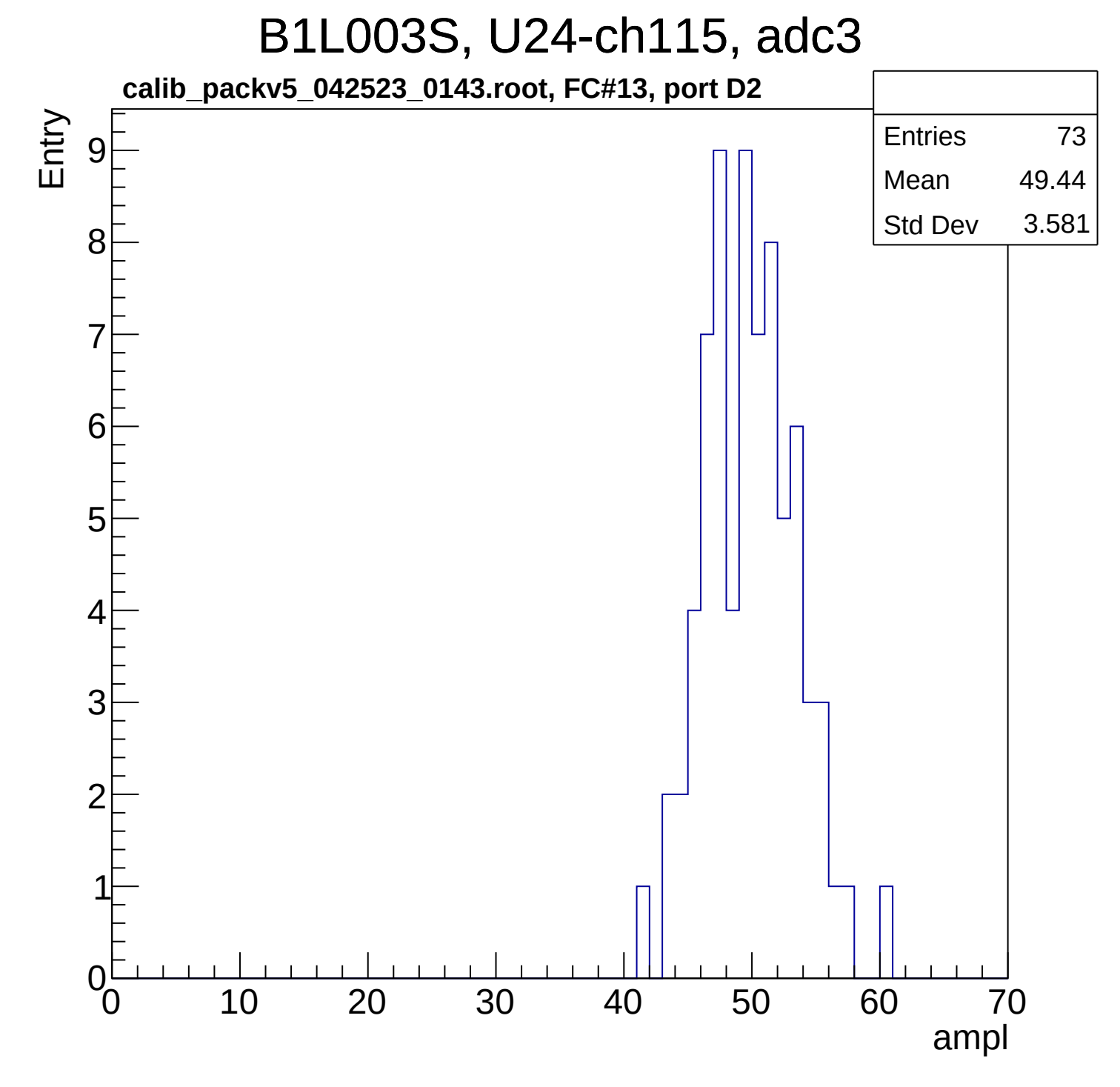
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch115, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.44
Std Dev 3.581
Entry
ampl
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3
4
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6
7
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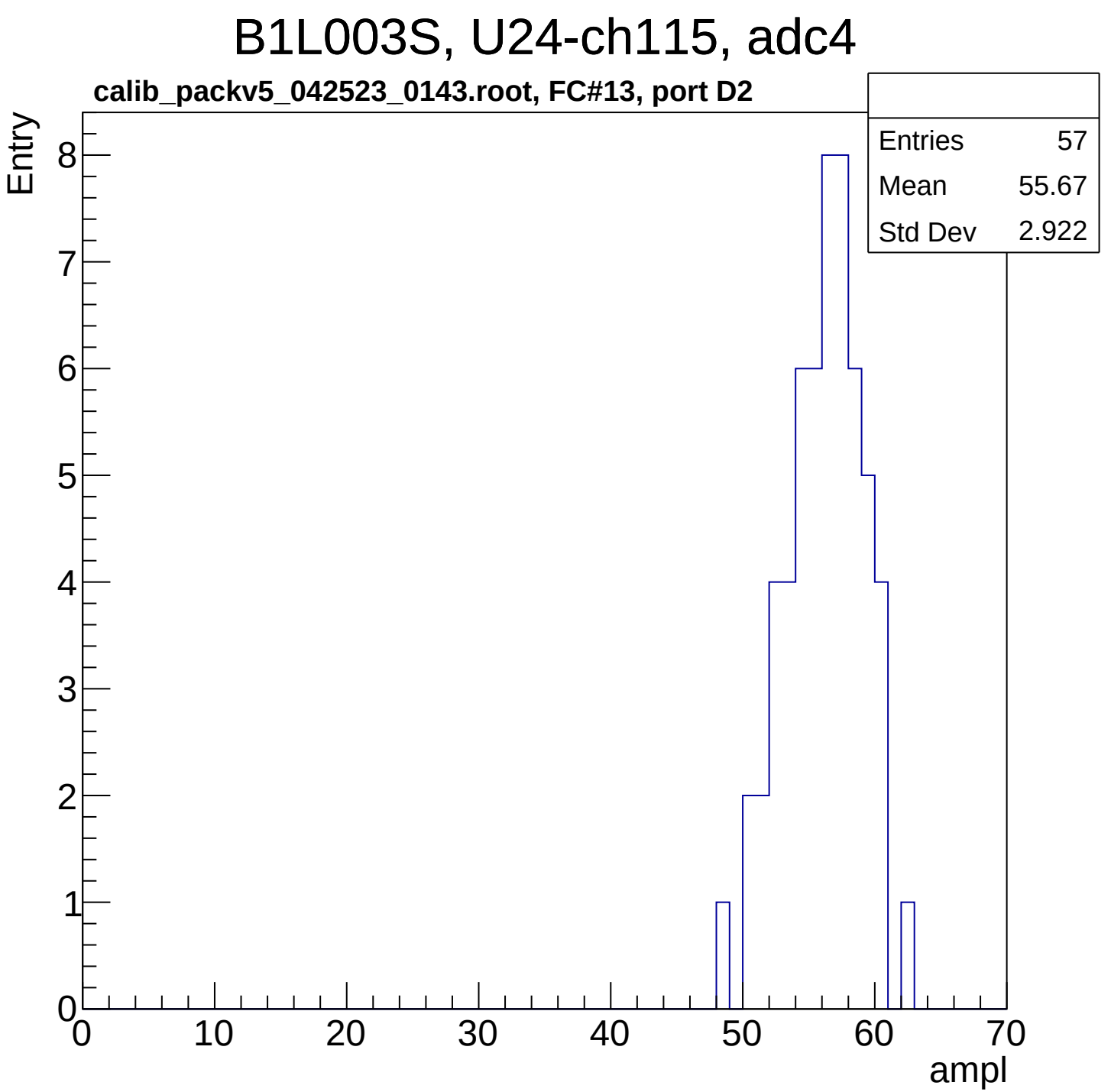
B1L003S, U24-ch115, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.67
Std Dev 2.922
Entry
ampl
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1
2
3
4
5
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7
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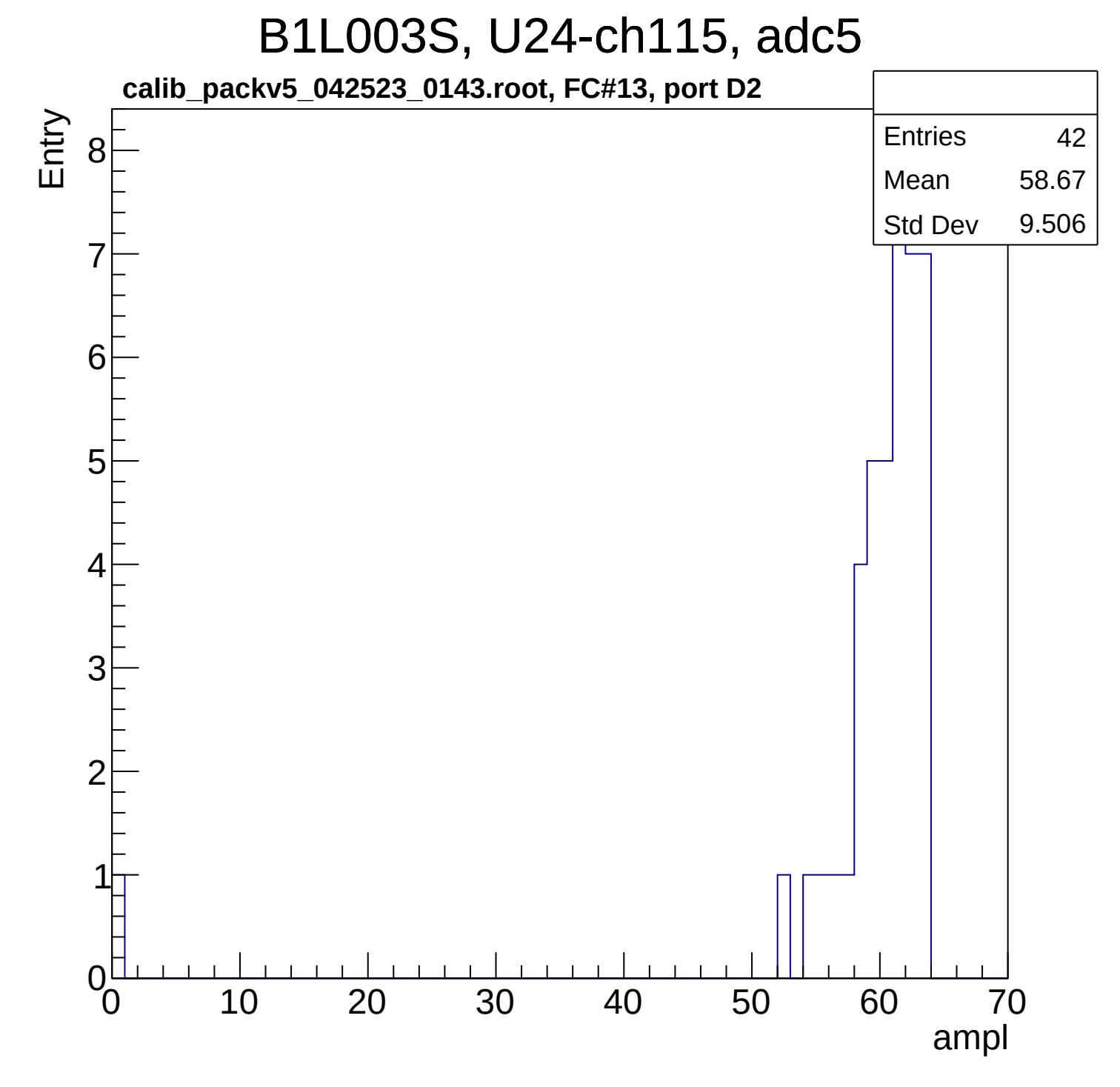
B1L003S, U24-ch115, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 58.67
Std Dev 9.506
Entry
ampl
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0
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60
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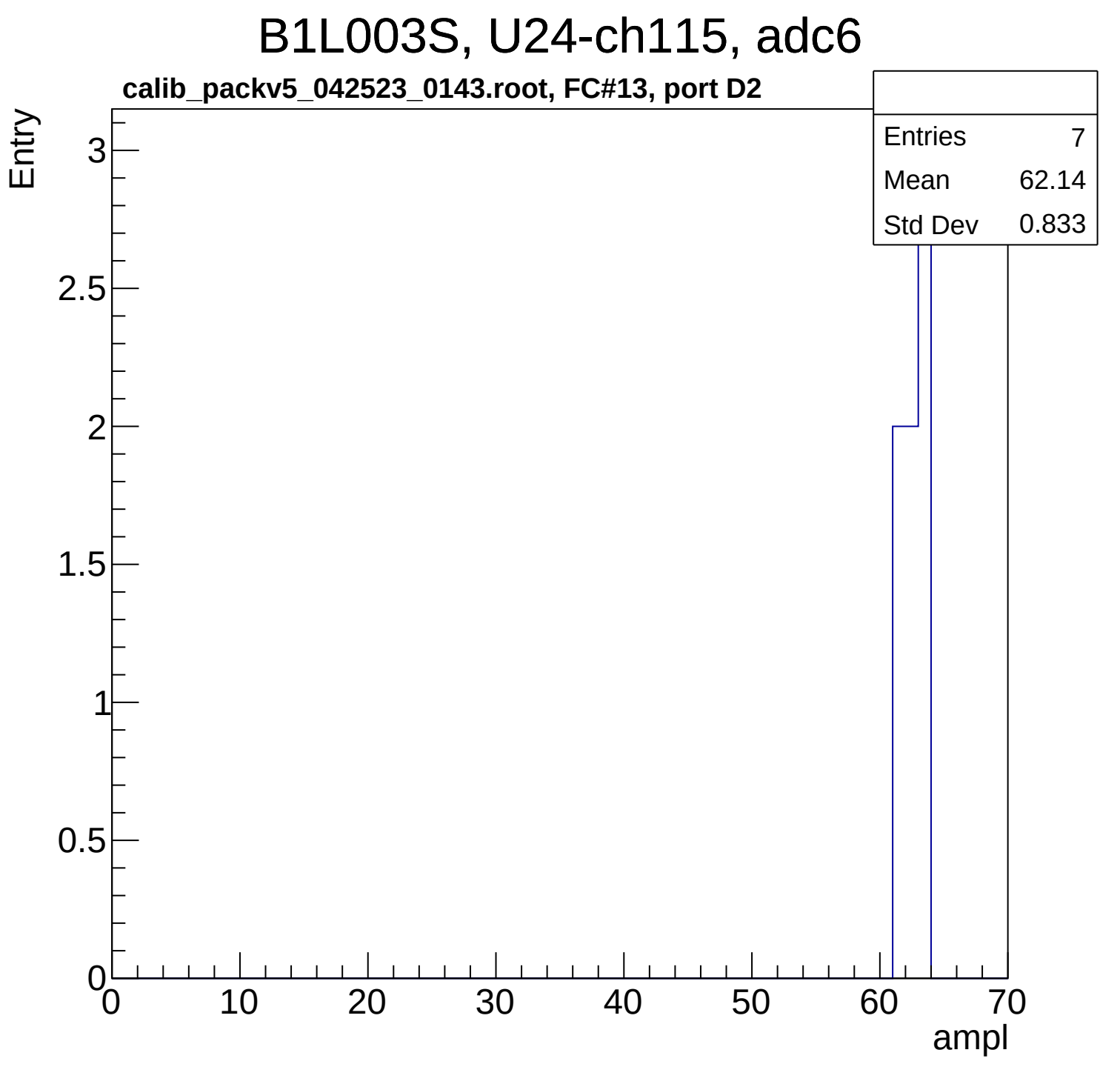
B1L003S, U24-ch115, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.14
Std Dev 0.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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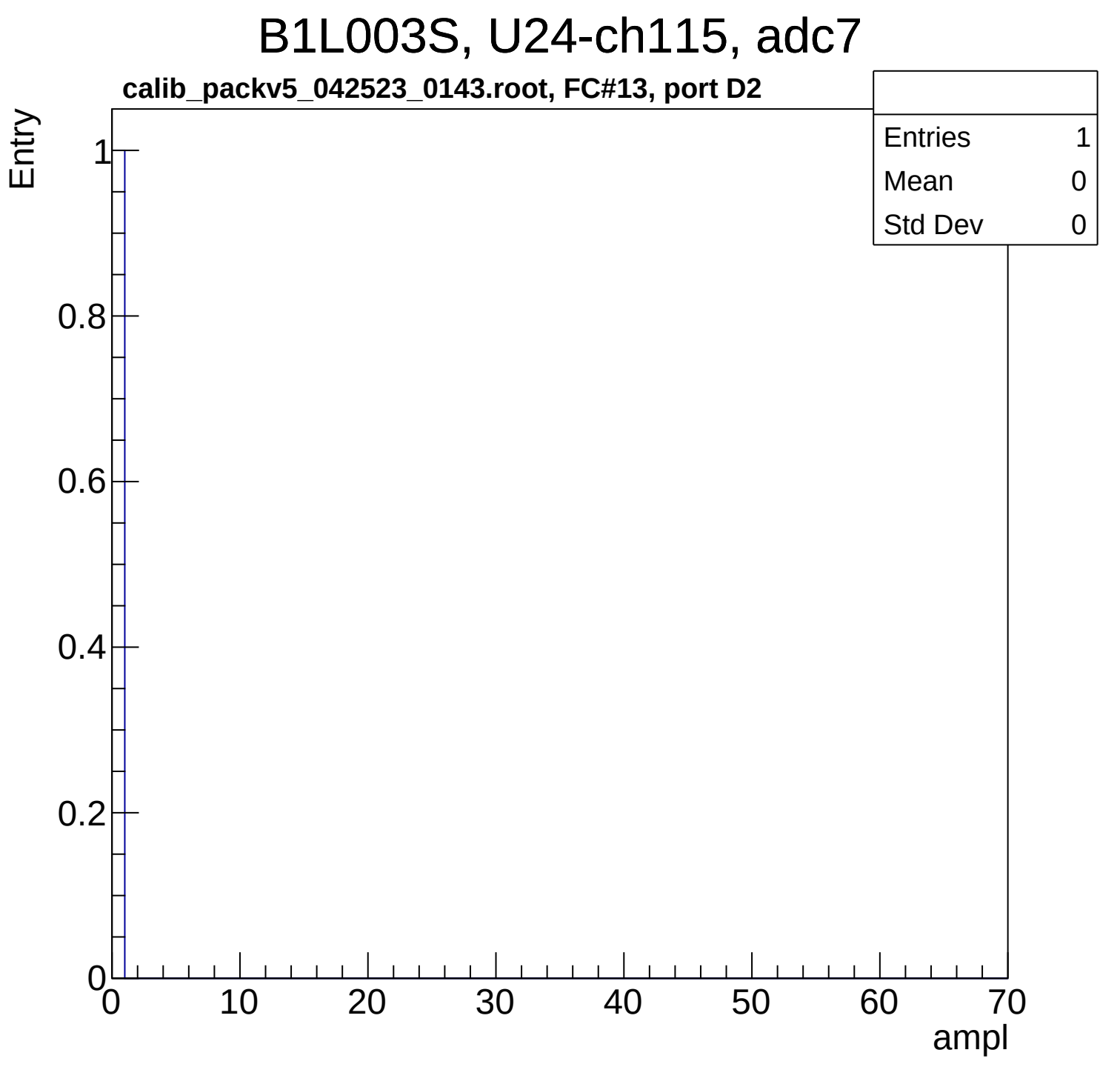
B1L003S, U24-ch115, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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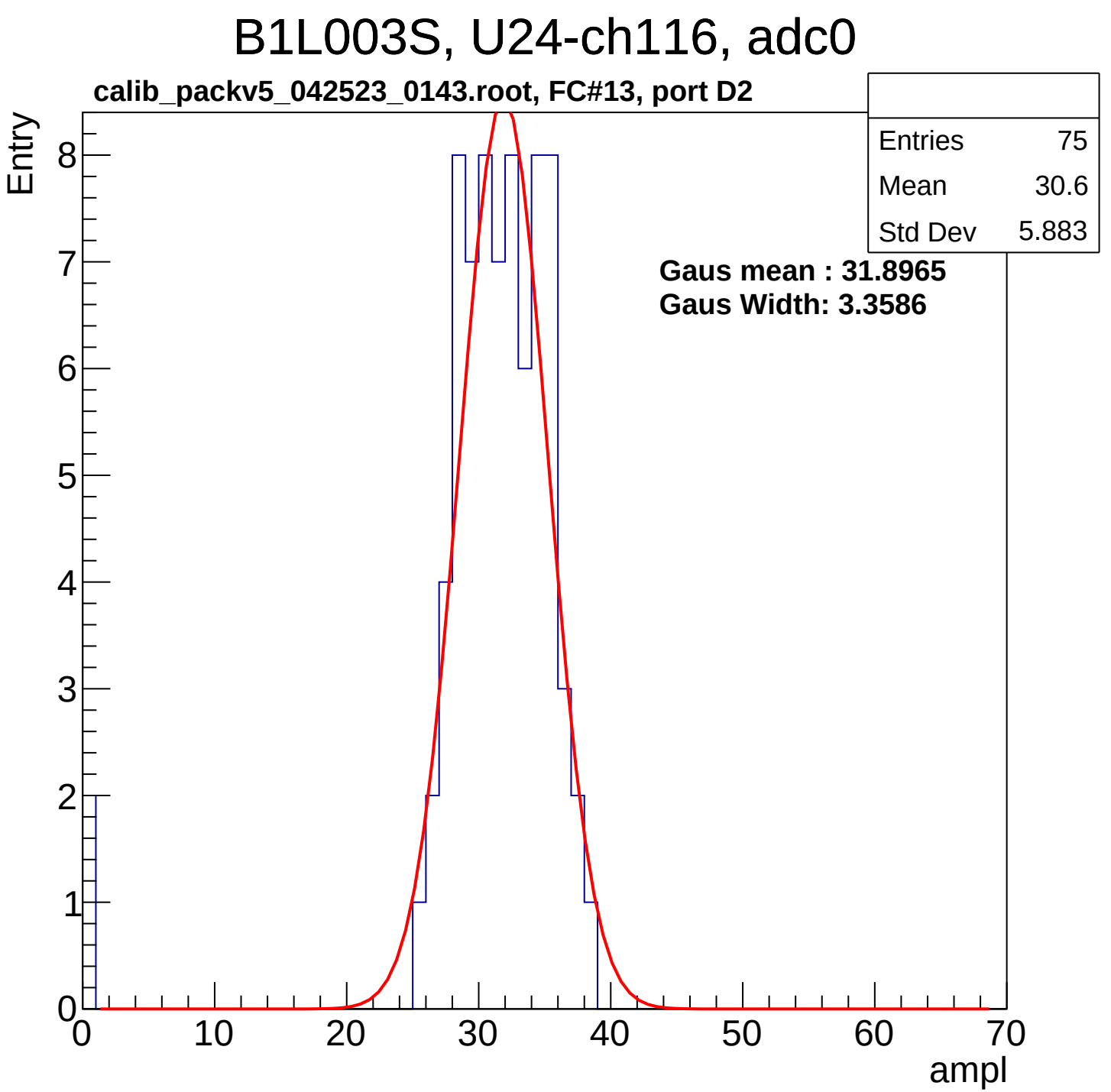
50
60
70
ampl


B1L003S, U24-ch116, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 30.6
Std Dev 5.883
Gaus mean : 31.8965
Gaus Width: 3.3586
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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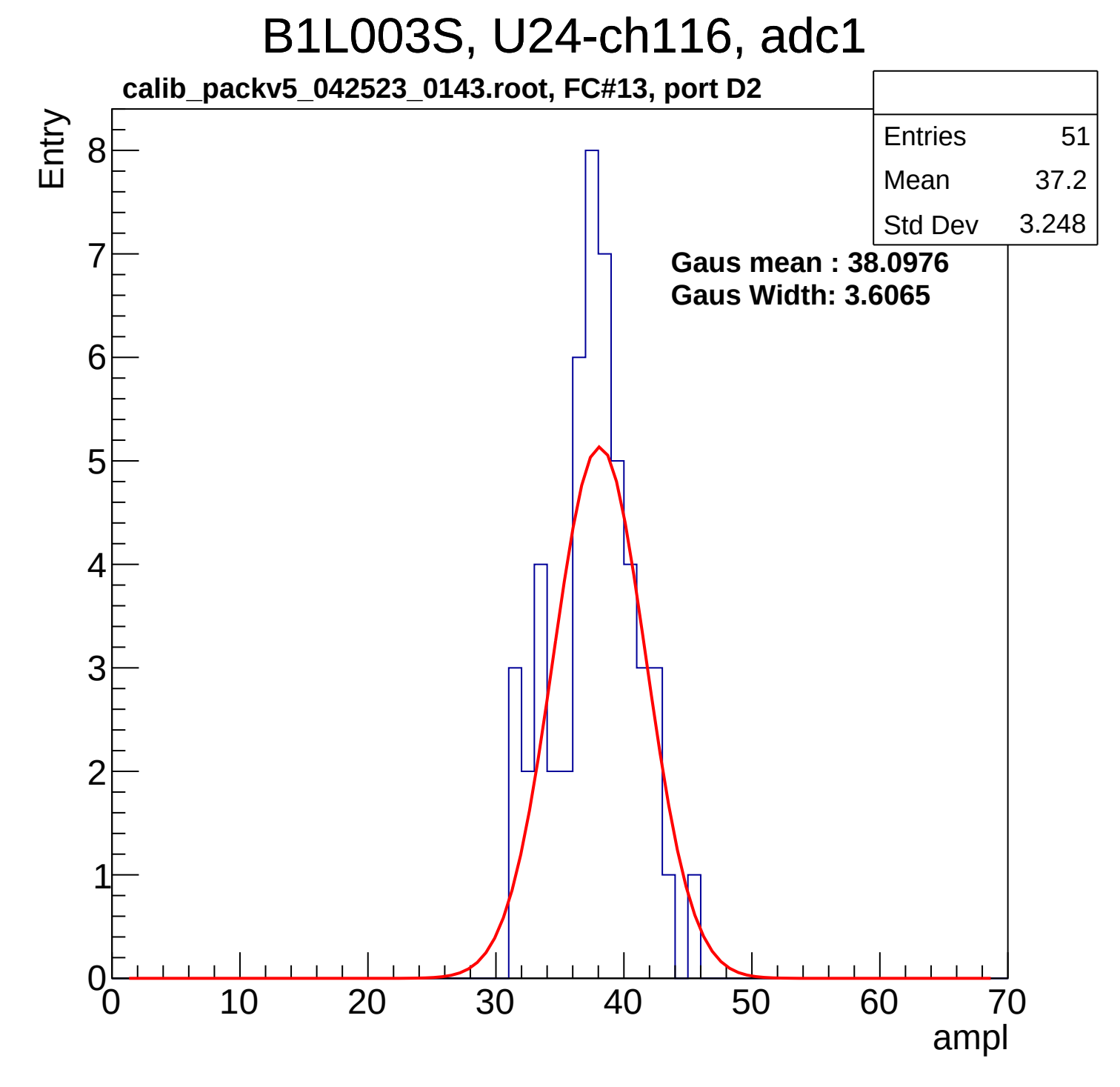
B1L003S, U24-ch116, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 37.2
Std Dev 3.248
Gaus mean : 38.0976
Gaus Width: 3.6065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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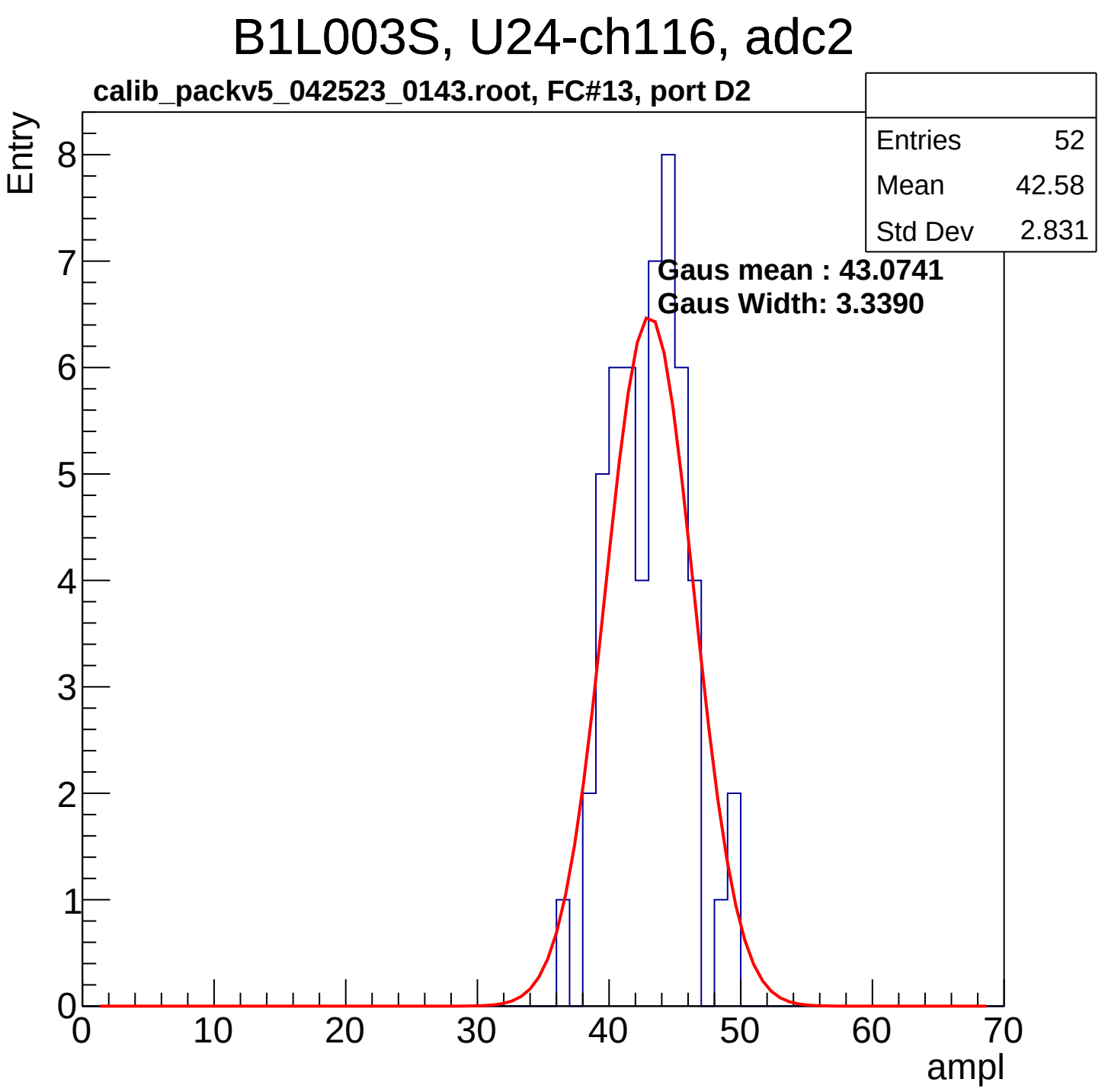
B1L003S, U24-ch116, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 42.58
Std Dev 2.831
Gaus mean : 43.0741
Gaus Width: 3.3390
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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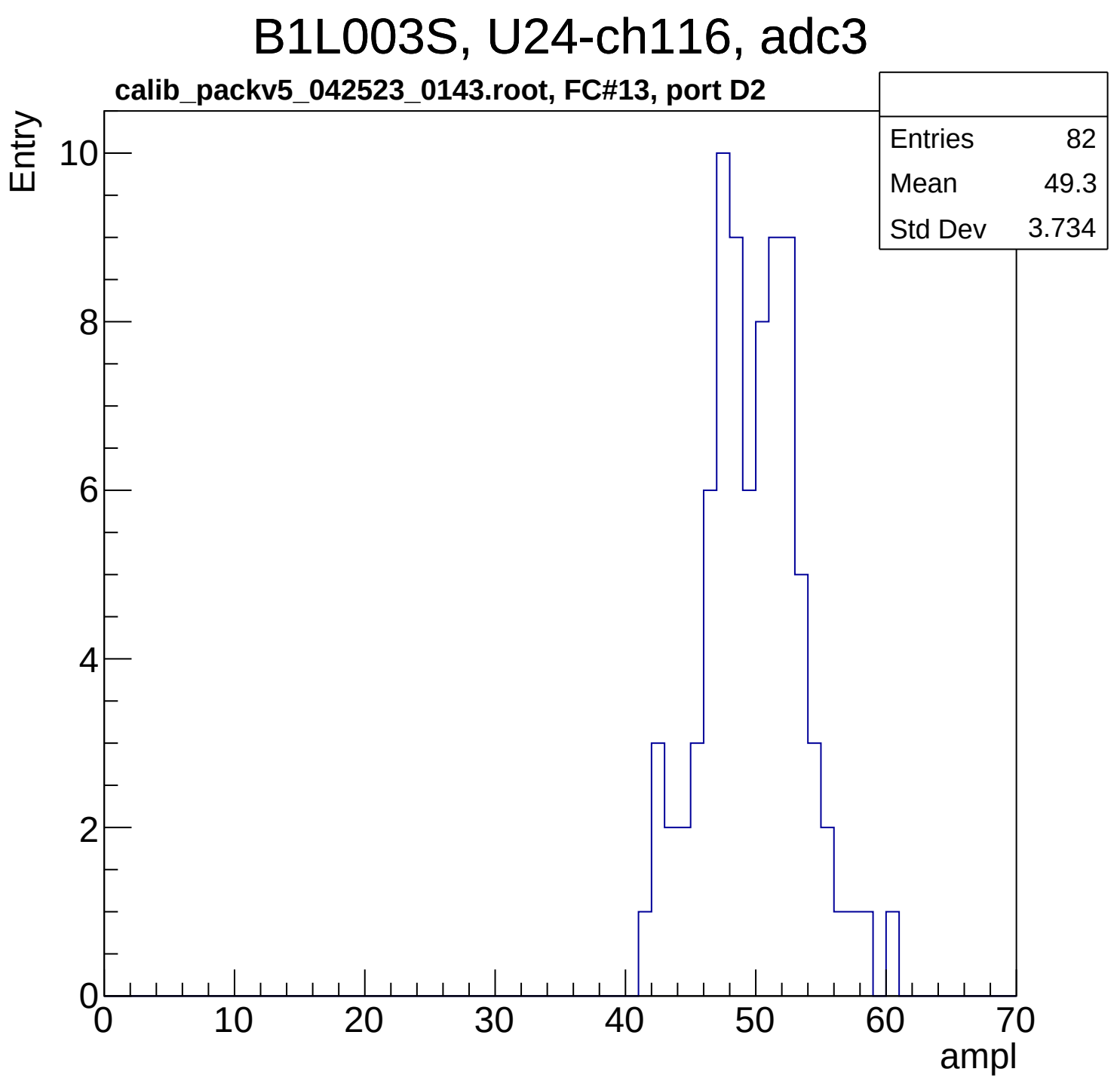
B1L003S, U24-ch116, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 49.3
Std Dev 3.734
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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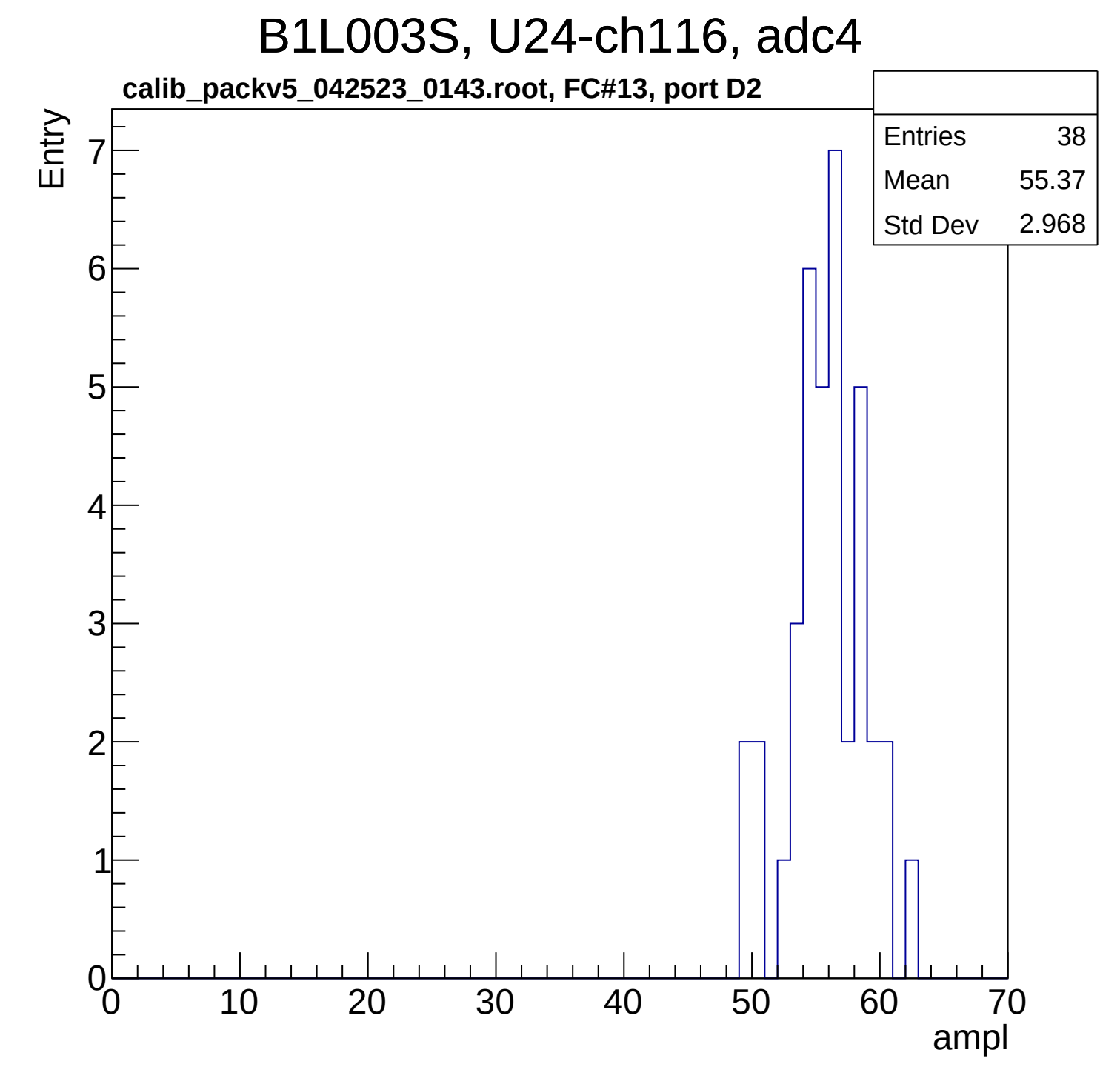
B1L003S, U24-ch116, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 55.37
Std Dev 2.968
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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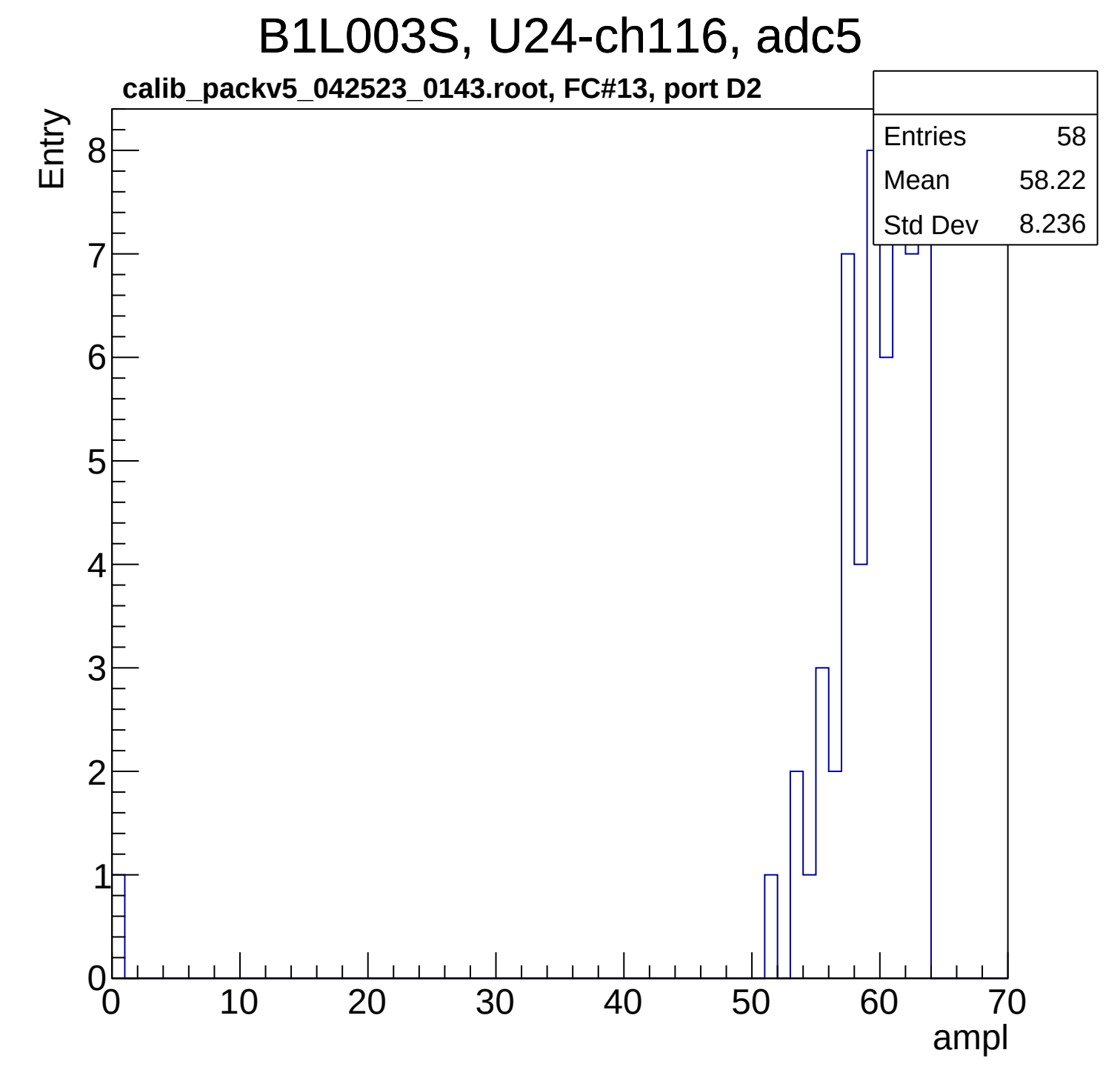
B1L003S, U24-ch116, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 58.22
Std Dev 8.236
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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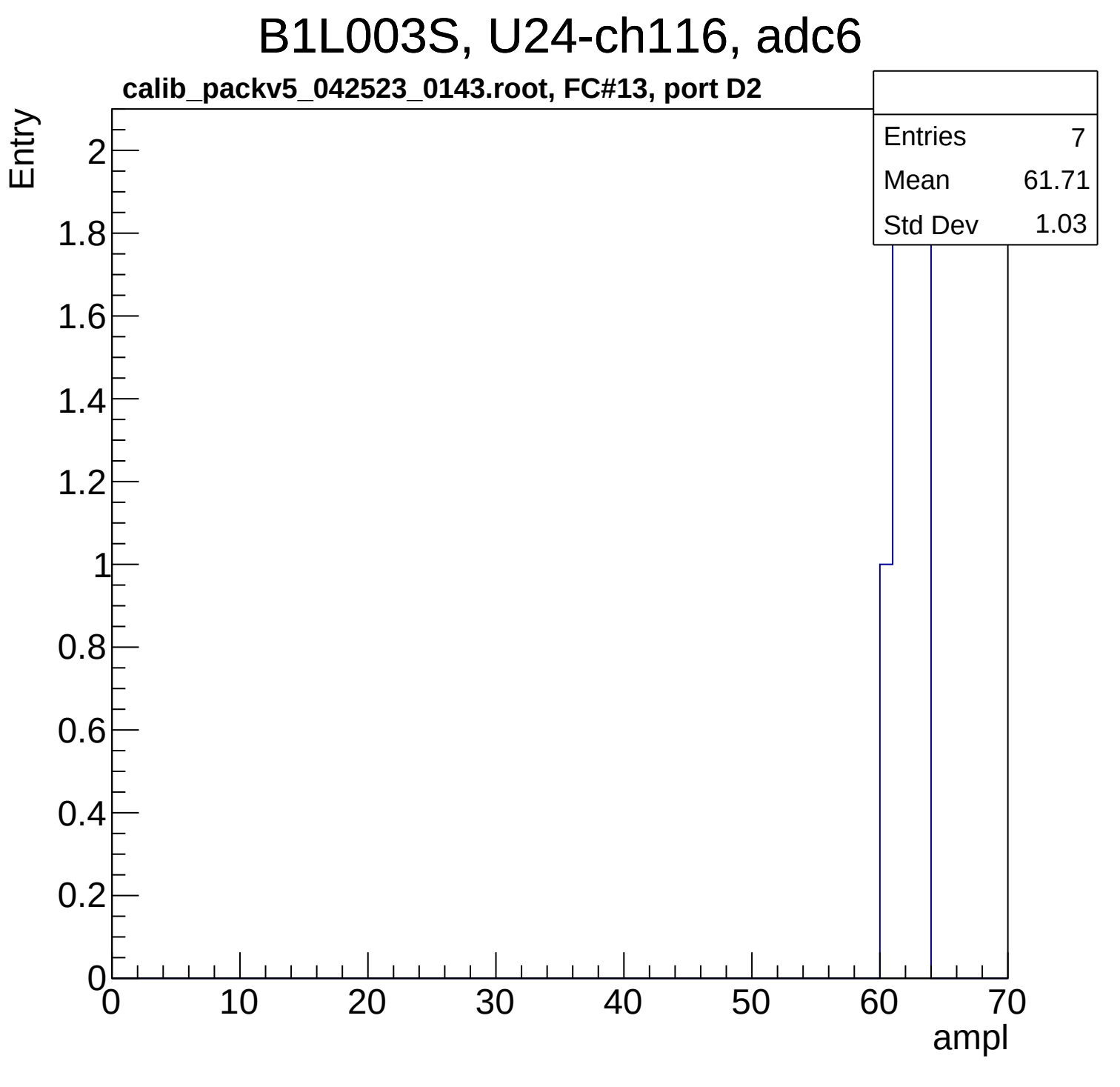
B1L003S, U24-ch116, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.71
Std Dev 1.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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60
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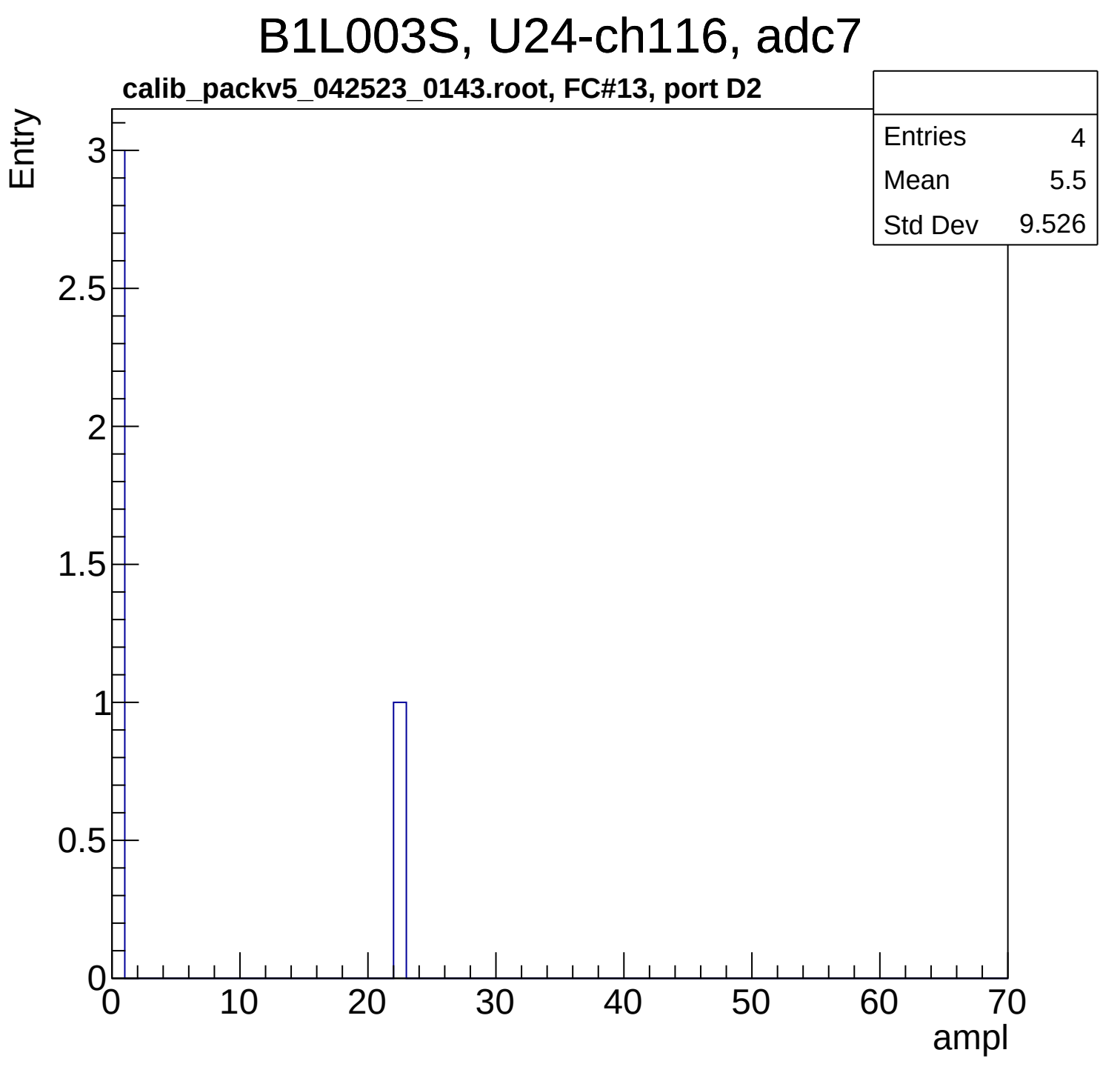
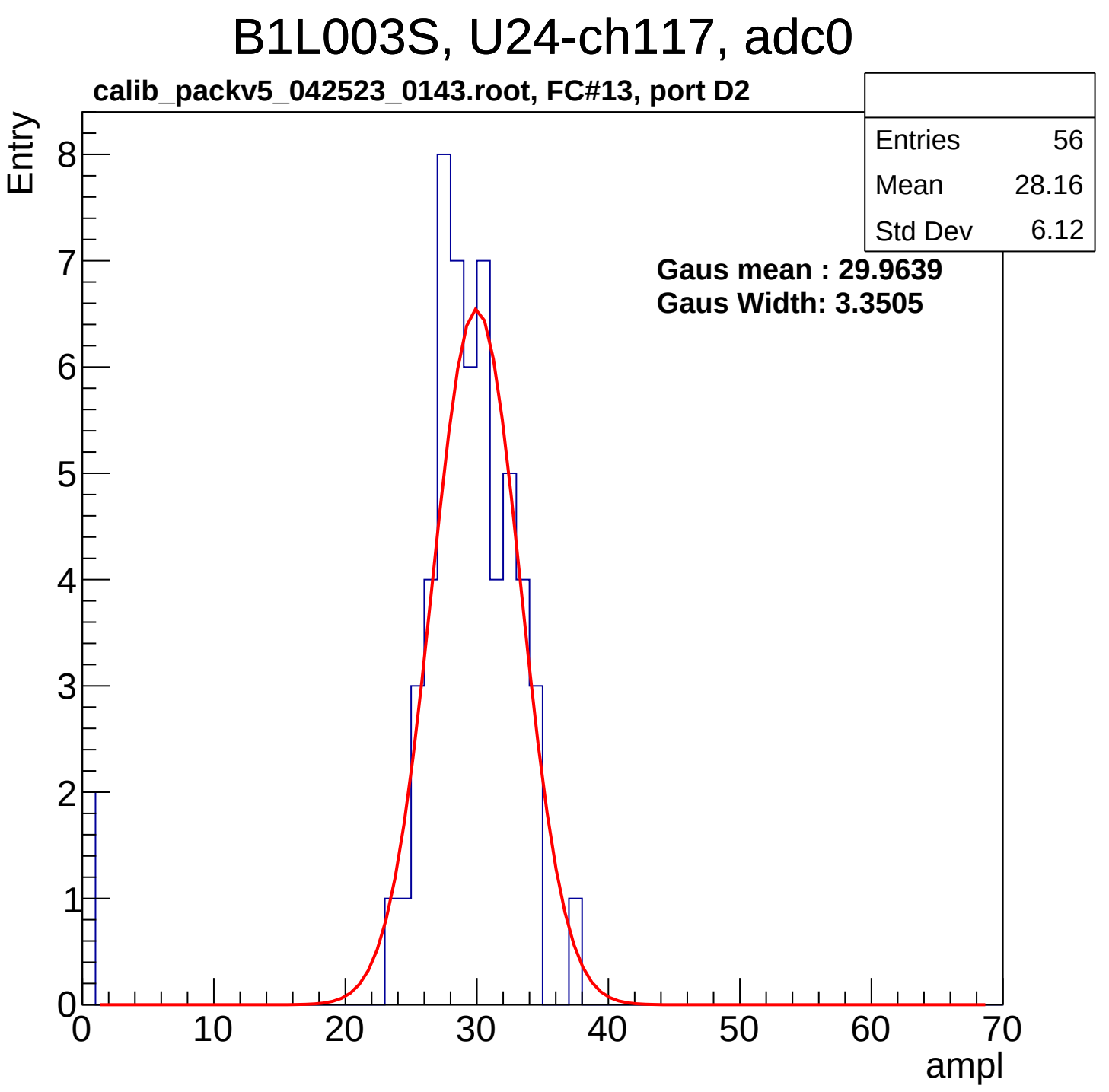

B1L003S, U24-ch116, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch117, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 28.16
Std Dev 6.12
Gaus mean : 29.9639
Gaus Width: 3.3505
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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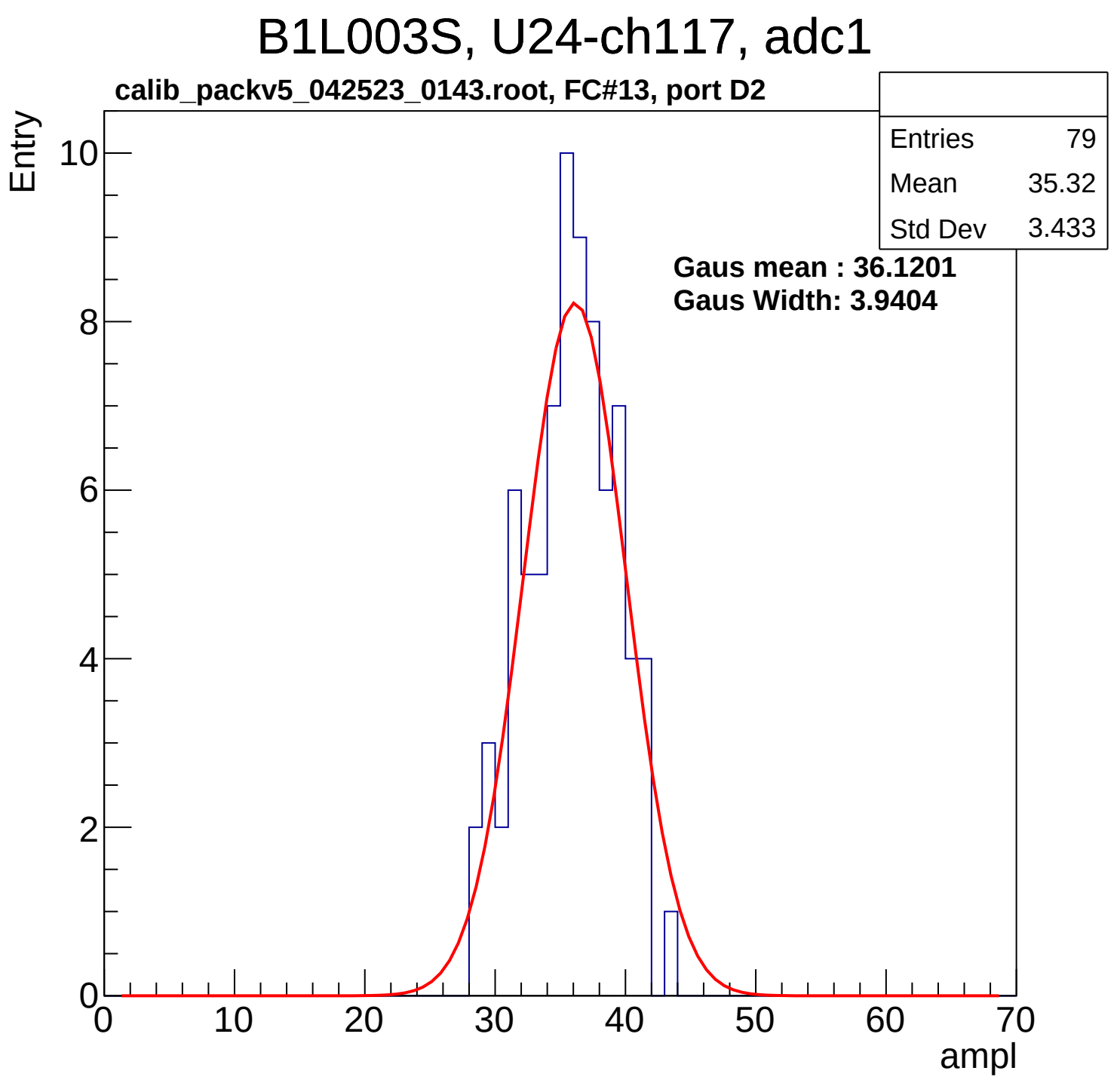
B1L003S, U24-ch117, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 35.32
Std Dev 3.433
Gaus mean : 36.1201
Gaus Width: 3.9404
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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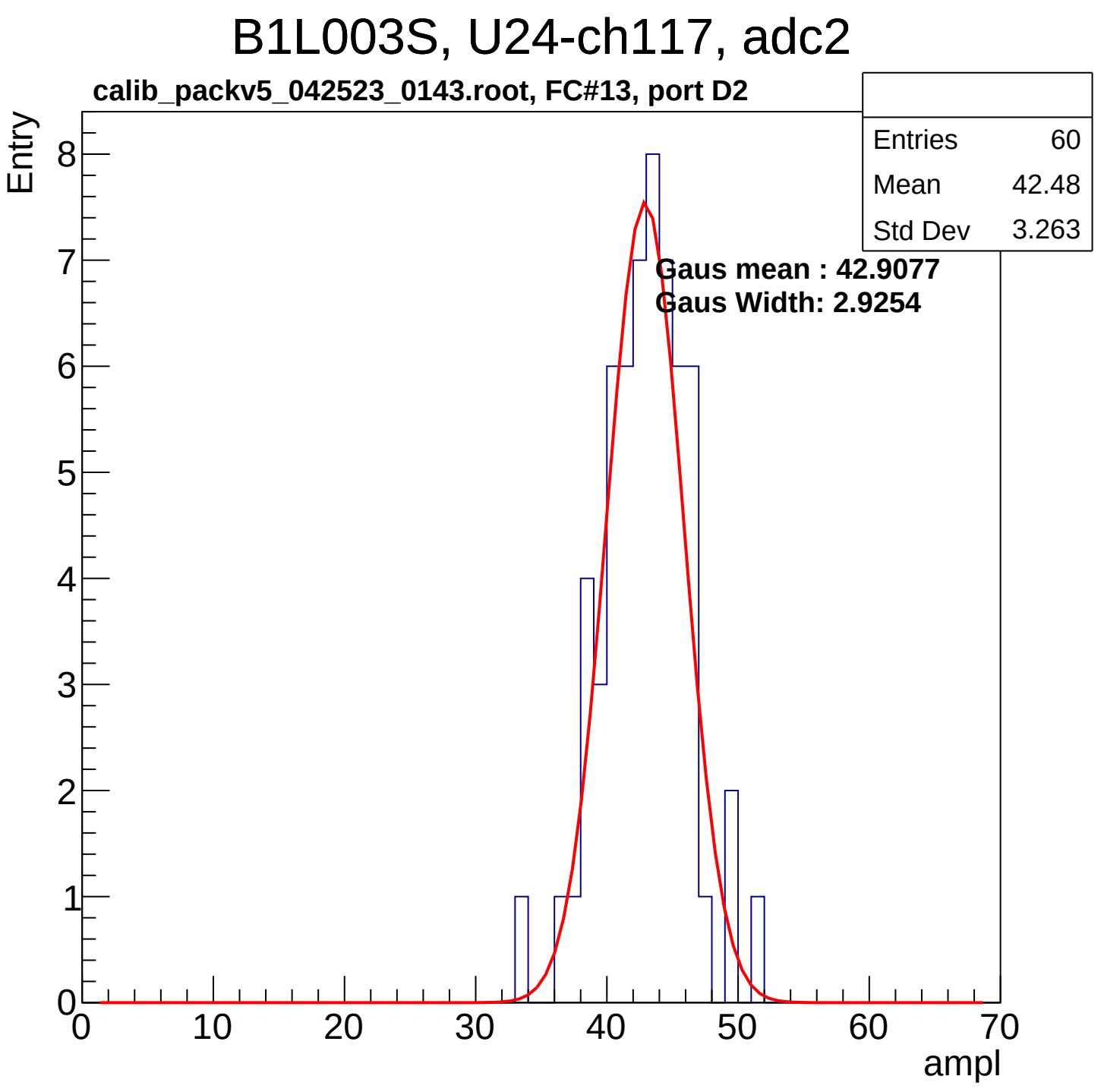
B1L003S, U24-ch117, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 42.48
Std Dev 3.263
Gaus mean : 42.9077
Gaus Width: 2.9254
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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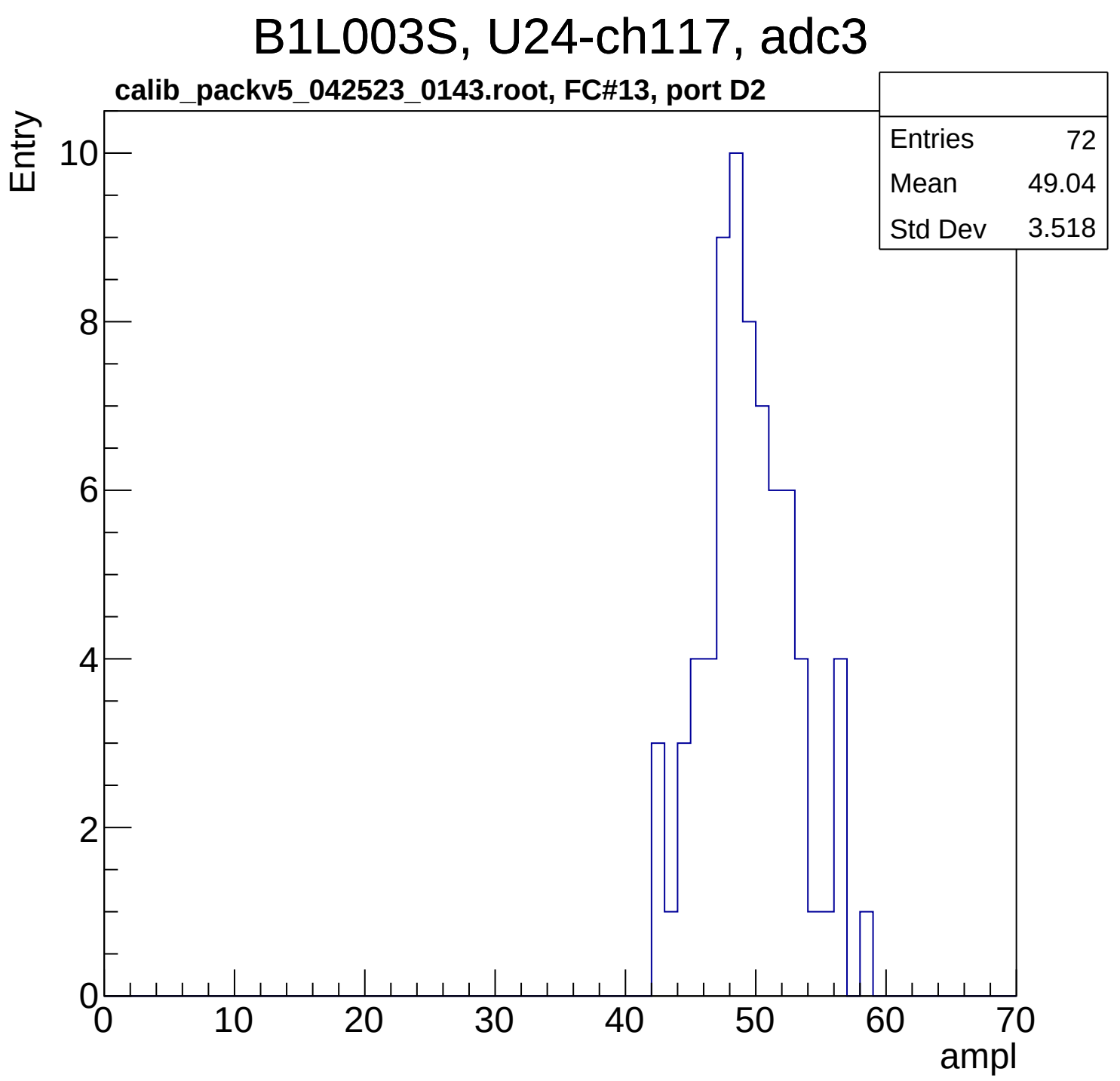
B1L003S, U24-ch117, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.04
Std Dev 3.518
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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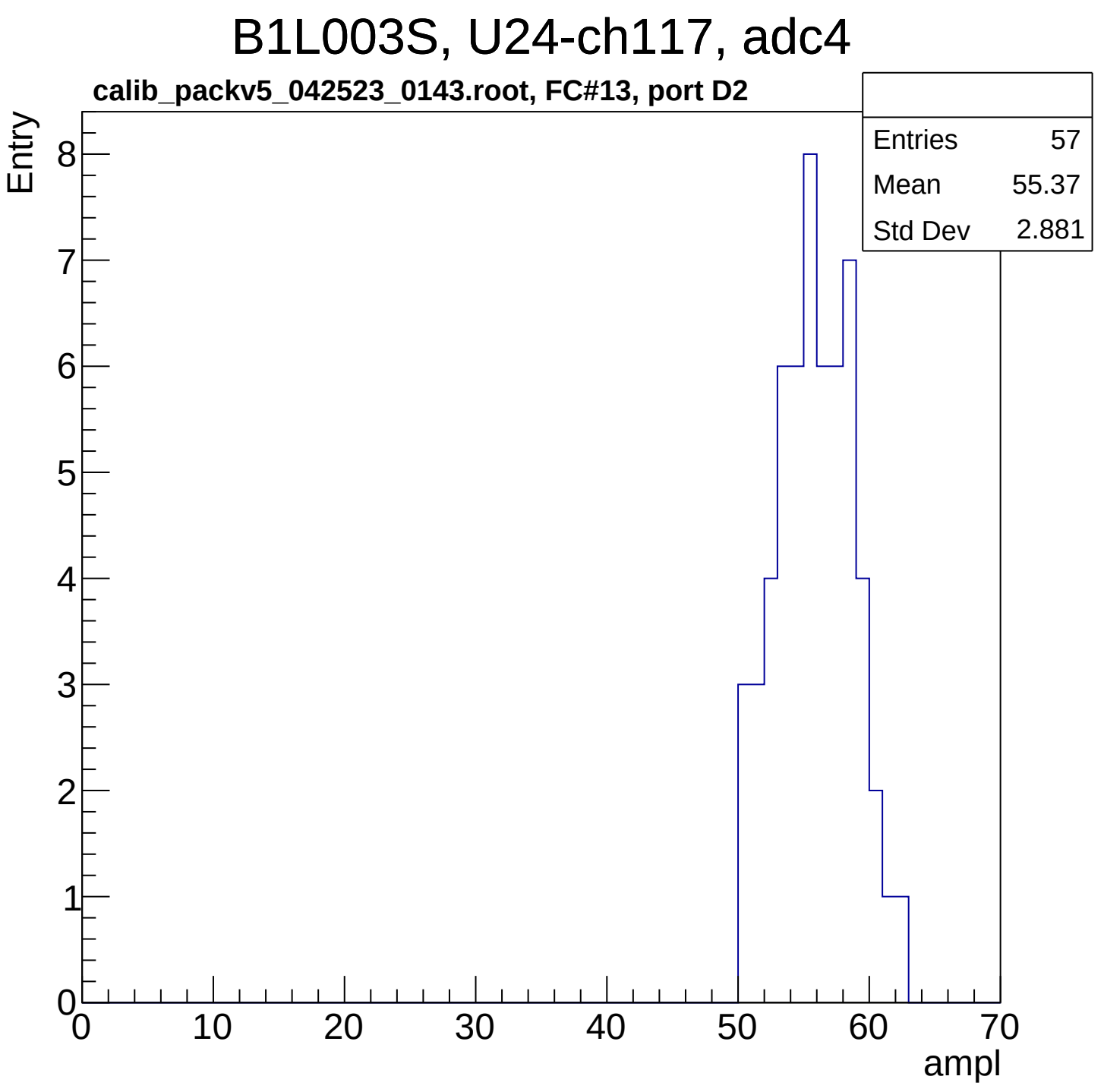
B1L003S, U24-ch117, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.37
Std Dev 2.881
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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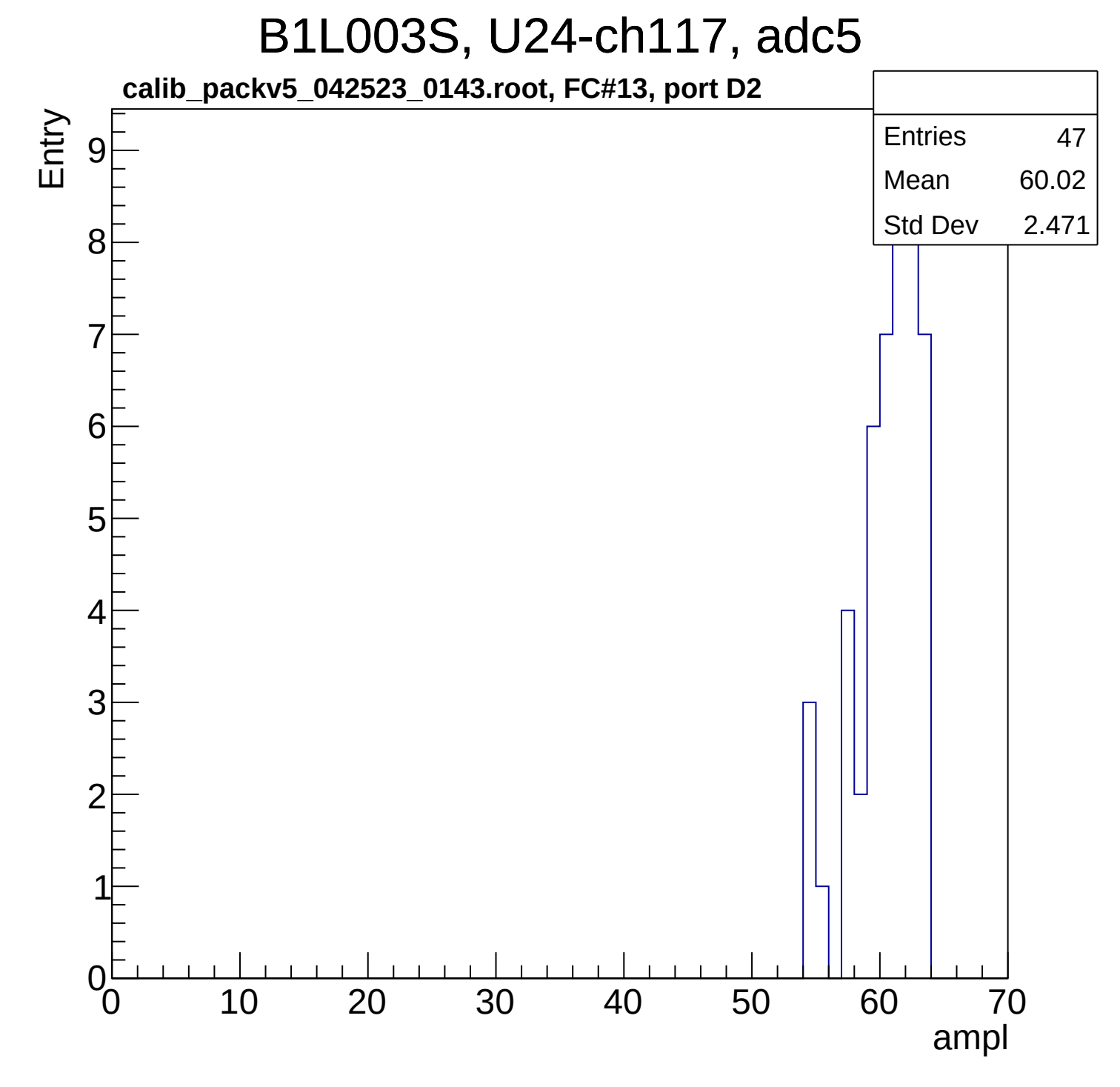
B1L003S, U24-ch117, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 60.02
Std Dev 2.471
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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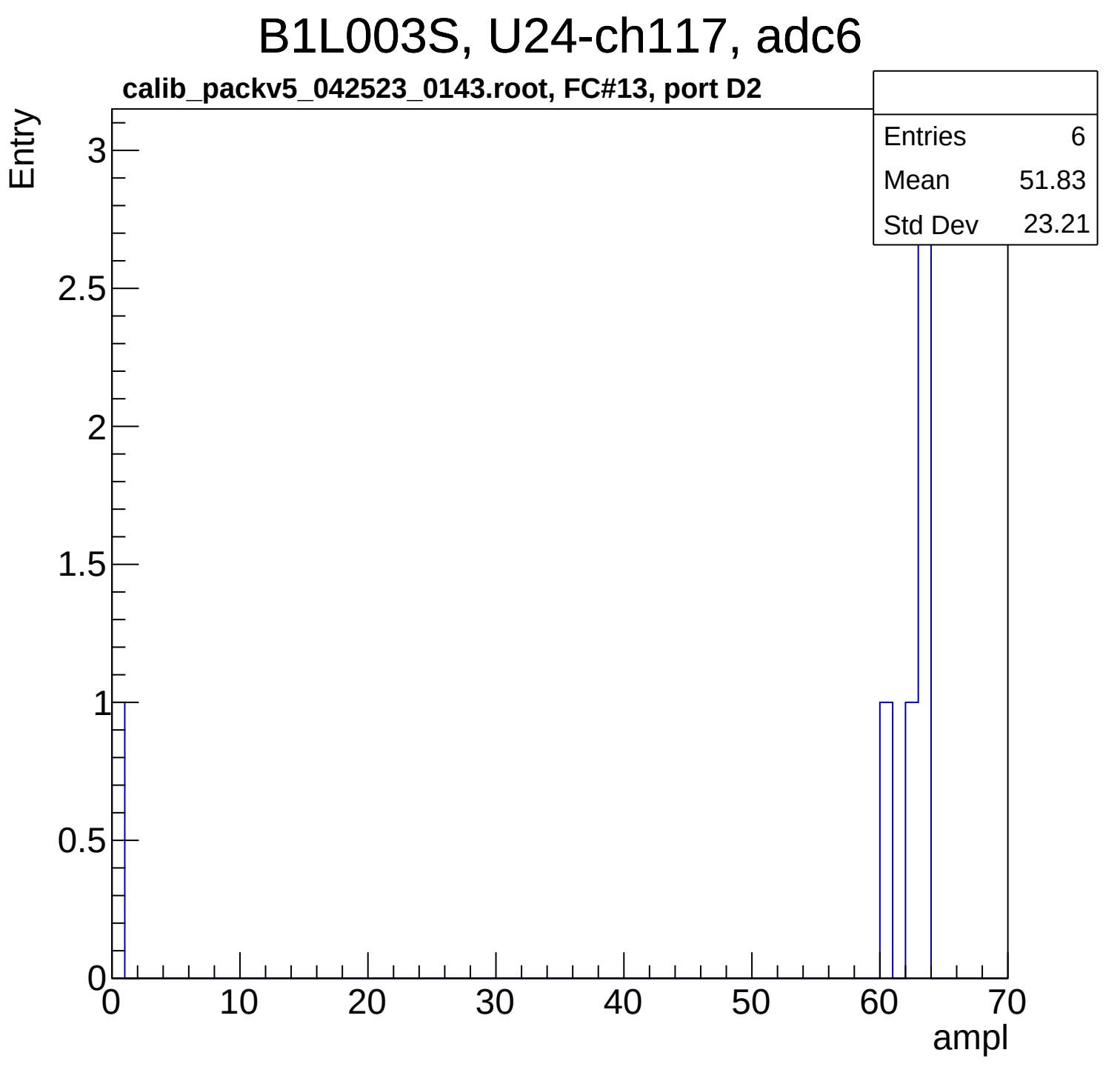
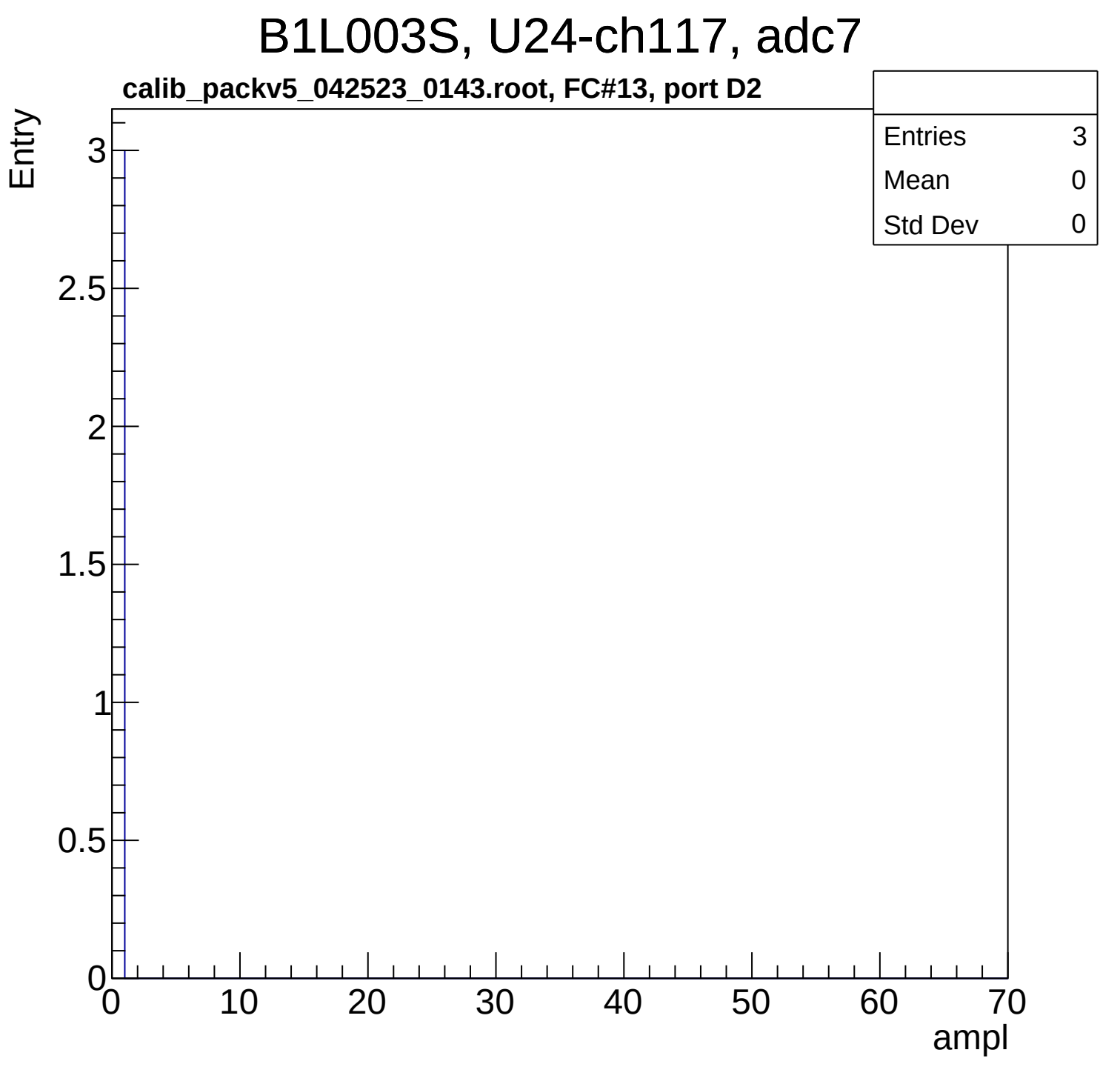
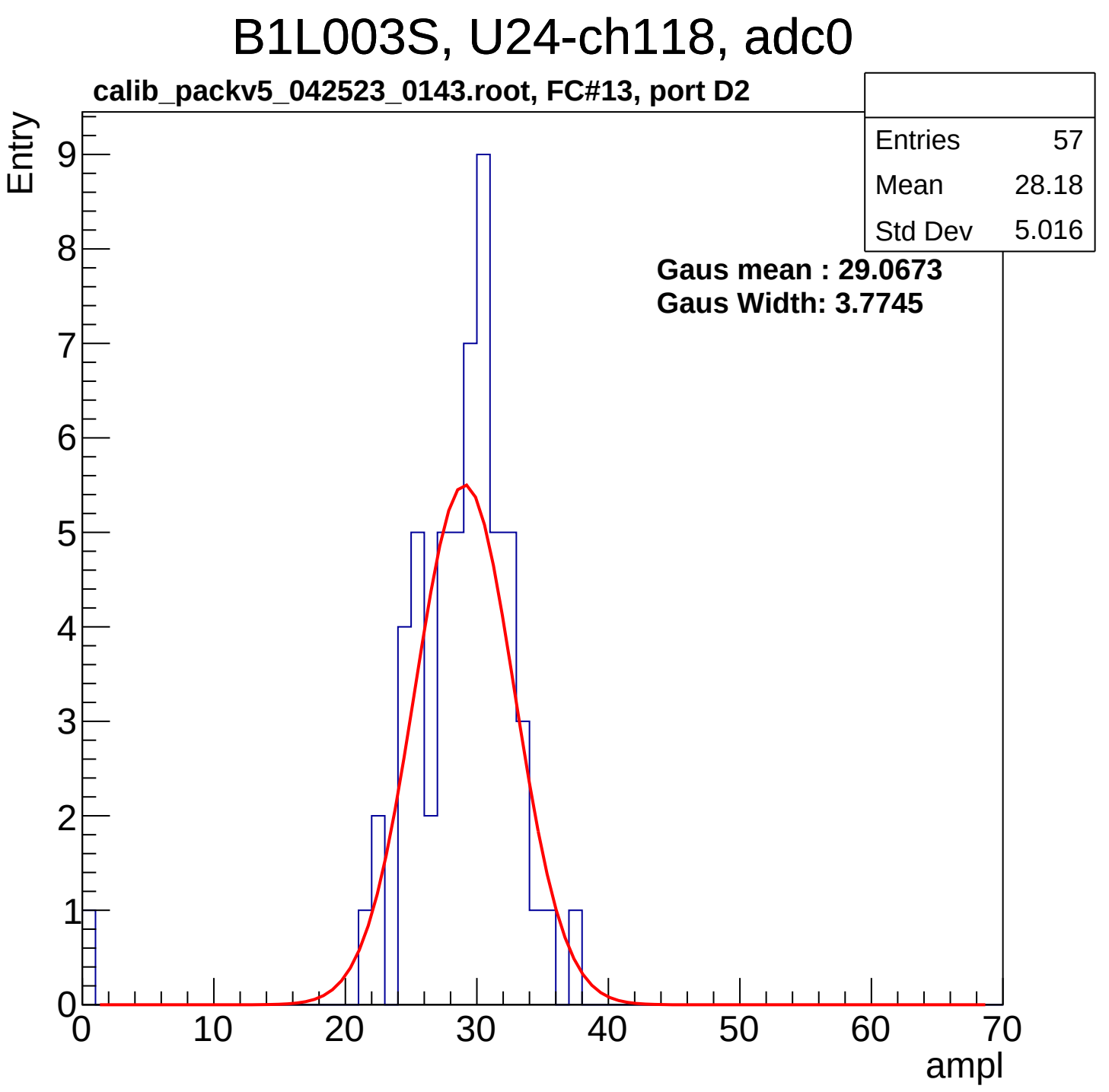

B1L003S, U24-ch117, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 51.83
Std Dev 23.21
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch117, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch118, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.18
Std Dev 5.016
Gaus mean : 29.0673
Gaus Width: 3.7745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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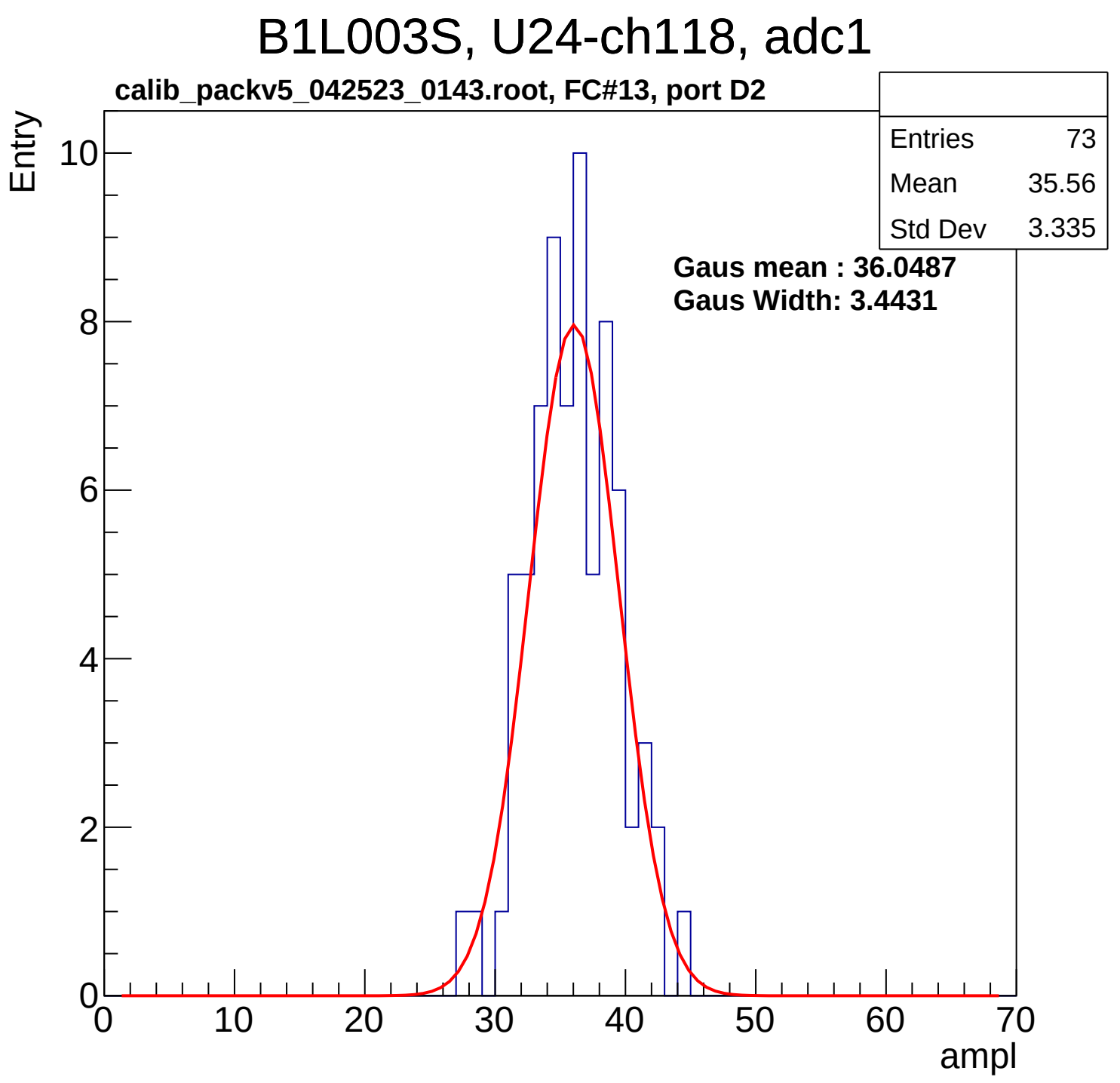
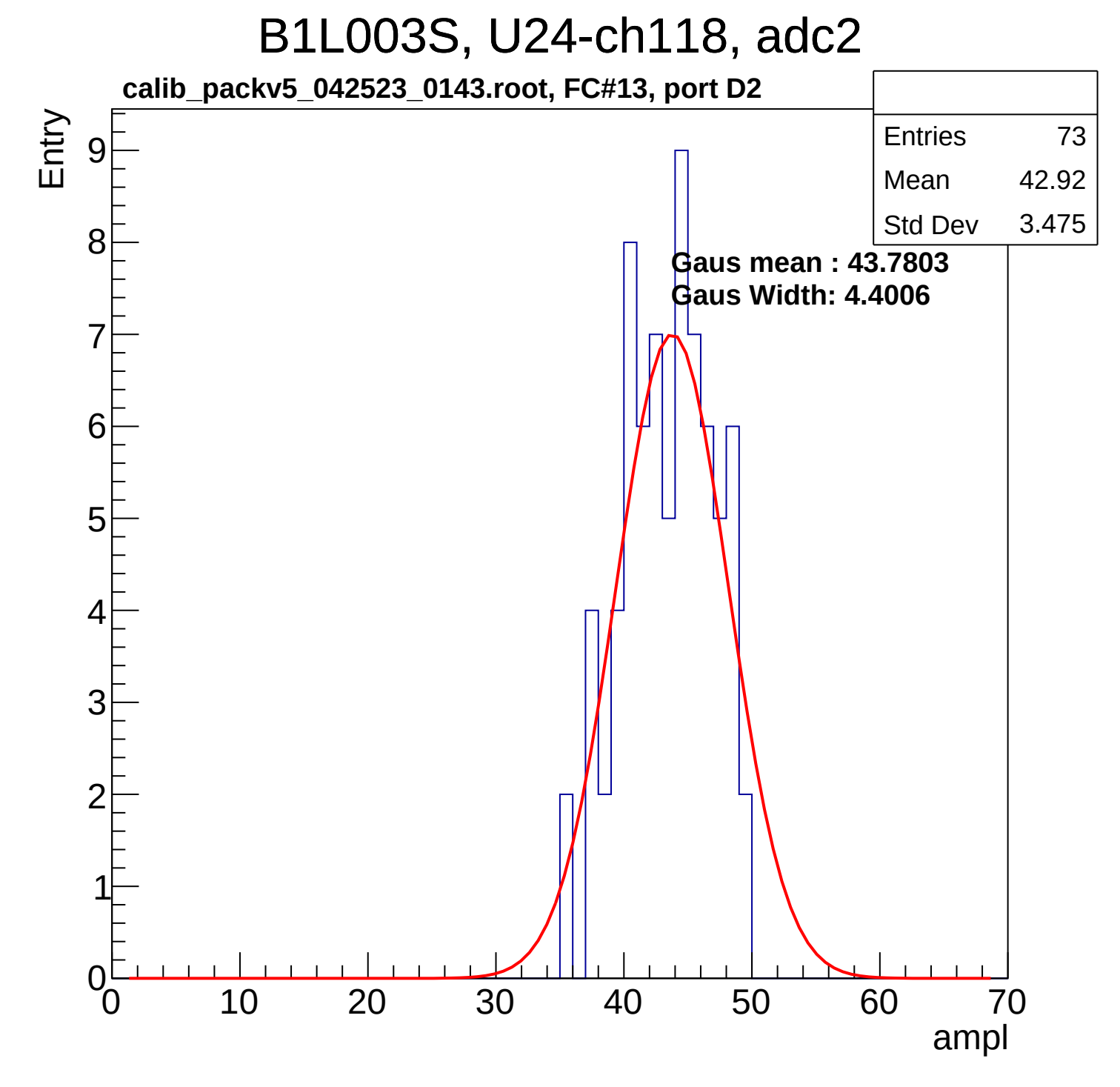
60
70


B1L003S, U24-ch118, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.56
Std Dev 3.335
Gaus mean : 36.0487
Gaus Width: 3.4431
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch118, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 42.92
Std Dev 3.475
Gaus mean : 43.7803
Gaus Width: 4.4006
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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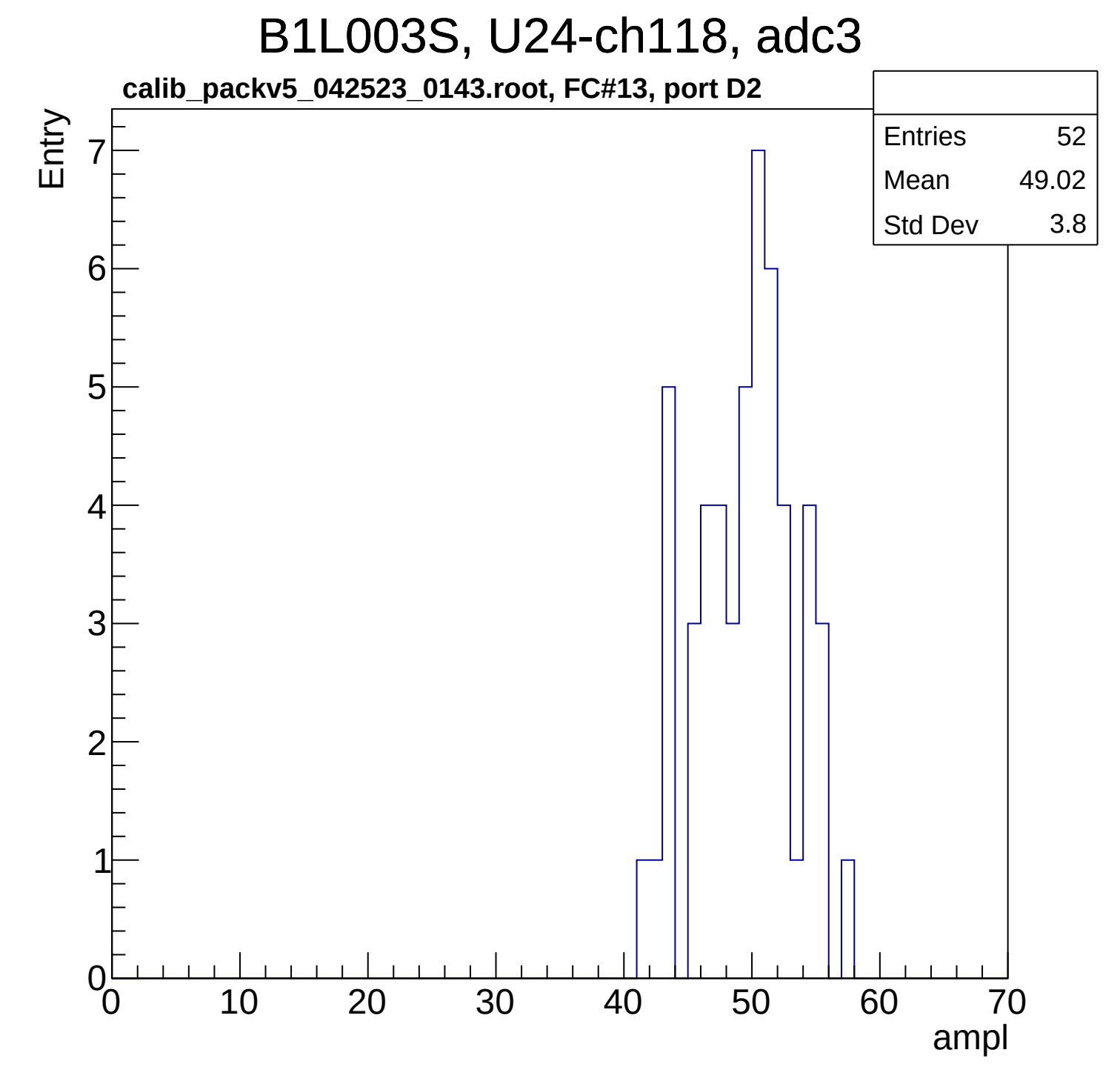
B1L003S, U24-ch118, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 49.02
Std Dev 3.8
Entry
ampl
0
1
2
3
4
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6
7
0
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40
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60
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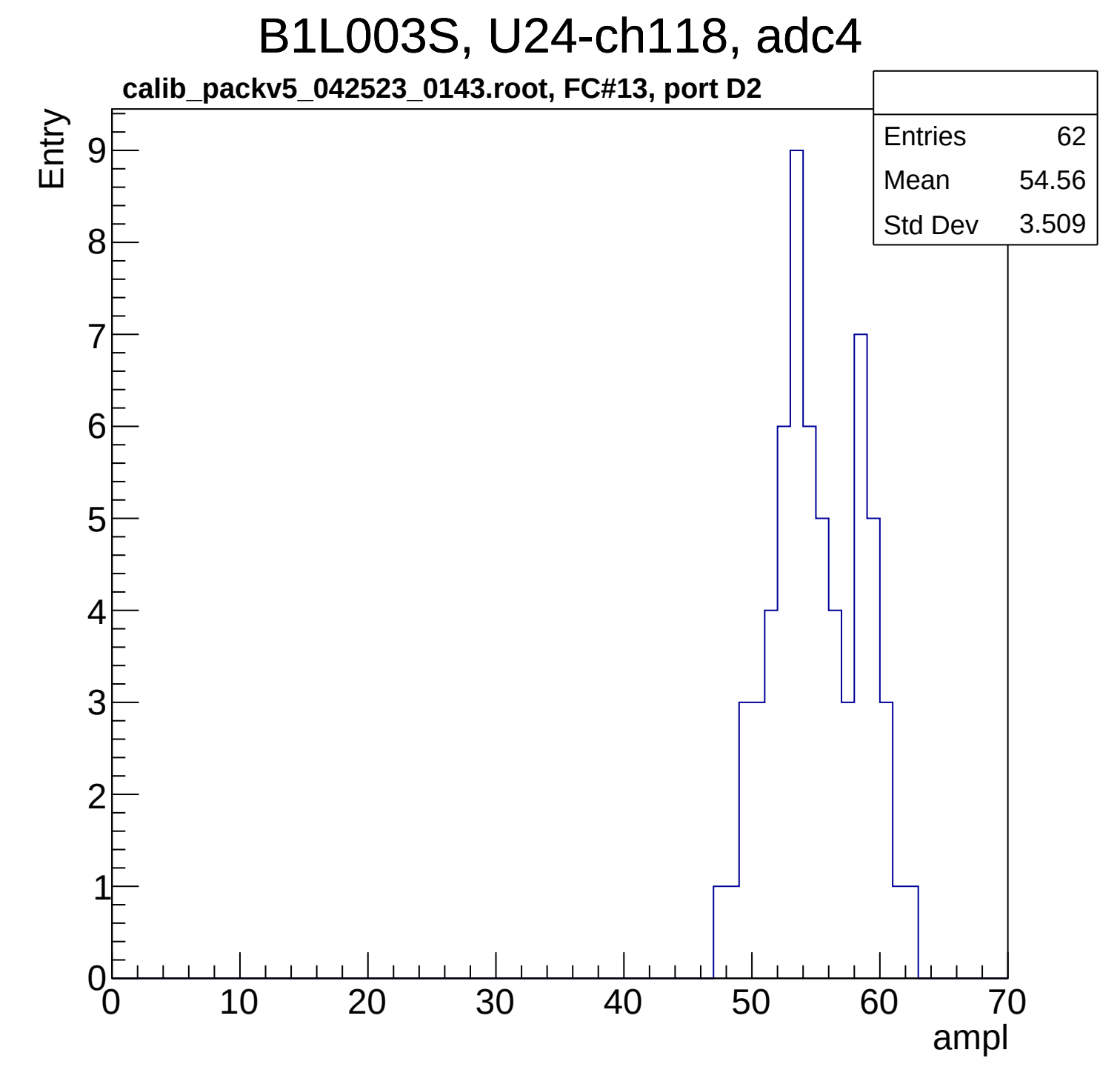
B1L003S, U24-ch118, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 54.56
Std Dev 3.509
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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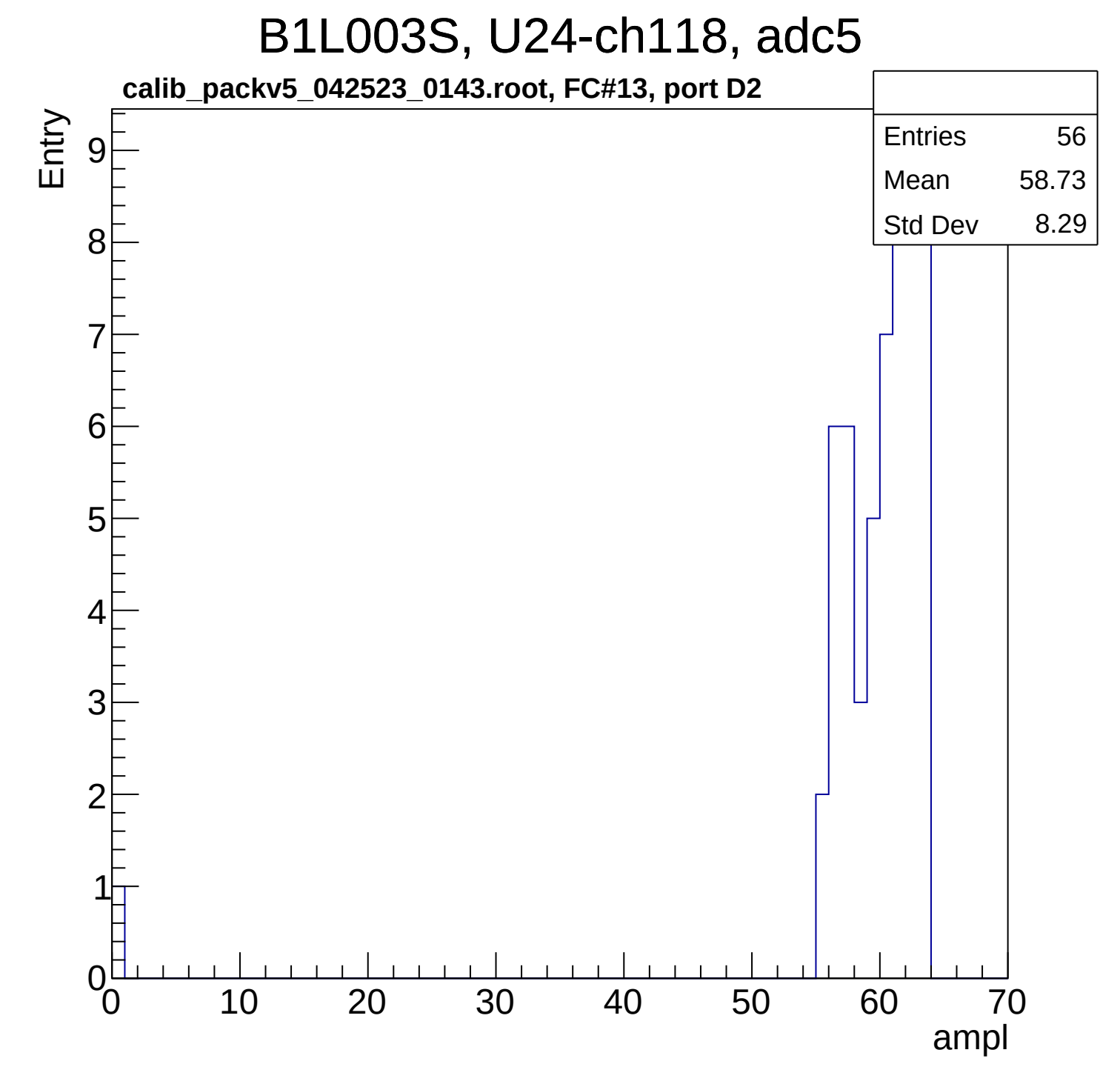
B1L003S, U24-ch118, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.73
Std Dev 8.29
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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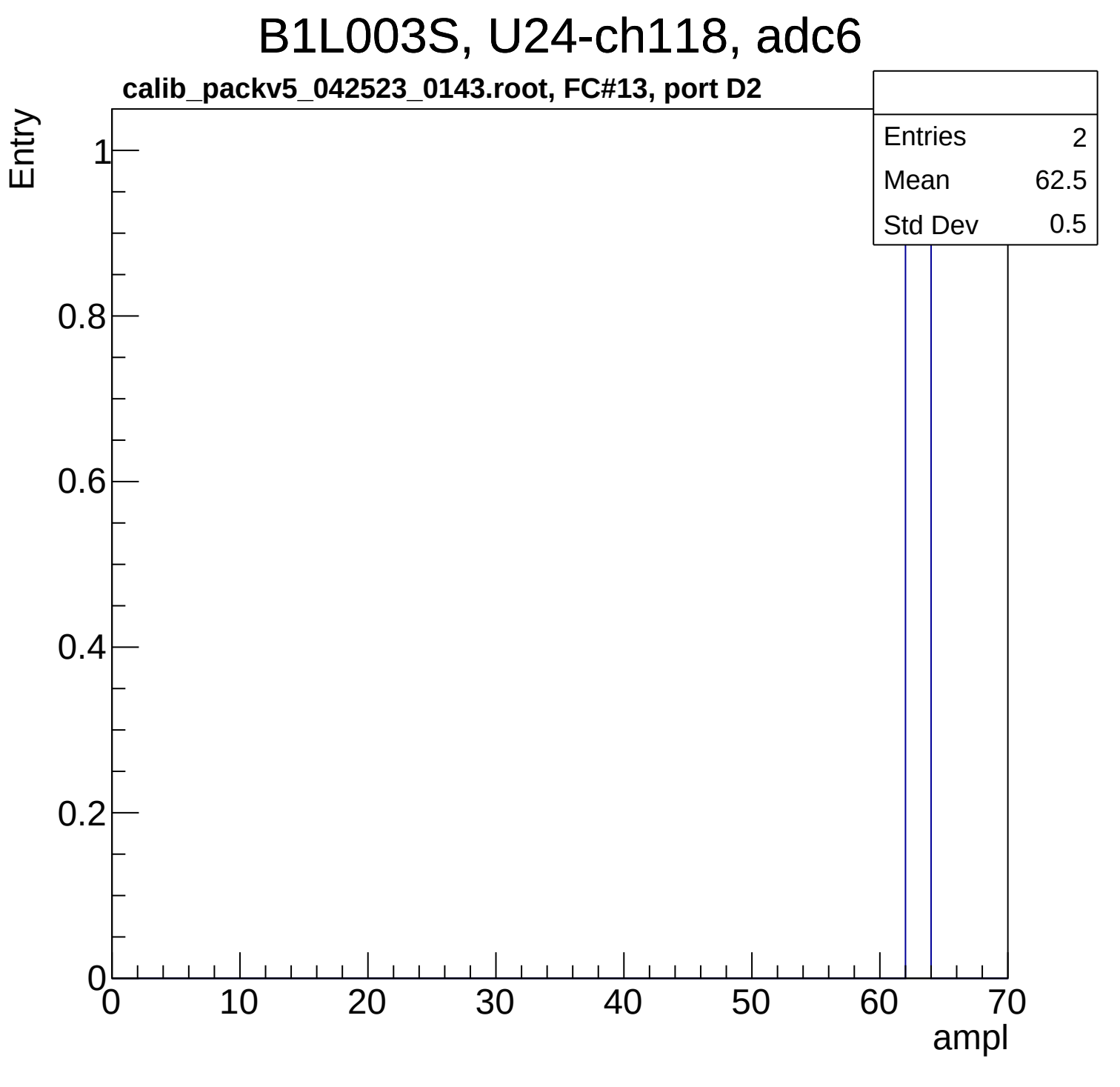
B1L003S, U24-ch118, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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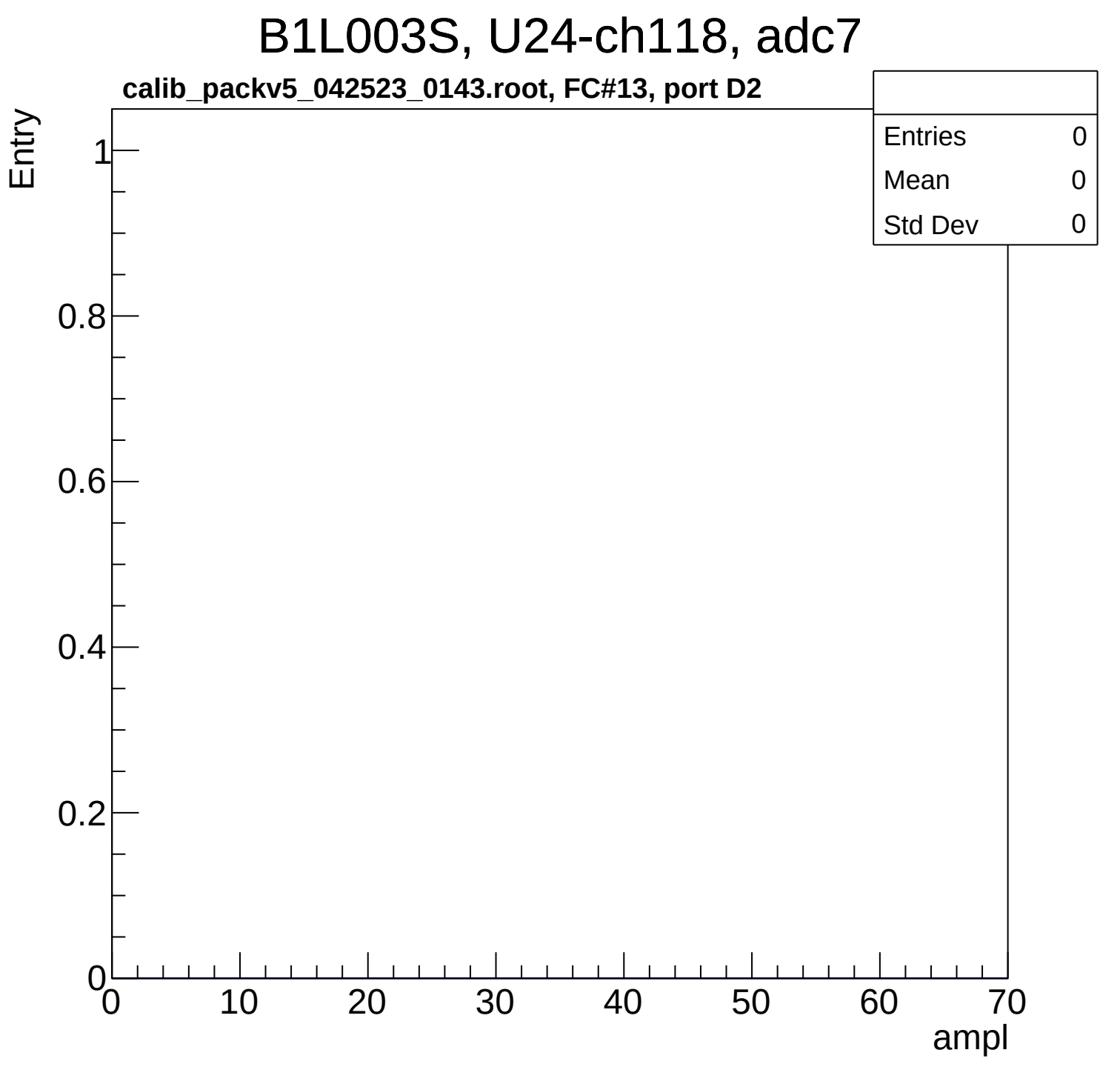
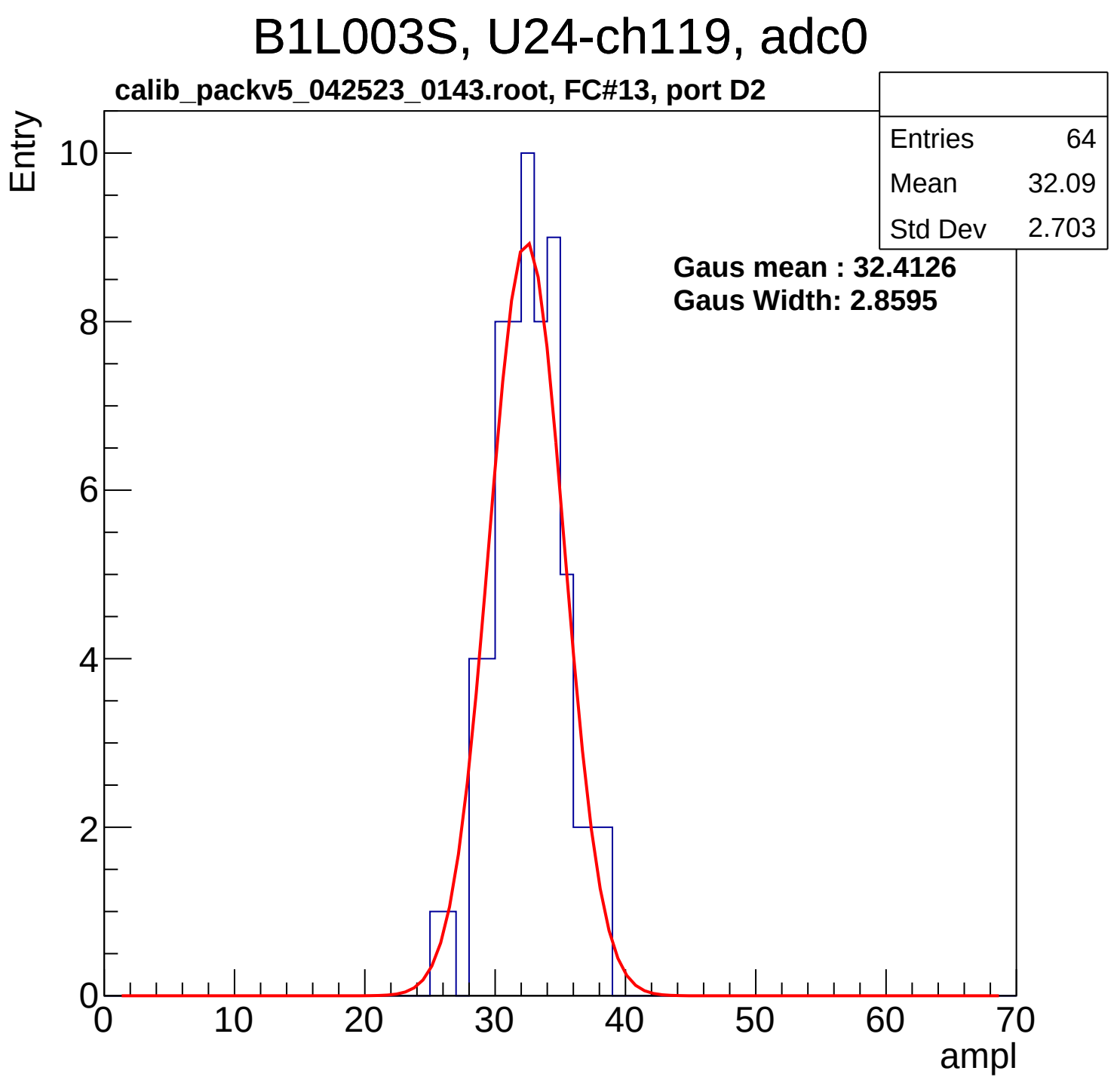
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch118, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch119, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 32.09
Std Dev 2.703
Gaus mean : 32.4126
Gaus Width: 2.8595
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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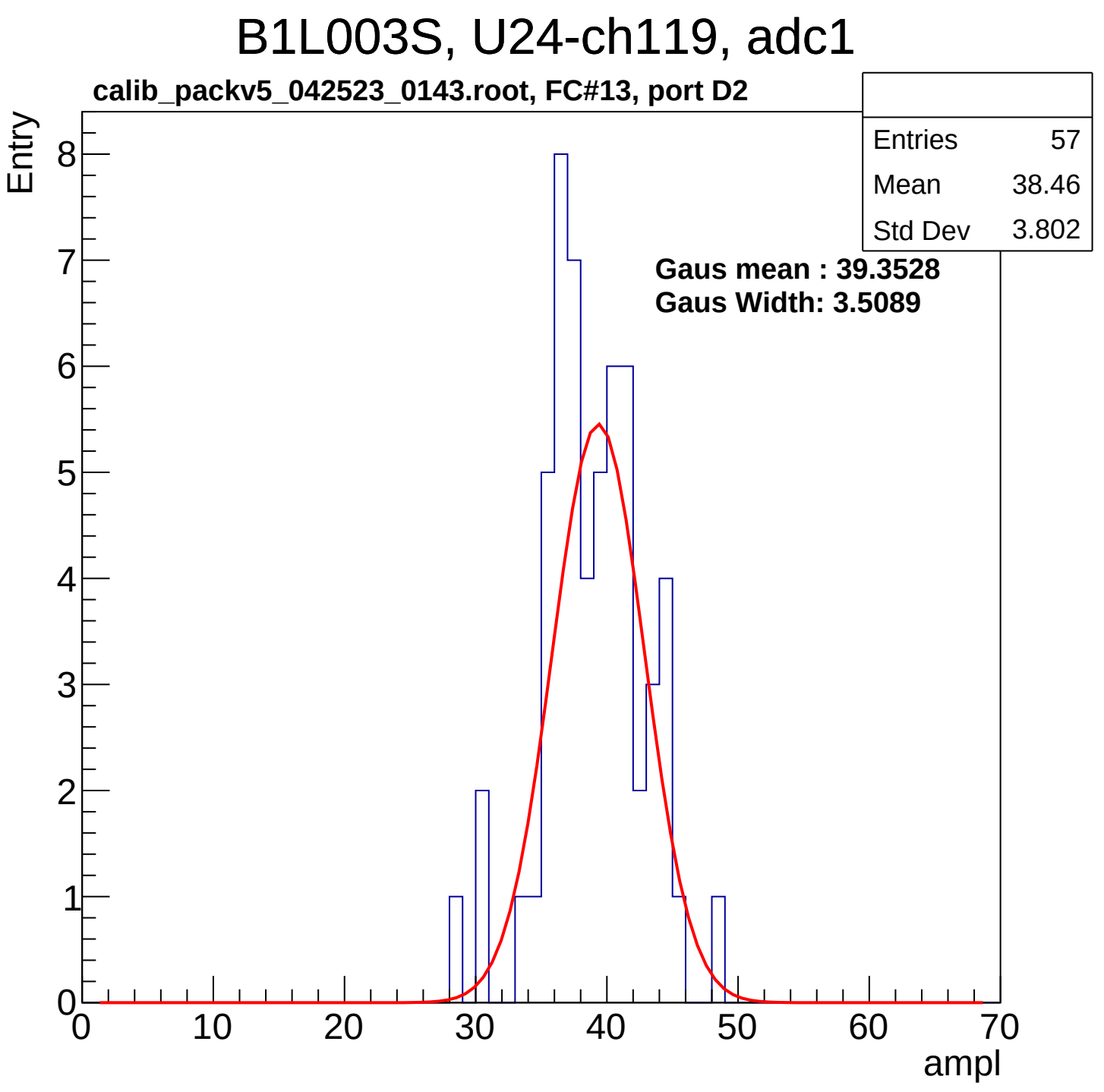
B1L003S, U24-ch119, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 38.46
Std Dev 3.802
Gaus mean : 39.3528
Gaus Width: 3.5089
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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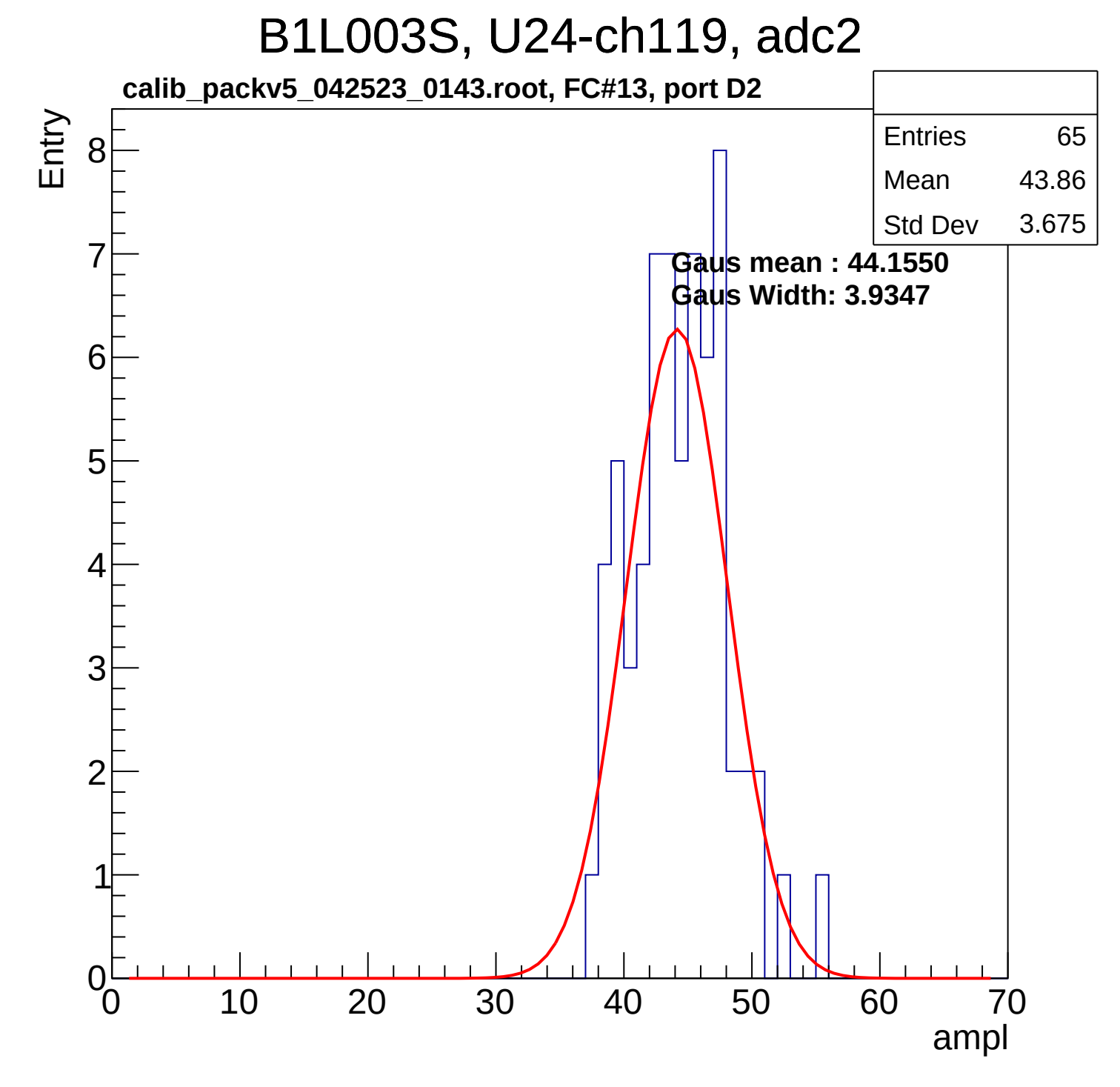
B1L003S, U24-ch119, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 43.86
Std Dev 3.675
Gaus mean : 44.1550
Gaus Width: 3.9347
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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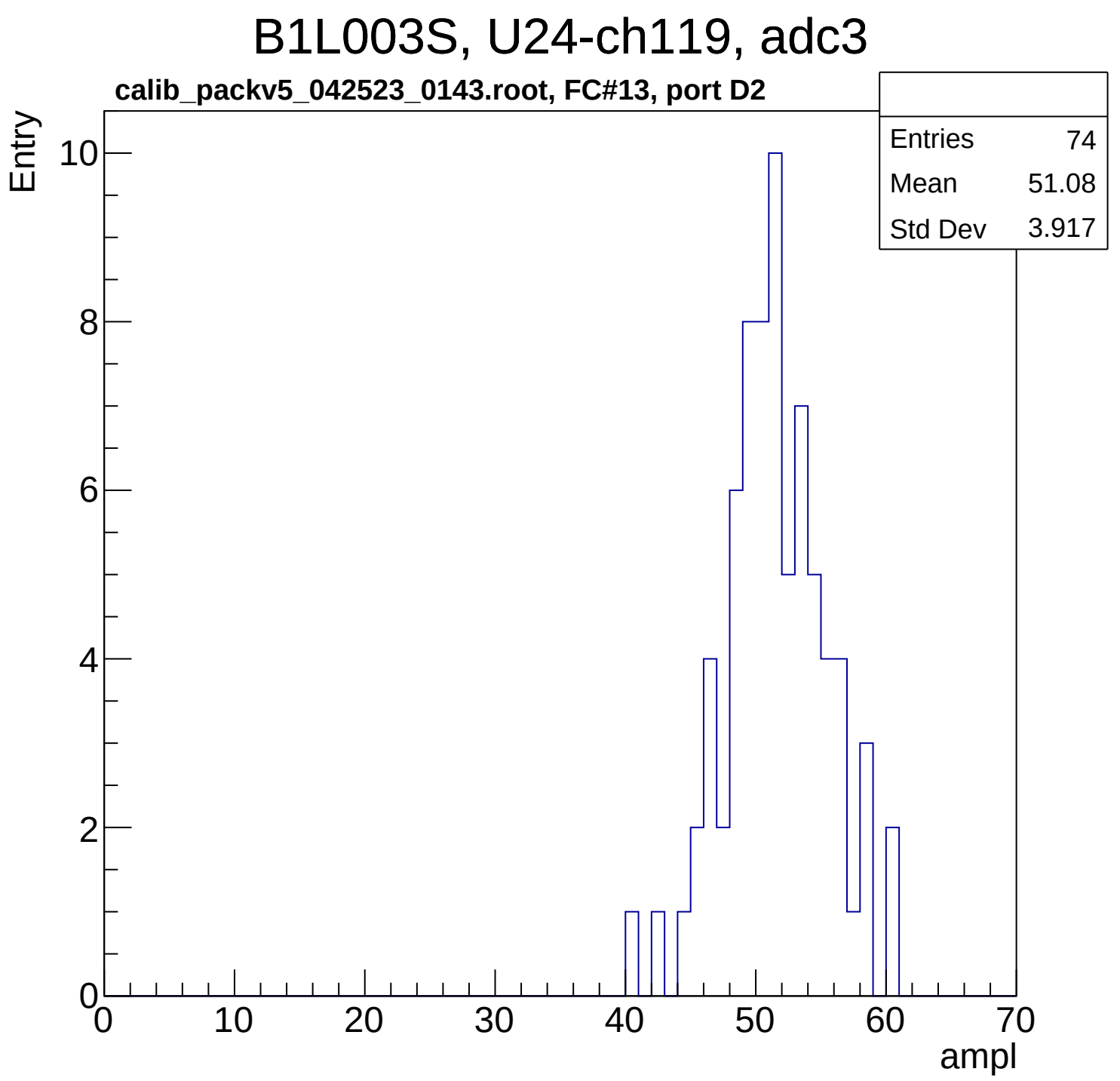
B1L003S, U24-ch119, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 51.08
Std Dev 3.917
Entry
ampl
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2
4
6
8
10
0
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20
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40
50
60
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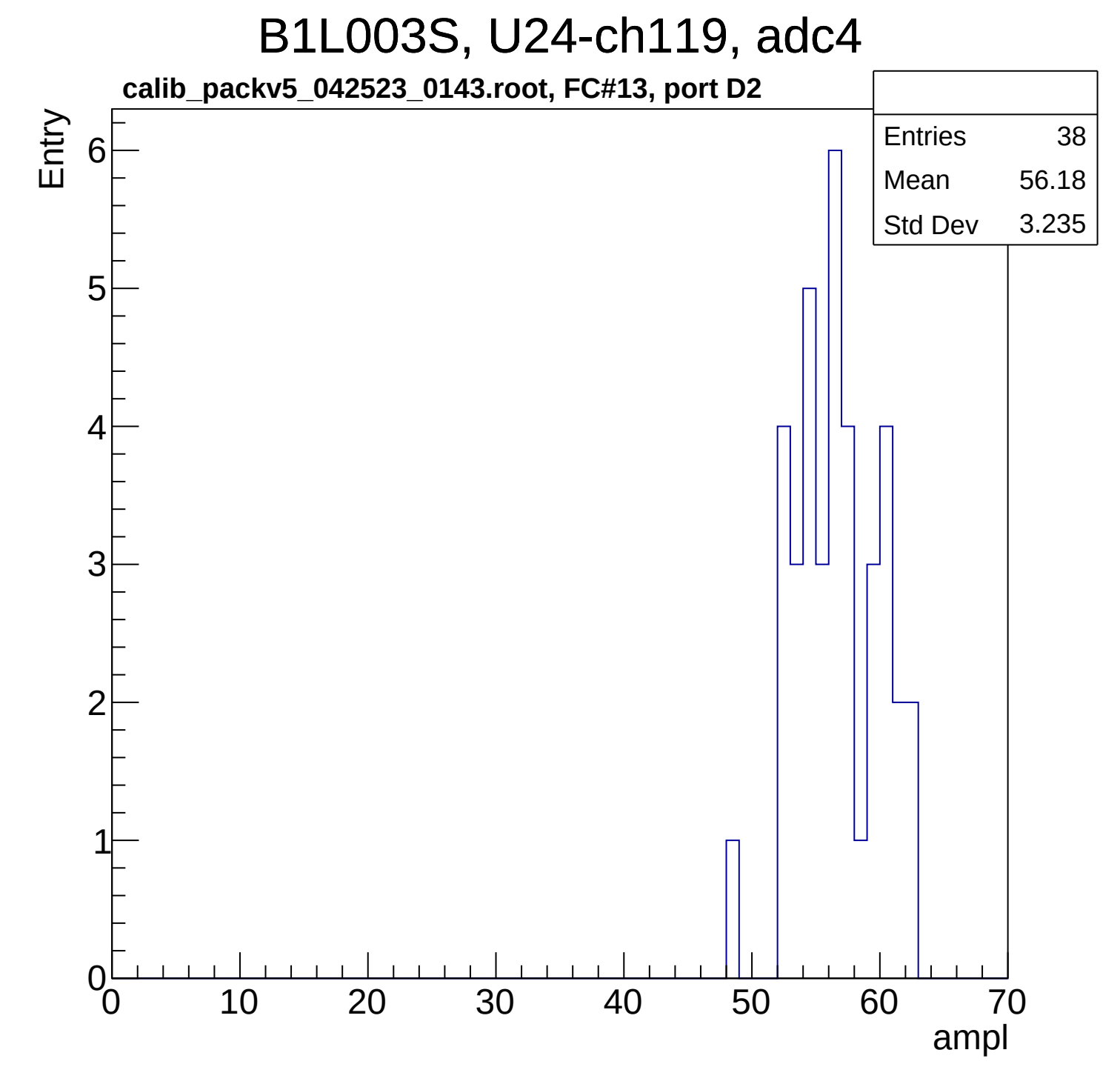
B1L003S, U24-ch119, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 56.18
Std Dev 3.235
Entry
ampl
0
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2
3
4
5
6
0
10
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40
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60
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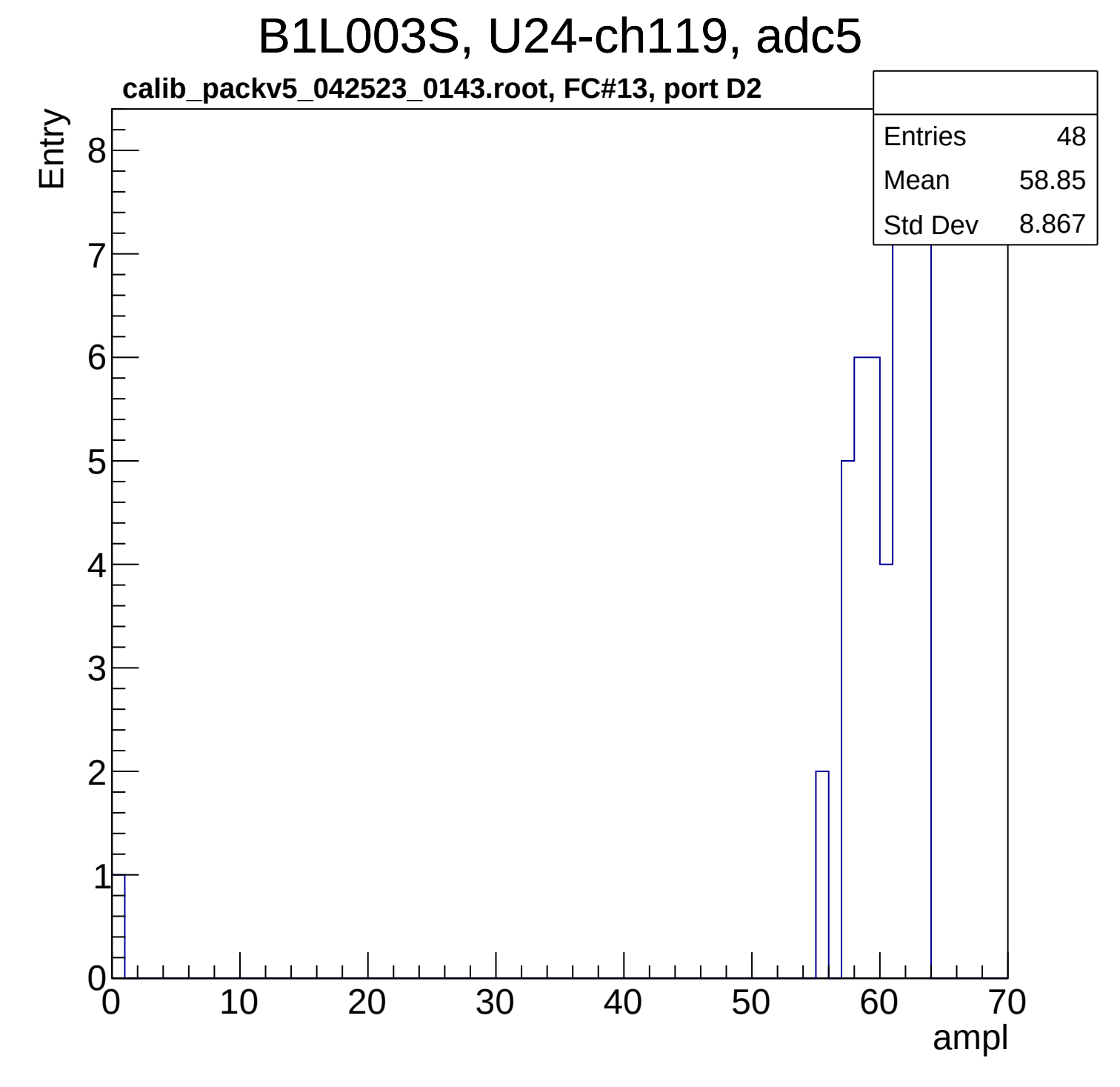
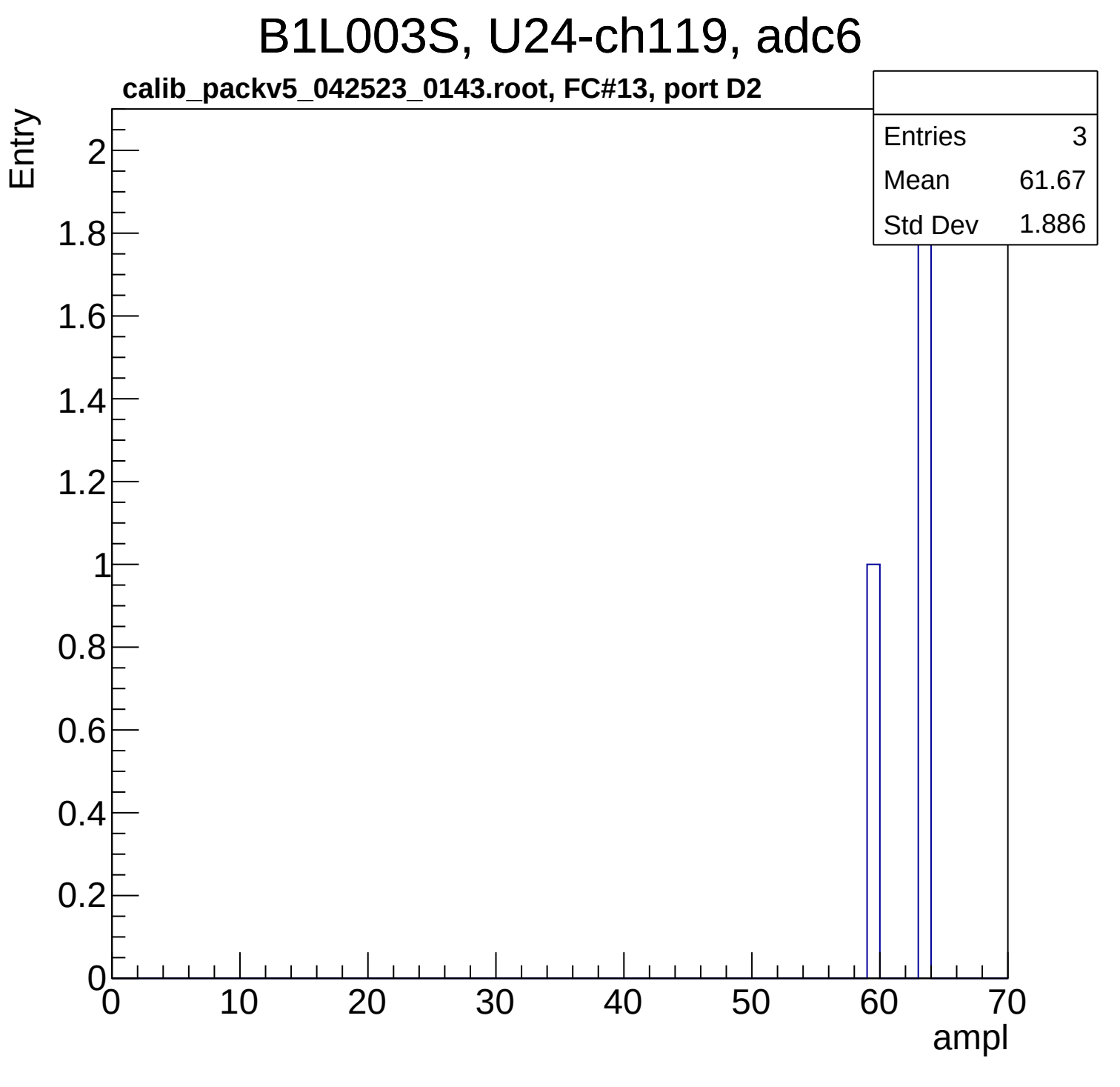

B1L003S, U24-ch119, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.85
Std Dev 8.867
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch119, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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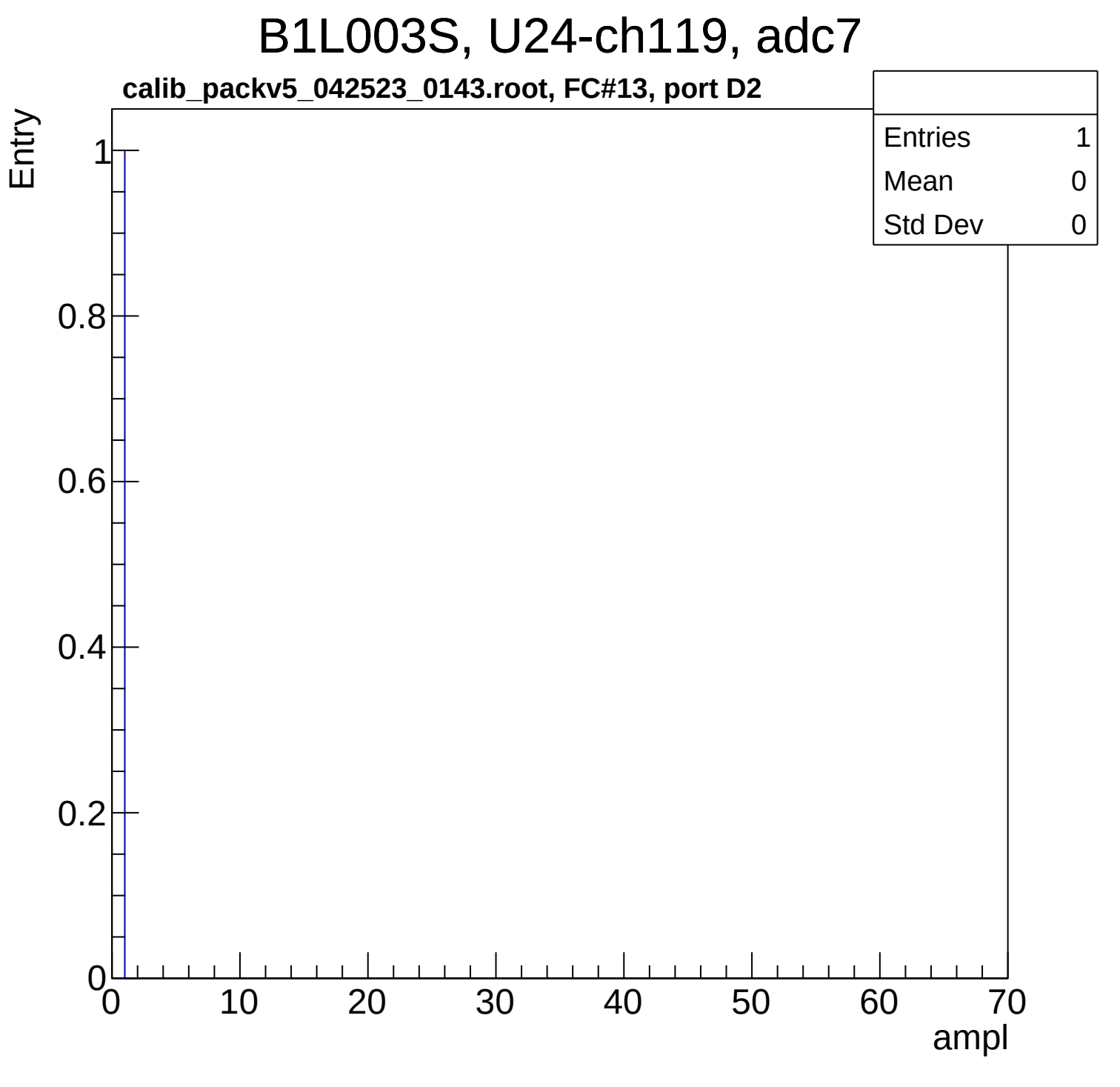
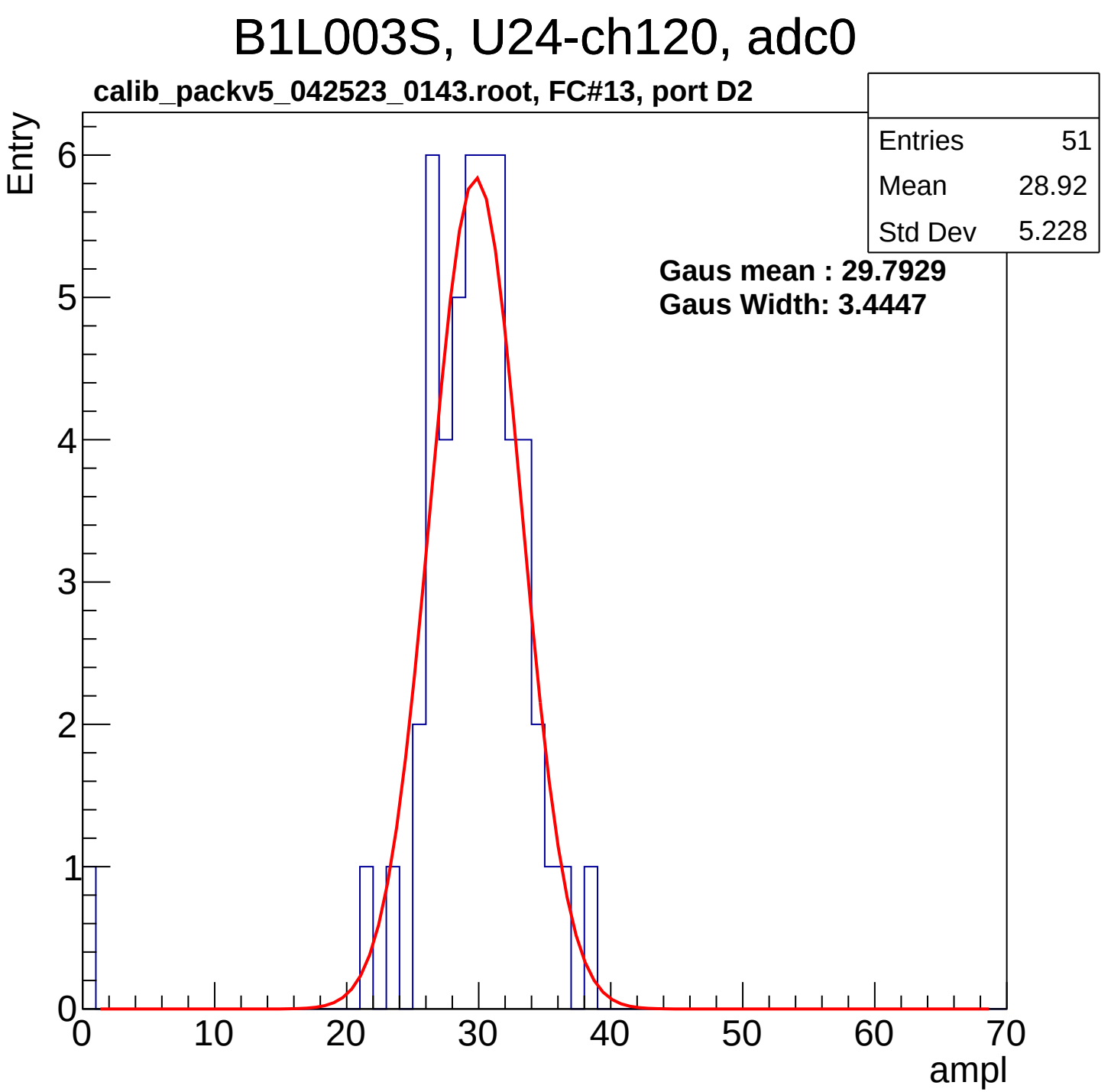
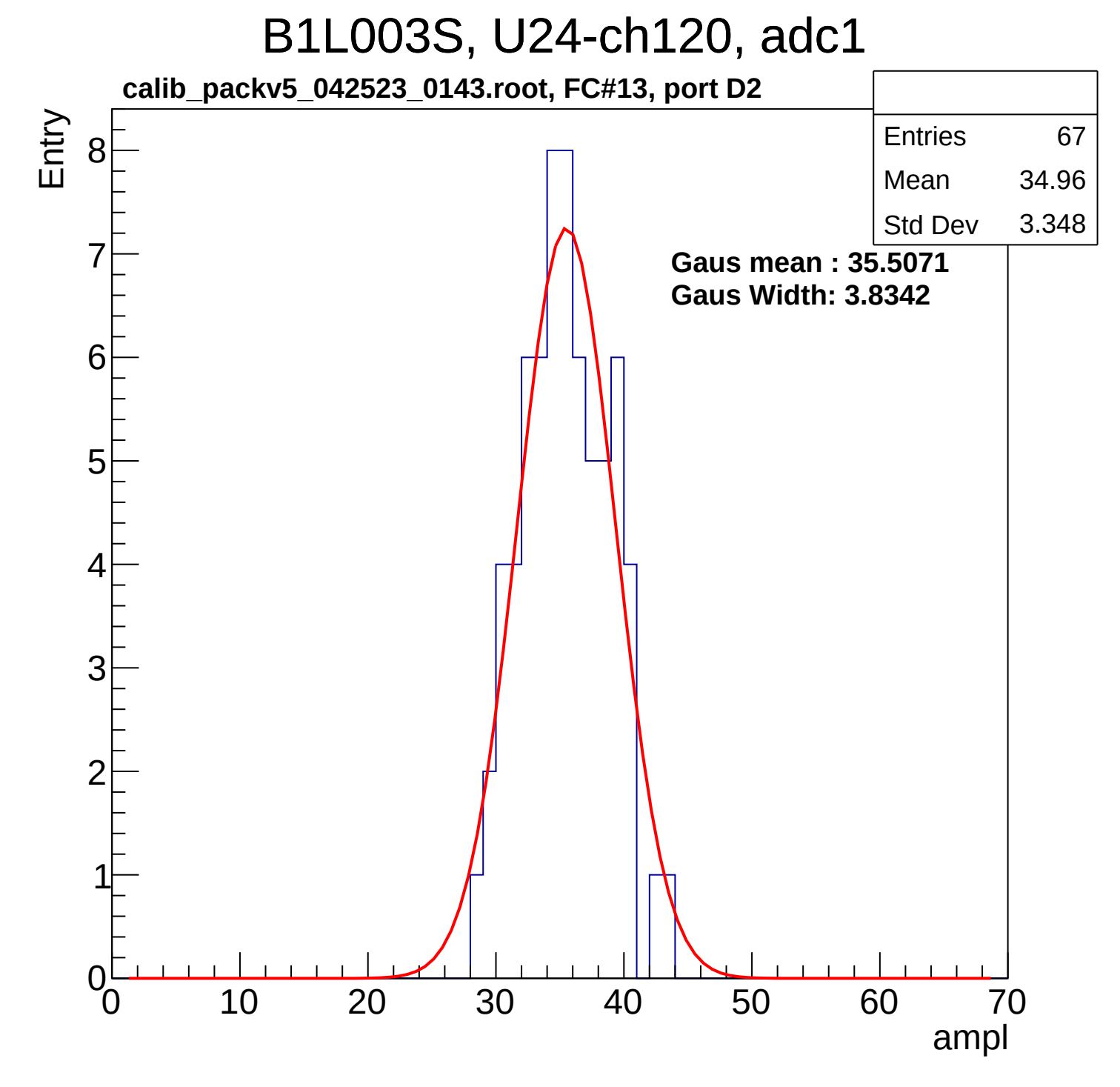

B1L003S, U24-ch119, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch120, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 28.92
Std Dev 5.228
Gaus mean : 29.7929
Gaus Width: 3.4447
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U24-ch120, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 34.96
Std Dev 3.348
Gaus mean : 35.5071
Gaus Width: 3.8342
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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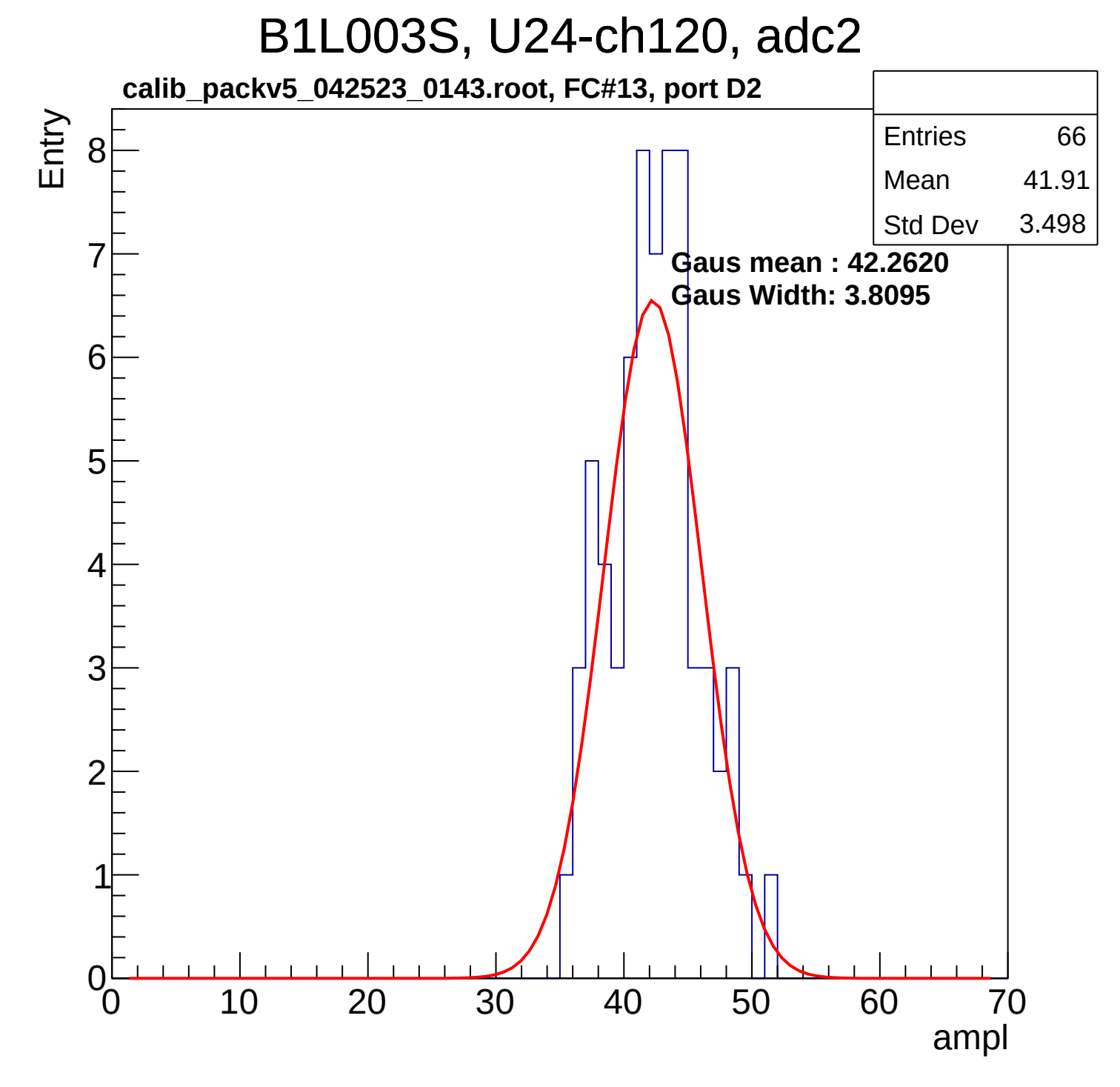
B1L003S, U24-ch120, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 41.91
Std Dev 3.498
Gaus mean : 42.2620
Gaus Width: 3.8095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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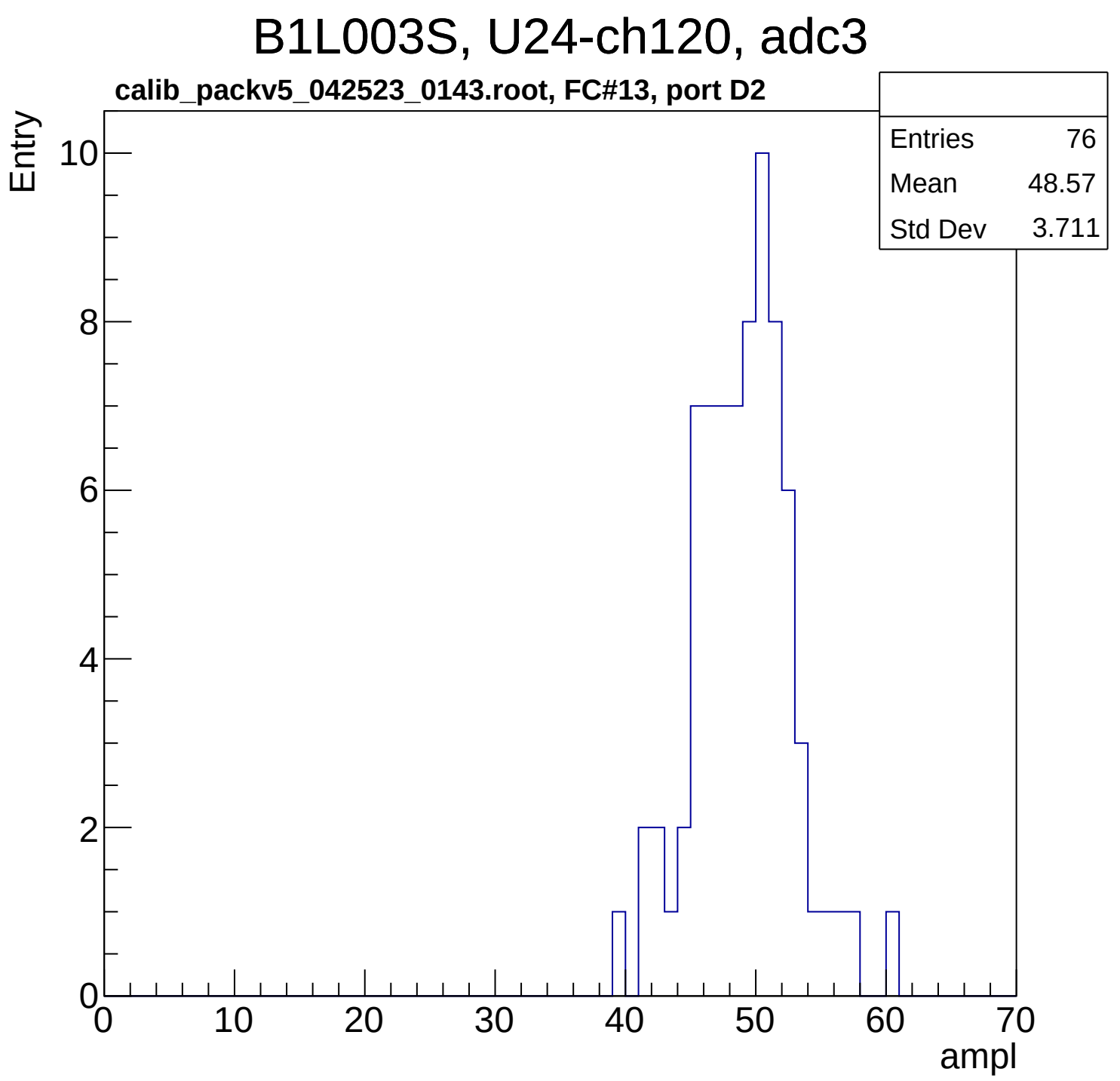
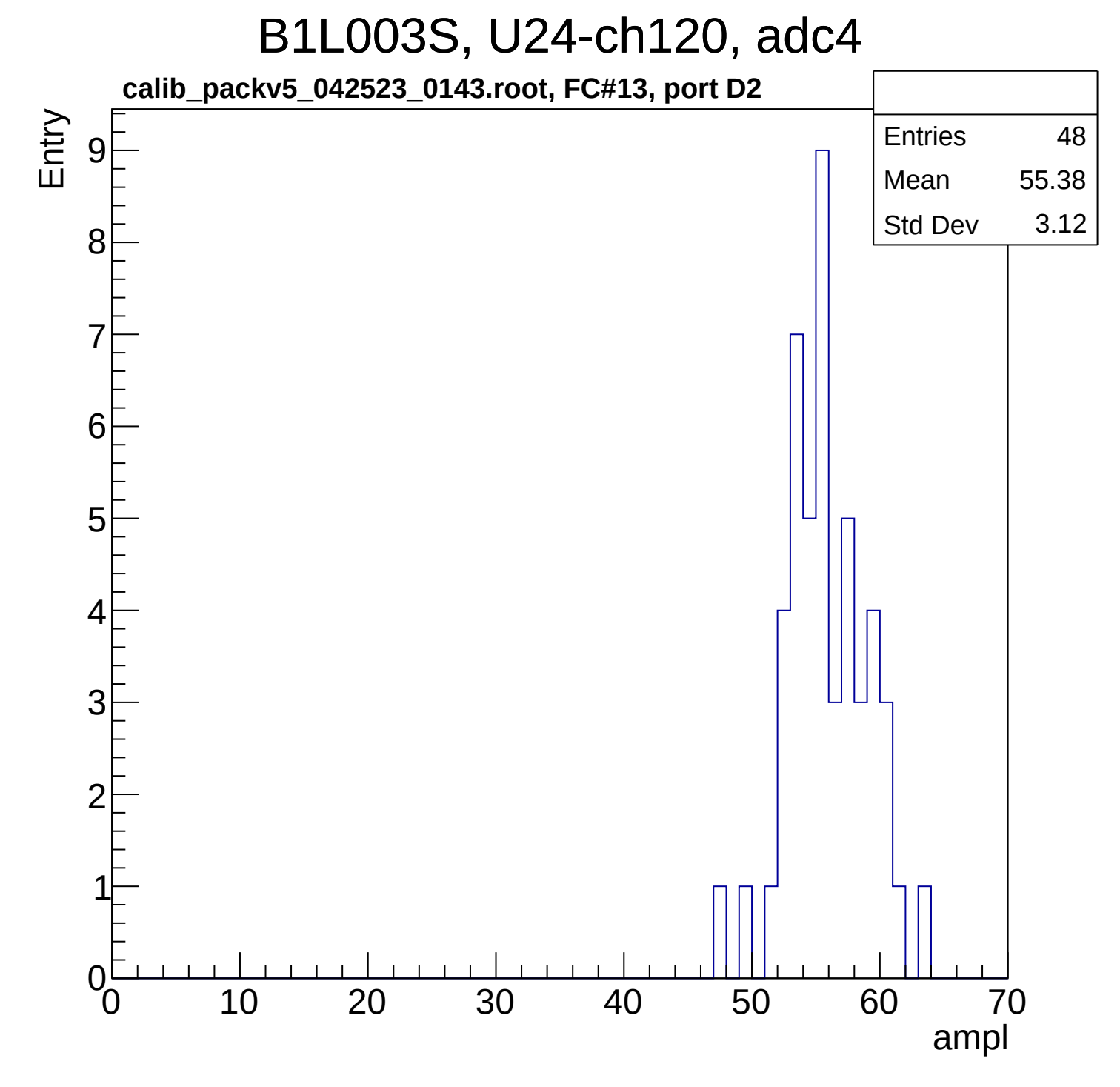

B1L003S, U24-ch120, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 48.57
Std Dev 3.711
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch120, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.38
Std Dev 3.12
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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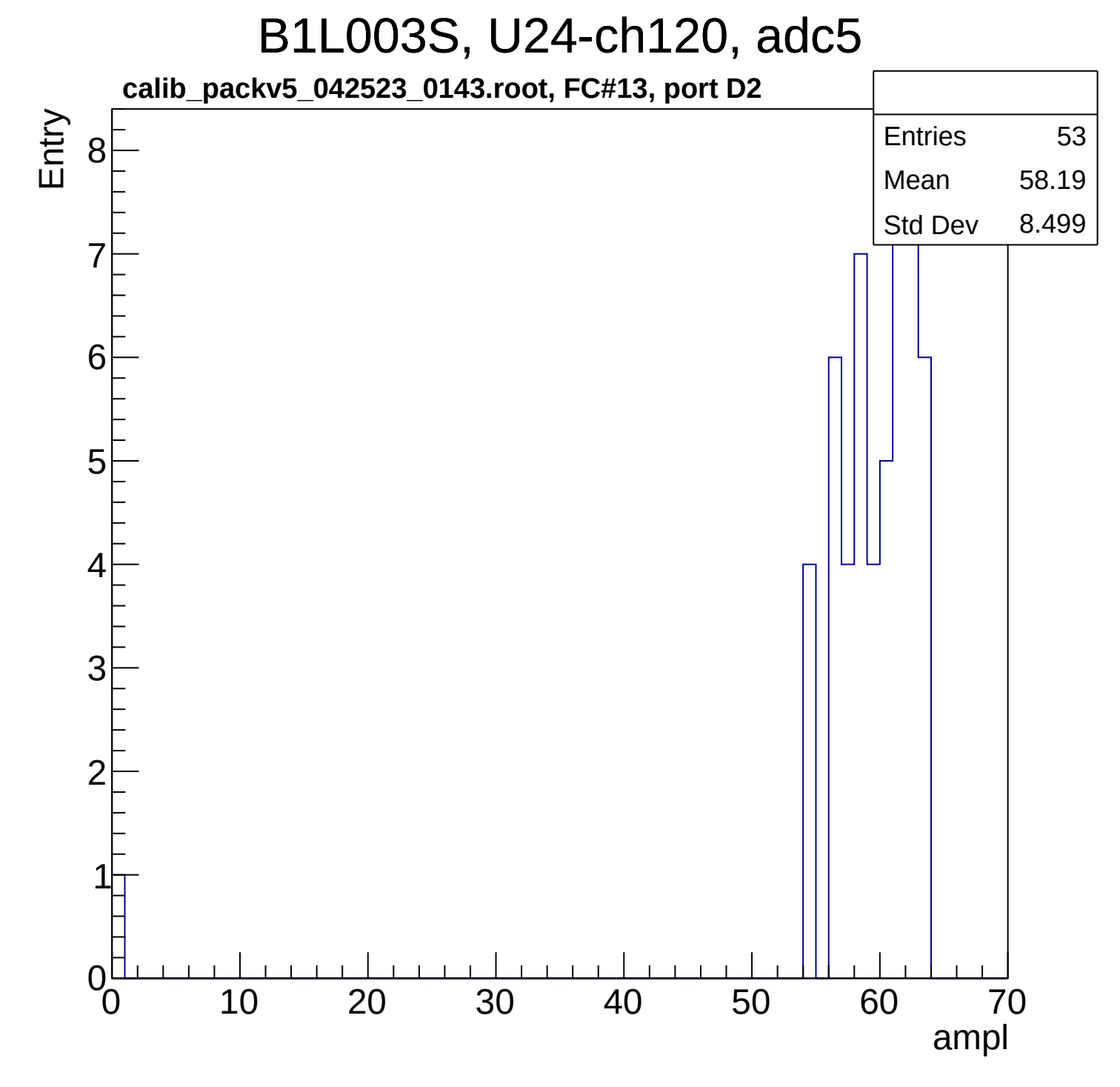
B1L003S, U24-ch120, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.19
Std Dev 8.499
Entry
ampl
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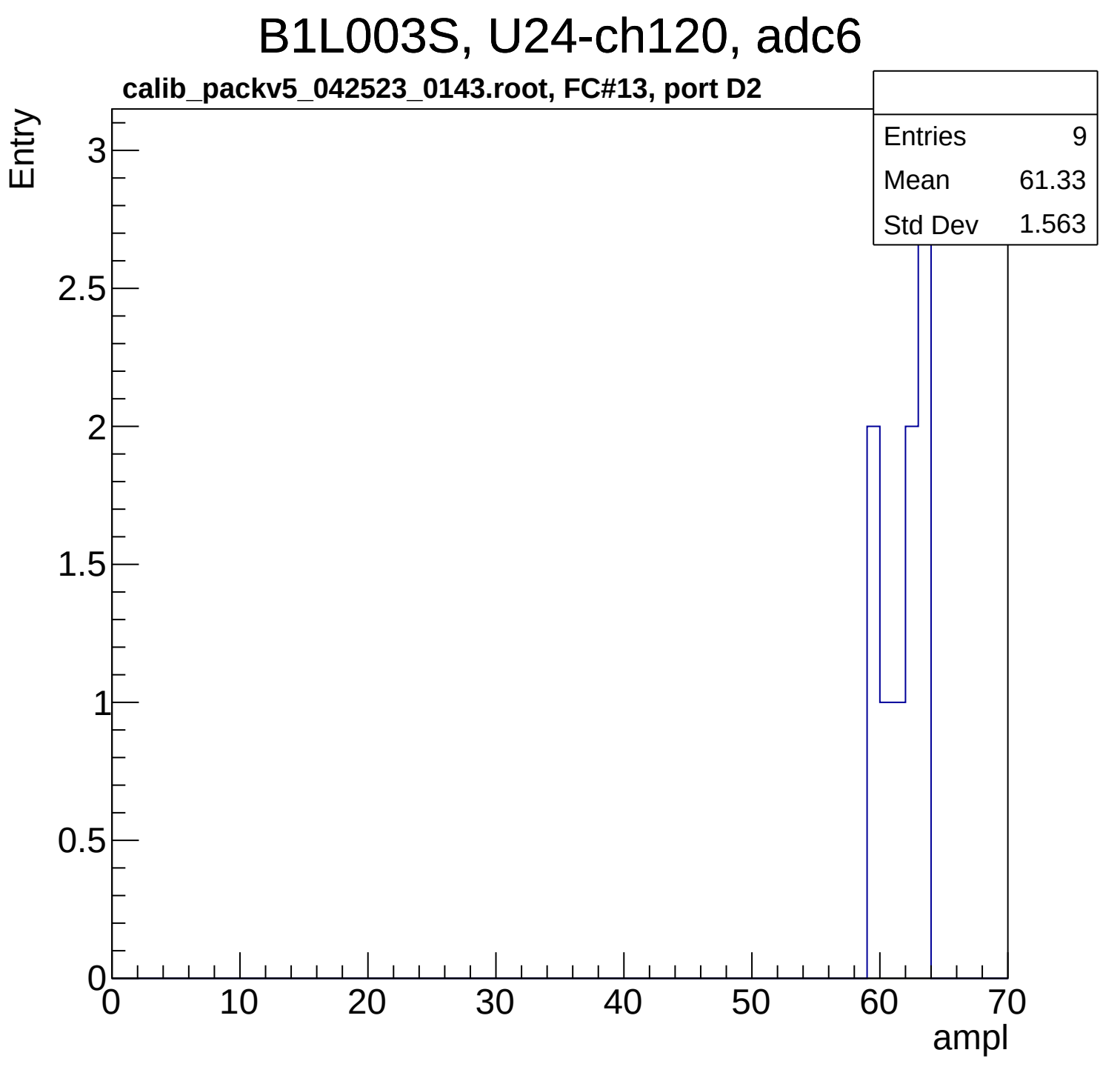
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch120, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.33
Std Dev 1.563
Entry
ampl
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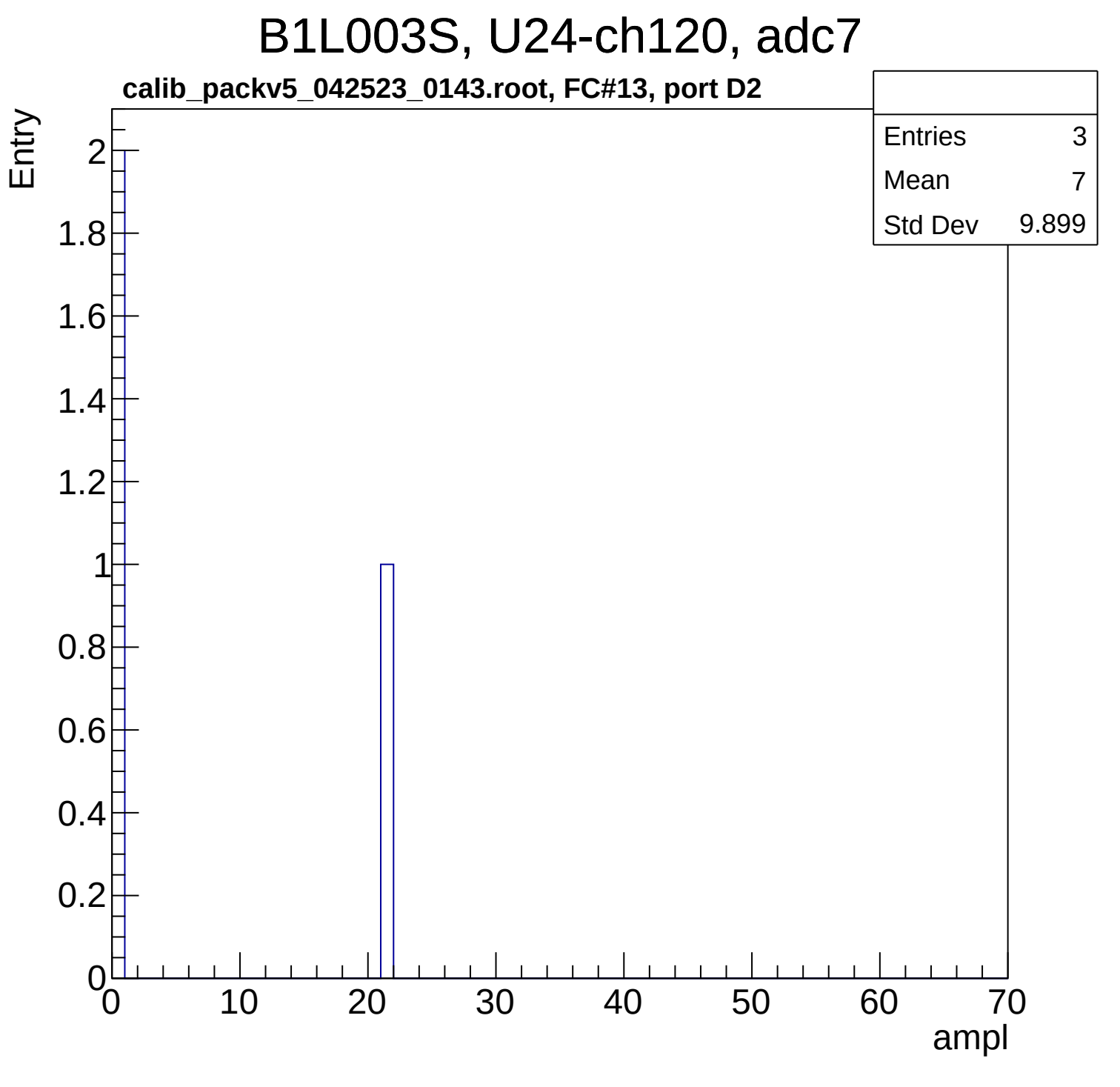
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U24-ch120, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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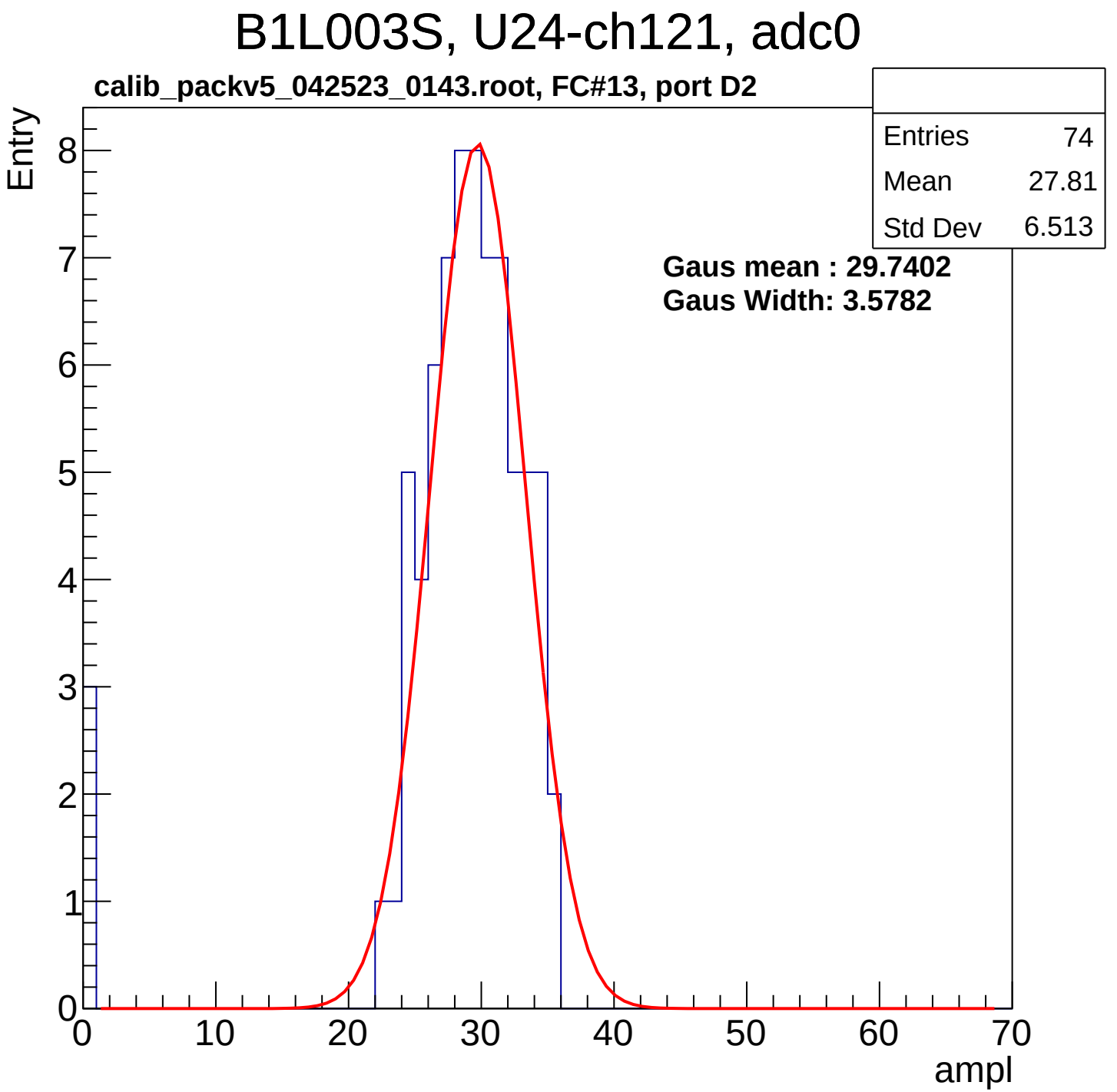
B1L003S, U24-ch121, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 27.81
Std Dev 6.513
Gaus mean : 29.7402
Gaus Width: 3.5782
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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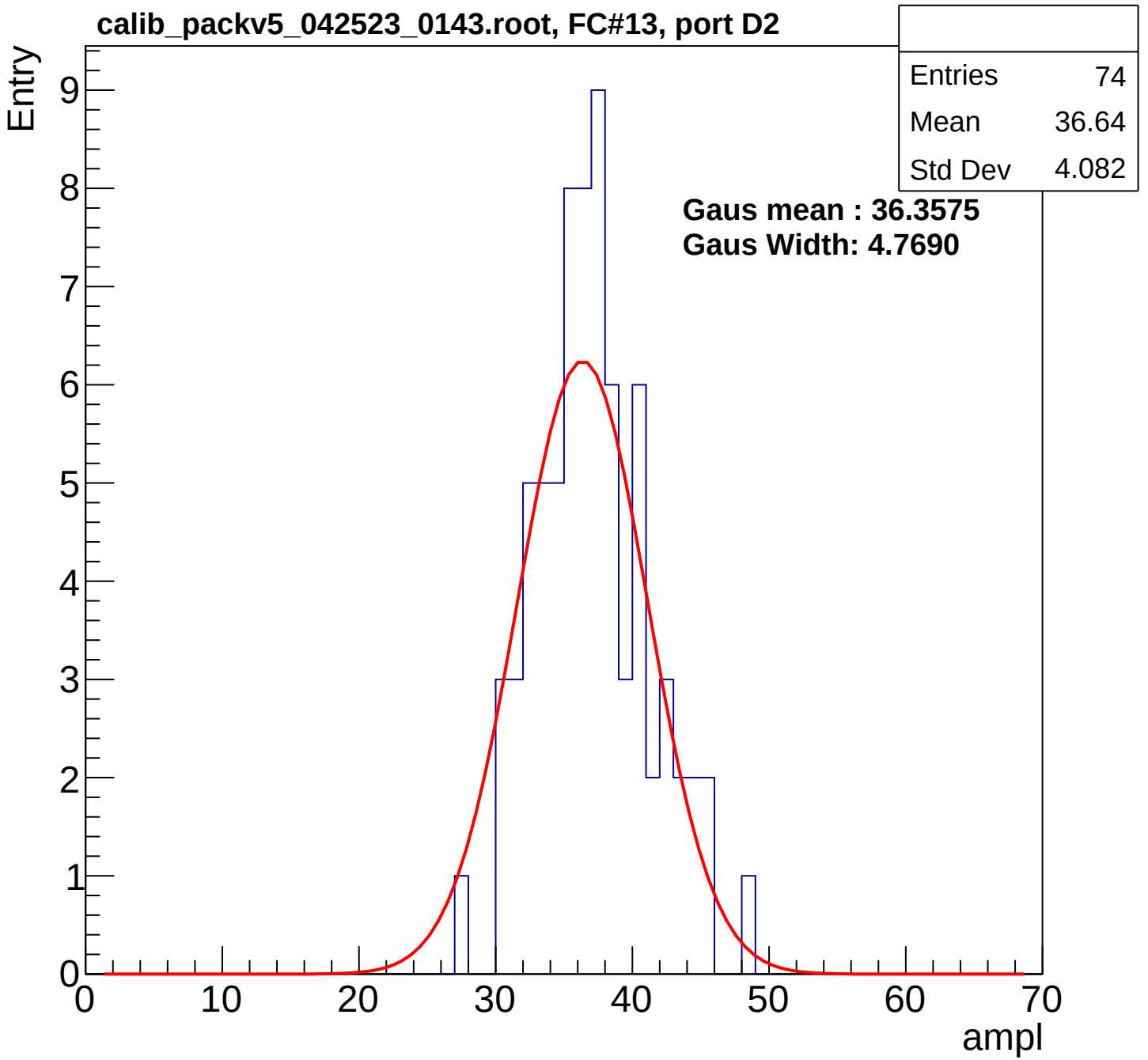
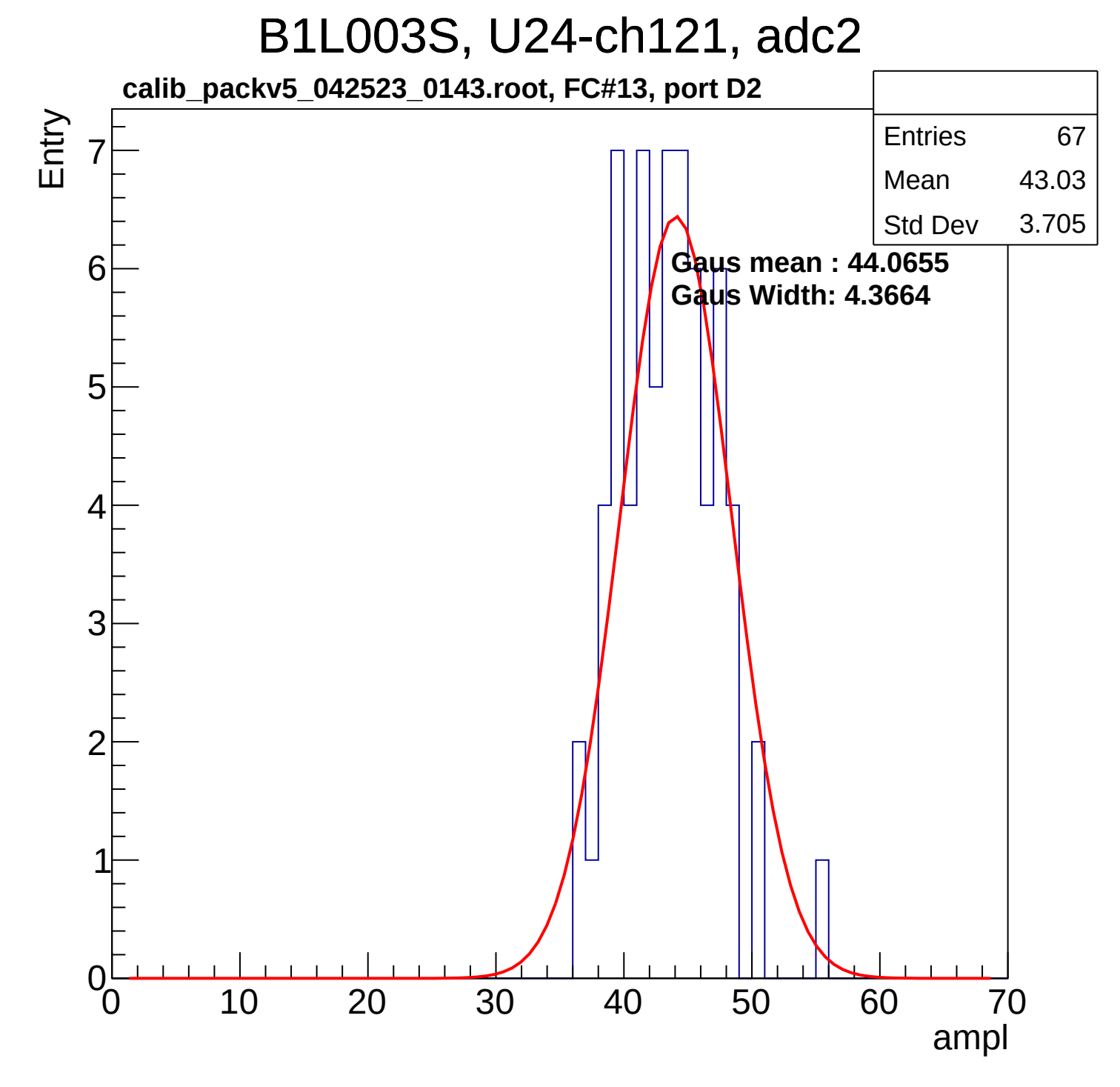

B1L003S, U24-ch121, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 36.64
Std Dev 4.082
Gaus mean : 36.3575
Gaus Width: 4.7690
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch121, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.03
Std Dev 3.705
Gaus mean : 44.0655
Gaus Width: 4.3664
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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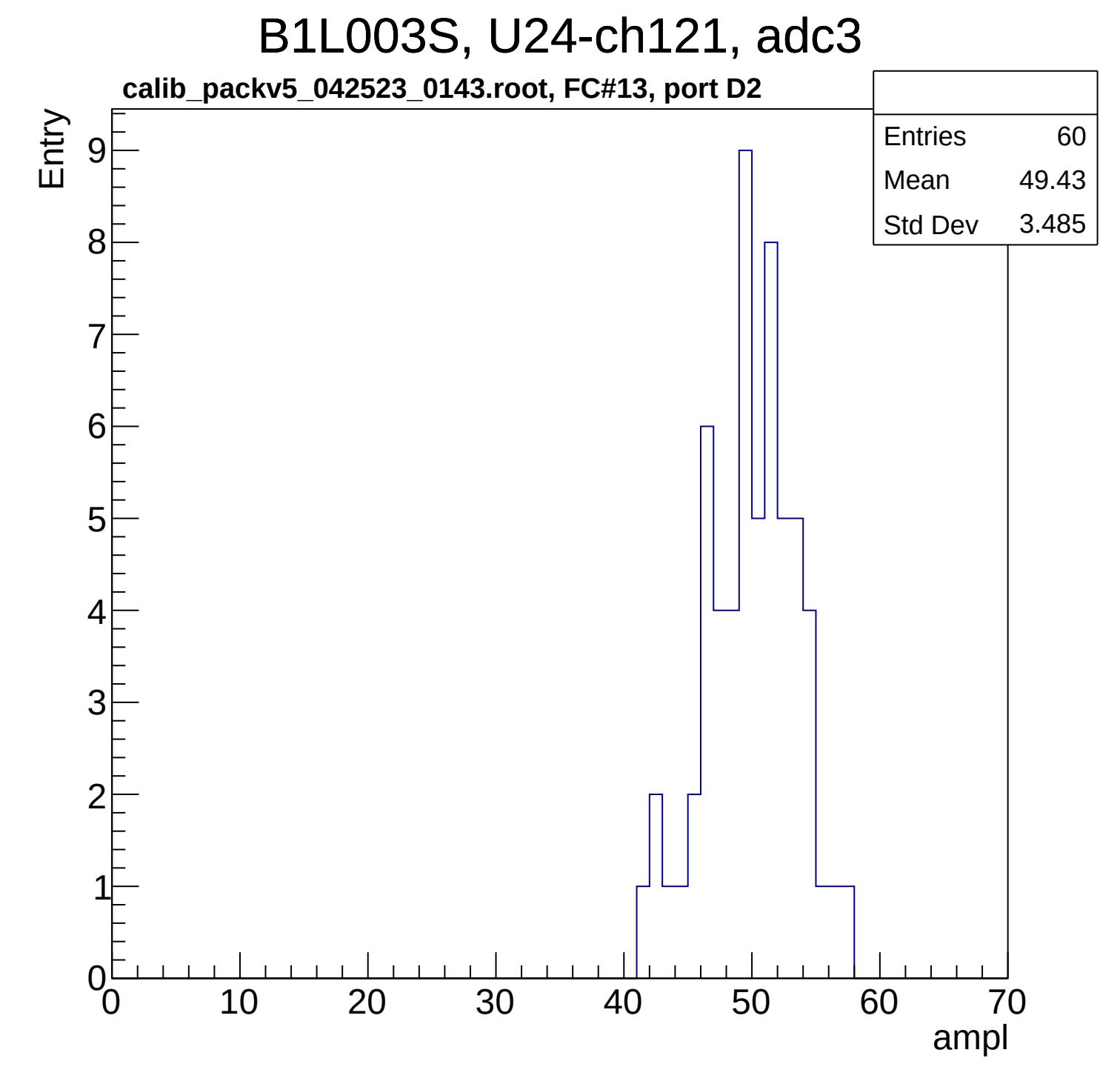
B1L003S, U24-ch121, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.43
Std Dev 3.485
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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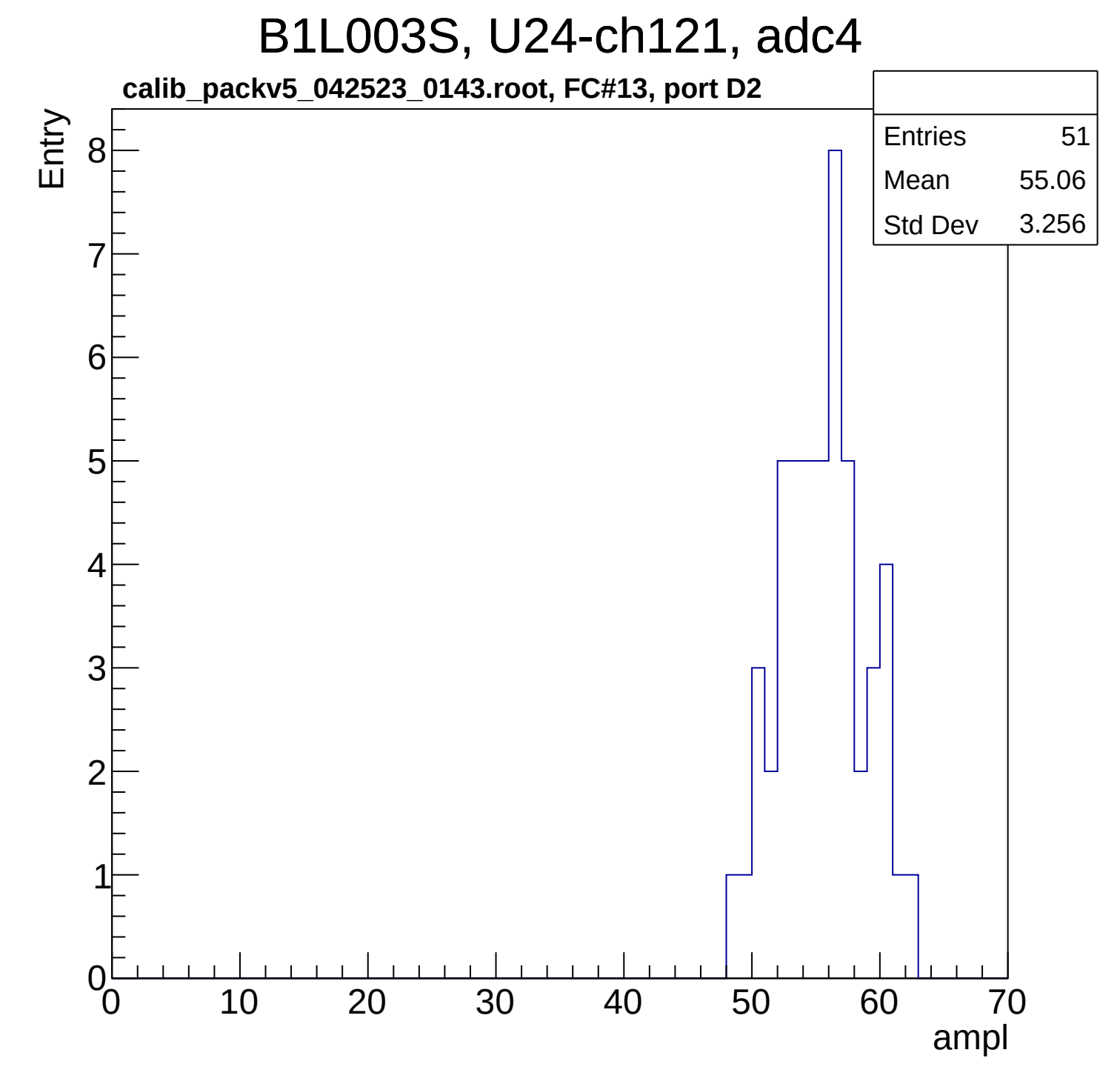
B1L003S, U24-ch121, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 55.06
Std Dev 3.256
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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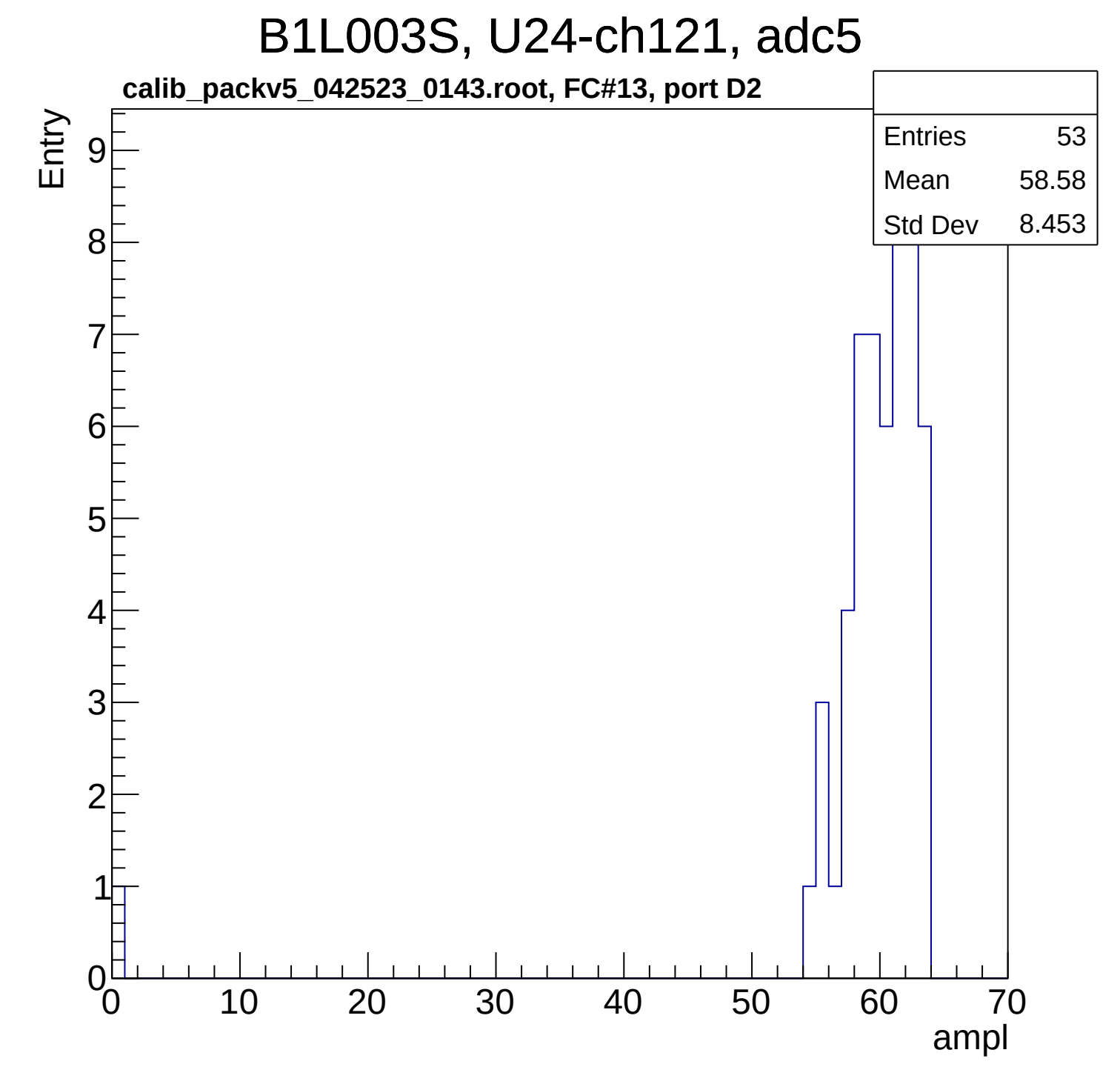
B1L003S, U24-ch121, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.58
Std Dev 8.453
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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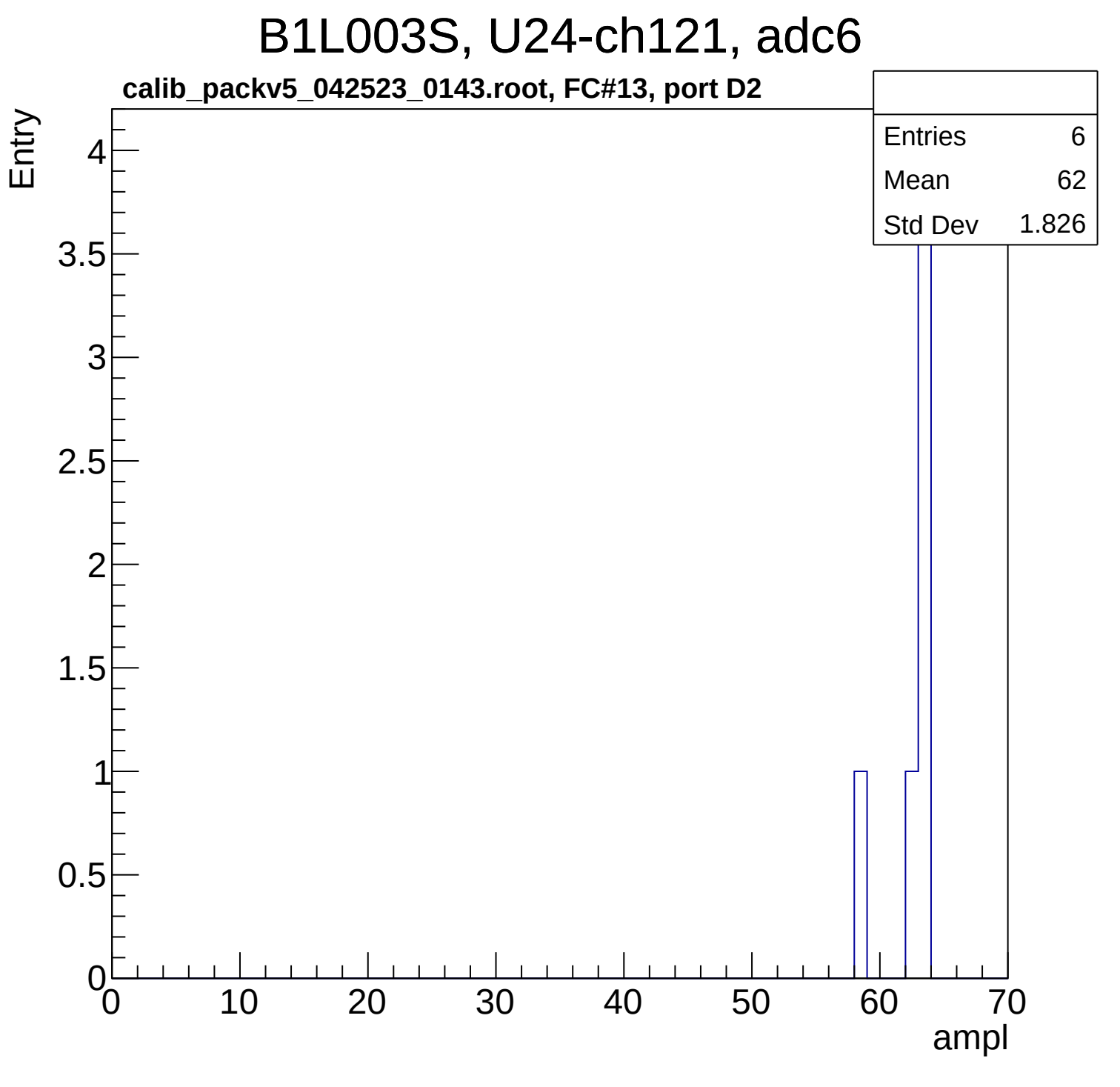
B1L003S, U24-ch121, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62
Std Dev 1.826
Entry
ampl
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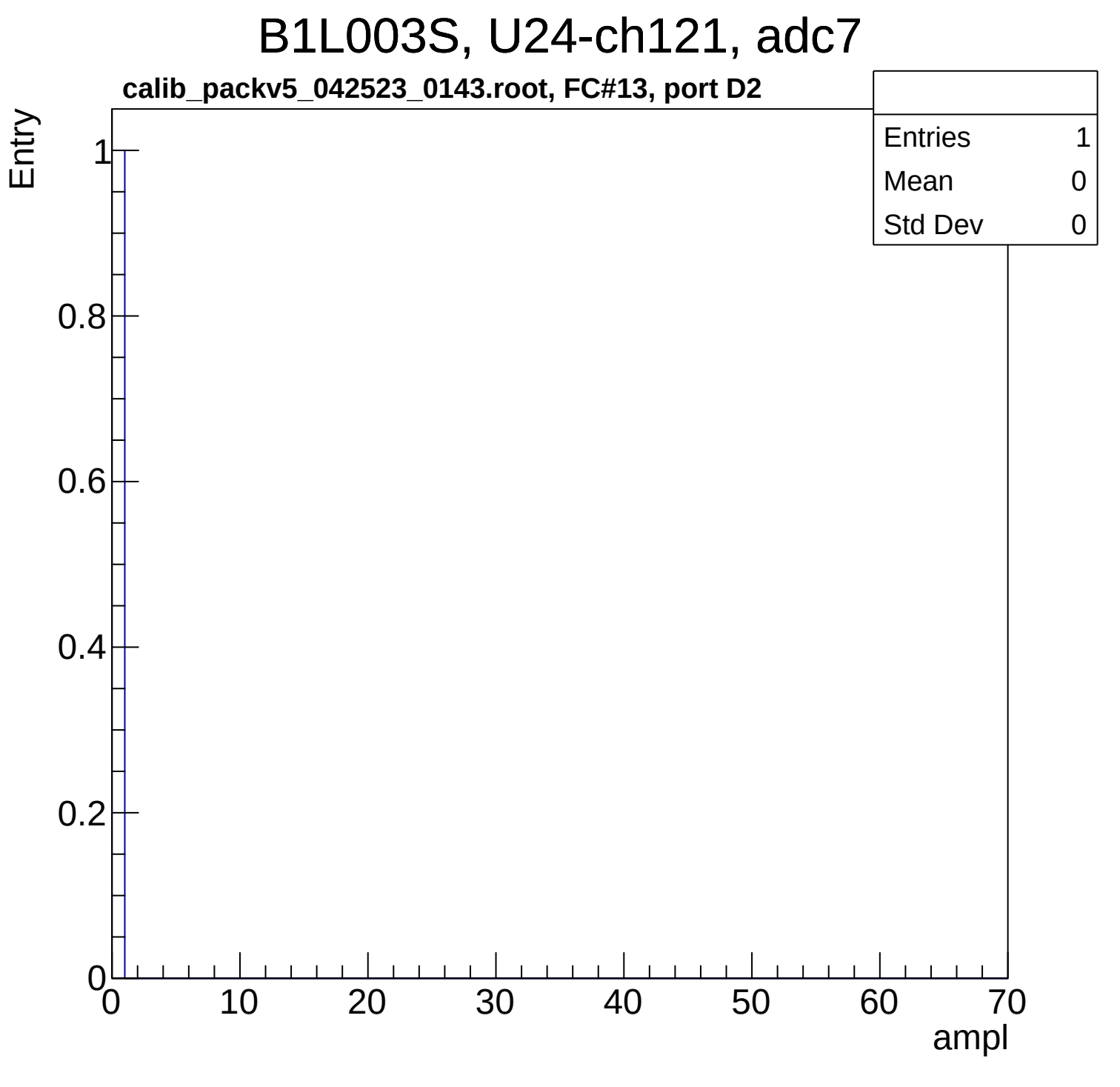
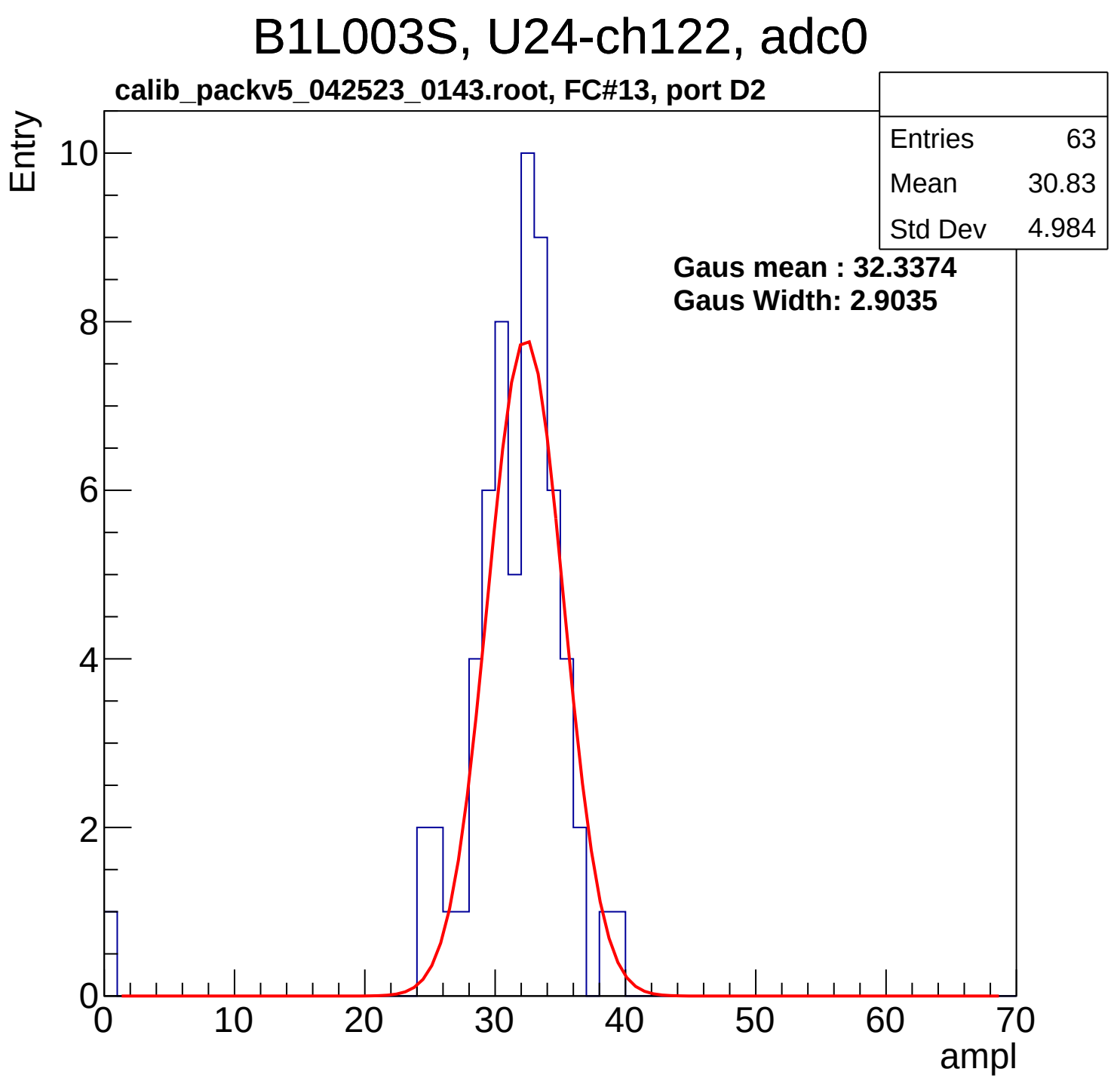
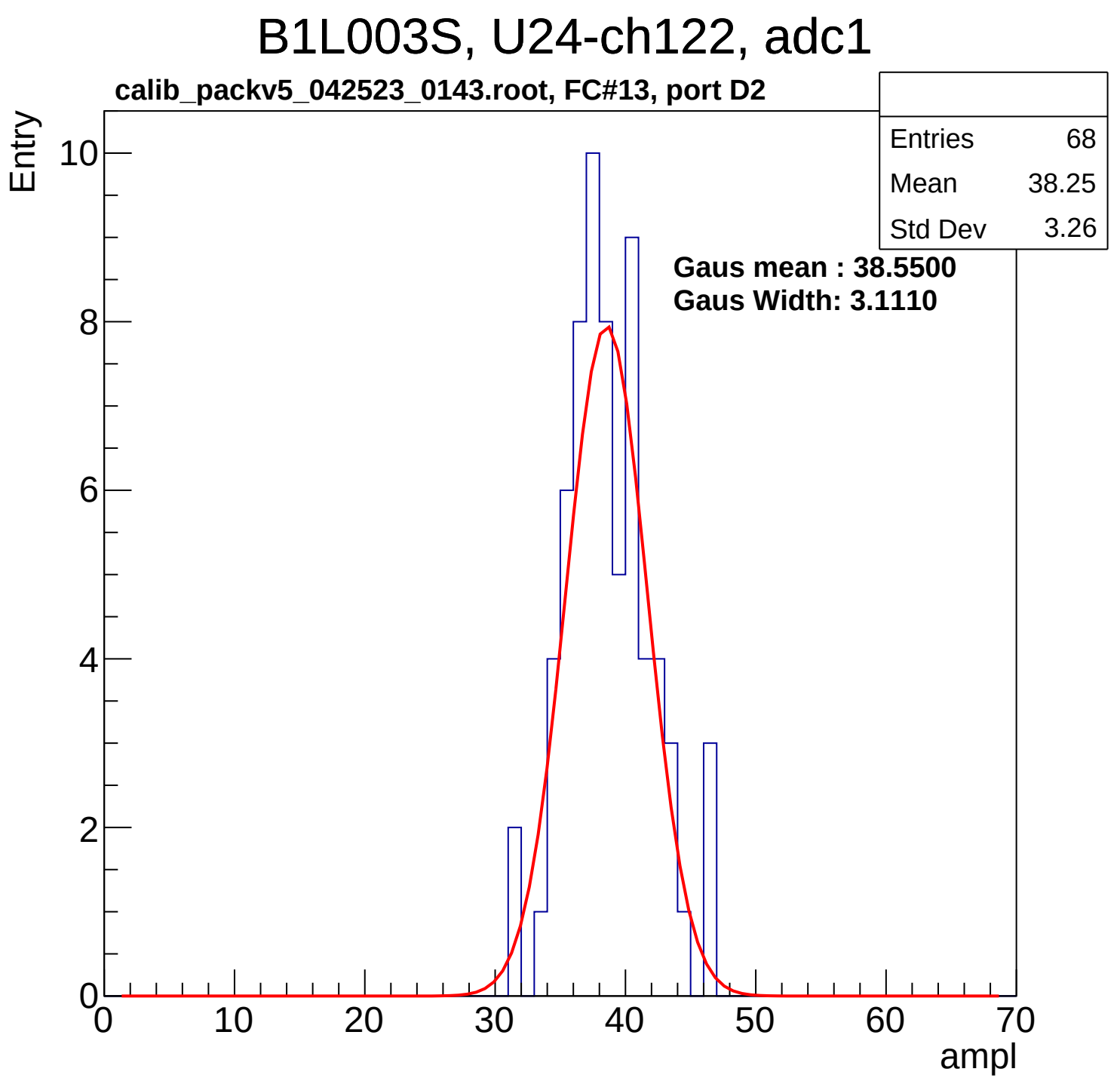
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U24-ch121, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch122, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 30.83
Std Dev 4.984
Gaus mean : 32.3374
Gaus Width: 2.9035
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch122, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 38.25
Std Dev 3.26
Gaus mean : 38.5500
Gaus Width: 3.1110
Entry
ampl
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2
4
6
8
10
0
10
20
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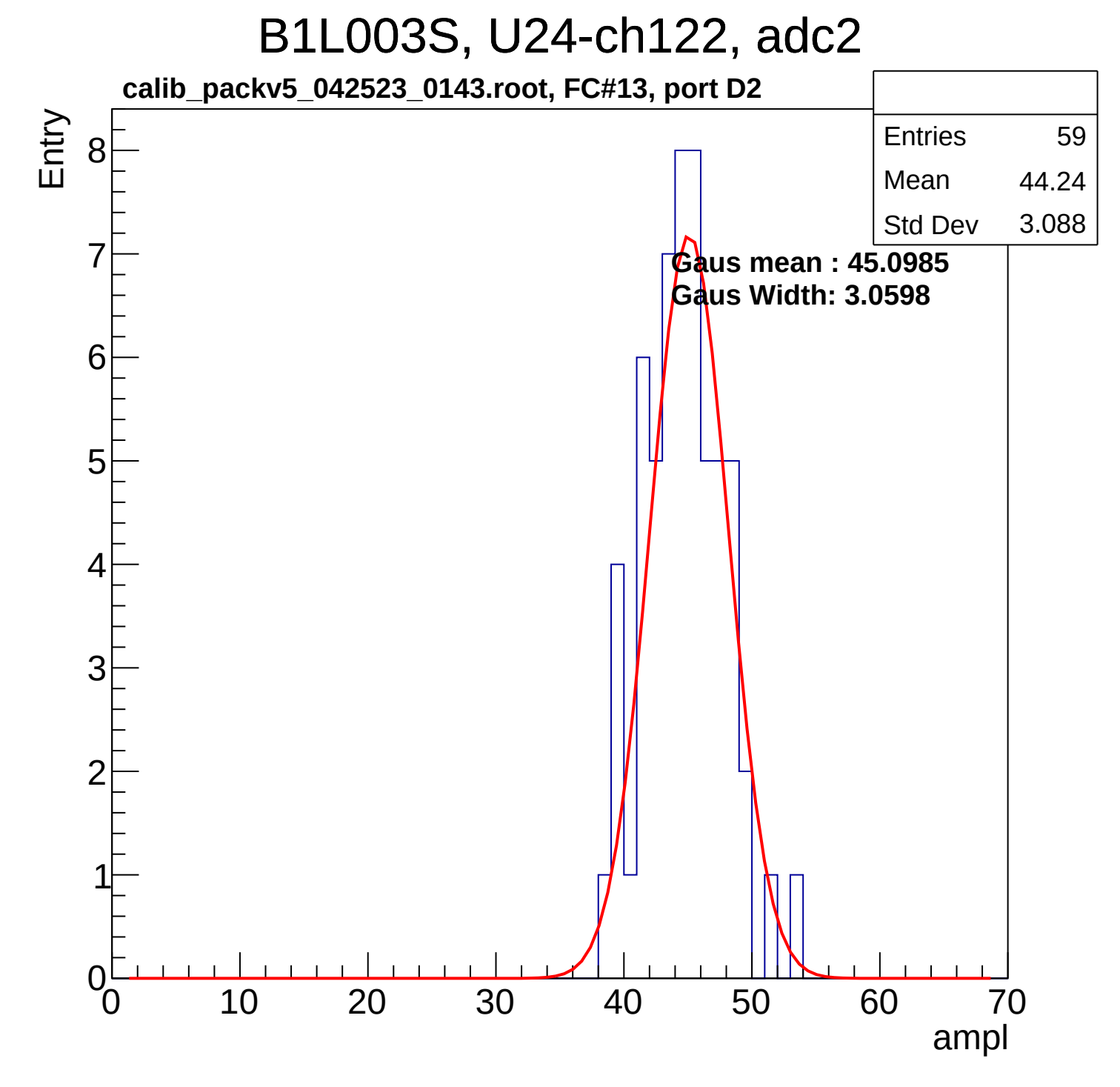
40
50
60
70


B1L003S, U24-ch122, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 44.24
Std Dev 3.088
Gaus mean : 45.0985
Gaus Width: 3.0598
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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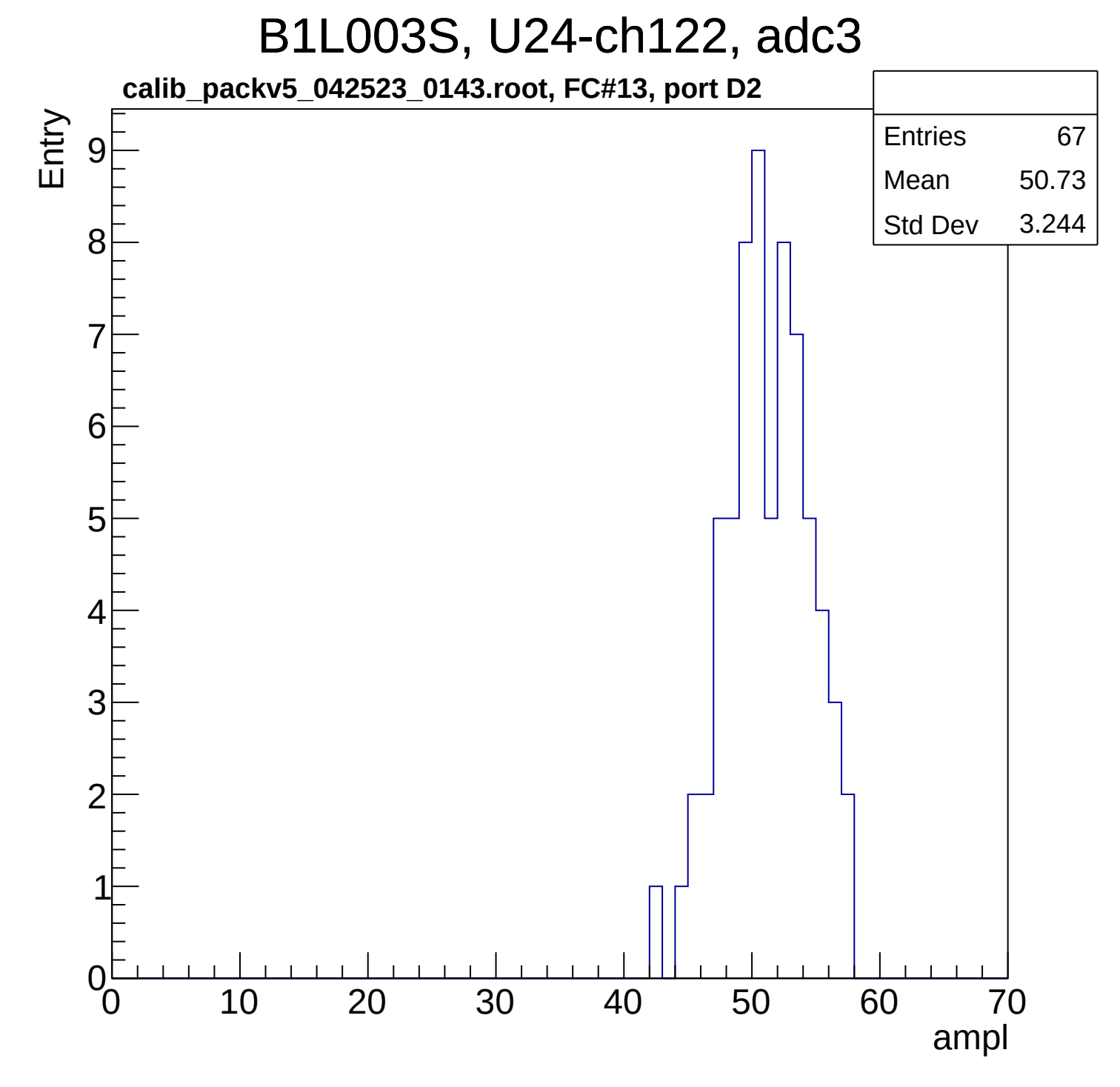
B1L003S, U24-ch122, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 50.73
Std Dev 3.244
Entry
ampl
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3
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6
7
8
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0
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40
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60
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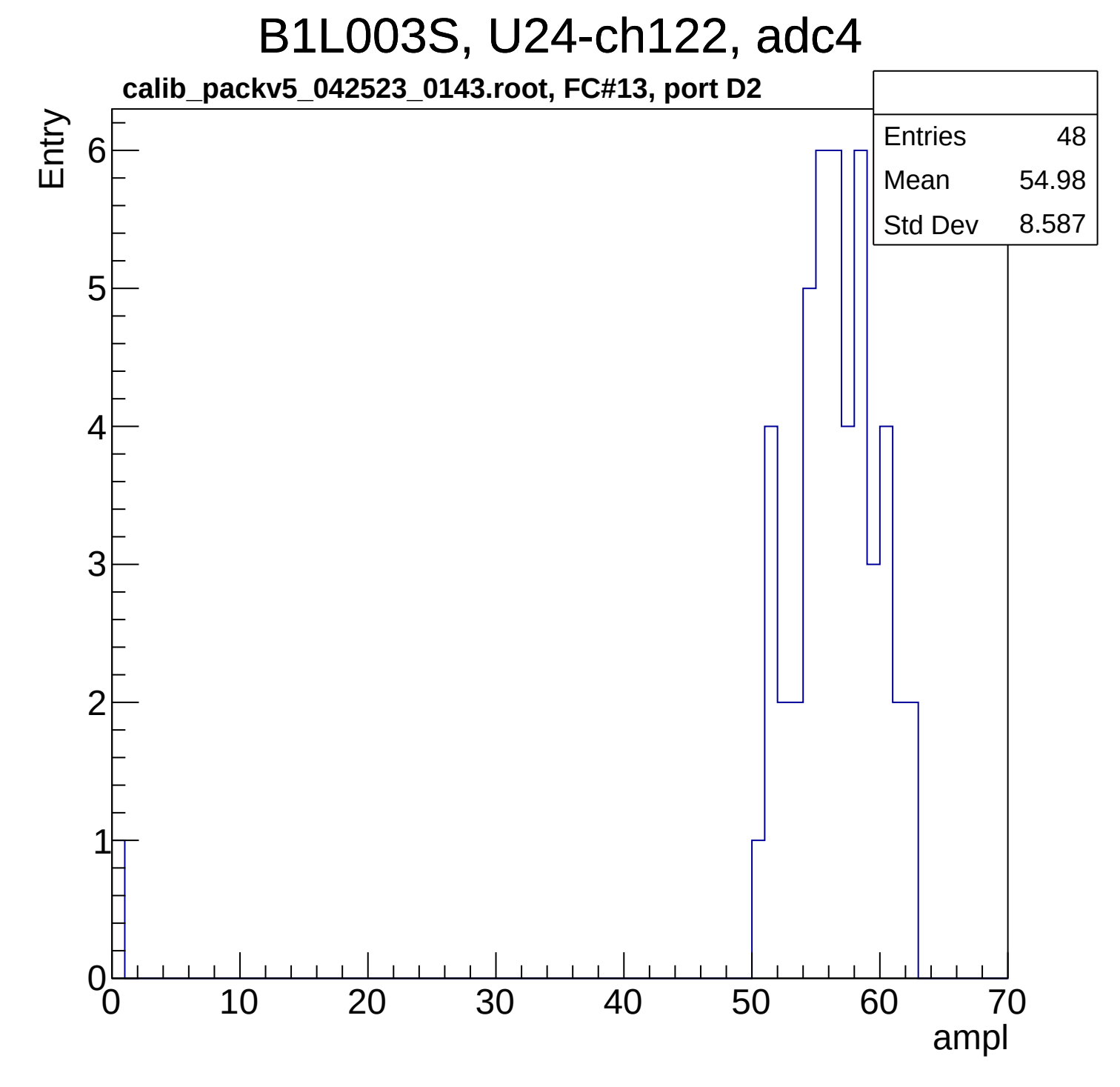
B1L003S, U24-ch122, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 54.98
Std Dev 8.587
Entry
ampl
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3
4
5
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0
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20
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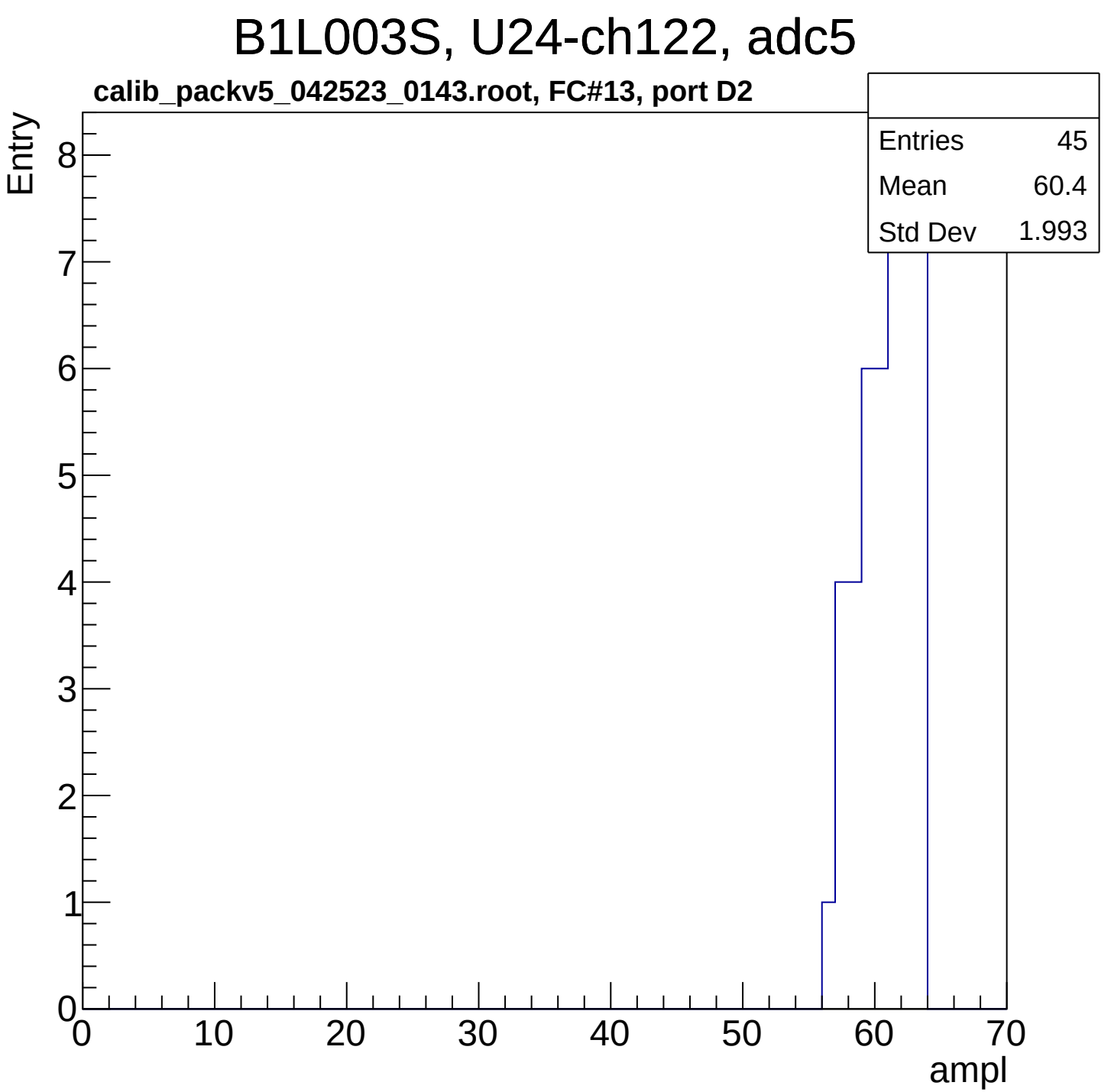
40
50
60
70


B1L003S, U24-ch122, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 60.4
Std Dev 1.993
Entry
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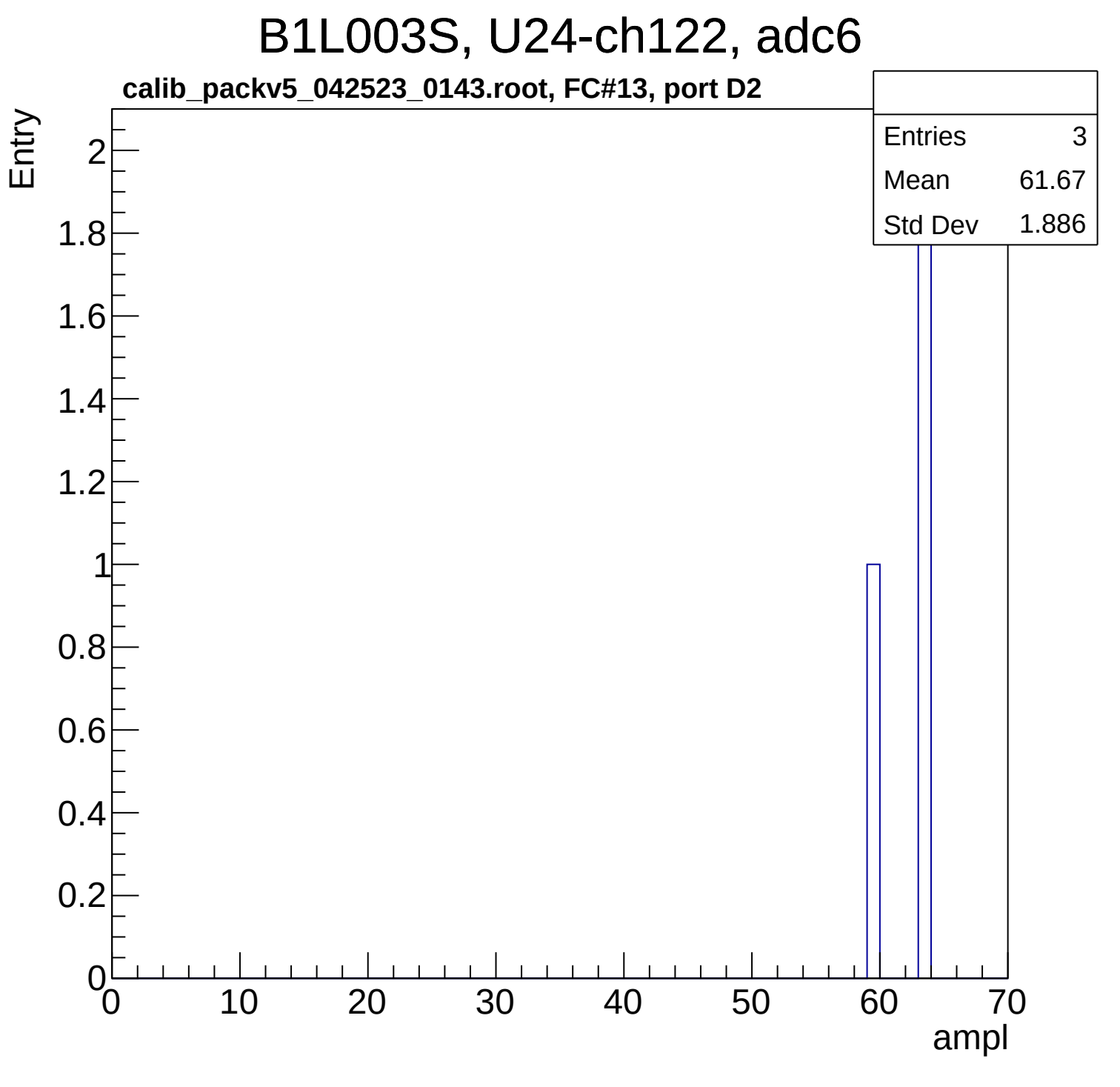
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch122, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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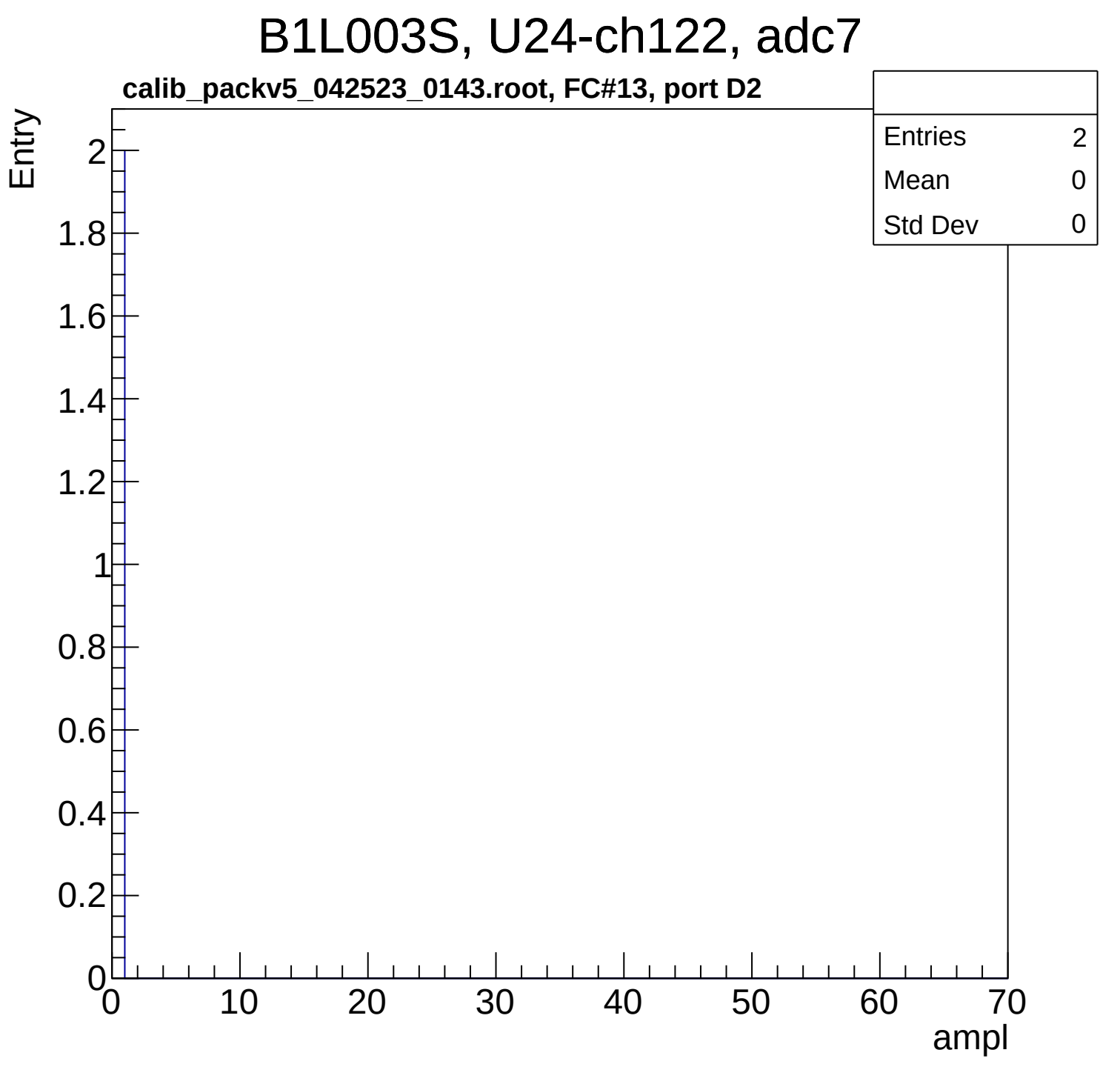
B1L003S, U24-ch122, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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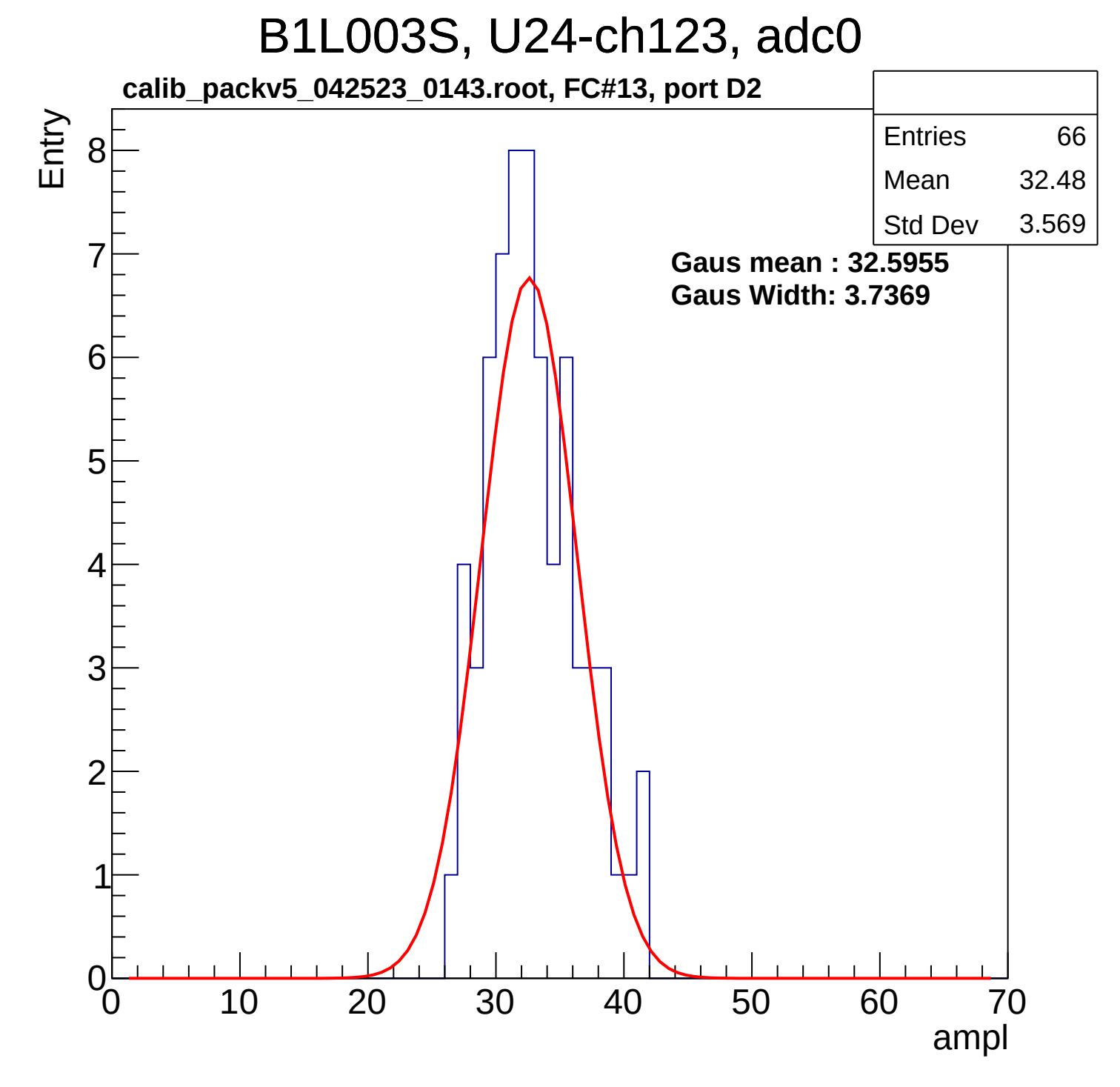
B1L003S, U24-ch123, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 32.48
Std Dev 3.569
Gaus mean : 32.5955
Gaus Width: 3.7369
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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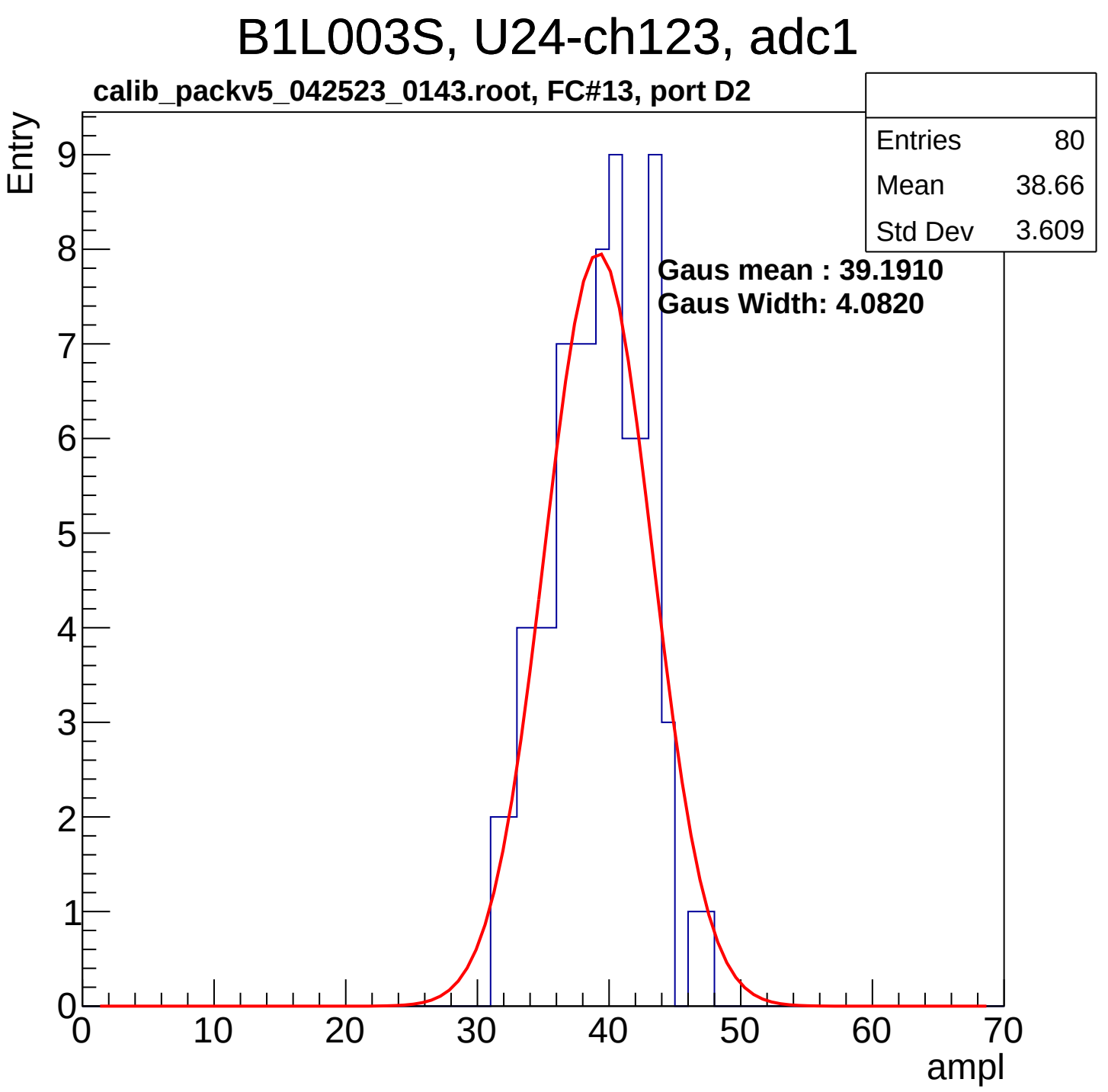
B1L003S, U24-ch123, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 38.66
Std Dev 3.609
Gaus mean : 39.1910
Gaus Width: 4.0820
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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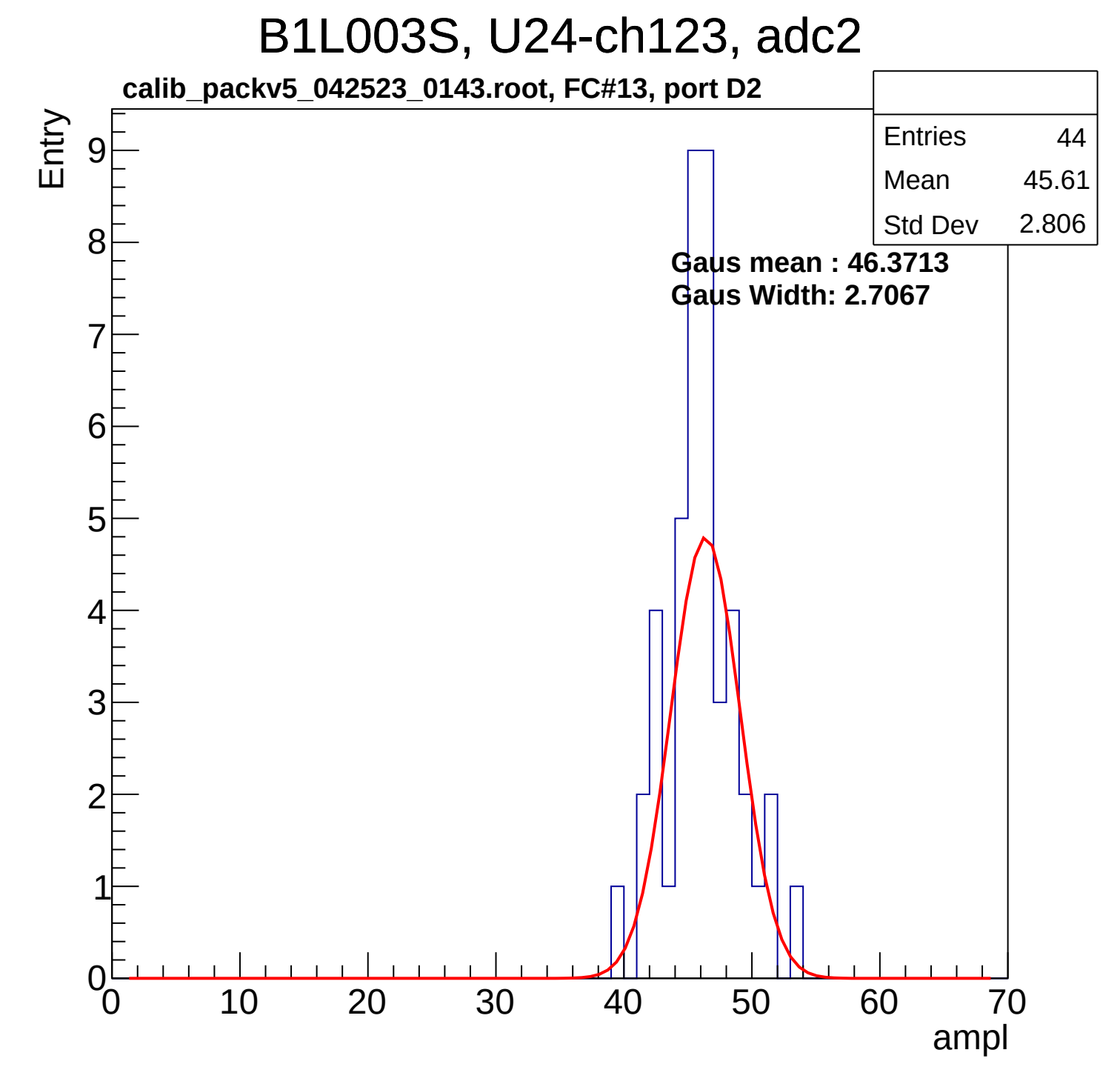
B1L003S, U24-ch123, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 45.61
Std Dev 2.806
Gaus mean : 46.3713
Gaus Width: 2.7067
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
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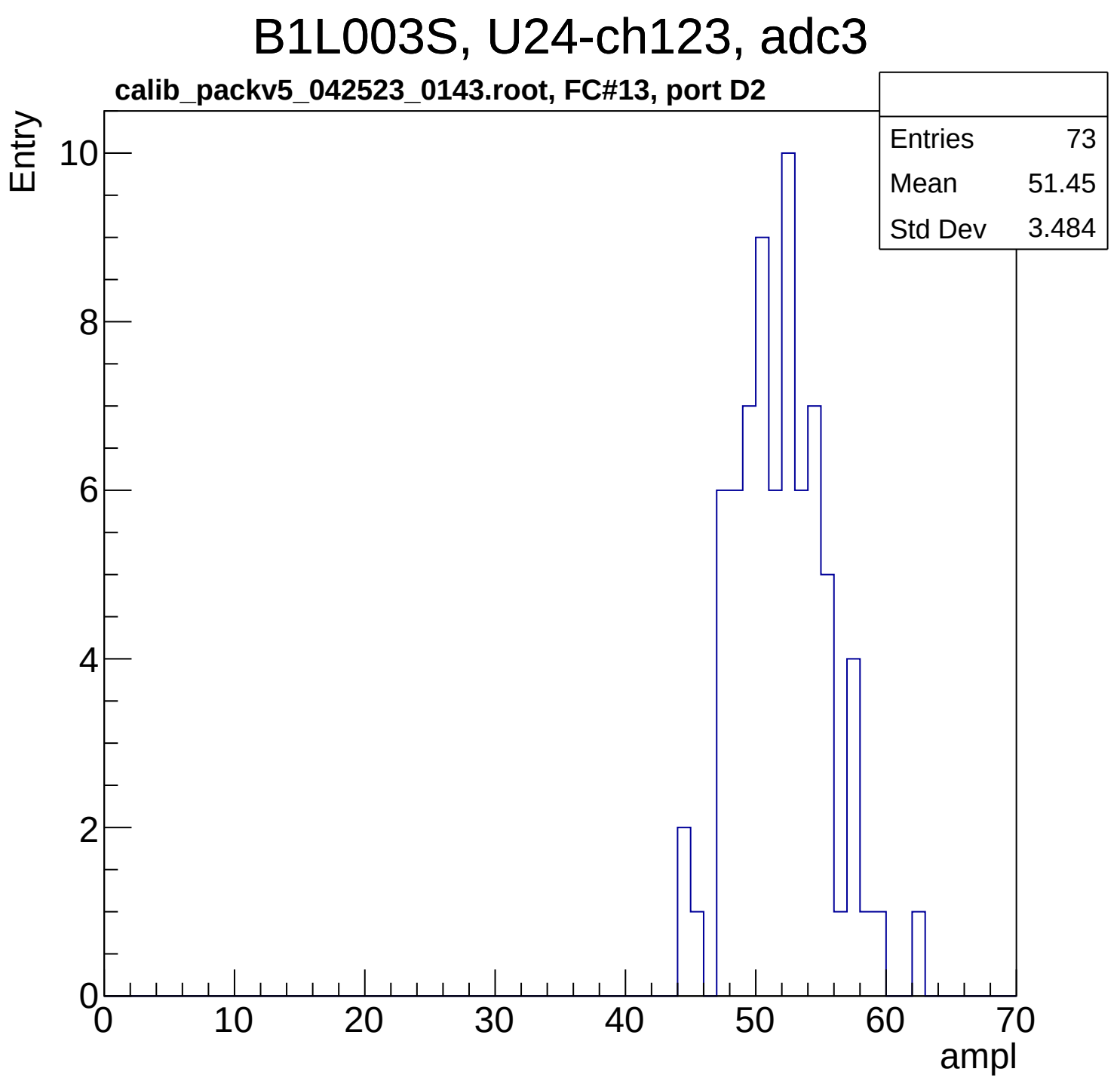
B1L003S, U24-ch123, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 51.45
Std Dev 3.484
Entry
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10
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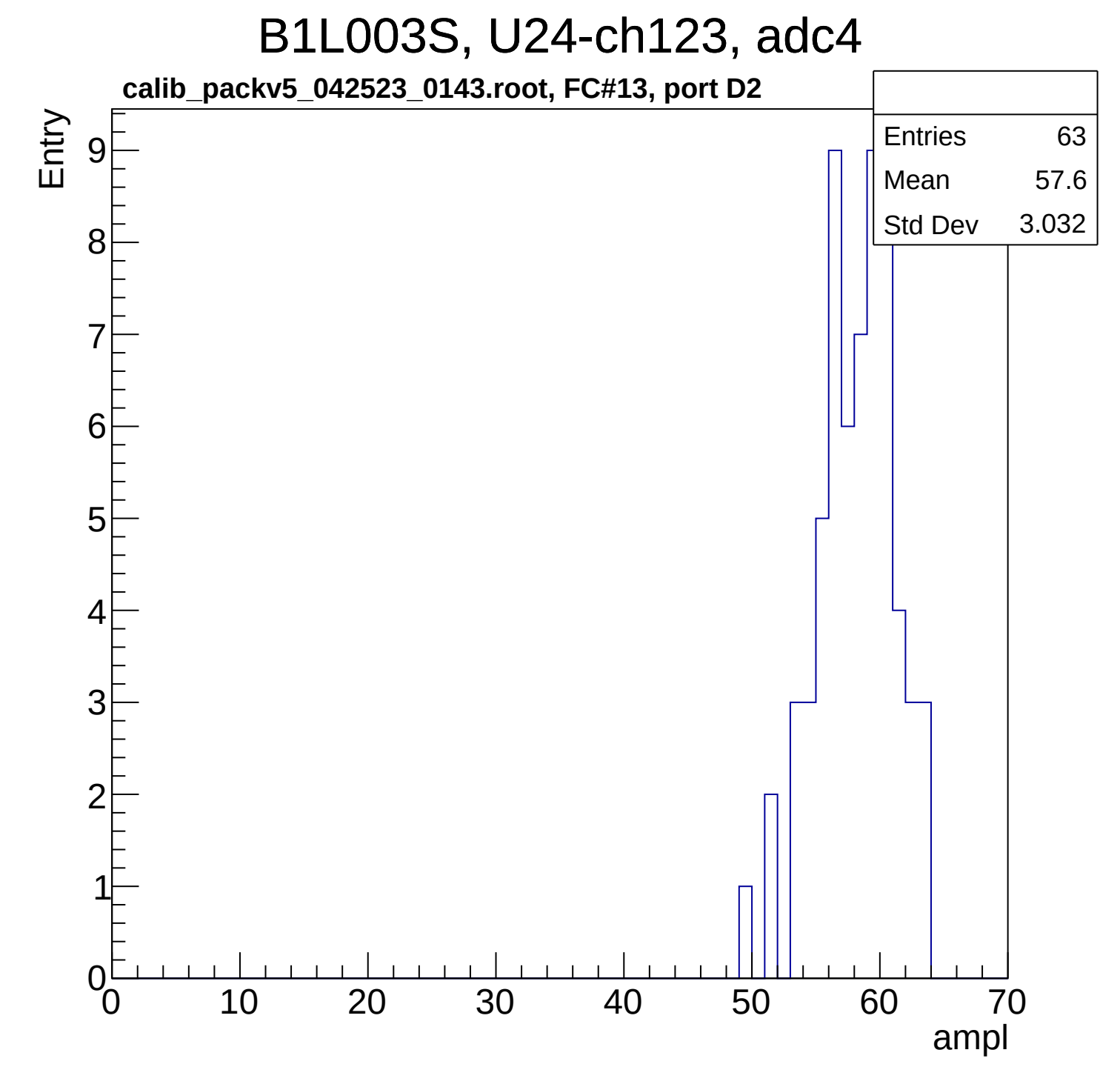
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch123, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 57.6
Std Dev 3.032
Entry
ampl
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6
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8
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0
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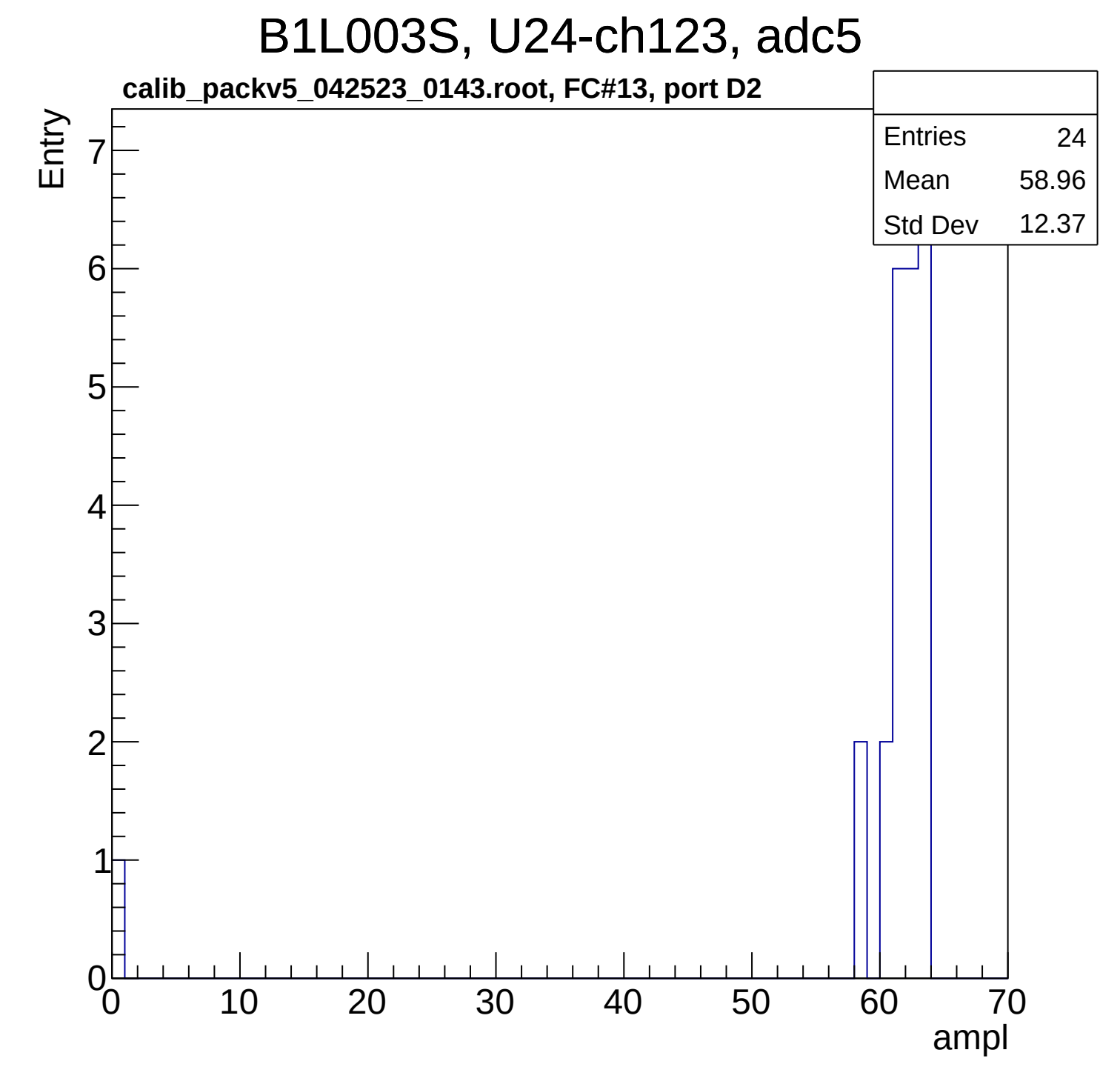
B1L003S, U24-ch123, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 58.96
Std Dev 12.37
Entry
ampl
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2
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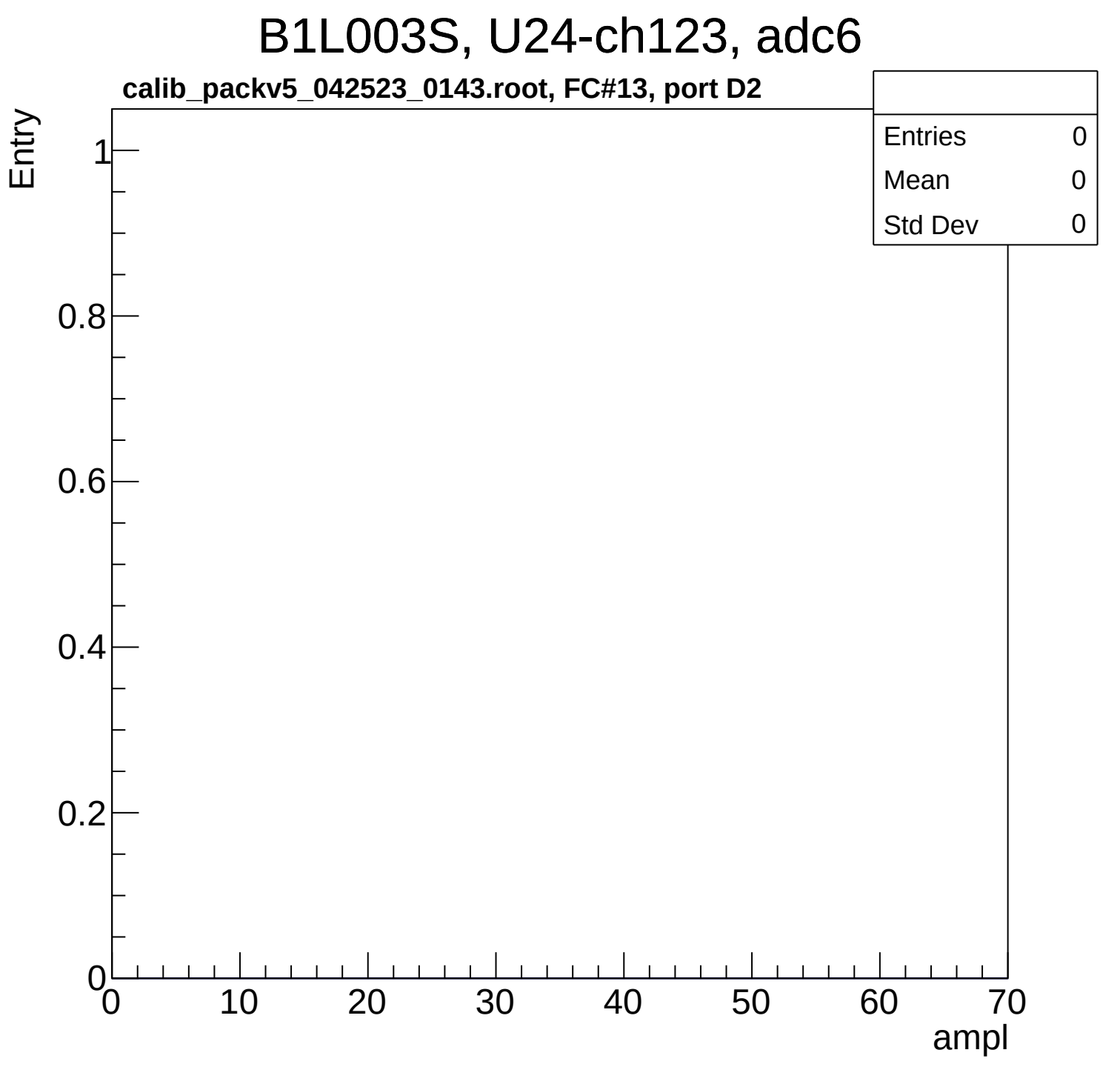
B1L003S, U24-ch123, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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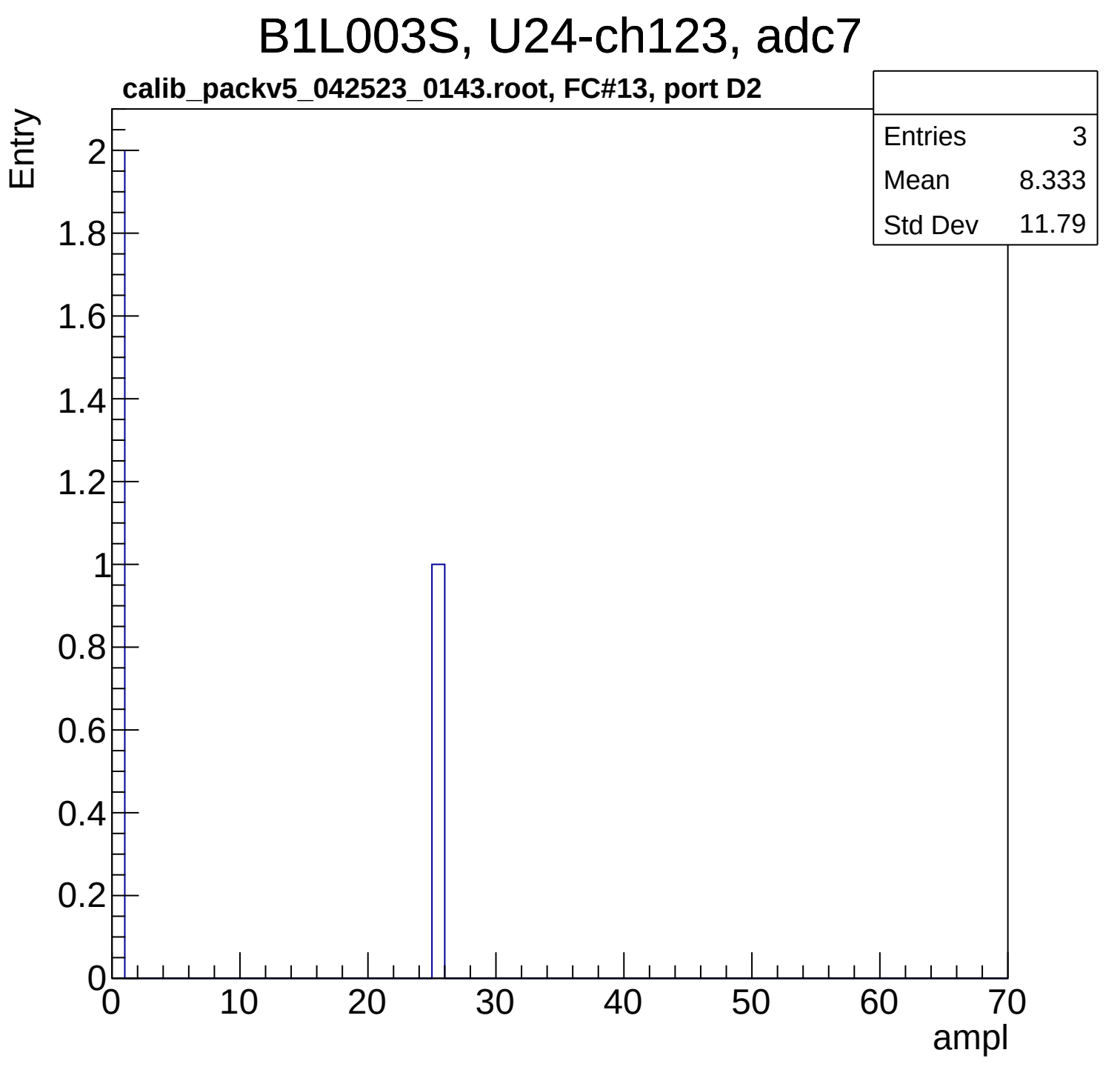
50
60
70
ampl


B1L003S, U24-ch123, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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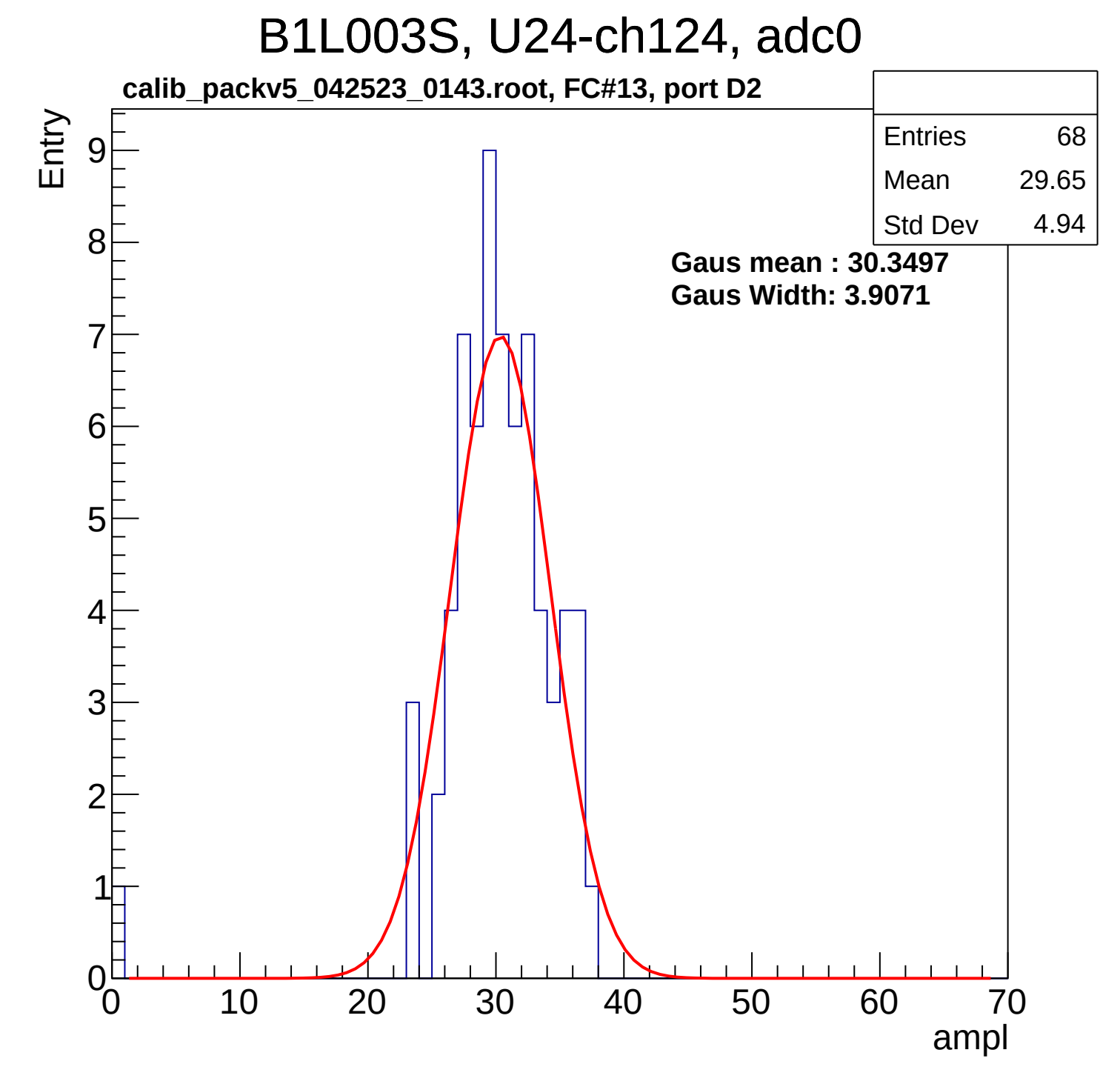
B1L003S, U24-ch124, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.65
Std Dev 4.94
Gaus mean : 30.3497
Gaus Width: 3.9071
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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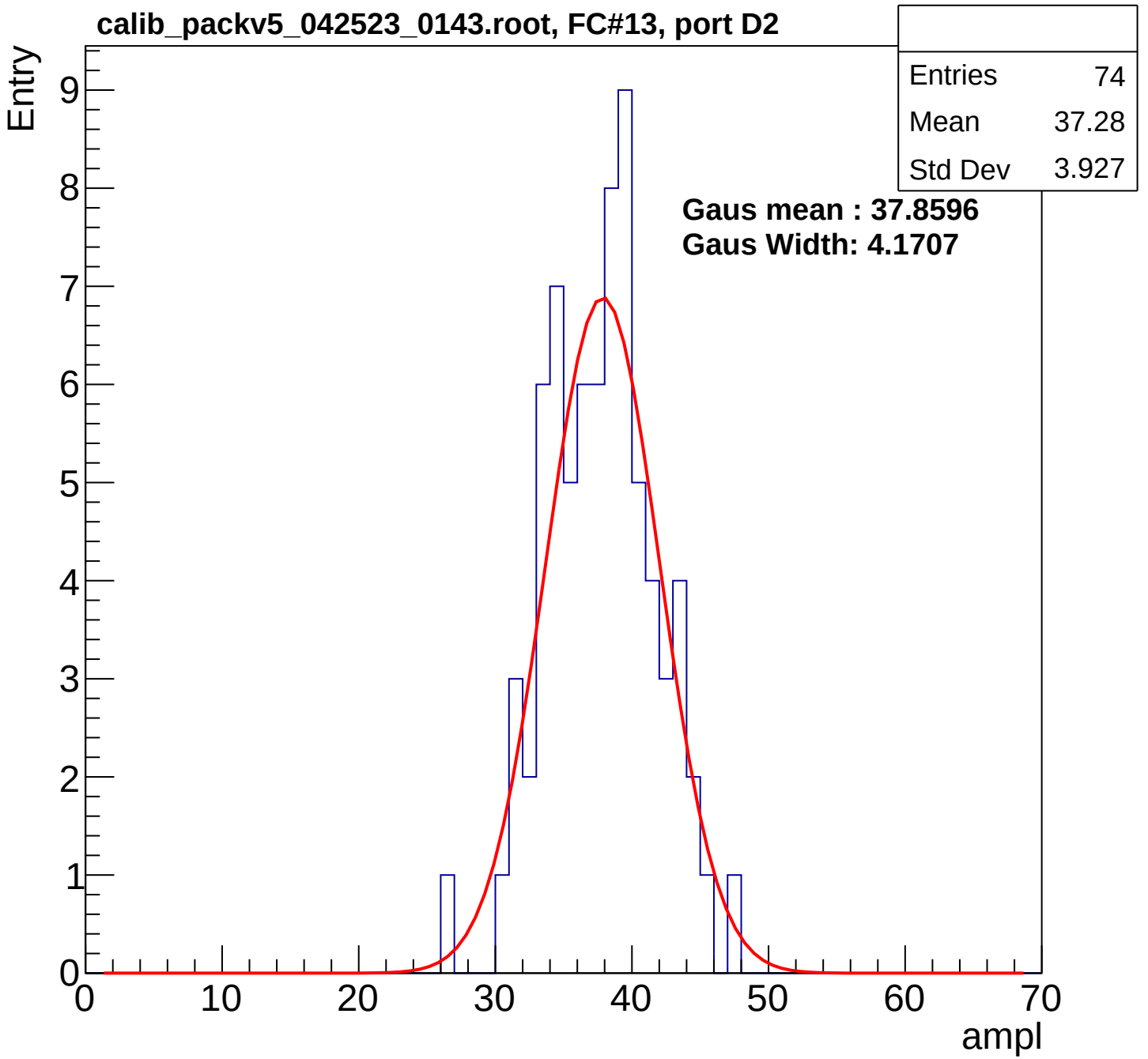
B1L003S, U24-ch124, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 37.28
Std Dev 3.927
Gaus mean : 37.8596
Gaus Width: 4.1707
Entry
ampl
0
10
20
30
40
50
60
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1
2
3
4
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6
7
8
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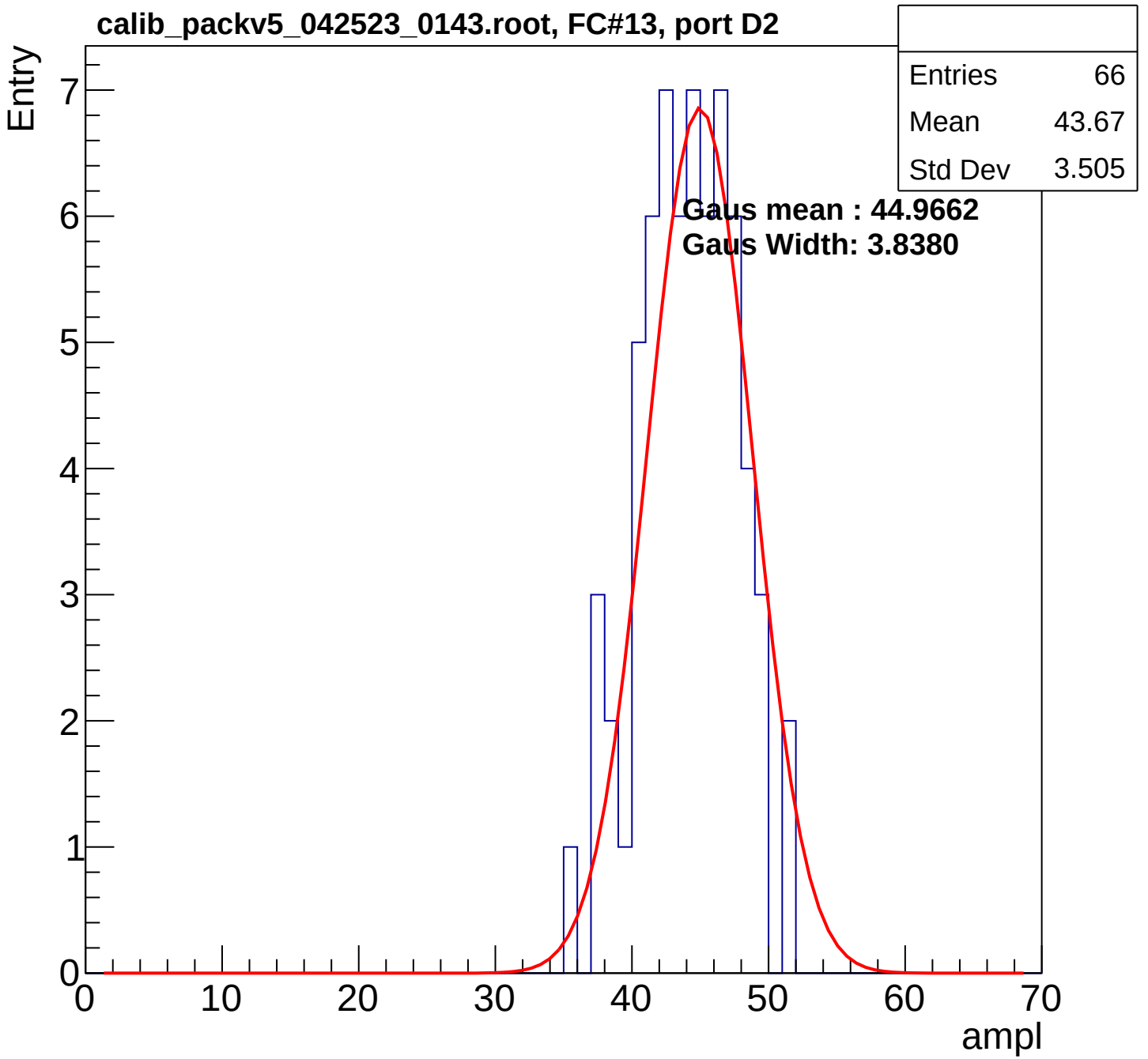
B1L003S, U24-ch124, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.67
Std Dev 3.505
Gaus mean : 44.9662
Gaus Width: 3.8380
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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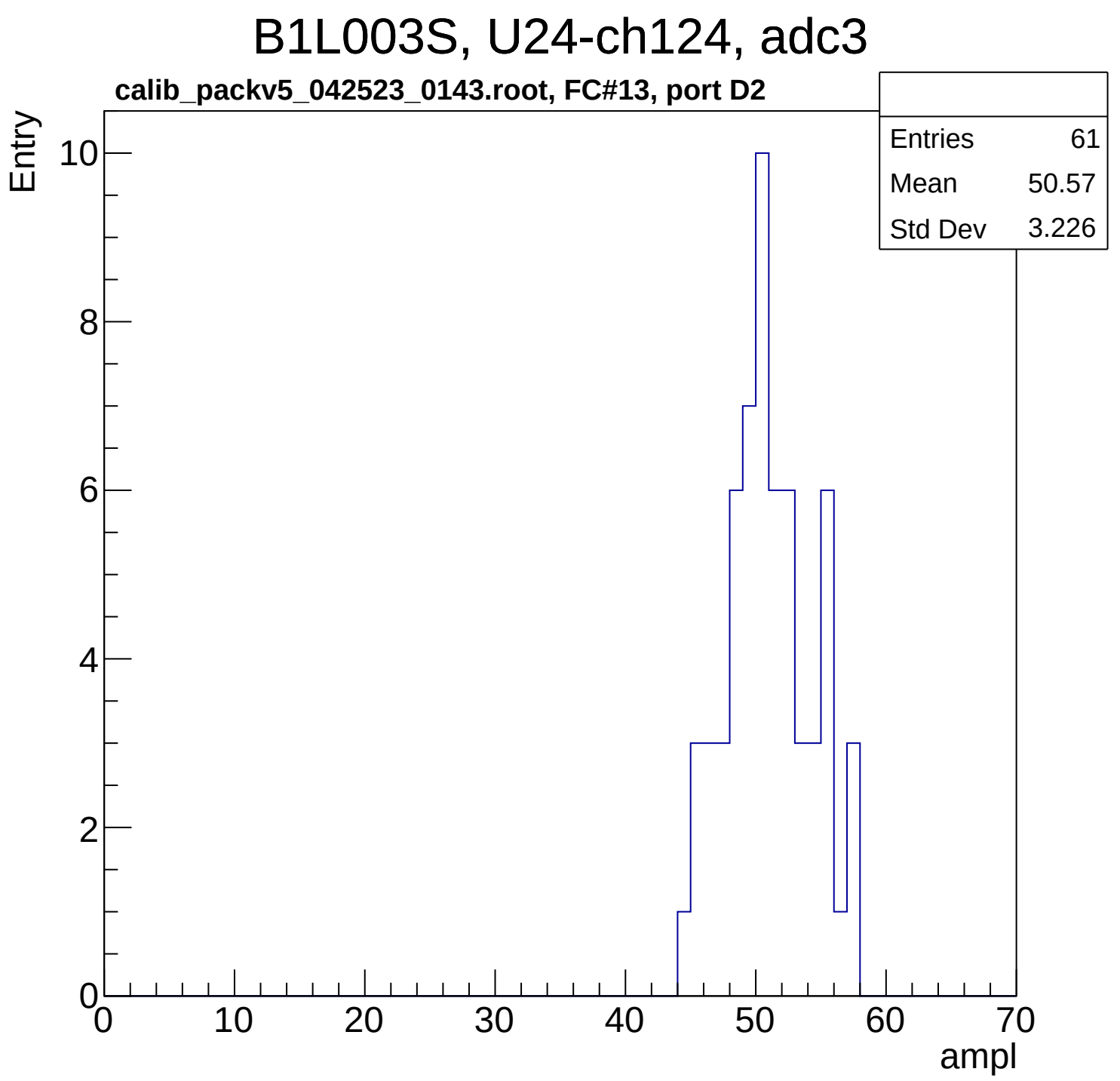
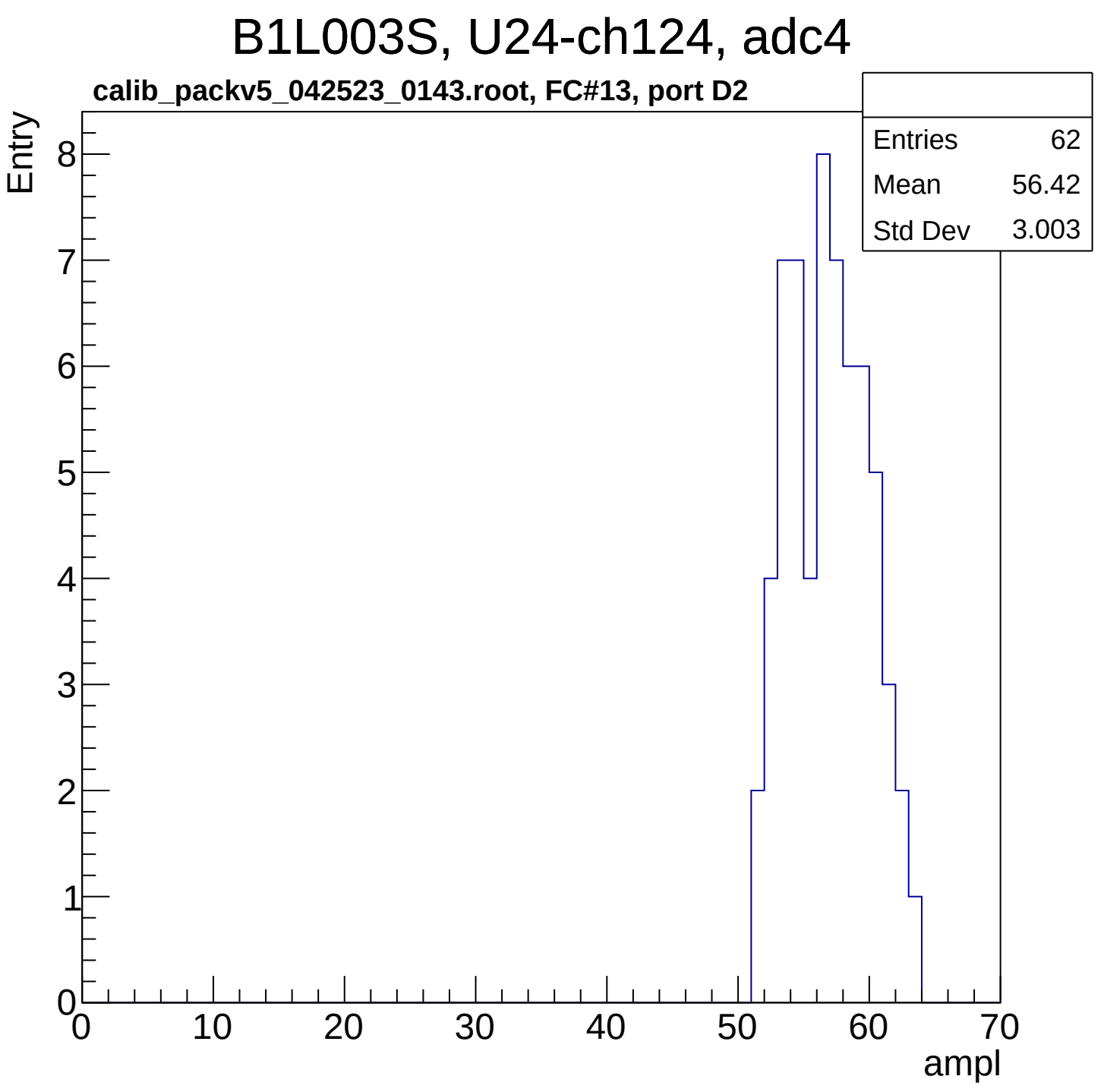
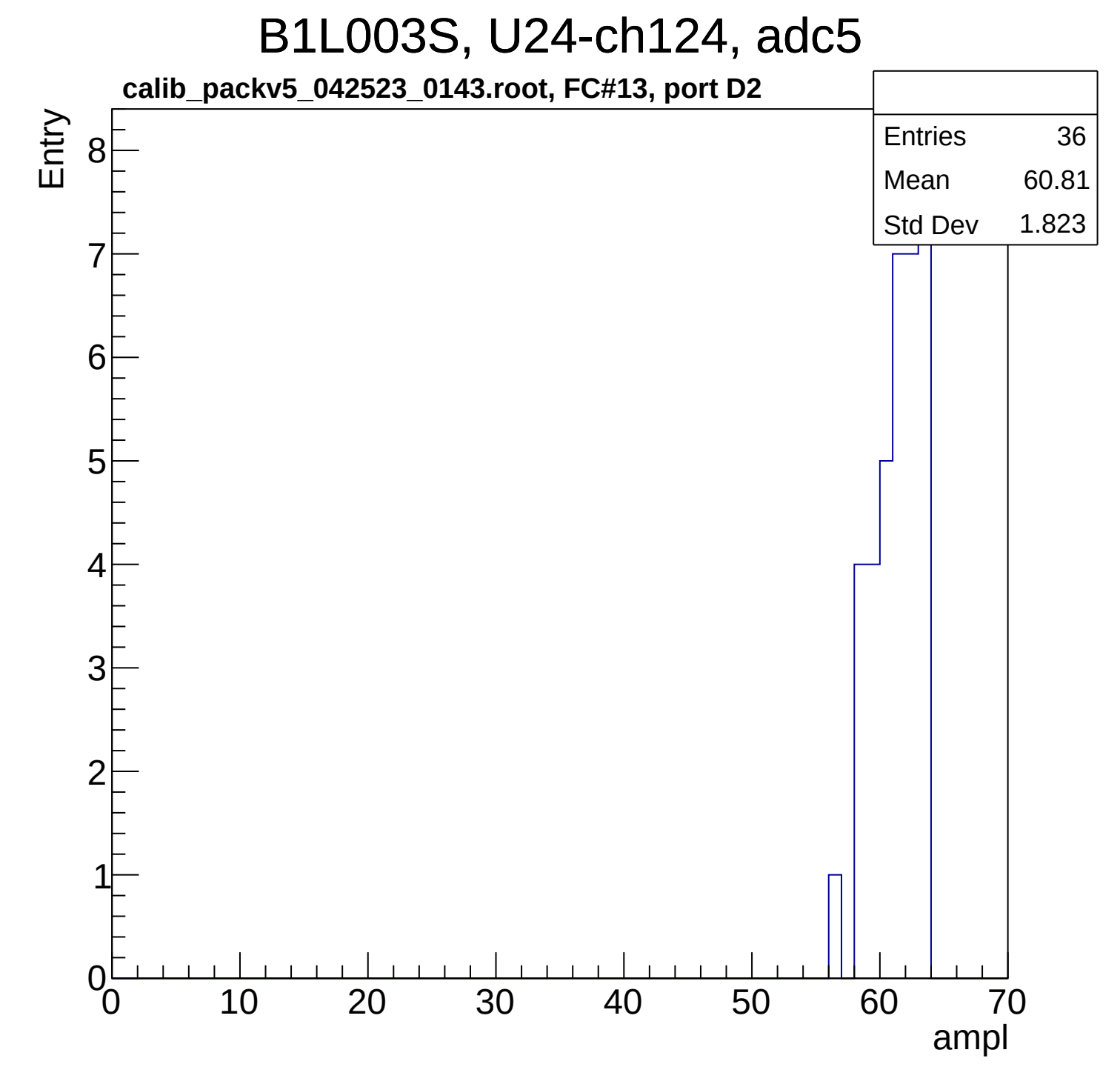

B1L003S, U24-ch124, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.57
Std Dev 3.226
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch124, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 56.42
Std Dev 3.003
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch124, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 60.81
Std Dev 1.823
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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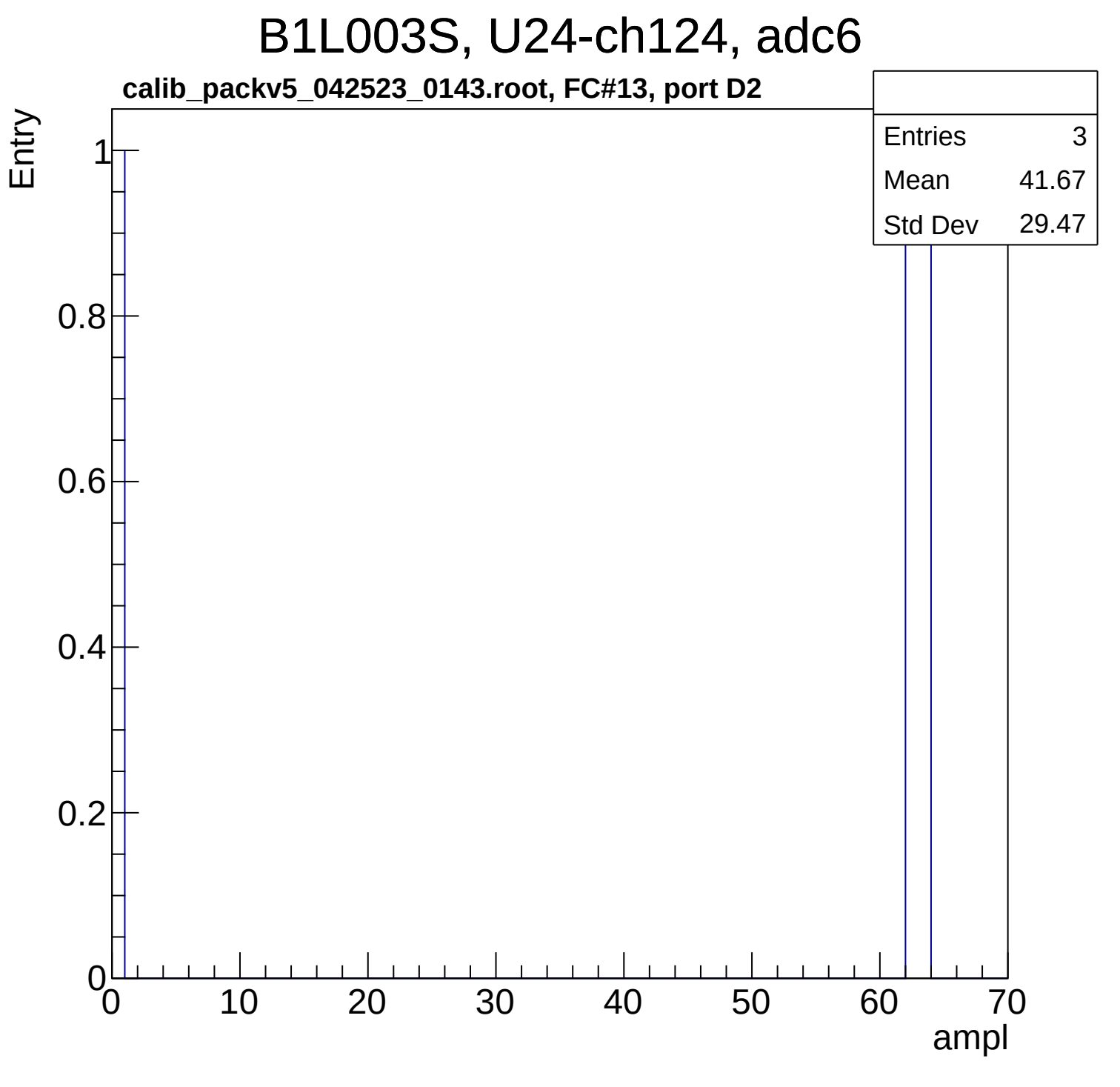
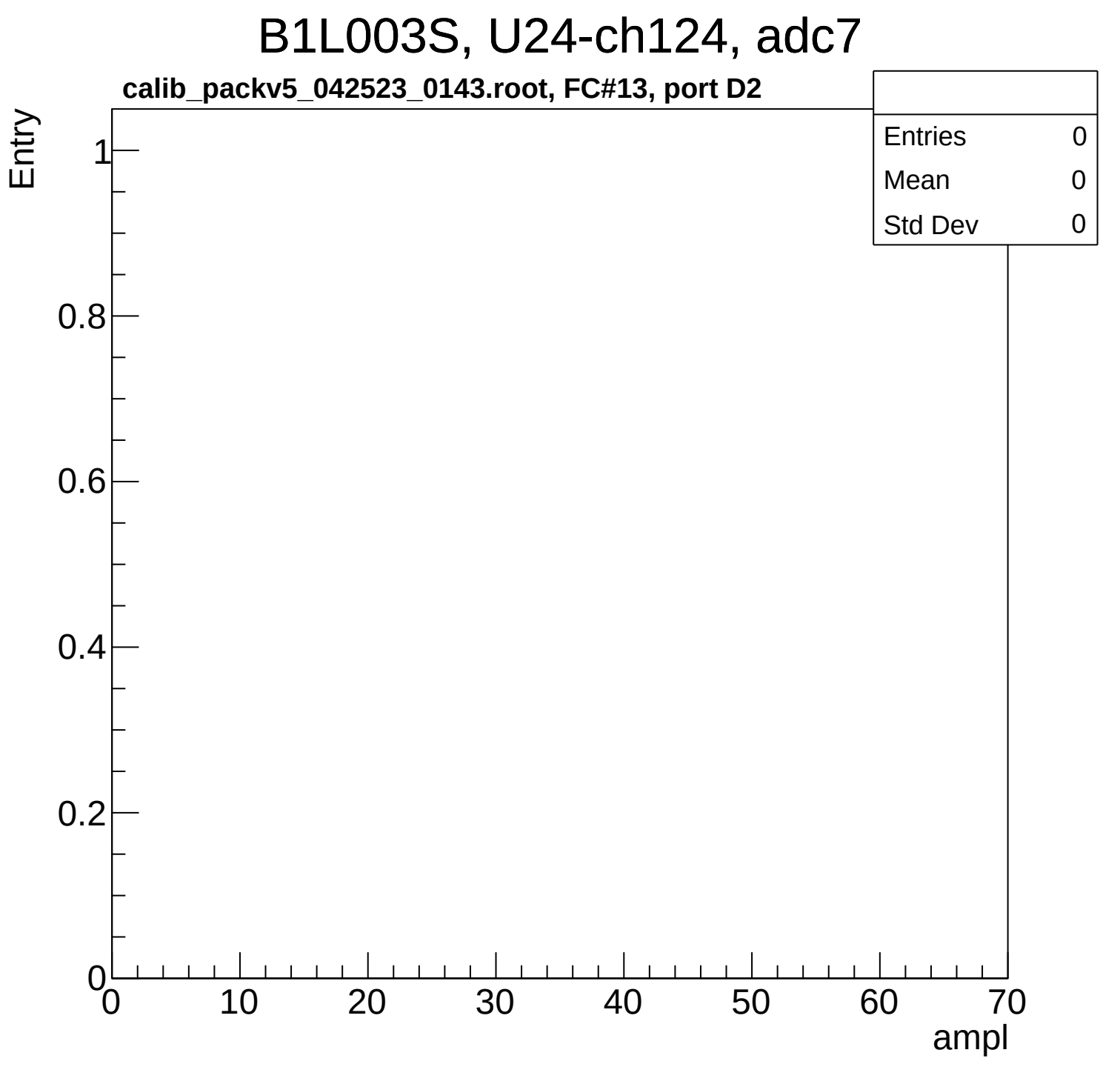
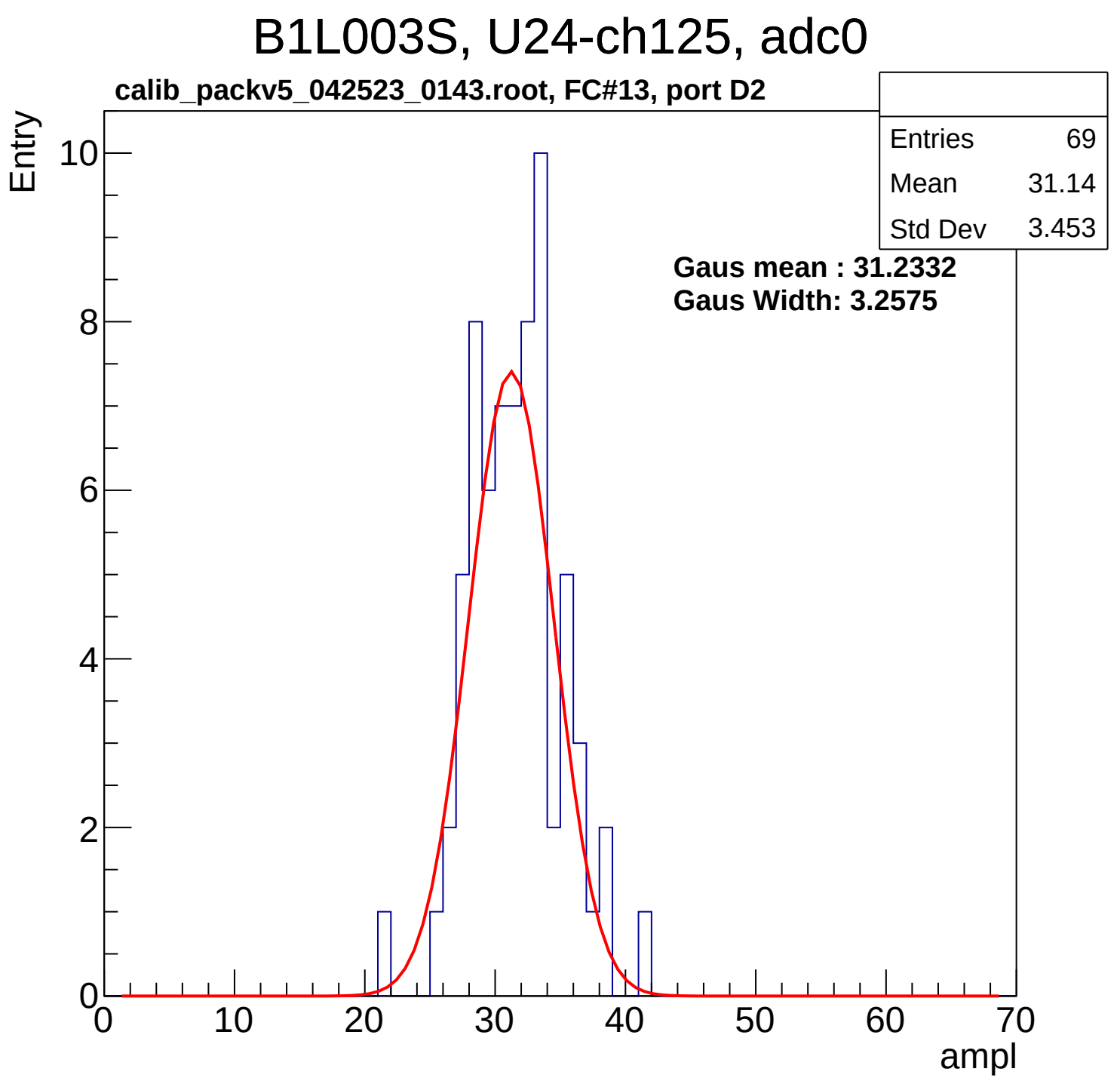
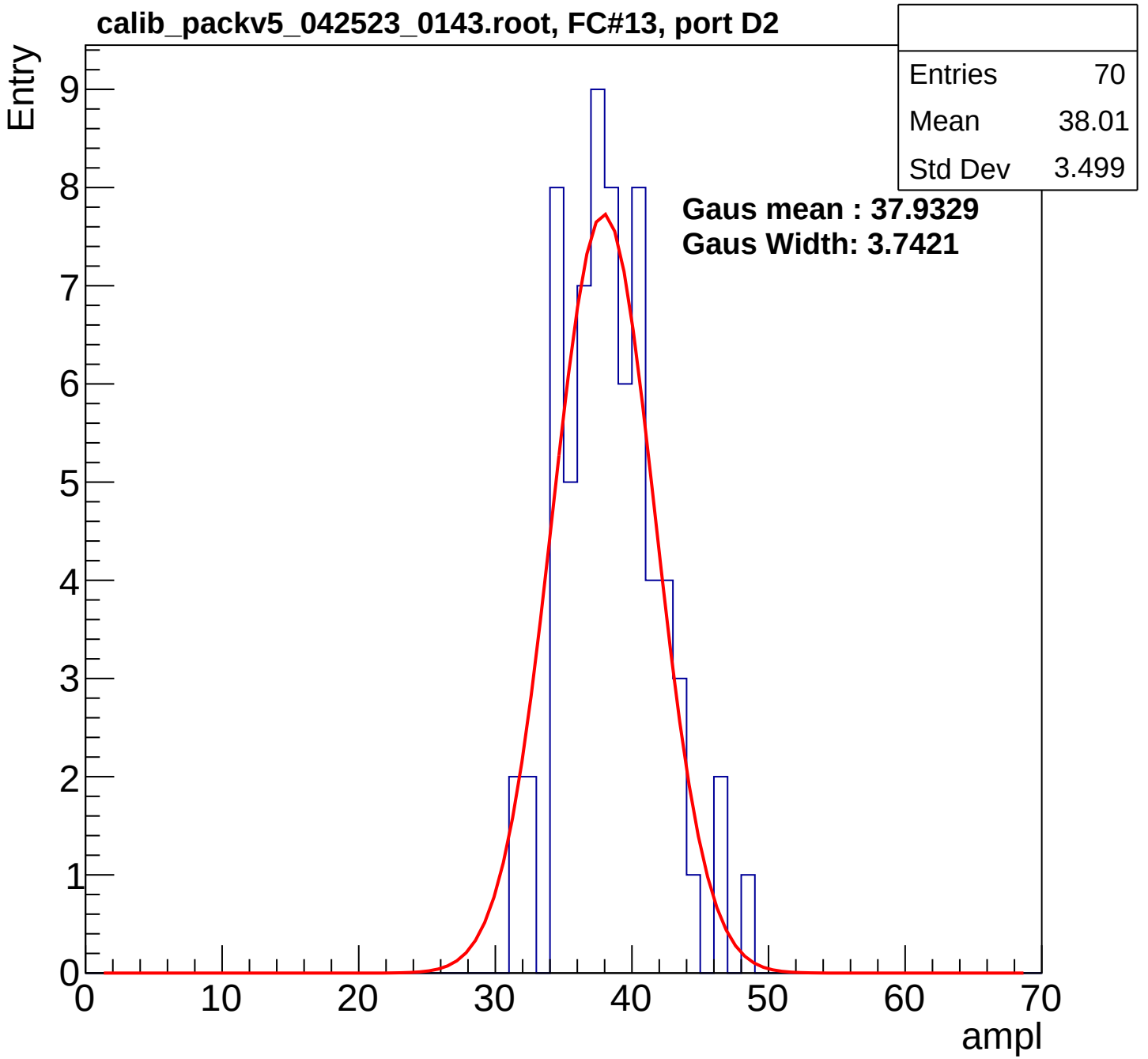

B1L003S, U24-ch124, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch124, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch125, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 31.14
Std Dev 3.453
Gaus mean : 31.2332
Gaus Width: 3.2575
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U24-ch125, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 38.01
Std Dev 3.499
Gaus mean : 37.9329
Gaus Width: 3.7421
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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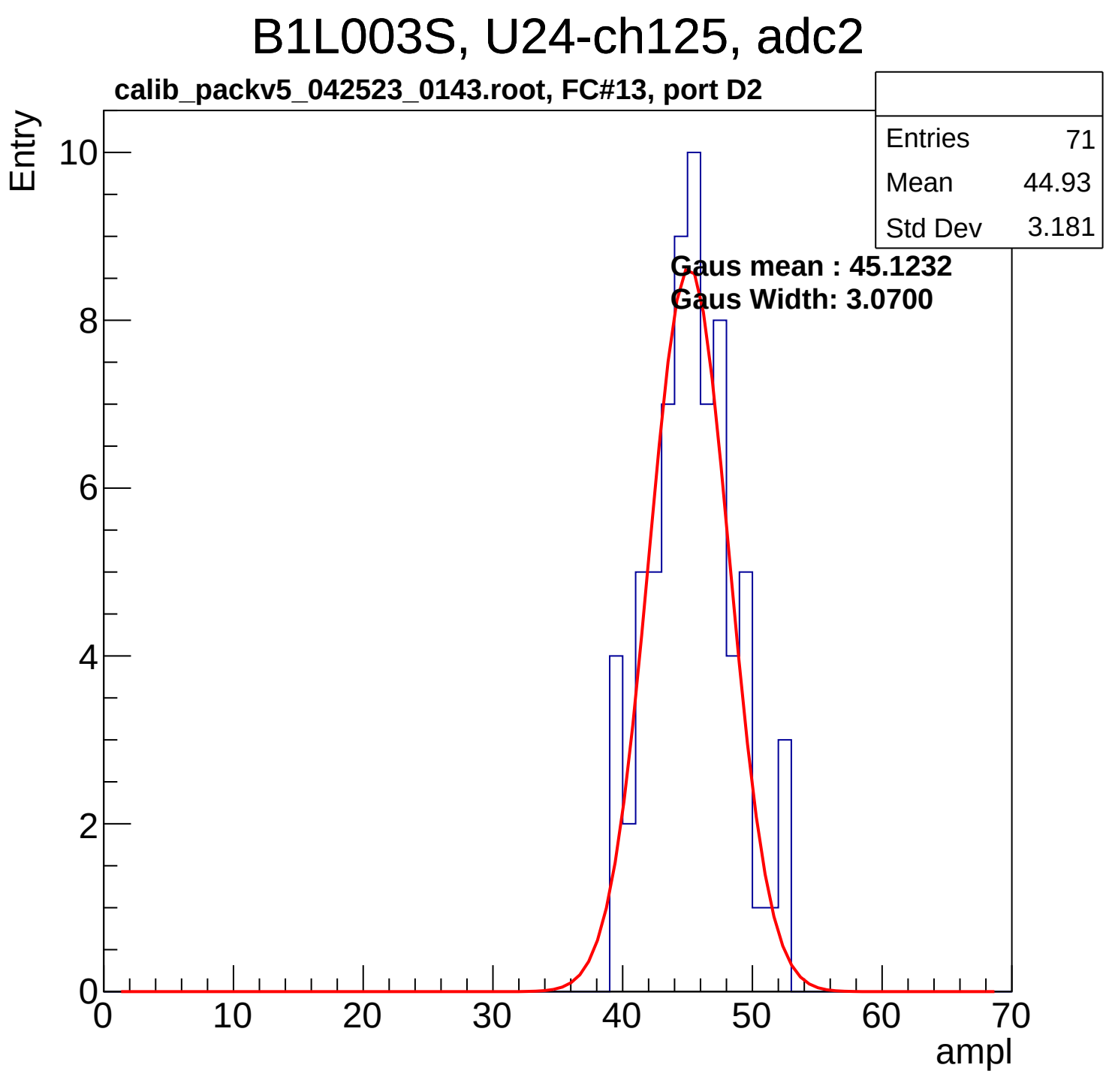
B1L003S, U24-ch125, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 44.93
Std Dev 3.181
Gaus mean : 45.1232
Gaus Width: 3.0700
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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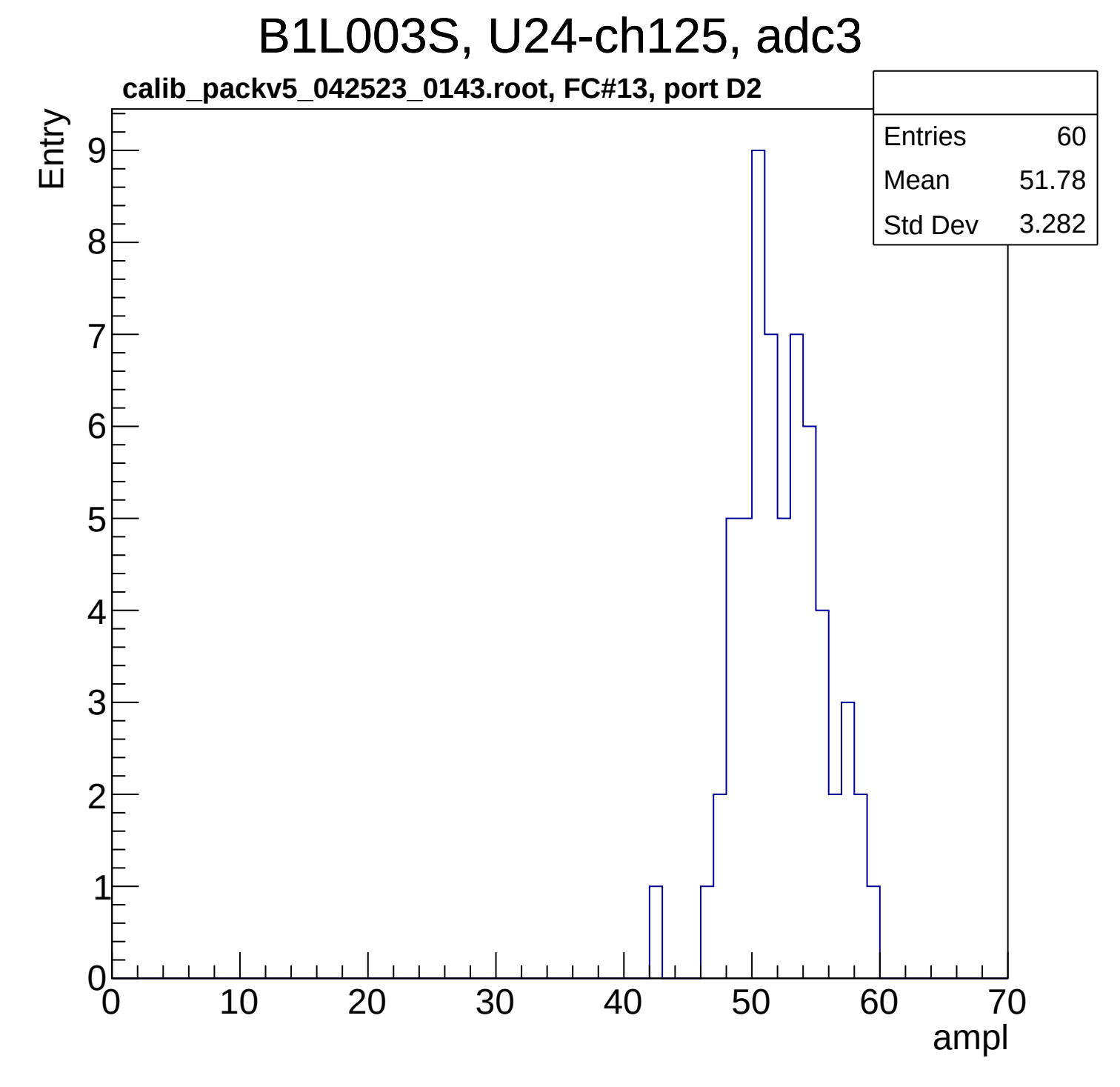
B1L003S, U24-ch125, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 51.78
Std Dev 3.282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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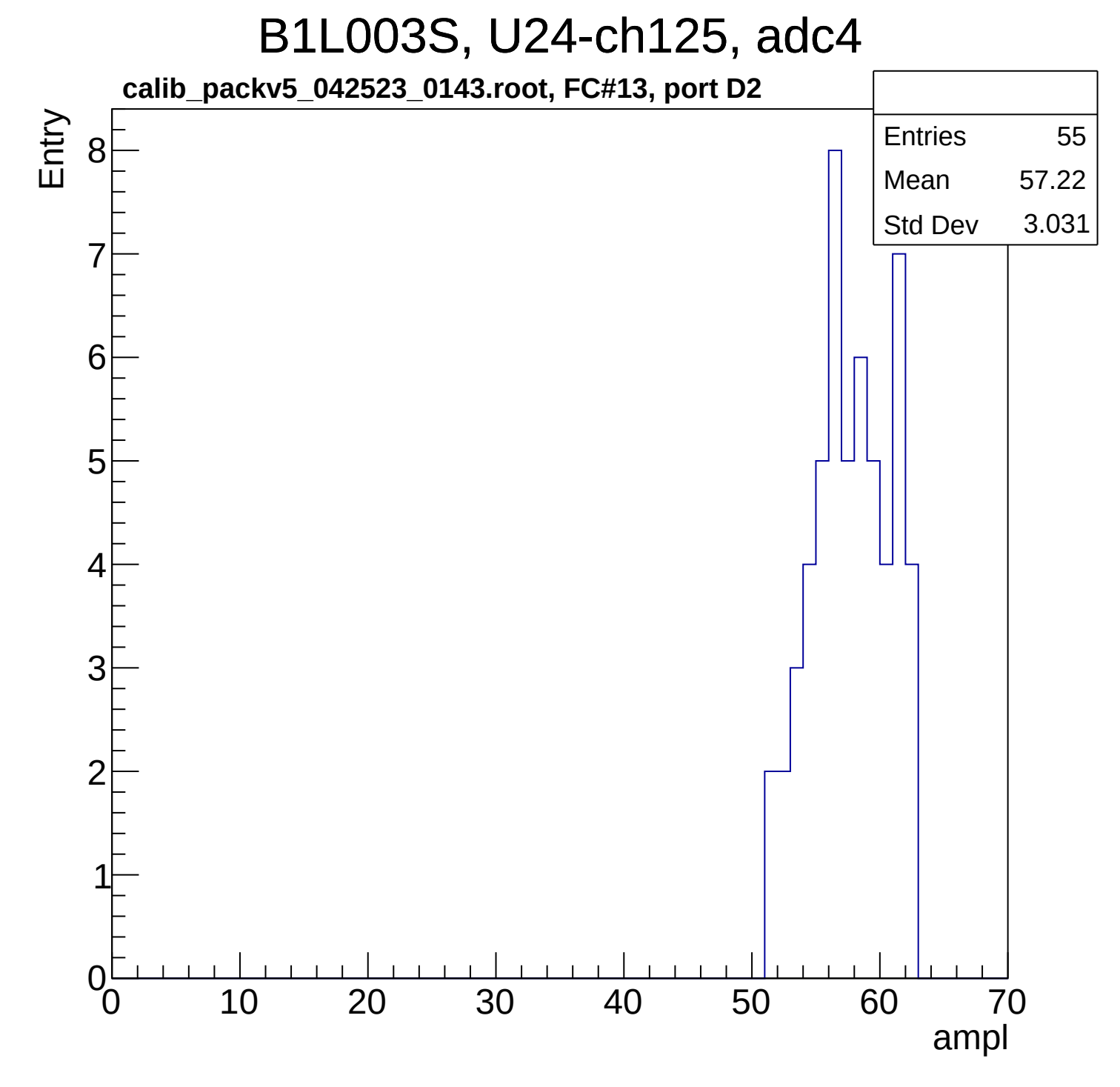
B1L003S, U24-ch125, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.22
Std Dev 3.031
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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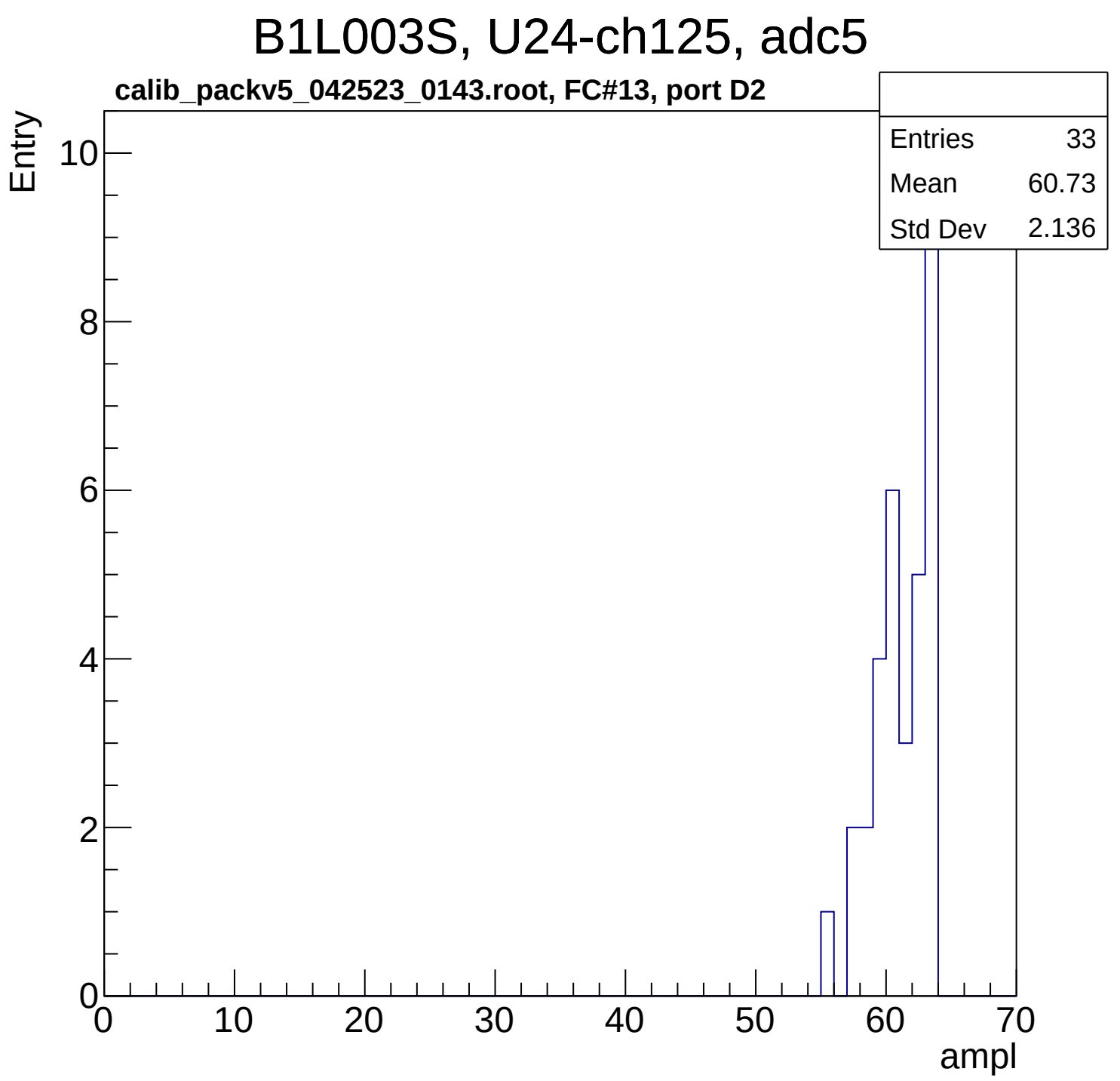
40
50
60
70


B1L003S, U24-ch125, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 60.73
Std Dev 2.136
Entry
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4
6
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10
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10
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30
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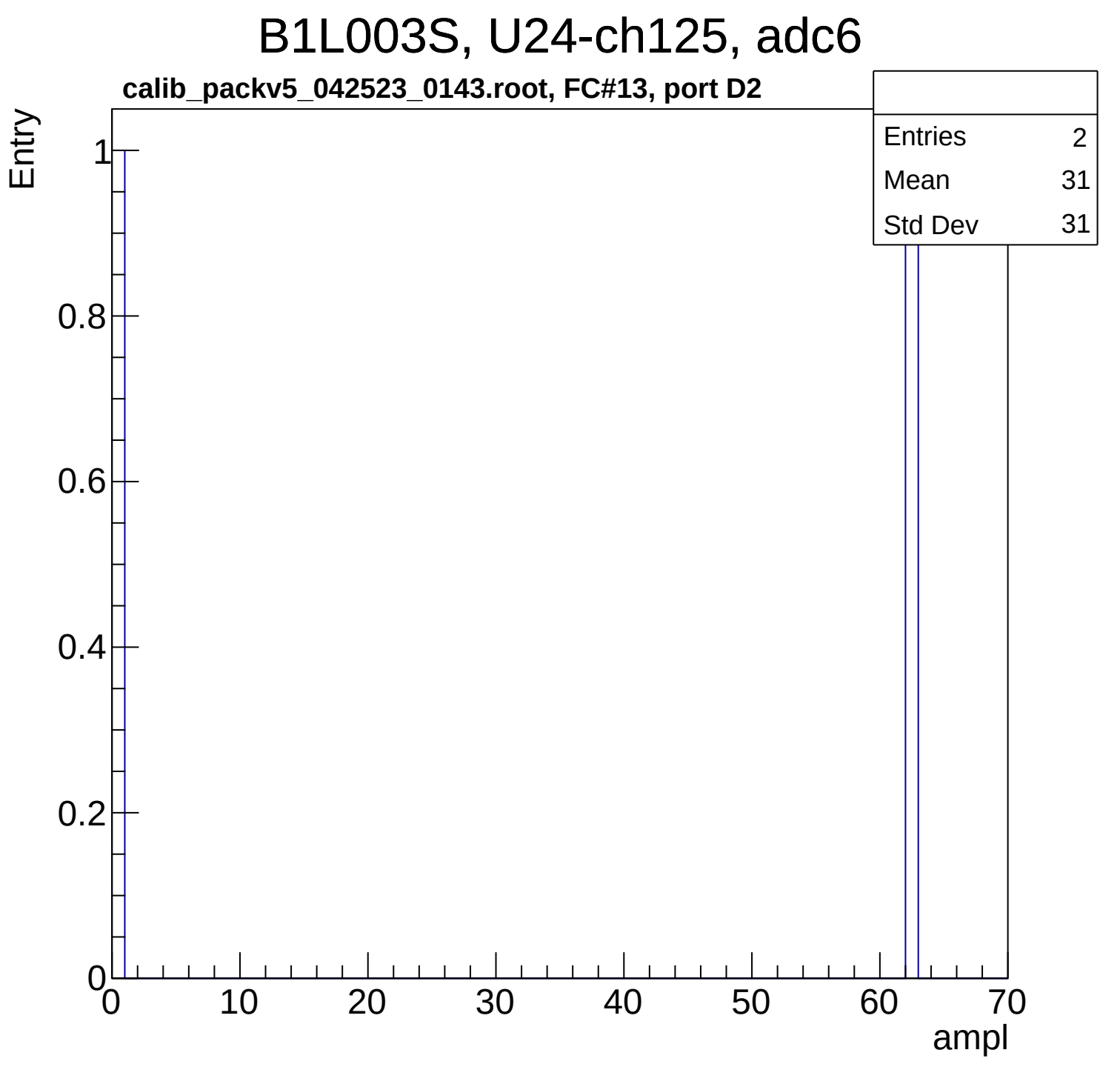
50
60
70
ampl


B1L003S, U24-ch125, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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0.6
0.4
0.2
0
0
10
20
30
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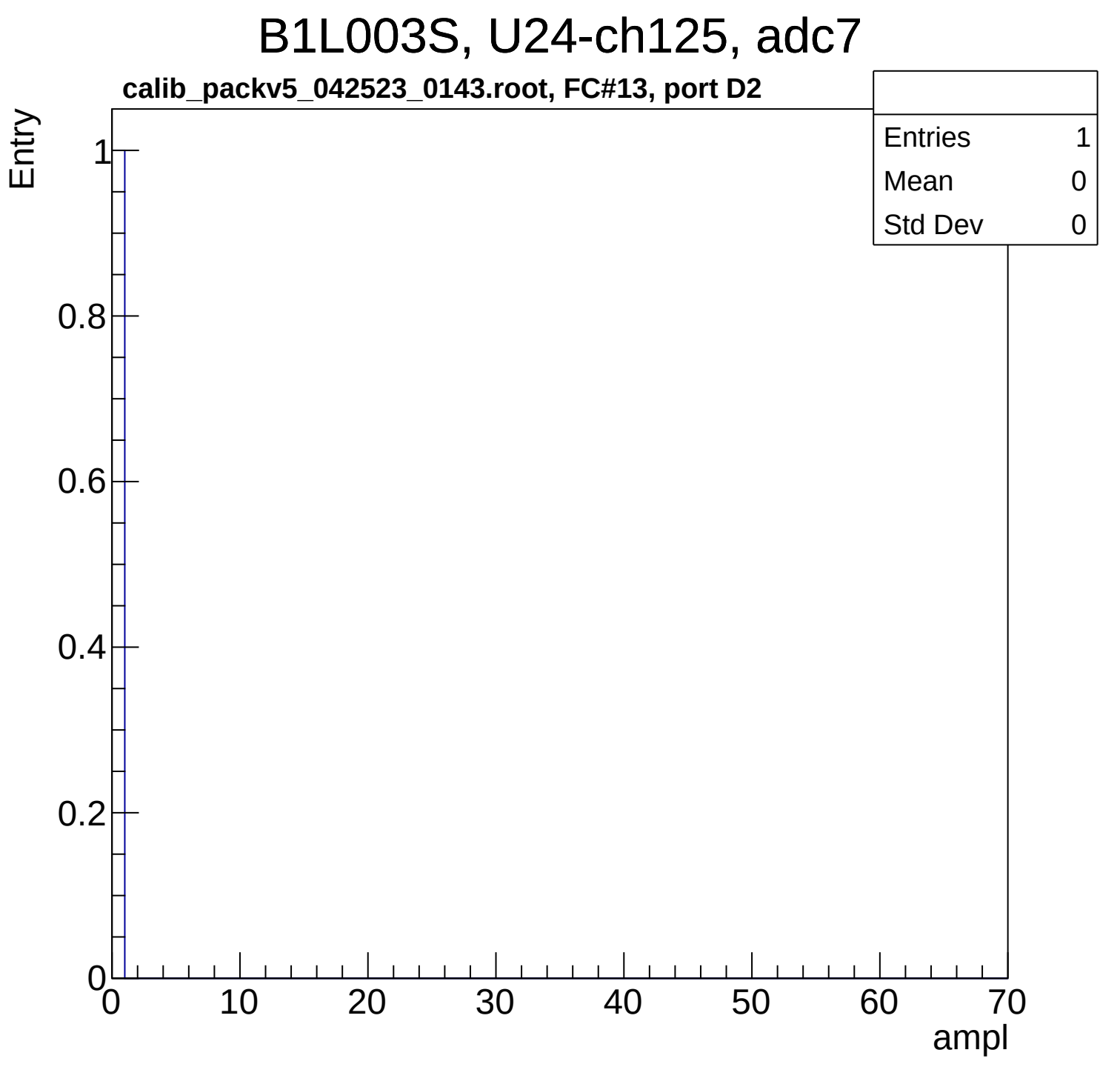
50
60
70
ampl


B1L003S, U24-ch125, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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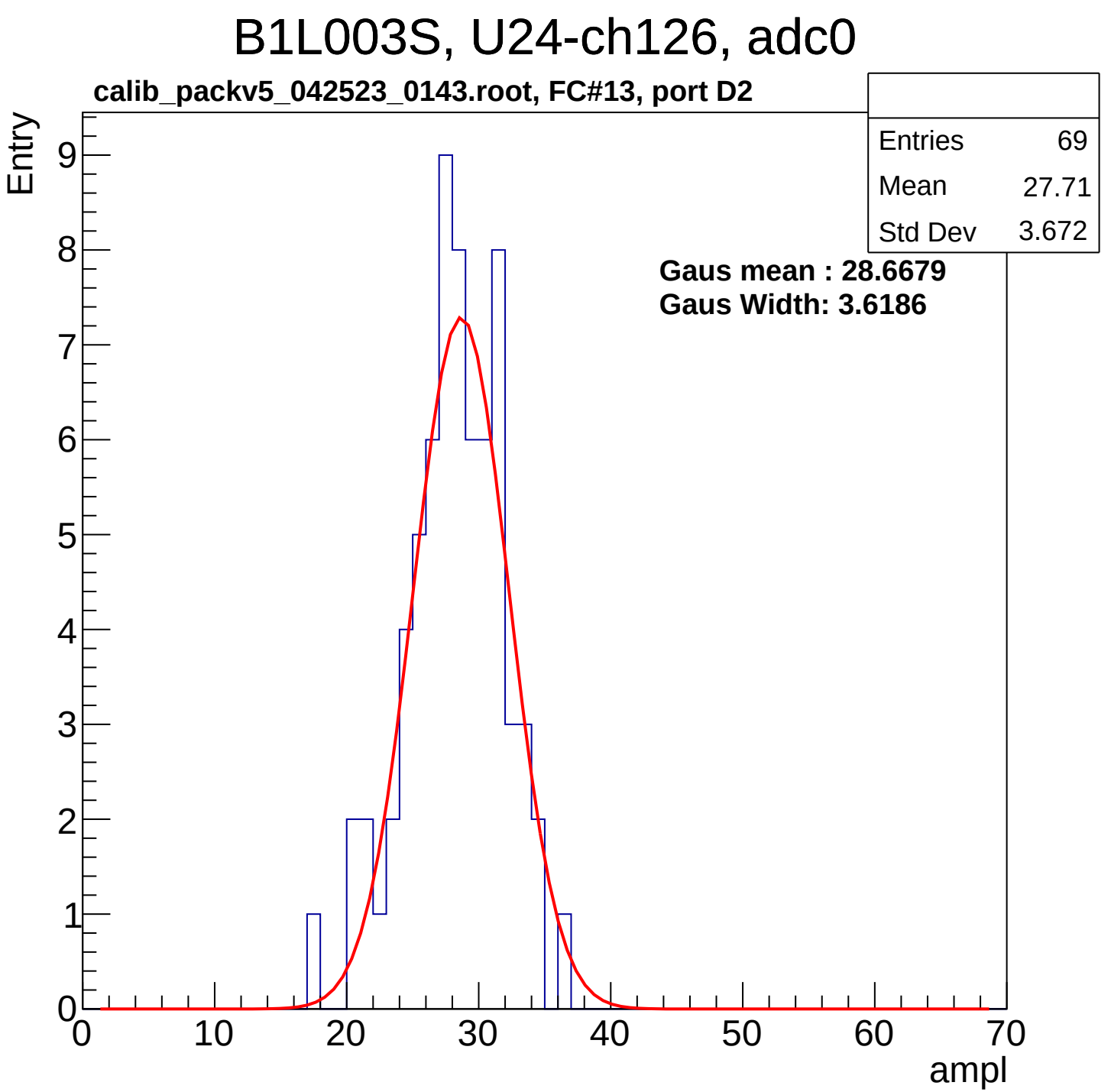
30
40
50
60
70
ampl


B1L003S, U24-ch126, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 27.71
Std Dev 3.672
Gaus mean : 28.6679
Gaus Width: 3.6186
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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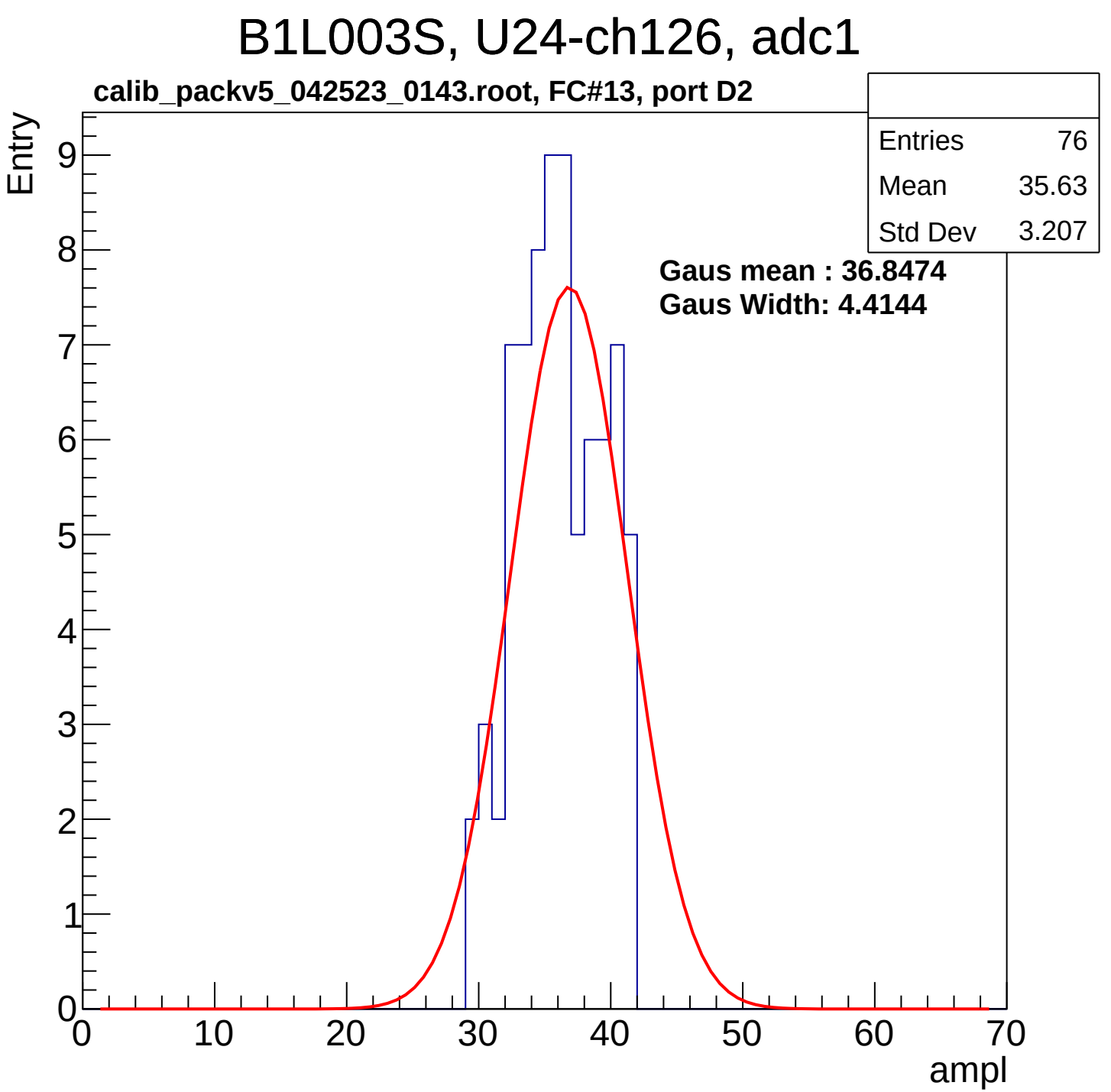
B1L003S, U24-ch126, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 35.63
Std Dev 3.207
Gaus mean : 36.8474
Gaus Width: 4.4144
Entry
ampl
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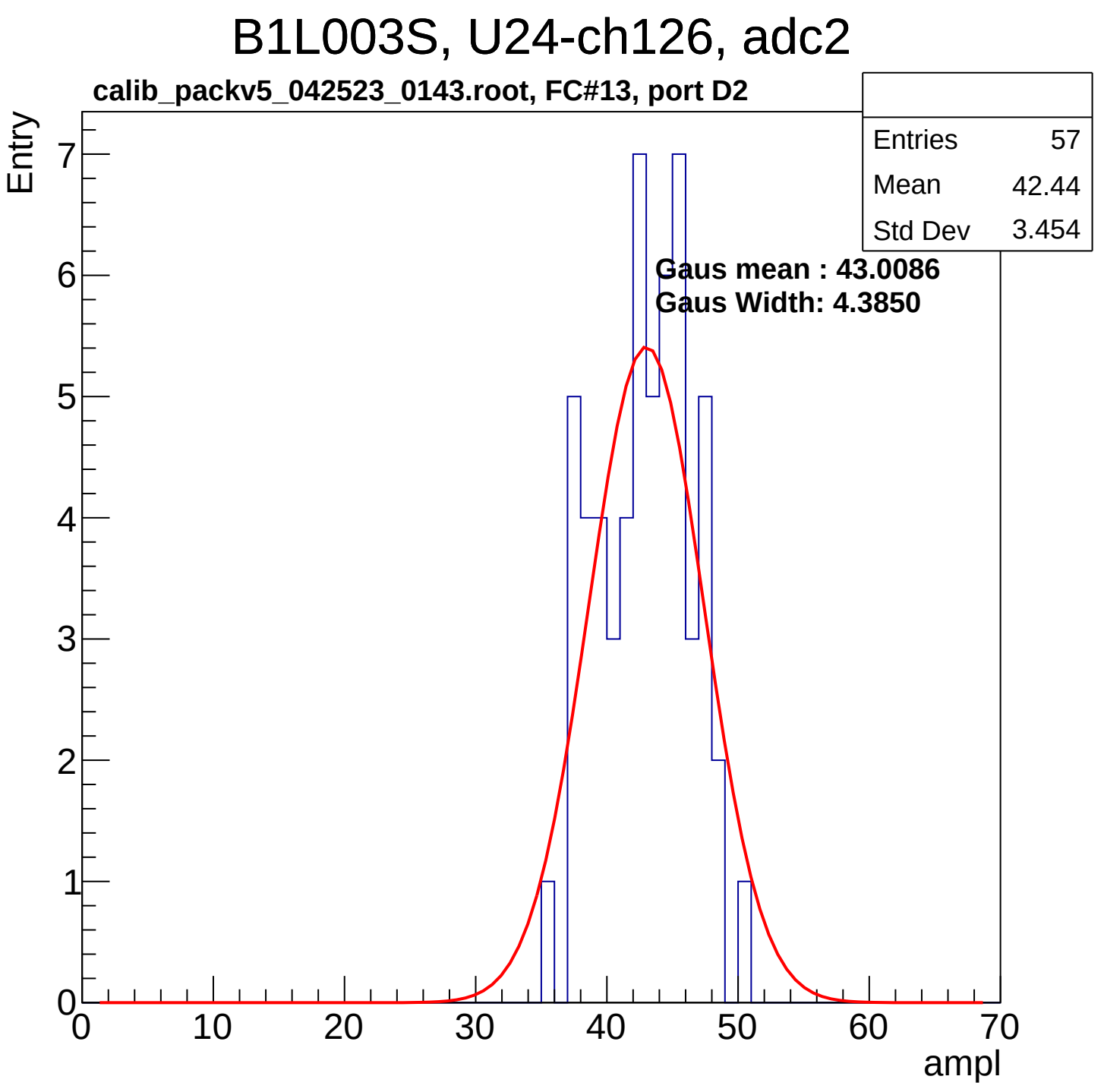
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U24-ch126, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 42.44
Std Dev 3.454
Gaus mean : 43.0086
Gaus Width: 4.3850
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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60
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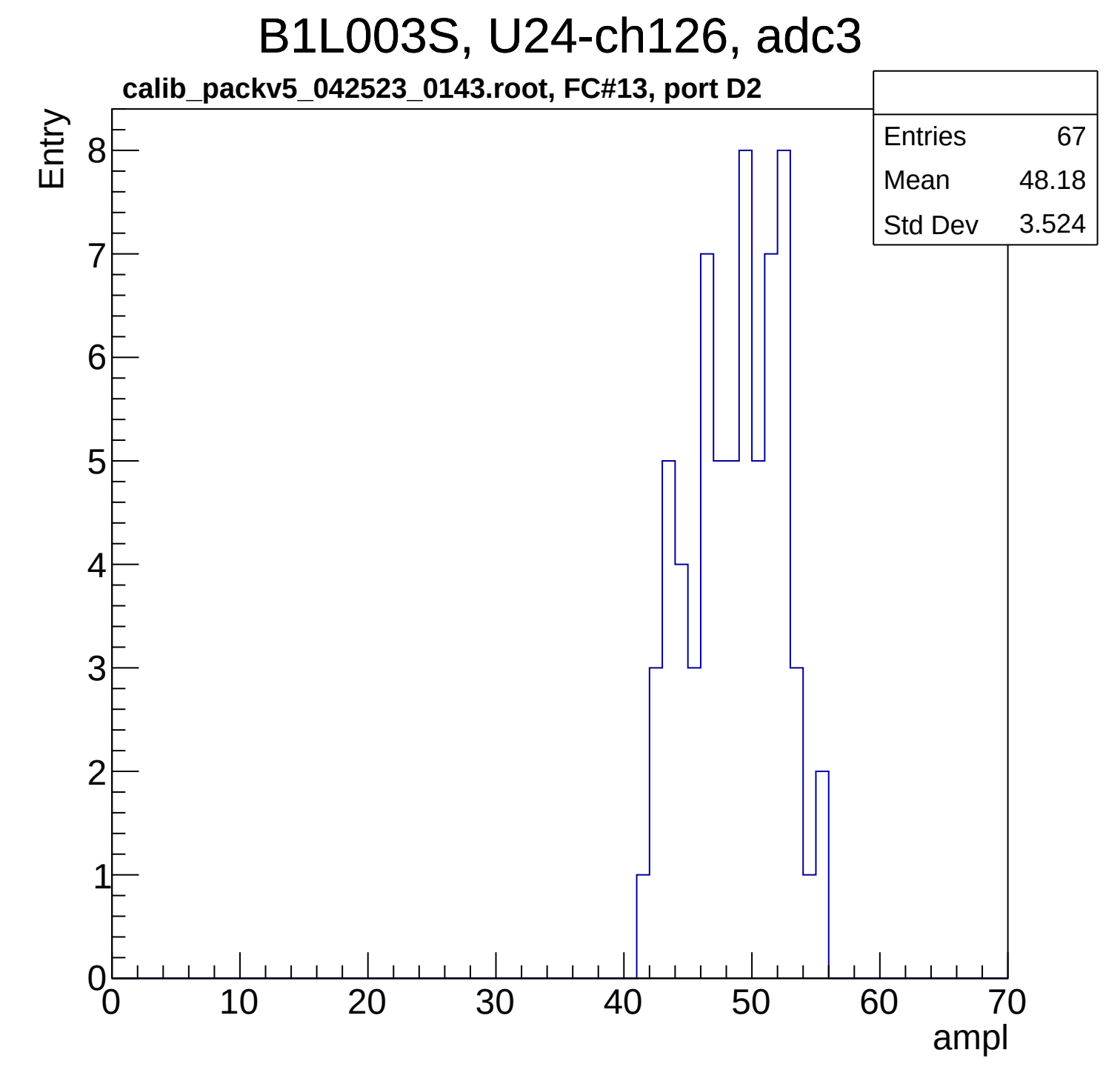
B1L003S, U24-ch126, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.18
Std Dev 3.524
Entry
ampl
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3
4
5
6
7
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0
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60
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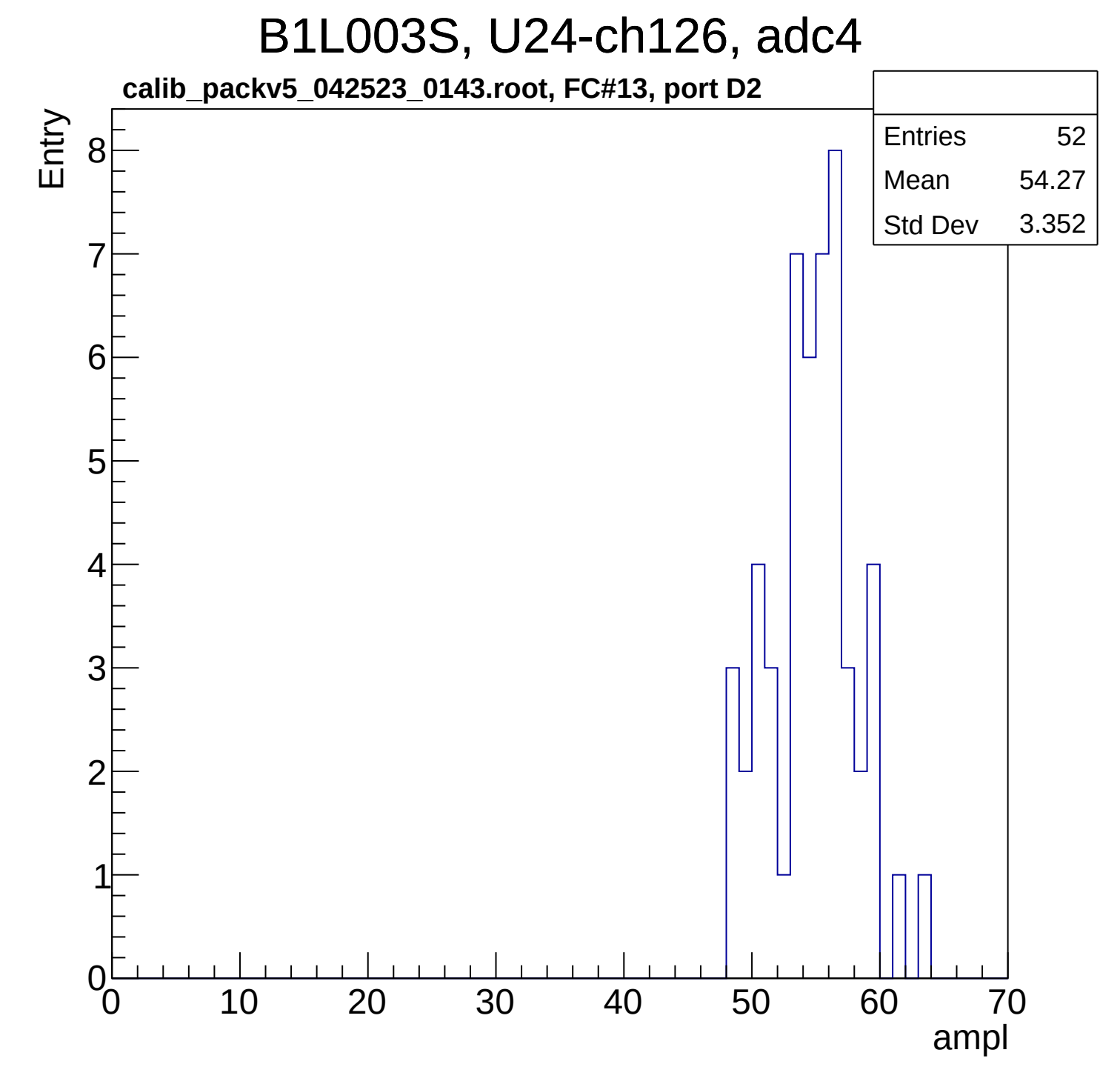
B1L003S, U24-ch126, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.27
Std Dev 3.352
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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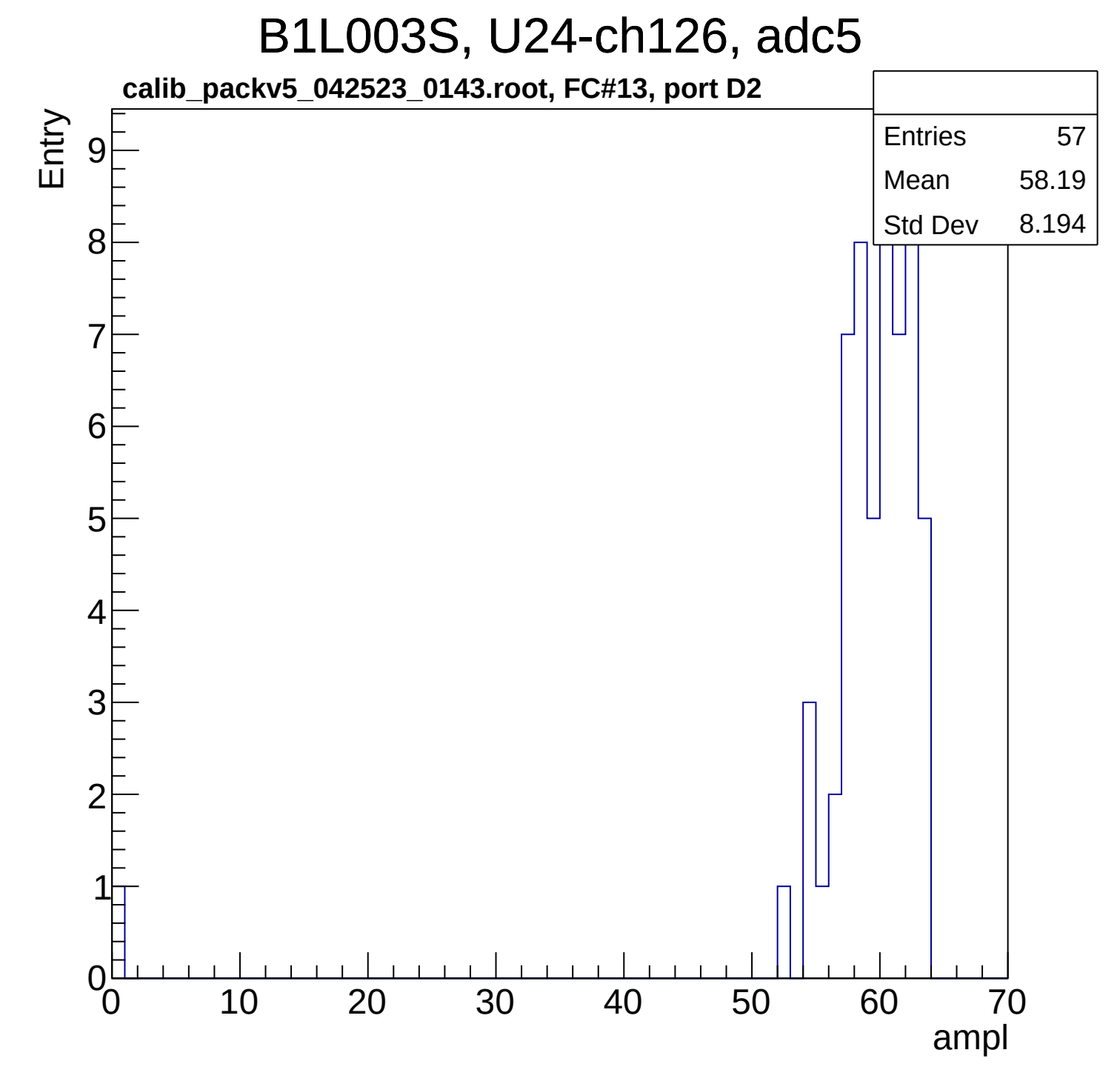
B1L003S, U24-ch126, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.19
Std Dev 8.194
Entry
ampl
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0
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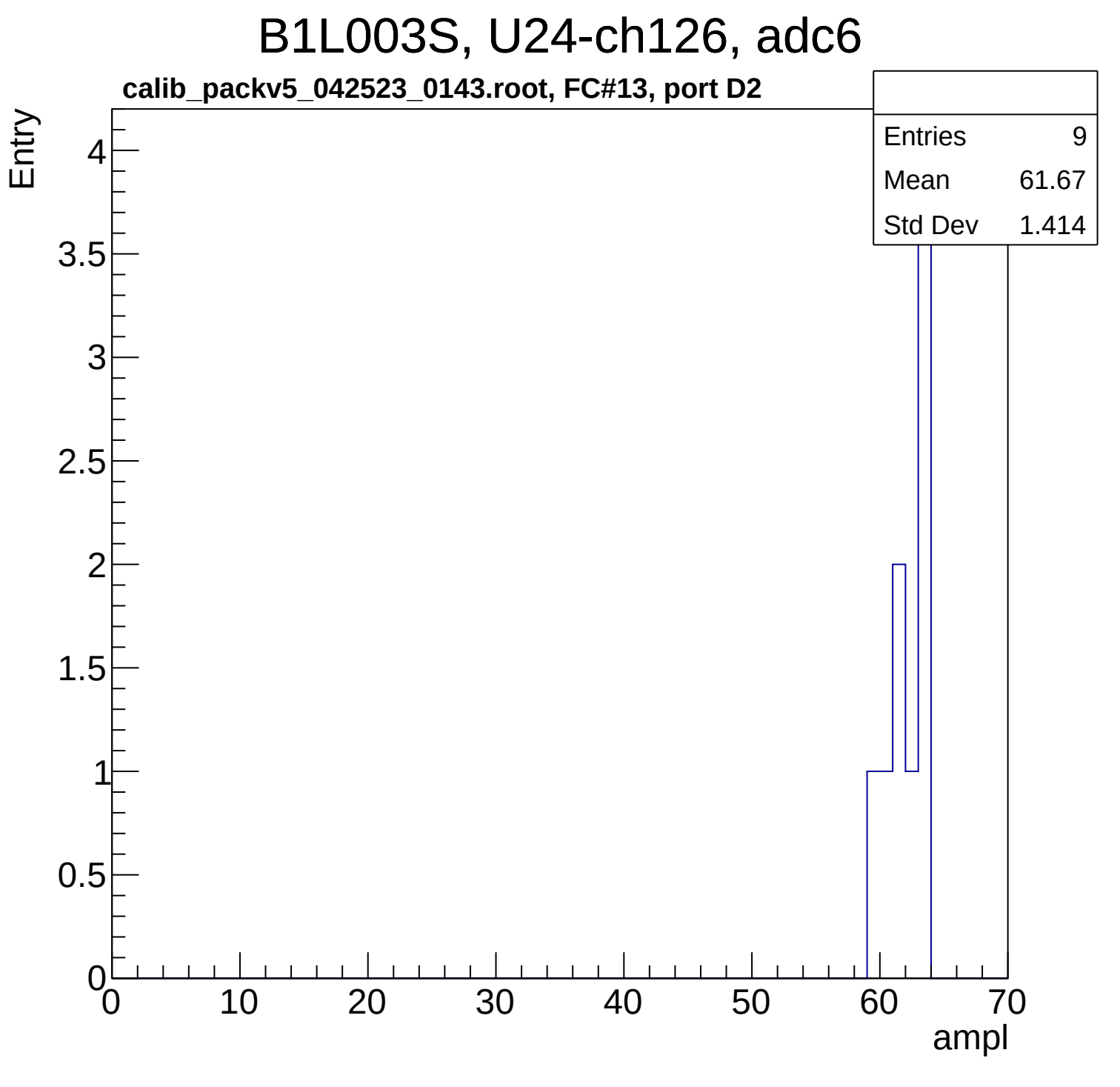
B1L003S, U24-ch126, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.67
Std Dev 1.414
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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50
60
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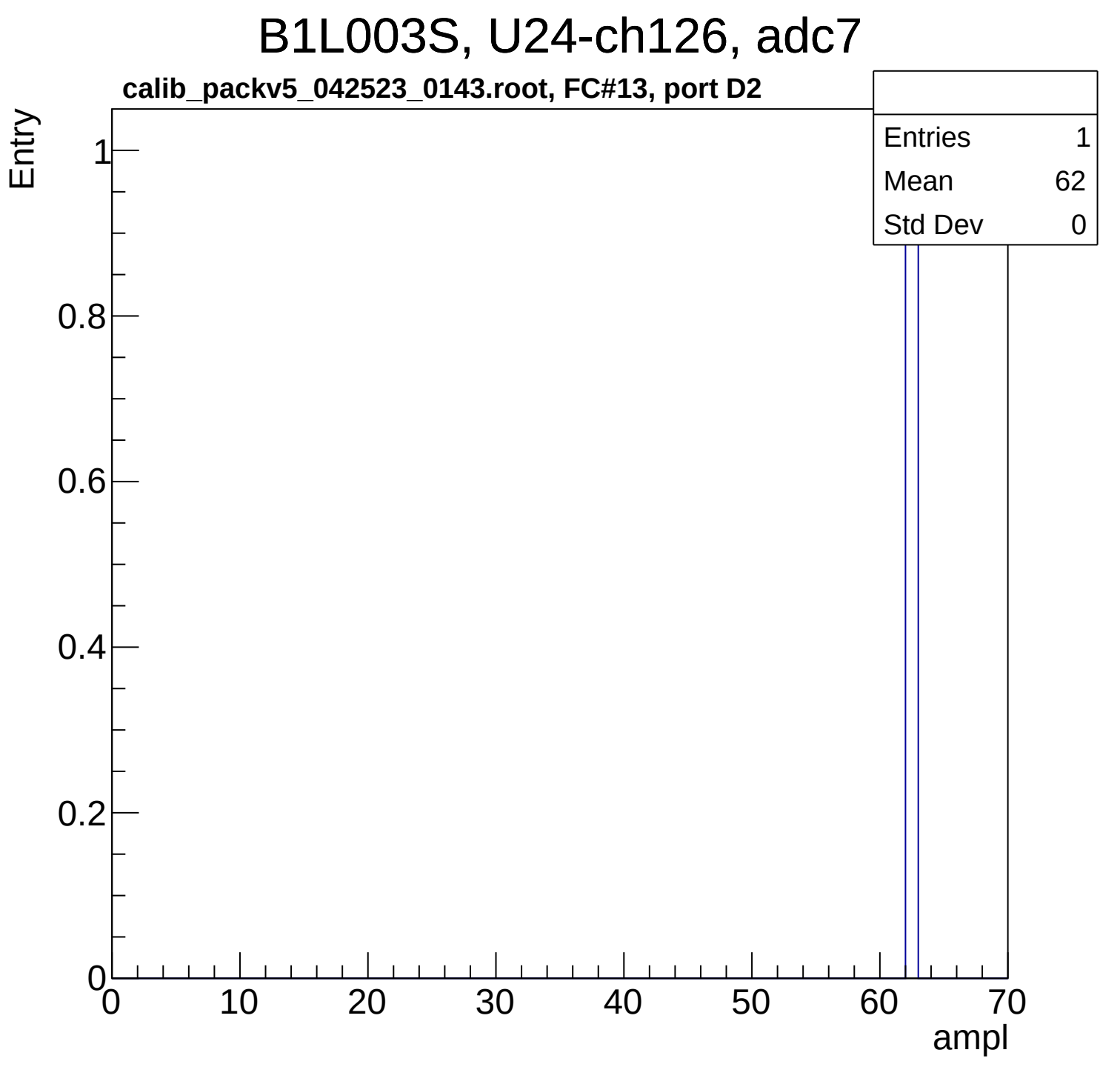
B1L003S, U24-ch126, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
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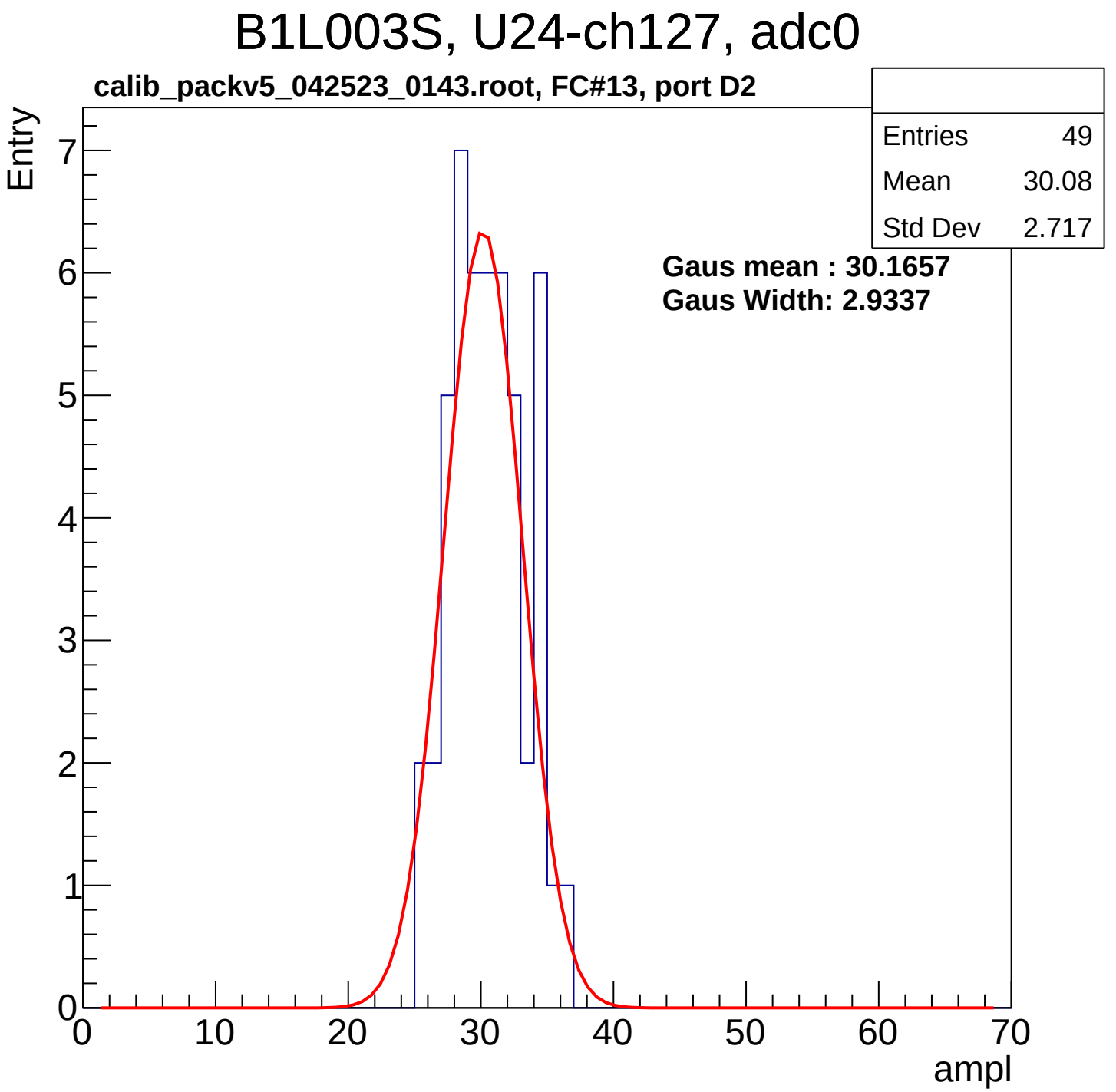
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch127, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 30.08
Std Dev 2.717
Gaus mean : 30.1657
Gaus Width: 2.9337
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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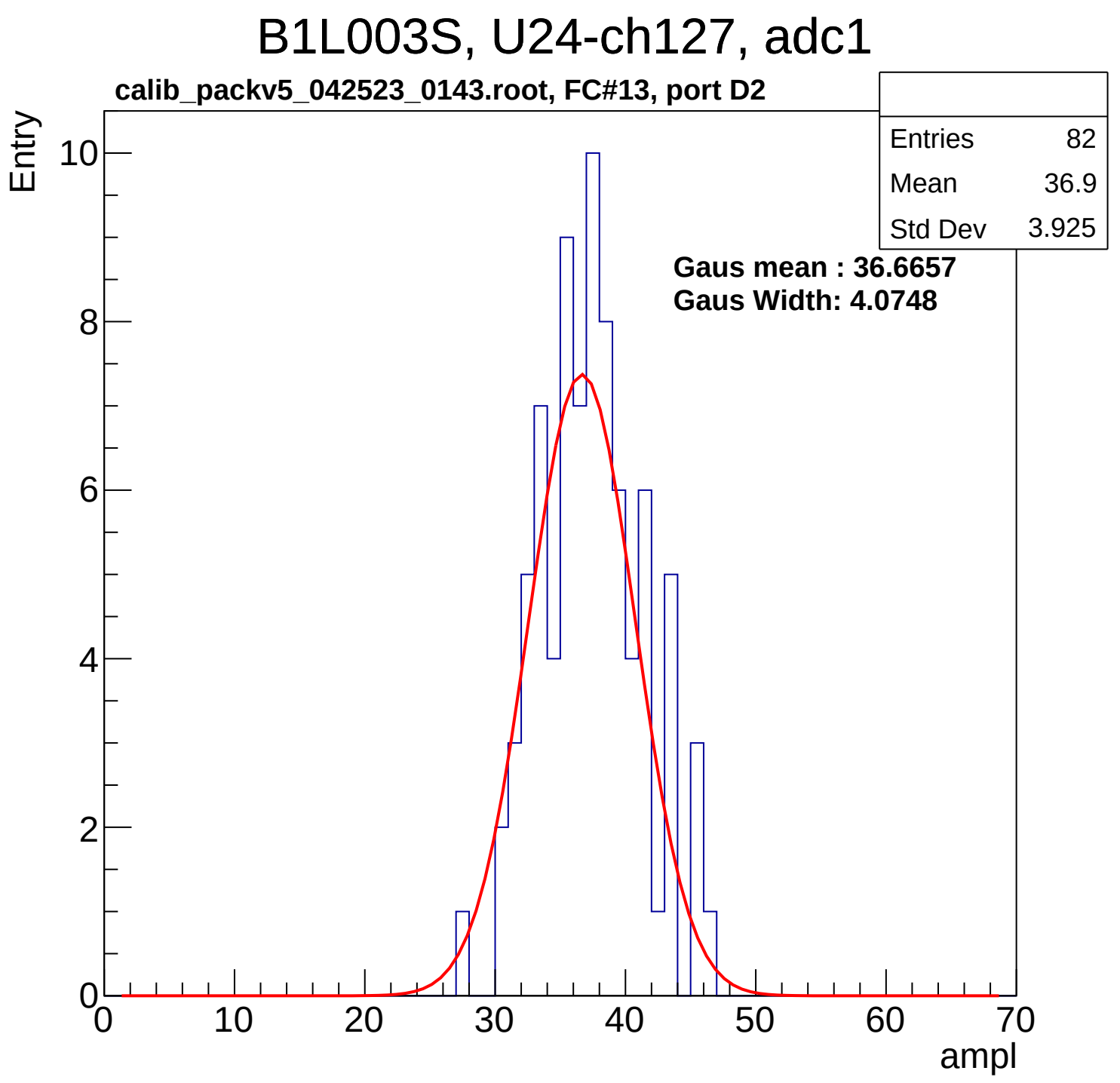
B1L003S, U24-ch127, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 36.9
Std Dev 3.925
Gaus mean : 36.6657
Gaus Width: 4.0748
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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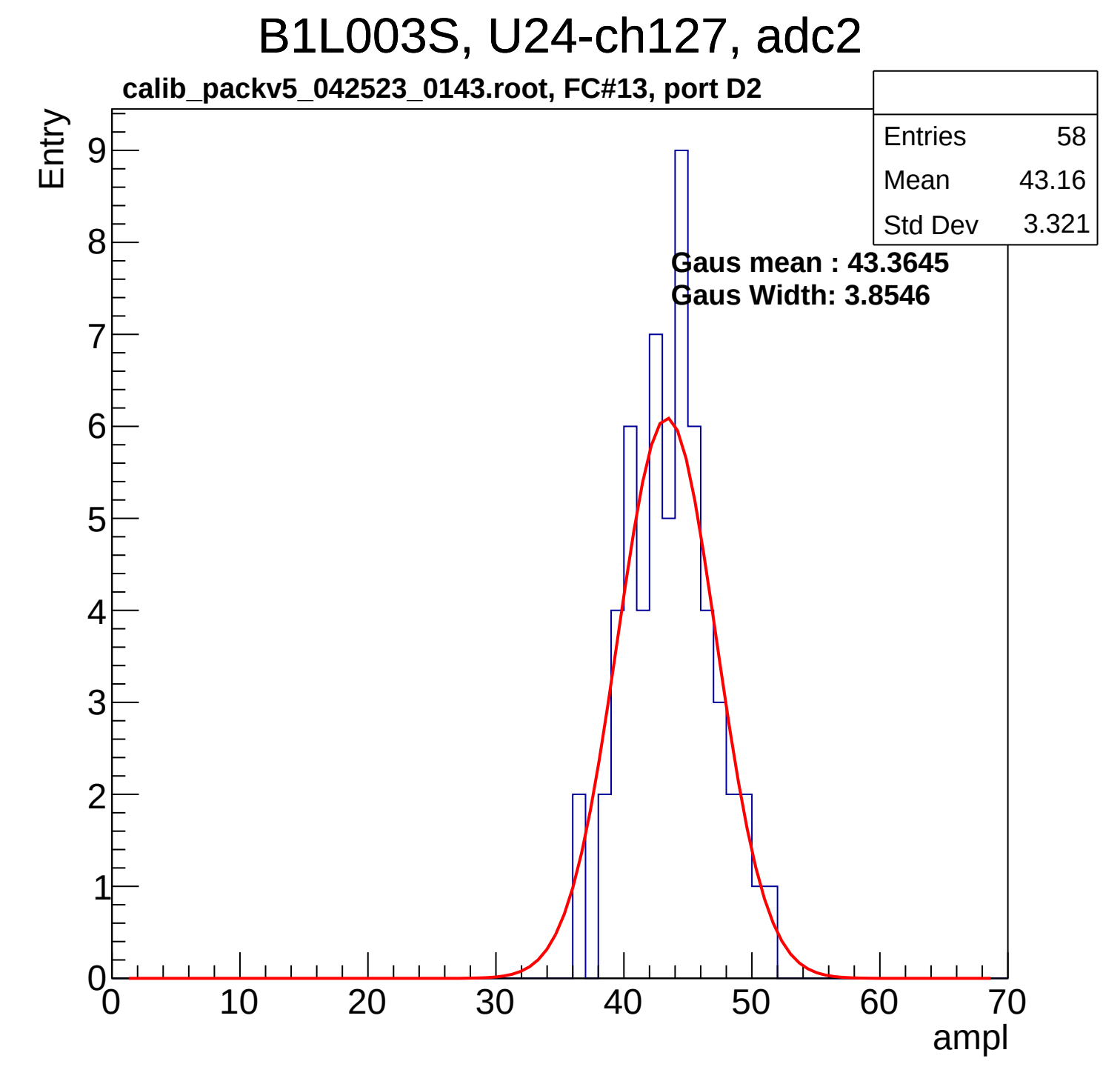
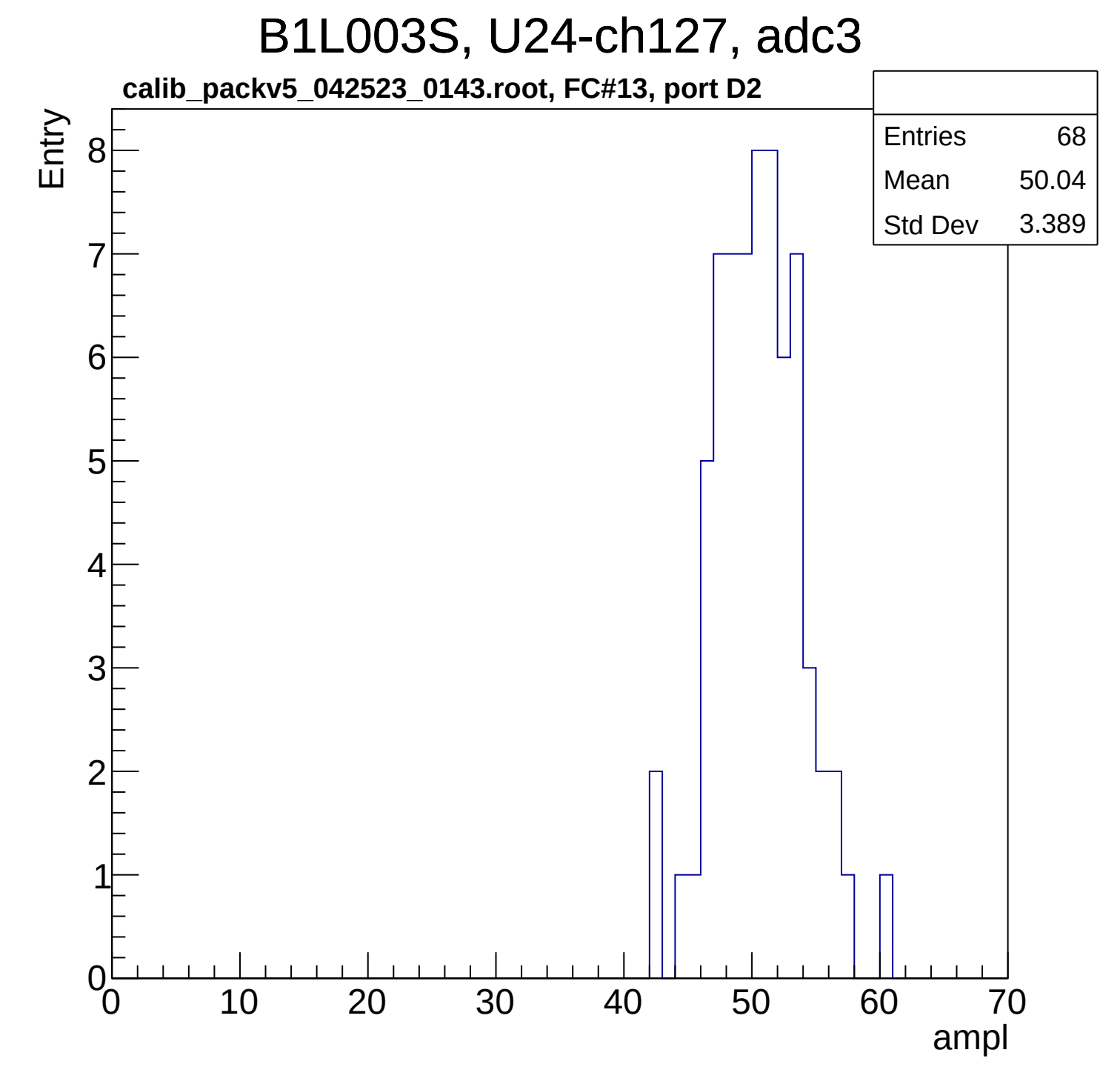
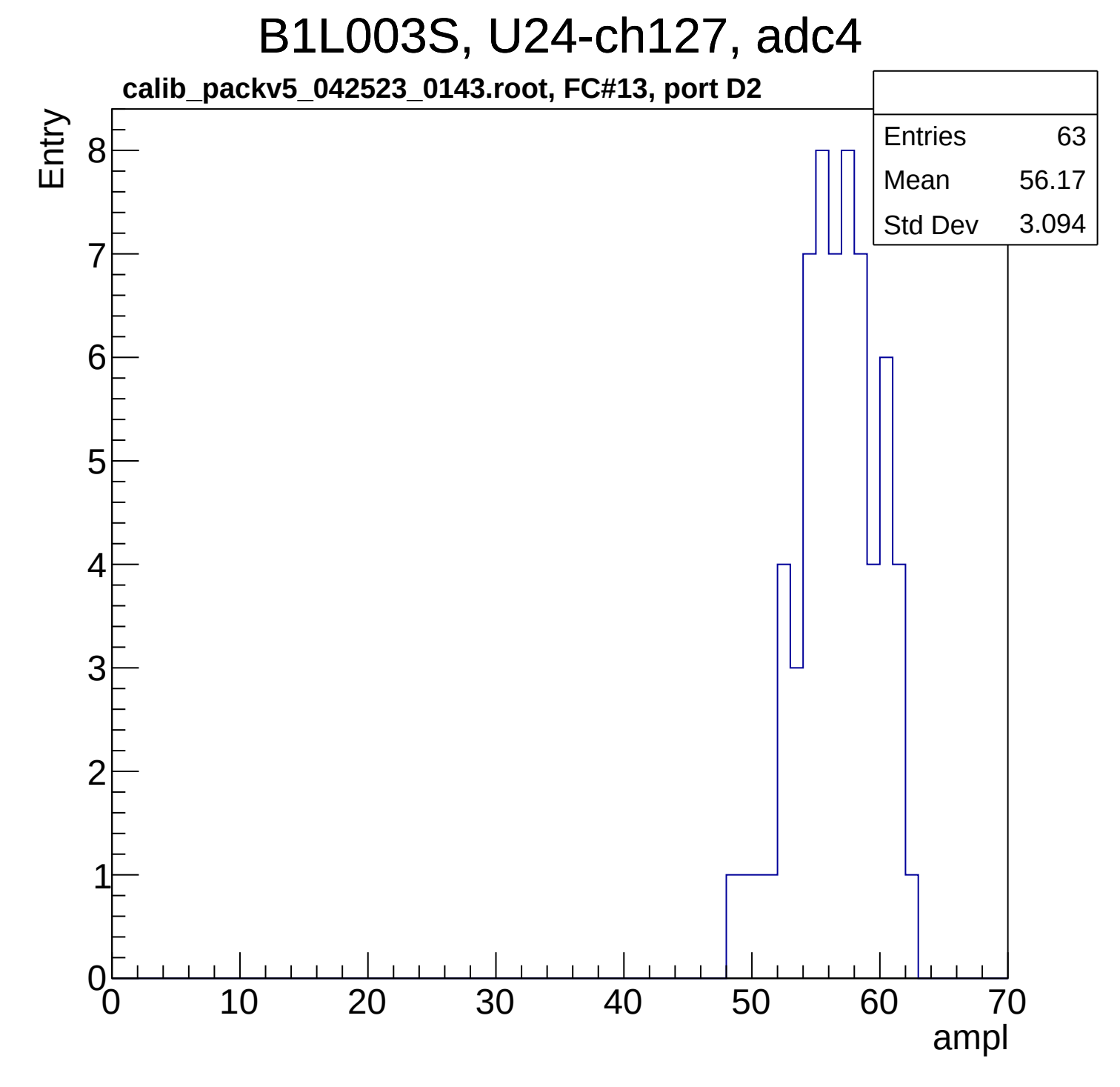
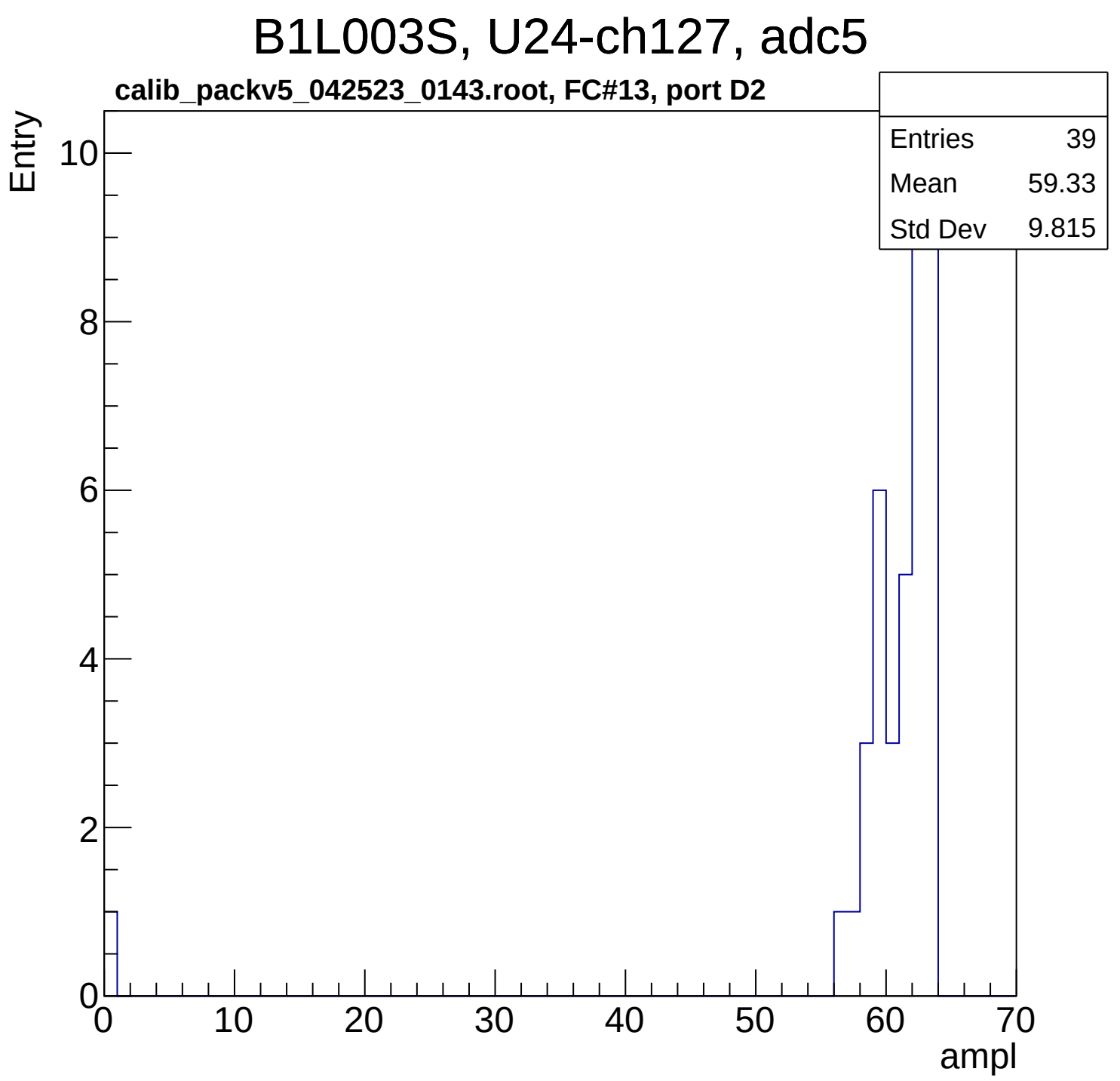
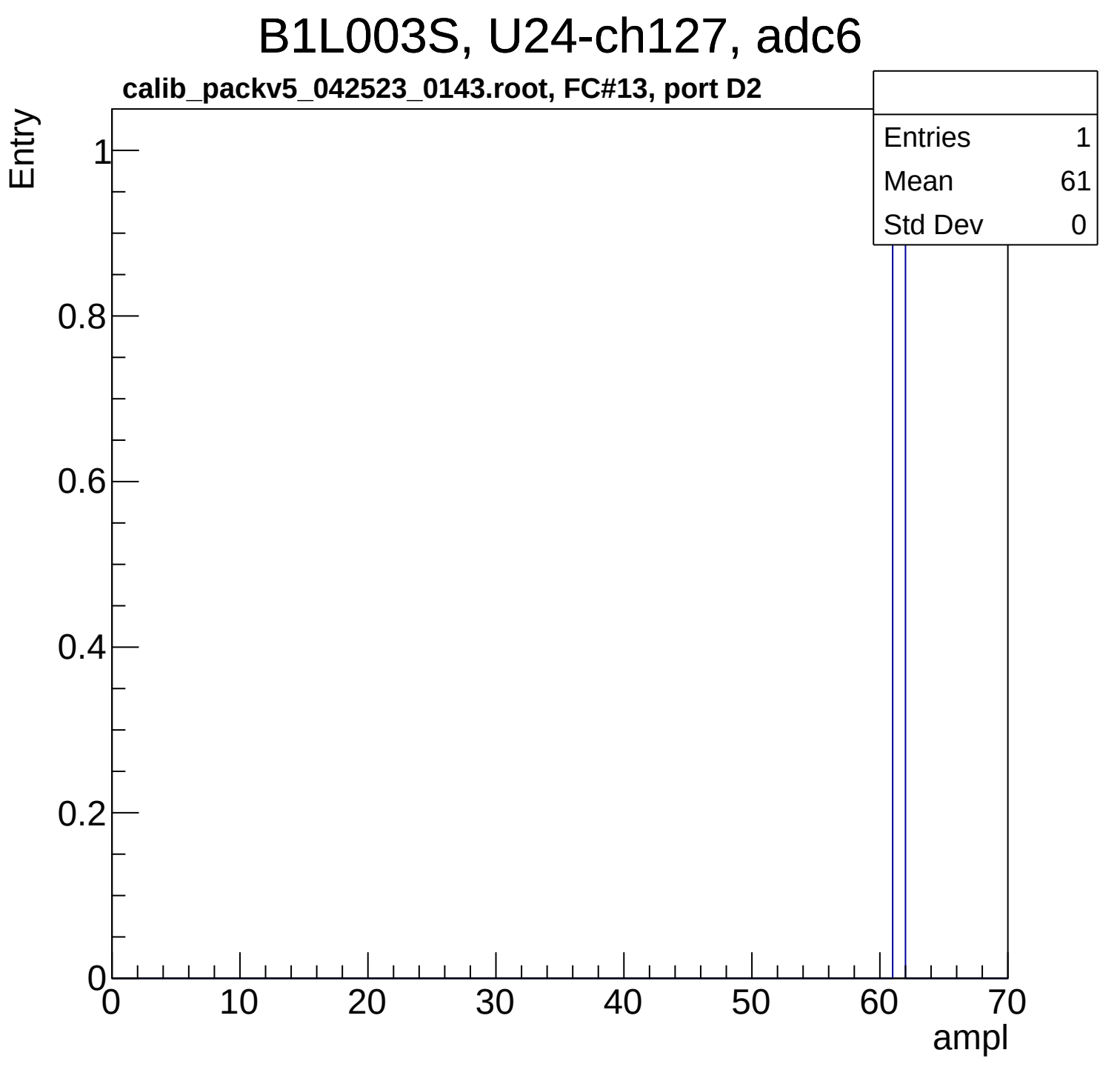
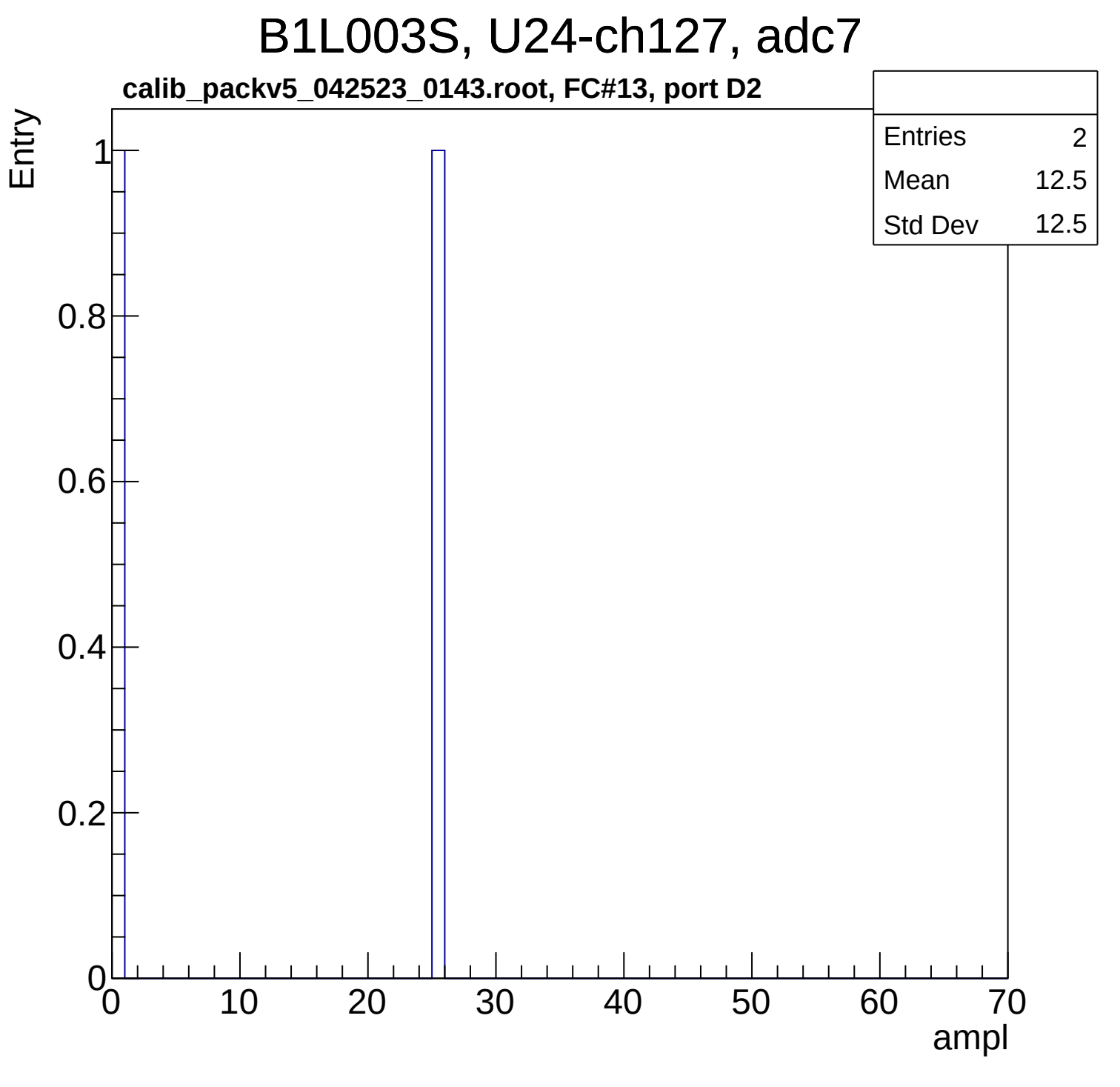

B1L003S, U24-ch127, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 43.16
Std Dev 3.321
Gaus mean : 43.3645
Gaus Width: 3.8546
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

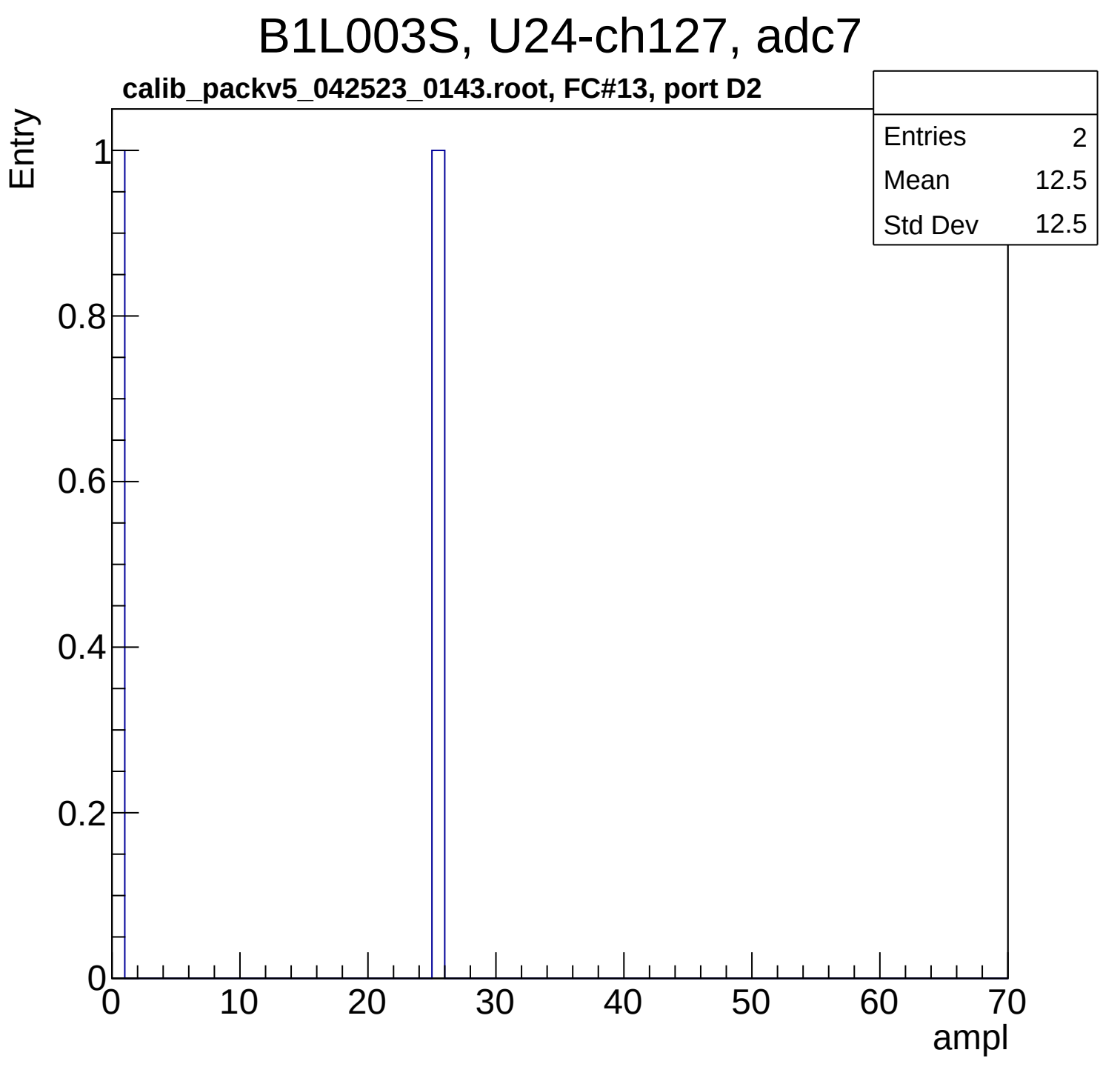

B1L003S, U24-ch127, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 50.04
Std Dev 3.389
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch127, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 56.17
Std Dev 3.094
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U24-ch127, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 59.33
Std Dev 9.815
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch127, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl