

The schematic diagram illustrates the internal components and connections of the STM32F103C8T6 microcontroller. The microcontroller (U1) is the central component, with its pins connected to various external components. The power supply section includes capacitors C1 through C11, with C1-C5 and C6-C8 connected to the VDD and VDDA pins. The reset section includes the NRST pin connected to a reset button (SW1) and the SW_BOOT0 pin connected to a boot button (SW2). The clock section includes a 16MHz crystal (Y1) connected to the PC13 and PC14 pins. The USB section includes the USB_B_Micro connector connected to the PA12, PA13, PA14, and PA15 pins. The diagram also shows the connection of the microcontroller to the +3.3V and +3.3VA power rails, and the connection of the USB connector to the +3.3V and GND pins.

Pin	Label	Conn_01x18
1	PA2	PA3
2	PA1	PA4
3	PA0	PA5
4	PC15	PA6
5	PC14	PA7
6	PC13	PB0
7	PB9	PB1
8	PB8	PB2
9	PB7	PB10
10	PB6	PB11
11	PB5	PB12
12	PB4	PB13
13	PB3	PB14
14	PA15	PB15
15	PA14	PA8
16	+3.3V	PA9
17	VBUS	PA10
18	Conn_01x18	PA13

VBUS

U2
AMS1117-3.3

+3.3V

D1
BLUE

C13
22u

C12
22u

R3
1k5

VI
VO
GND

VBUS from USB connector. Min 22uF inp/oupt caps

- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole