Shenzhen P&O Technology Co.,Limited	Rev No	Issued Date.	Page	
	Α	2021.12.27	1/18	

Project Size.		2.8 inch					
Model No.		P028X101-10-CTP					
Samples No.							
Product type		240xRGBx320					
Product type.		SPI mode					
Signature by cus	tomer						
			1				
Prepared		Checked	Approved				

Email: polcd@polcd.com

Mobile: 86-136 0019 7172

Rev No	Issued Date.	Page
Α	2021.12.27	2/18

1.0 GENERAL DESCRIPTION

1.1 Introduction

Display model P028X101-10-CTP is a (TM)Transmissive type color active matrix thin Film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit, a back light system. The resolution of a 2.8" contains 240_{RGB}x320 dots and can display up to 262K colors.

Item	Item Specification		
Screen Size	2.8 inch	Diagonal	
Number of Pixel	240RGB(H)x320(V)	Pixels	
Display area	43.20(H)x57.60(V)	mm	
Pixel pitch	0.06(H)x0.18(V)	mm	
Outline Dimension	50.00x69.20x3.20	mm	
Pixel arrangement	RGB Vertical Stripe		
Display mode	Normally White		
Viewing Direction(eye)	12 0'CLOCK		
Gray inversion direction			
Display Color	262K		
Luminance(cd/m²)	300	nit	
Contrast Ratio	500:1		
Surface treatment			
Interface	4wire SPI		
Back-light	LED Side-light type		
Drive IC	ST7789V		
Operation Temperature	-20~70	$^{\circ}$	
Storage Temperature	-30~80	$^{\circ}$	
Weight		g	

1.2 Features

n 4wire SPI interface.

1.3 Applications

- n MPOS Device.
- n Personal Navigation Device.
- n Other devices which require high quality displays.

Rev No	Issued Date.	Page
Α	2021.12.27	3/18

2.0 INPUT INTERFACE PIN ASSIGNMENT

LCM FPC connector is used for electronics interface

PinNo.	Symbol	Function
1	GND	Ground
2	VCC	Power Supply. 2.8V
3	SDA	SPI interface input/output pin
4	SCLK	This pin is used to be serial interface clock
5	CS	Chip select input pin
6	RESET	External reset input
7	LEDA	LED back light(Anode)
8	LEDK	LED back light(Cathode)
9	NC	NC
10	RS	Display data/command selection pin in 4-line serial interface

CTP FPC connector is used for electronics interface

1	GND	Ground
2	CTP_RST	Touch screen reset
3	CTP_INT	Touch screen interrupt signal
4	CTP_SDA	Touch screen data signal
5	CTP_SCL	Touch screen clock signal
6	VCC	Power Supply

Rev No	Issued Date.	Page
А	2021.12.27	4/18

3.0 ABSOLUTE MAXIMUM RATINGS

3.1 Electrical Absolute Rating

3.1.1 TFT LCD Module

Item	Symbol	Min	Max	Unit	Note
Digital supply voltage	VDDI	-0.3	+4.6	V	GND=0
Analog supply voltage	VCI	-0.3	+4.6	V	GND=0
Logic Signal Input Level	VIN	-0.3	VDDI+0.5	V	GND=0

3.1.2 Back-Light Unit

ltem	Symbol	Min	Max	Unit	Note
LED current	I _{BL}	60	80	mA	-
LED voltage	V _{BL}	2.8	3.2	V	-

3.2 Environment Absolute Rating

ltem	Symbol	Min	Max	Unit	Note
Operating temperature	TOPR	-20	70	°C	-
Storage temperature	TSTG	-30	80	°C	-

Note:

Permanent damage may occur to the LCD module if beyond this specification.

Shenzhen P&O Technology Co.,Limited Rev No Issued Date. Page A 2021.12.27 5/18

4.0 OPTICAL CHARACTERISTICS

4.1 Optical specification

Item		Symbol	Condition	Min	Туре	Max	Unit	Note	
White luminance (Center))	Lv	0.0	1	300	1	cd/m ²	(4)(5)(7)	
Response time	Response time		Θ=0 Normal	Normal Viewing		16		ms	(3)
Contrast ratio		CR	1		500	1		(2)(4)	
Color Chromaticity	white	Wx	Angle I _{BL} =80mA		0.301			(6)	
(CIE1931)	wille	Wy	IBL—COIII (0.337			(6)	
	Hor OL	ΘL			45				
Viewing Angle	1101	ΘR	CR≥10		45			(1)	
Viewing Angle	Ver	ΘU	CN210		50			(1)	
	Ver				20				
Brightness uniformity		Avg	Θ=0	80	90		%	(5)	
Color Gamut		NTSC	Θ=0		55		%	(6)	
Optima View Dir	ection			12 0'CLC	OCK			(1)	

4.2 Measuring Condition

n Measuring surrounding: dark room

n LED current IL: 80mA

n Ambient temperature: 25±2℃

n 15min. warm-up time

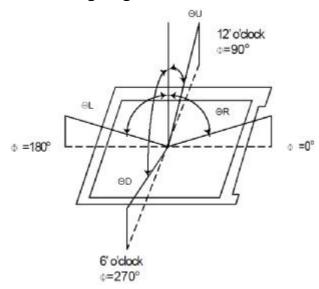
4.3 Measuring Equipment

n FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-7 for other optical characteristics.

n Measuring spot size: 20 ~ 21 mm

F	Rev No	Issued Date.	Page
	Α	2021.12.27	6/18

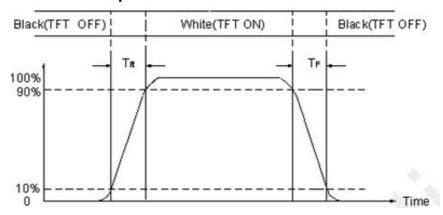
Note (1) Definition of Viewing Angle



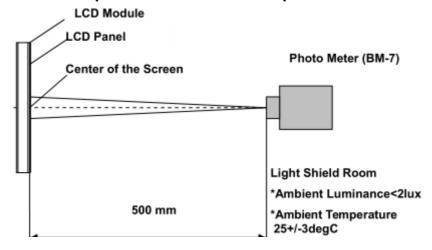
Note (2) Definition of Contrast Ratio(CR):

Measured at the center point of panel

Note (3) Definition of Response Time: Sum of TR and TF



Note (4) Definition of optical measurement setup



Rev No	Issued Date.	Page
А	2021.12.27	7/18

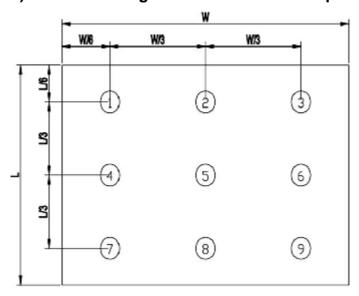
Note (5) Definition of brightness uniformity

The luminance uniformity is calculated by using following formula.

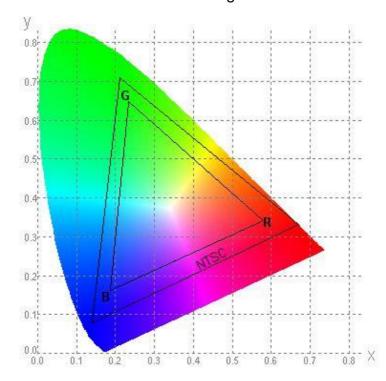
 \triangle Bp = Bp (Min.) / Bp (Max.)×100 (%)

Bp (Max.) = Maximum brightness in 9 measured spots

Bp (Min.) = Minimum brightness in 9 measured spots .



Note (6) Definition of Color of CIE1931 Coordinate and NTSC Ratio. Color gamut:

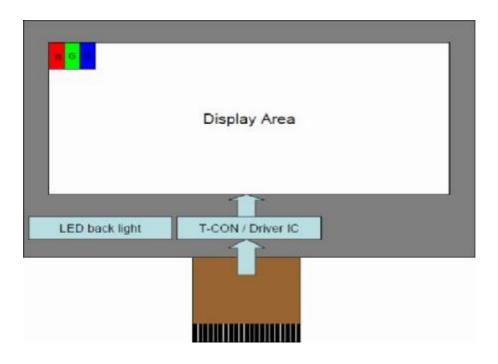


Note (7) Measured the luminance of white state at center point.

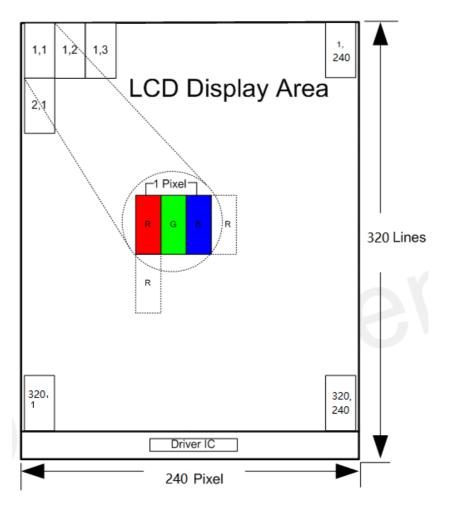
Shenzhen P&O Technology Co.,Limited	Rev No	Issued Date.	Page
	А	2021.12.27	8/18

5.0 BLOCK DIAGRAM

5.1 TFT LCD Module



5.2 Pixel Format



Rev No	Issued Date.	Page
Α	2021.12.27	9/18

6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Analog supply voltage	VDD	2.4	2.8	3.3	V	
Digital supply voltage	VDDI	1.65	1.8	3.3		
Input signal Voltage	VIH	0.7VDDI	-	VDDI	V	
Input signal Voltage	VIL	GND	-	0.3VDDI	V	

6.2 Back-Light Unit

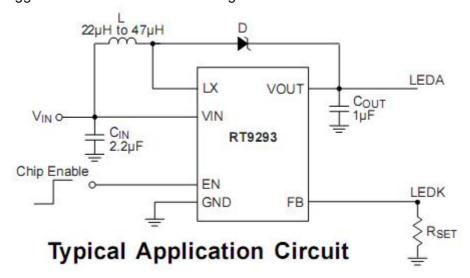
The backlight system is an edge-lighting type with 4 LED Dies. The characteristics of the LED are shown in the following tables.

Item	Symbol	Min	Тур	Max	Unit	Note
LED current	IL	-	60	80	mA	(2)
LED voltage	VL	-	2.8	3.2	V	
Operating LED life time	Hr	-	4500	5000	Hour	(1)(2)

Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition: $Ta=25\pm3$ °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note (2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL=80mA. The LED lifetime could be decreased if operating IL is larger than 100mA. The constant current driving method is suggested.

Note (3) Suggested schematic of LED backlight driver



Rev No	Issued Date.	Page
Α	2021.12.27	10/18

6.3 Interface Characteristics

Serial Interface Characteristics (4-line serial)

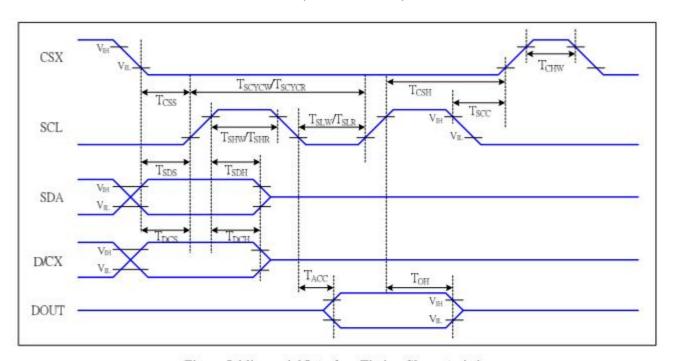


Figure 5 4-line serial Interface Timing Characteristics

VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=-30 to 70 ℃

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
	T _{CSS}	Chip select setup time (write)	15		ns	
CSX	T _{CSH}	Chip select hold time (write)	15		ns	
	T _{CSS}	Chip select setup time (read)	60		ns	
	T _{SCC}	Chip select hold time (read)	65		ns	
	T _{CHW}	Chip select "H" pulse width	40		ns	
SCL	T _{SCYCW}	Serial clock cycle (Write)	66		ns	unite command 0 date
	T _{SHW}	SCL "H" pulse width (Write)	15	P2 X	ns	-write command & data
	T _{SLW}	SCL "L" pulse width (Write)	15		ns	ram
	T _{SCYCR}	Serial clock cycle (Read)	150		ns	
	T _{SHR}	SCL "H" pulse width (Read)	60		ns	-read command & data
	T _{SLR}	SCL "L" pulse width (Read)	60		ns	ram
D/CX	T _{DCS}	D/CX setup time	10		ns	
D/GX	T _{DCH}	D/CX hold time	10		ns	
SDA	T _{SDS}	Data setup time	10		ns	
(DIN)	T _{SDH}	Data hold time	10	70 X	ns	
DOUT	T _{ACC}	Access time	10	50	ns	For maximum CL=30pF
DOOL	Тон	Output disable time	15	50	ns	For minimum CL=8pF

Shenzhen P&O Technology Co.,Limited Rev No Issued Date. Page A 2021.12.27 11/18

7.0 Reliability conditions

NO	Item	Conditions	Notes
1	High Temperature Storage	Ta=80℃±2℃, 72hrs	
2	Low Temperature Storage	Ta=-30℃±2℃, 72hrs	
3	High Temperature Operation	Ta=70°C±2°C, 72hrs(Operation state)	
4	Low Temperature Operation	Ta=-20°C ±2°C, 72hrs(Operation state)	
5	High Temperature and High Humidity (Storage)	Ta=+60°C, 90%RH, 72hrs	
6	Thermal Cycling Test (non operation)	-20°C(30min) → +70°C(30min), 10cycles	
7	Electro static Discharge	Human Body Mode $100pF\pm10\%/1500~\Omega\pm1\%$ Air $\pm8kV$ / contact $\pm6kV$ Consecutive 10times/ Each discharge $\frac{R}{V=0}$ CLASS STRESS LEVELS (LASS 11 2999-3999V CLASS 11 4998-15988 V	
8	Vibration test(with carton)	Total fixed amplitude:15mm Vibration Frequency:10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	
9	Drop (with carton)	Height: 60cm 1 corner, 3 edges, 6 surfaces	

Note: There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.

Shenzhen P&O Technology Co.,Limited	Rev No	Issued Date.	Page
onenzhen ao reennelegy con, zmitea	Α	2021.12.27	12/18

8.0 Precautions

8.1 Operation

Burn-in sometimes happens when the same character was displayed at along time. Therefore, to prevent Burn-in, it is recommended to set up a Screen-saver function.

8.2 Safety

The liquid crystal in the LCD is poisonous, DO NOT put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

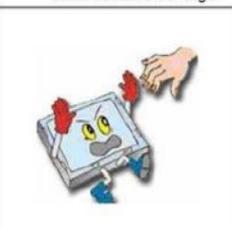
8.3 Handling

 a. The LCD module shall be installed flat, without twisting or bending. b. COF or FPC has narrow pattern width, so easily become open circuit by external force. DO NOT apply pressure to COF or FPC especially in bending area.
c. To avoid damage in appearance or malfunction, DO NOT subject the module to mechanical shock or to excessive force on its surface.
d. The polarizer attached to the display is very easy to damage, handle it with care to avoid scratching.
e. To avoid contamination on the display surface, DO NOT touch the display surface with bare hands. f. Provide a space so that the LCD module does not come into contact with other components.

Rev No	Issued Date.	Page
Α	2021.12.27	13/18

8.4 Static Electricity

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge.



- The LCD module shall be installed flat, without twisting or bending. Ground soldering iron tips, tools and testers when they operate.
- Ground your body when handling the products.
- DO NOT apply voltage to the input terminal without applying power supply.
- DO NOT apply voltage that exceeds the absolute maximum rating.
- e. Store the products in an anti-electrostatic container.
- Peel off protect tape, attached to polarizer, slowly to minimize ESD damage.

8.5 Storage



Store the products in a dark place at $+5 \sim +25$ degree C, low humidity (50%RH or less).

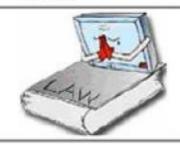
DO NOT store the products in an atmosphere containing organic solvents or corrosive gases.

8.6 Cleaning



- DO NOT wipe the polarizer with dry cloth, as it might cause scratch.
- Wipe the polarizer with a soft cloth soaked with petroleum IPA, other chemical might damage.

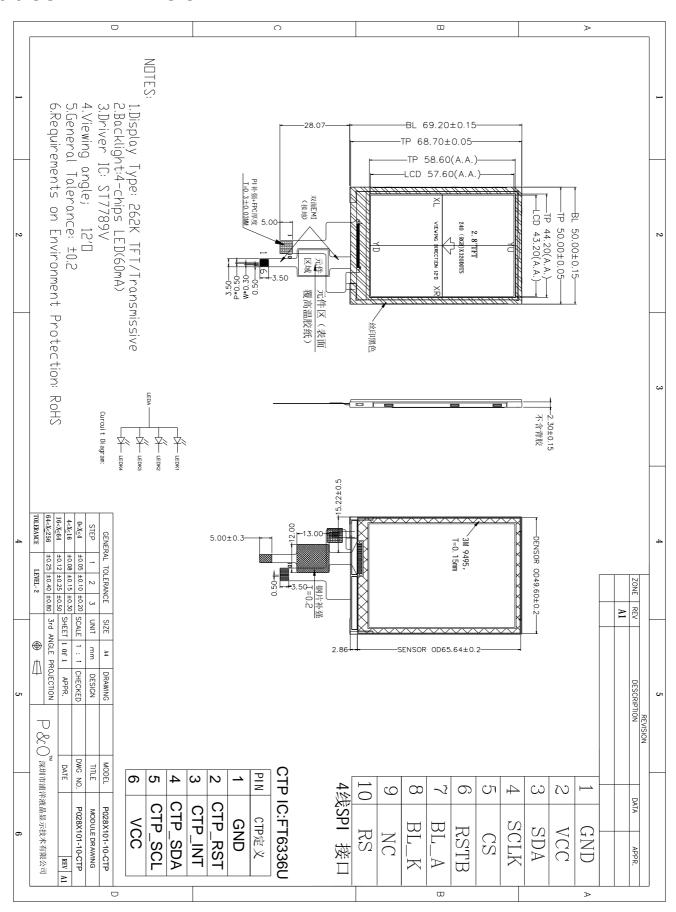
8.7 Waste



When dispose of LCD module, manage it at the production waste according to the relevant laws and regulations.

Rev No	Issued Date.	Page
Α	2021.12.27	14/18

9.0 OUTINE DIMENSION



Shenzhen P&O Technology Co.,Limited	Rev No	Issued Date.	Page
ononization i dio roomiology con, zimitod	Α	2021.12.27	15/18

1 0.0 LOT MARK

10.1 Location of Lot Mark

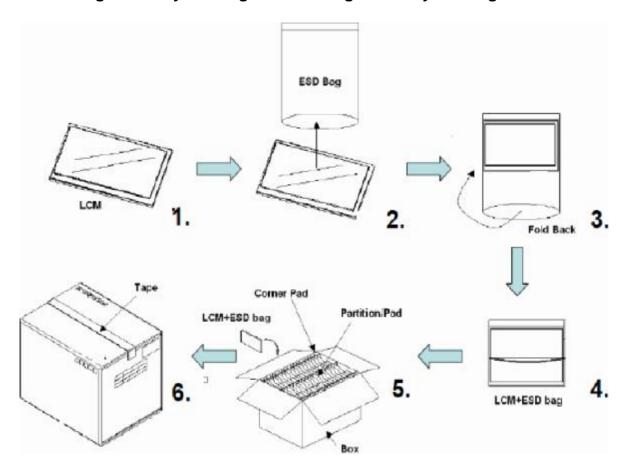
- (1) Location: The label is attached to the backside of the LCD module.
- (2) Detail of the Mark: as attached below.
- (3) This is subject to change without prior notice.

11.0 PACKAGE SPECIFICATION

11.1 Packing form

LCM Model	LCM Qty. in the box	Inner Box Size (mm)	Notice
	TDB	TDB	

11.2 Packing assembly drawings11.2 Packing assembly drawings



Items	Material	Notice
Box	Corrugated Paper Board	AB Flute
Partition/Pad	Corrugated Paper Board	A/B Flute
Corner Pad	Corrugated Paper Board	AB Flute
ESD bag	PE	

Shenzhen P&O Technology Co.,Limited Rev No Issued Date. Page A 2021.12.27 16/18

12.0 Items and Criteria:

12.1 Guarantee

APEX warrants the quality of our products for *1 year* (from the date of delivery). If there are functional defects found during the period of warranty, the defective products would be replaced on a one-to-one backapex would not be responsible for any direct /indirect liabilities consequential to any parties.

All the products should be stored or used as specified conditions described in these sheets. If module productions are not stored or used as specified conditions, herein, it will be void the *1 year* warranty(guarantee).

12.2 Visual inspection criterion in cosmetic

(1) Glass defect

		Glass defect	
NO	Defect	Criteria	Remark
1	Dimension(Minor)	By engineering diagram	↑ ↑
2	Cracks(Major)	Extensive crack 【Reject】	

(2) LCM appearance defect

NO	Defect	Criteria		Remark
		Spec	Permissible Qty	1.ψ=(L+W)/2, L: Length, W: Width
		ψ≦0.10mm	Disregard	2. Disregard if out of A.A.
1	Round type(Minor)	0.10mm<ψ ≦ 0.20mm	3	1
		0.20mm<ψ	0	₩ V
		Spec	Permissible	1. L: Length, W: Width
			Qty	2. Disregard if out of A.A.
	Line type(Minor)	W ≦ 0.03mm	Disregard	1
_		L≦3.0mm and	2	← ∟ →
2		0.03mm <w≦0.05mm< td=""><td></td><td></td></w≦0.05mm<>		
		L≦3.0mm and	1	V 7/1
		0.05mm <w≦0.10mm< td=""><td></td><td>W</td></w≦0.10mm<>		W
		W>0.10mm orL>3.0mm	0	1000
		Spec.	Permissible	1.ψ=(L+W)/2 , L: Length,
			Qty	W: Width
3		ψ≦0.20mm	Disregard	2.Disregard if out of A.A.
	Polarizer	0.20mm<ψ≦ 0.30mm	2	
	dent(Minor)	0.30mm<ψ≦ 0.50mm	1	

Rev No	Issued Date.	Page
Α	2021.12.27	17/18

(3) FPC

NO	Defect	Criteria	Remark
1	Copper peeling(Minor)	Copper peeling [Reject]	
2	Golden finger	FPC golden finger broken, dead fold, indentation makes FPC surface broken 【Reject】 Tin plating layer(or gold plating) scratch, but not hurt circuit 【Accept】	
		Except circuit, other position scratch but not expose metal wire 【Accept】	
3	Pin	FPC PI layer delamination 【Reject】 Material and color are inconsistent with sample, FPC burrs 【Reject】 FPC Pin deformation but not affect function. 【Accept】 FPC Pin area is dirty 【Reject】 Other than FPC Pin area is dirty but not affect function 【Accept】	
4	Golden finger	Golden finger edge has burrs, foreign material [Reject] Golden finger oxidation (dark), uneven electroplating, pinhole, foreign material [Reject] Golden finger soldering pad crack exceeds 1/3 length of soldering pad, and soldering pad crack exceed 2 Pins [Reject] Golden finger tin plating(or gold plating)scratch, but not hurt circuit [Accept] Other than golden finger area scratch but not expose metal circuit [Accept]	
5	FPC Silk printing	Ghosting, incomplete silk printing, wrong printing [Reject]	
6	FPC Circuit line width	Line width deviation exceed 1/3 line width 【Reject】	

(4) Black tape

NO	Defect	Criteria	Remark
1	Shift(Minor)	IC exposed 【Reject】	
2	No black tape(Minor)	No black tape 【Reject】	

(5) Silicon

NO	Defect	Criteria	Remark
1	Amount of silicon (Minor)	ITO exposed 【Reject】	

Shenzhen P&O Technology Co.,Limited A 2021.12.27 18/18

12.3 Visual inspection criterion in electrical display

NO	Defect	Criteria			Remark
1	No display (Major)	Not	allowed		
2	Missing line (Major)	Not allowed			
3	Darker or lighter Line (Major)	Not allowed			
4	Weak line(Major)	By limited sample			
5	Bright / Dark point (Minor)	Spec. Permissible Qt Bright 1 point 2 point 2		e Qty	1:1sub-pixel: 1R or 1G or1B 2:Point defect area ≥ 1/2 sub pixel.
6	Round type (Minor)	Spec Ψ≤0.10mm 0.10mm<ψ≤ 0.20mm 0.20mm<ψ		Permissible Qty Disregard 3	1.ψ=(L+W)/2, L: Length, W: Width 2. Disregard if out of A.A. W
7	Line type (Minor)	Spec. $W \le 0.03 mm$ $L \le 3.0 mm \text{ and } 0.03 mm < W \le 0.05 mm$ $L \le 3.0 mm \text{ and } 0.05 mm < W \le 0.10 mm$ $W > 0.10 mm \text{ or } L > 3.0 mm$		Permissible Qty Disregard 2 1	1. L: Length, W: Width 2. Disregard if out of A.A.
8	Mura (Minor)	By 5% ND filte	r invisible		

9.2.4. Others

- 1. Issues that are not defined in this document shall be discussed and agreed with both parties. (Customer and supplier)
- 2. Unless otherwise agreed upon in writing, the criteria shall be applied to both parties. (Customer and supplier)