Sulaimon Adijat Ajoke 2190652

1a.

Rate

Binary digit: 0 0 1 0 1 1 0 0

NRZ

FM

MFM

Manchester

1b)
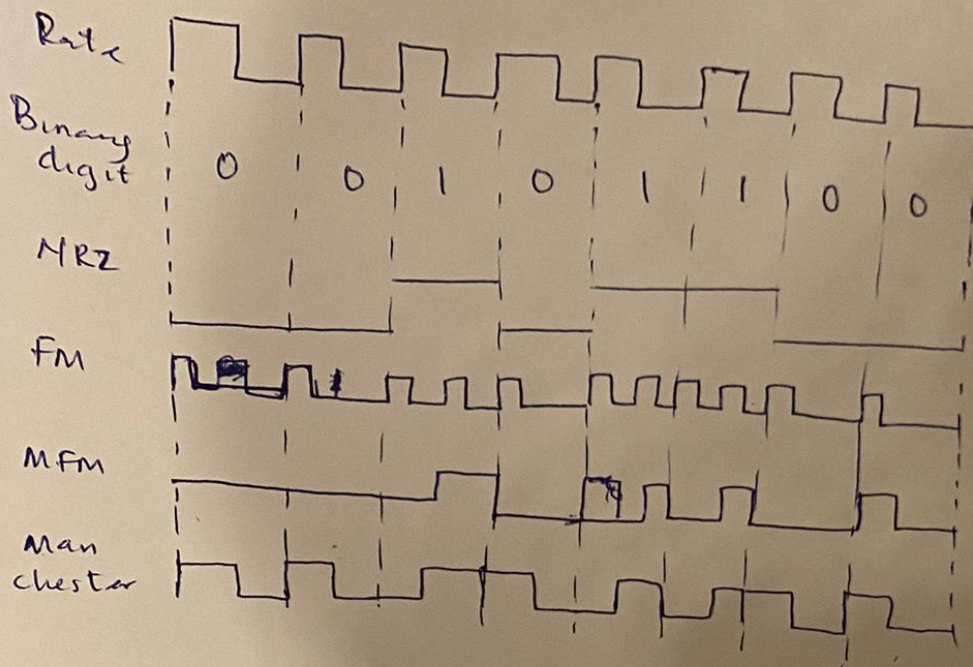
No	Name	Data width	Clock Rate	Transistor	Address width
1	8080	8 bits	2 MHz	6 000	16 bits
2	8085	8 bits	3-6 MHz	6500	16 bits
3	8086	16 bits	5-10 MHz	29000	20 bits
4	8088	8 bits	5-16 MHz	29000	20 bits
5	80286	16 bit	5-25 MHz	134000	24 bits
6	80386	32 bits	12-40 MHz	275000	32 bits
7	80486	32 bit	16-100 MHz	More than 1m	32 bits
8	Pentium	32/64 bits	60-300 MHz	7.5m	64 bits
9	Pentium II	64 bits	233-450 MHz	7.5 million	64 bits
10	Pentium III	64 bits	450 MHz -1.4 GHz	188 million	64 bits
11	Pentium IV	64 bits	1.5 GHz		

Question 2

Initial state [Off]

· Turn on

wait state

user presses button —

changes to Serving user State

Check flow level — current flow

Serving user State ← control lever
Desired flow

guarded transition ·

open door

c)

$1 =$ Taken

$0 =$ not taken