

Shenzhen K&D Technology Co.,Ltd

SPECIFICATION FOR LCD MODULE

Customer						
Product M	KD070D10-50TB-A57					
Sample code:						
Designed by	Che	cked by		Approved by		
Final Appro	Final Approval by Customer					
LCM Mac	hinery OK		LCM O	<		
Checked By			NG, Pr	oblem survey:		
LCM Disp	olay OK					
Checked By	Approve	d By				

^{**}The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

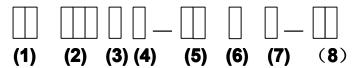
Revision History

Version	Contents	Date	Note
A	Original	2013-5-21	
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1 Numbering System



No	Definition	Specifications
(1)	TFT LCM Productor No.	KD Kingdisplay technologiy Co.,Ltd
(2)	Display monitor opposite angle line size	Unit :mm or mmm (size <10 inch: takes two integers; size >=10 inch: takes three integers)
(3)	Productor Types	D Digital photo frame / DVD GGPS MMP PMobil-Phone
(4)	Productor Development Series No.	By two figures characters expression from 01 to 99
(5)	Interface PIN Number	By two figures characters expression from 01 to 99
(6)	With Touch Panel Or Not	TWith T/P; NWithout T/P
(7)	LCD Type	AAUO; MCMO; CCPT; BBOE; LLG; WWintek; HHSD; TTianma; YHydis; IINNOLUX; SSharp
(8)	Productor Development edition No.	By The English litters : A 1~ Z9

4/26

2 Scope

This specification applies to the TFT LCD module which is designed and manufactured by LCM Factory of Shenzhen K&D Technology Co.,Ltd.

3 Normative Reference

GB/T4619-1996 《 Liquid Crystal Display Test Method》

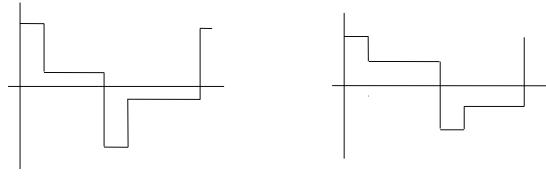
GB/T2424 《Basic environmental Testing Procedures for Electric and Electronic Products.》

GB/T2423 《Basic Testing Procedures for Electric and Electronic Products》 IEC61747-1 《SIXTH PARTGB2828`2829-87《National Standard of PRC》

4 Definitions

4.1 Definitions of Vop

The definitions of threshold voltage Vth1, Vth2 the following typical waveforms are applied on liquid crystal by the method of equalized voltage for each duty and bias.



[selected waveform]

I non-selected waveform 1

- ① Vth1: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of selected waveform $(f_f=80Hz, \Phi=10^\circ \theta=270^\circ \text{ at } 25^\circ\text{C})$
- ② Vth2: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of non-selected waveform $(f_f=80\text{Hz}, \Phi=10^\circ \theta=270^\circ \text{ at } 25^\circ\text{C})$
 - ③ Vop: (Vth1(50%)+Vth2(50%))/2 ($f_f=80$ Hz, $\Phi=10^{\circ}$ $\theta=270^{\circ}$ at 25°C)

4.2 Definition of Response Time Tr, Td

①Tr: The time required which the brightness of segment becomes 10% from 100% when waveform is switched to selected one from non-selected one. (f_f=80Hz, Φ =10 ° θ =270° at 25°C)

②Td: The time required which the brightness of segment

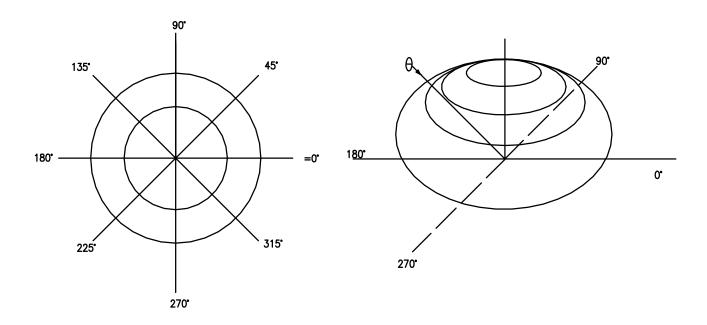
becomes 90% from 10% when waveform is switched to selected one from selected one. (f_f=80Hz, Φ =10° θ =270° at 25°C)

4.3 Definition of Contrast Ratio Cr

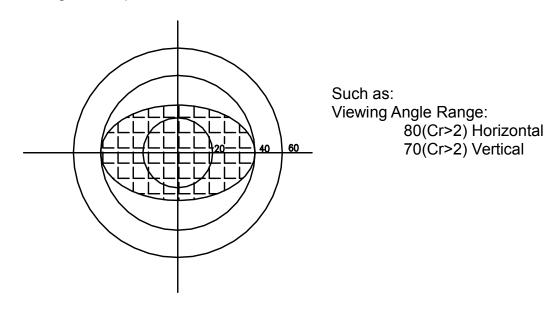
Cr=A/B

- 1 A: Segments brightness in case of non-selected waveform
- ② B: Segments brightness in case of selected waveform

4.4 Definition of Angle and Viewing Range



Angular Graph: Constrast Ratio



5 Technology Specifications

5.1 Feature

This single-display module is suitable for use in Multidedia Player products.

The LCD adopts one backlight with High brightness 18-lamps white LED.

- 1) Construction: 7" a-Si color TFT-LCD ,White LED backlight and FPC.
- 2) LCD:
 - 2.1 Amorphous-TFT 7-inch display, transmissive, normally white type.
 - 2.2 800(RGB) × 480 dots Matrix.
 - 2.3 Narrow-contact ledge technique.
- 3) RGB interface.
- 4) Video signal interface: Parallel RGB.

5.2 Mechanical Specifications

Item	Specifications	Unit			
Dimensional outline	164.9(W) ×100.0(H) ×4.9(D)	mm			
Active area	154.08(W) × 85.92(H)	mm			
Pixel size	63.2(W) ×RGB×179(H)	um			
Resolution	800(RGB) ×480	pixel			
Luminance	180 (TYP)	cd/m2			

5.3 Absolute Max. Rating

Item	Symbol	Values		Unit	Remark
item	Symbol	Min.	Max	Offic	Remaik
	DV_DD	-0.5	5.0	V	
Dower voltage	AV_DD	-0.5	15	V	
Power voltage	V_{GH}	-0.3	40	V	
	V_{GL}	-20	0.3	V	
Operation temperature	Тор	-10	60	${\mathfrak C}$	
Storage temperature	Тѕт	-20	70	${\mathfrak C}$	

Note: The absolute maximum rating values of this product not allowed to be exceeded at any times. Should be module be used with any of absolute maximum ratings exceeded. The characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

5.4 Electrical Characteristics

Note 1

Item	Cymbol	Symbol			Unit	Remark	
item	Symbol	Min.	Тур	Max	Offic	Remark	
	DV_DD	3.0	3.3	3.6	\	Note 2	
Power voltage	AV_DD	10.1	10.3	10.5	V		
Fower voitage	V_{GH}	17.5	18	18.5	V		
	V_{GL}	-8.5	-8	-7.5	V		
Input signal voltage	V_{COM}	3.1	3.3	3.4	V		
Input logic high voltage	V _{IH}	$0.7DV_{DD}$	-	DV_DD	V	Note 3	
Input logic low voltage	V_{IL}	0	-	0.3DV _{DD}	\	Note 3	

Note 1:Be sure to apply DV_{DD} and V_{GL} to the LCD first, and then apply V_{GH} .

Note 2: DV_{DD} setting should match the signals output voltage(refer to Note 3) of Customer's system board.

Note 3: DCLK,HS.VS,RESET,U/D,L/R,DE,R0-R7,G0-G7, G0-G7,MODE,DITHB.

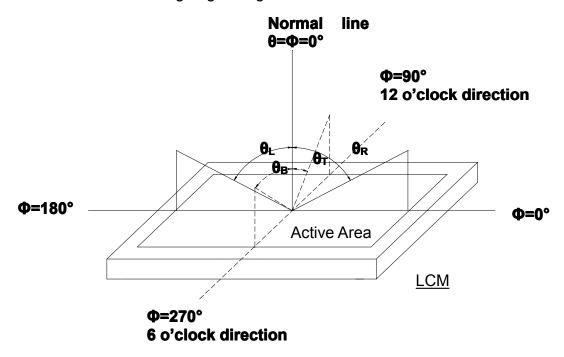
5.5 Optical specifications

ltom	Cumbal	Condition	Values			Unit	Remark
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	θι	Ф=180°(9 o'clock)	70	80	-		
Viewing	θ_{R}	Φ=0°(3 o'clock)	70	80	-		Note 1
angle (CR≥ 10)	θτ	Ф=90°(12 o'clock)	60	70	-	degree	14010 1
	θв	Φ=270°(6 o'clock)	65	75	-		
Response	T _{ON}		-	10	20	msec	Note 3
time	T _{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		350	500	-	-	Note 4
Color	W _X	Normal	0.249	0.299	0.349	-	Note 2
chromaticity	W _Y	θ=Φ=0°	0.281	0.331	0.381	-	Note 5 Note 6
Luminance	L		150	180	_	cd/m2	Note 6
Luminance uniformity	Yu		70	80	-	%	Note 6,7

Test Conditions:

- 1. DVDD=3.3V, I_{LED}=120mA, the ambient temperature is 25℃.
- 2. The test systems refer to Note 2.

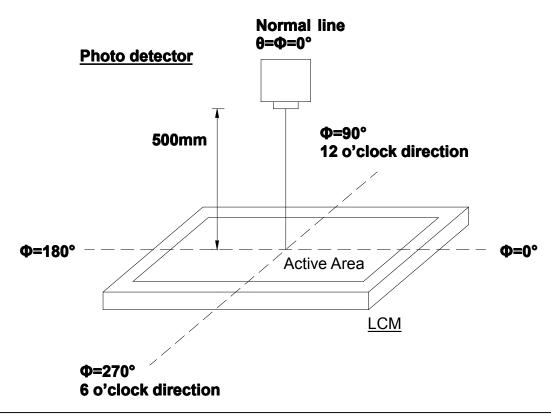
Note 1: Definition of viewing angle range



Note 2: Definition of optical measurement system.

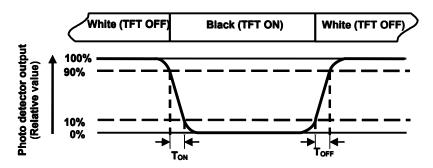
The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Viewing angle is measured by ELDIM-EZ contrast/Height :1.2mm ,Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/

Field of view: 1° /Height: 500mm.)



Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 4: Definition of contrast ratio

Contrast ratio (CR) = $\frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

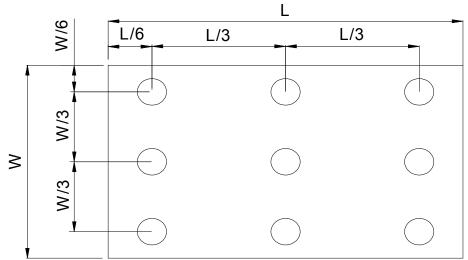
Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is I_{LED}=120mA.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas. Every measuring point is placed at the center of each measuring area.

Luminance Uniformity
$$(Yu) = \frac{B_{min}}{B_{max}}$$

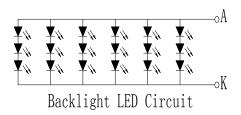
L-----Active area length W----- Active area width



B_{max}: The measured maximum luminance of all measurement position. B_{min}: The measured minimum luminance of all measurement position.

5.6 LED back light specification (18 White Chips)

Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage	Vf	lf=120mA	9	9.6	9.9	V
Uniformity (with L/G)	ΔBp	lf=120mA	80	-	-	%



5.7 Interface Pin Connections

Pin No.	Symbol	I/O	Function	Remark
1	VLED+	Р	Power for LED Backlight(Anode)	
2	VLED+	Р	Power for LED Backlight(Anode)	
3	VLED-	Р	Power for LED Backlight(Cathode)	
4	VLED-	Р	Power for LED Backlight(Cathode)	
5	GND	Р	Power ground	
6	V _{COM}	ı	Common voltage	
7	DV_{DD}	Р	Power for Digital Circuit	
8	MODE	I	DE/SYNC mode select	Note 1
9	DE	ı	Data Input Enable	
10	VS	ı	Vertical Sync Input	
11	HS	ı	Horizontal Sync Input	
12	B7	I	Blue data(MSB)	
13	В6	ı	Blue data	
14	B5	ı	Blue data	
15	B4	I	Blue data	
16	В3	I	Blue data	
17	B2	I	Blue data	
18	B1	I	Blue data	Note 2
19	В0	I	Blue data(LSB)	Note 2
20	G7	ı	Green data(MSB)	

21	G6	I	Green data	
22	G5	I	Green data	
23	G4	I	Green data	
24	G3	I	Green data	
25	G2	I	Green data	
26	G1	I	Green data	Note 2
27	G0	I	Green data(LSB)	Note 2
28	R7	I	Red data(MSB)	
29	R6	I	Red data	
30	R5	I	Red data	
31	R4	I	Red data	
32	R3	I	Red data	
33	R2	I	Red data	
34	R1	I	Red data	Note 2
35	R0	I	Red data(LSB)	Note 2
36	GND	Р	Power Ground	
37	DCLK	I	Sample clock	Note 3
38	GND	Р	Power Ground	
39	L/R	I	Left / right selection	Note 4,5
40	U/D	I	Up/down selection	Note 4,5
41	V_{GH}	Р	Gate ON Voltage	
42	V_{GL}	Р	Gate OFF Voltage	
43	AV _{DD}	Р	Power for Analog Circuit	
44	RESET	I	Global reset pin.	Note 6
45	NC	-	No connection	
46	V _{COM}	I	Common Voltage	
47	DITHB	I	Dithering function Note 7	
48	GND	Р	Power Ground	
49	NC	-	No connection	
50	NC	-	No connection	

I: input, O: output, P: Power

Note 1: DE/SYNC mode select. Normally pull high.
When select DE mode, MODE="1", VS and HS must pull high. When select SYNC mode, MODE="0", DE must be grounded.

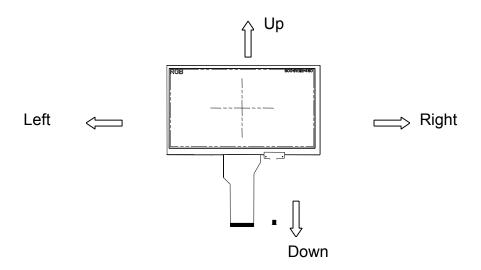
Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode

Setting of scan control input		Coopping direction	
U/D	L/R	Scanning direction	
GND	DV _{DD}	Up to down, left to right	
DV_{DD}	GND	Down to up, right to left	
GND	GND	Up to down, right to left	
DV _{DD}	DV _{DD}	Down to up, left to right	

Note 5: Definition of scanning direction. Refer to the figure as below:



Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

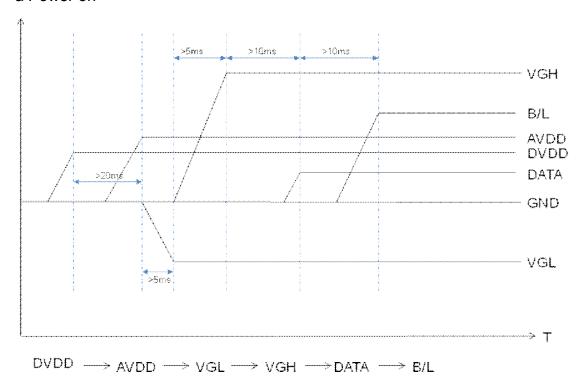
Note 7: Dithering function enable control, normally pull high. When DITHB="1", Disable internal dithering function, When DITHB="0", Enable internal dithering function,

6 Signal timing diagram

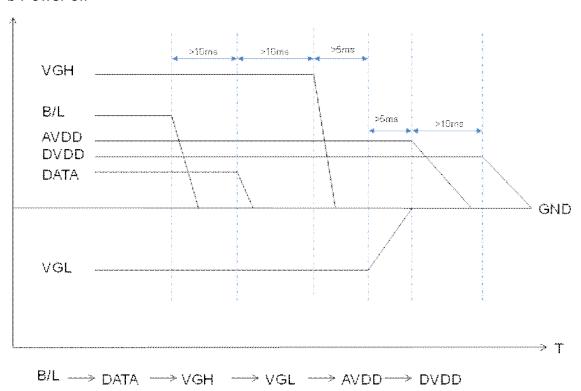
6.2 Signal Timing Diagram

6.2.1 Power ON/OFF Sequence

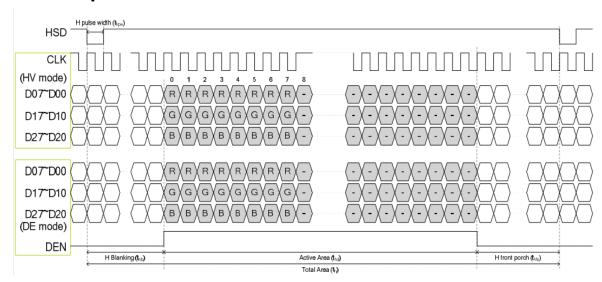
a Power on

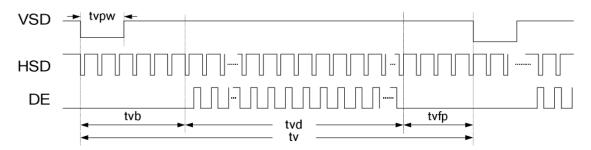


b Power off



6.2.2 Data input format





6.2.3 Timing Diagram

Item	Symbol	Values		Unit	Remark	
item	Cymbol	Min	Тур	Max	Offic	Kemark
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK frequency	fck	-	30	50	MHz	
One horizontal line	th	889	928	1143	DCLK	
HS pulse width	thpw	1	48	255	DCLK	
HS Blanking	thb	88	88	88	DCLK	
HS Front Porch	thfp	1	40	255	DCLK	

Item	Symbol	Values			Unit	Remark
item	Gymbol	Min	Тур	Max	Offic	Kemark
Vertiacl dispaly Area	tvd	-	480	-	TH	
VS period time	tv	513	525	767	TH	
VS pulse width	tvpw	3	3	255	TH	
VS Blanking	tvb	32	32	32	TH	
VS Front Porch	tvfp	1	13	255	TH	

7 Reliability Test Conditions And Methods

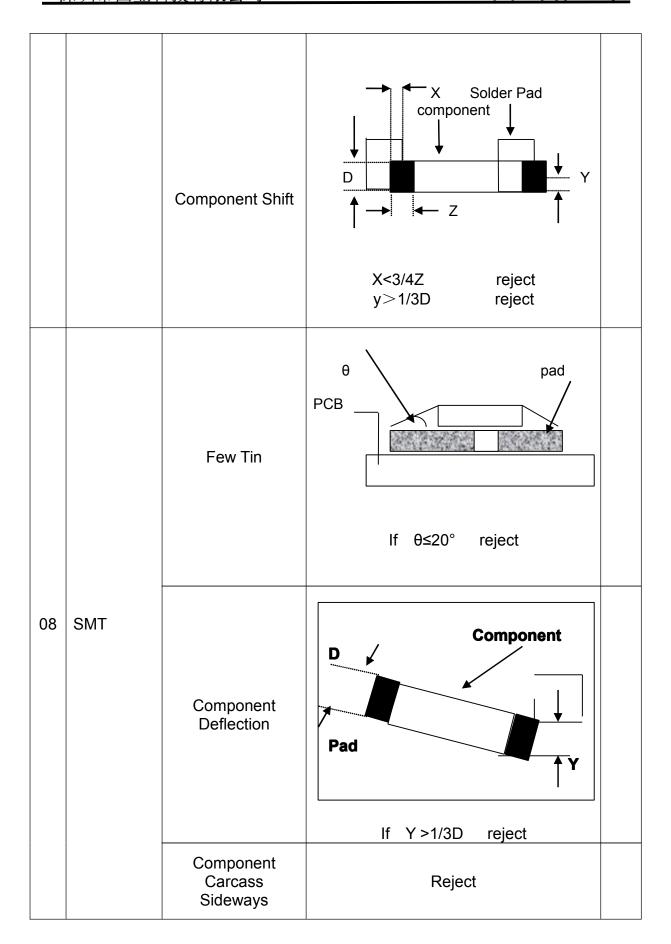
NO	Item	Condition	Method
1	High / Low Temperature Storage	70℃/-20℃ 120hrs	Check and record every 48Hrs
2	High / Low Temperature Life	60°C/-10°C 120hrs (operating mode)	Check and record every 48Hrs
3	High Temperature High Humidity Operating	60℃,90% RH, 96Hrs	Check and record every 48hrs
4	Thermal Shock	-20°C(30Min) → 25°C(5Min) 70°C(30Min) (conversion time, : 5 sec) 20 cycles	Each 10 cycles end , check
5	Static Electricity	Gap mood: ±1KV~±8KV (10 times air discharge with positive/negative voltage voltage gap : 1kv) Touch mood: ±1KV~±4KV	Each discharge end, Check the Electrical Characteristics

8 Inspection standard

No	Item	Criterion				
01	Outline Dimension	In accord with drawing				
02	Position-fin ding Dimension Assemble Dimension	In accord with drawing				
		Round type: non display 3.1 Small area LCD Unit: mm				
		$\begin{array}{c c} & \xrightarrow{y} \\ & \xrightarrow{y} \end{array}$	Dimension	Qualified Quantity		
		→ × ← ↑	D≪0.1	Ignore		
			0.1 <d≤0.15< td=""><td>2</td></d≤0.15<>	2		
	LCD black spots, white spots		D>0.15	0		
03		3.2Large area LCD				
	(Round type)	<u> </u>	Dimension	Qualified Quantity		
		\rightarrow x \leftarrow \uparrow	D≤0.1	Ignore		
			0.1 <d≤0.15< td=""><td>2</td></d≤0.15<>	2		
			0.15 <d≤0.20< td=""><td>1</td></d≤0.20<>	1		
			D>0.20	0		
		C-STN : if D>0.1 , unqualified				

		Unit : mm	4.1	Small a	area LCD	
		w	Length	Width	Qualified Quantity	
			-	≤0.015	Ignore	
			≤1.0	0.015 <w≤< td=""><td>2</td></w≤<>	2	
			≤2.0	0.025	1	
			≤1.0	0.025 <w≤ 0.05</w≤ 	1	
	LCD black		-	D>0.05	According to circle	
04	spots, white spots (Line Style)		4.2Larg	je area LCD		
	(Line Otyle)	Line Style)	Length	Width	Qualified Quantity	
			-	≤0.015	Ignore	
			├	≤2.0	0.015 <w≤ 0.025</w≤ 	2
			≤1.0	0.025 <w≤ 0.05</w≤ 	1	
			-	D>0.05	According to circle	
05	LCD Scratch , Threadlike	Same to NO.3 of sightline and su (2)Same to NO.	circle rface of LCE		015 , unqualified nd viewing area	
06	POL	It is not admissible that POL is beyond the edge of glass, else, unqualified. It is essential that POL is over the 50 percent of width of frame, else, unqualified. According to the drawing in case of special definition.				
07	IC/FPC Bonding	Scratch		Reject		

		Intensity Of Adhesion	If lower than specification, reject		
	Gold Fold Twist		Reject		
07	Silicon		According to outline, no gold outside, seal can not be higher than LCD		
07	Bonding	FPC Gold Sever	Reject		
		Lack of Component Polarity Inverse	If exist, reject		
		Leak Solder、 Virtual Solder	If exist, reject		
		Short Circuit In Solder Point	If exist, reject		
08	SMT	Tin Ball	If exist, reject		
		Tin Acumination	If visual, reject		
		Height Solder Point	If higher 0.5mm than component. reject		
		Height of component	Either side higher 0.5mm than component, reject		



		Component Carcass Sideways	If exist with visual inspection , reject	
Lot Tin		Lot Tin	A: Tin accrete the solder side completely , hollowly ,Ok B: Tin accrete the solder side completely , full circle arc , ok C: Jointing include whole solder side, height of tin>50 percent of height of component, reject	
		Few Tin	A: Tin accrete the solder side completely , hollowly ,Ok B: height of tin > 1/3 of solder side of component , ok C: height of tin ≤ 1/3 of solder side of component, reject	
08	SMT	Normal Jointing side		
		Short circuit \ Open circuit	Forbid	
09	09 Light Quality of CSTN Display		1. Rolling strake with visual inspection, forbid 2. Differentness of color in viewing area with visual inspection (full white, red, green, blue), forbid 3. Display change with visual inspection, forbid	

			Х	у	
		white	±0.05	±0.05	
		Red	±0.05	±0.05	
	Color Of	Green	±0.05	±0.05	Drive LCD under normal
10	COIOI OI	Blue	±0.05	±0.05	condition, 25°C Φ=0 ⊖=0
	Coordinate	According or samp approved	to the specification		Test white red green blue with DMS Record
			In accord with		ndition is according to on ocation is in Follow Picture orightness instrument tozero, ainst the surface of LCD, easure", record when the steady. (OGAWA-3298)
11	11 Brightness product specification				
					Measure location
12	CR (Max)	According to specification			ng to product specification re instrument (DMS-501)
13	Response time	Accord specific	•		ng to product specification re instrument (DMS-501)
14	Viewing angle	Accord specific			ng to product specification re instrument (DMS-501)
15	Vibration、 Ring	Compare sample c sup	ustomer	Compare with the sample customer supply when assemble	
16	Frequency Of FPC Bend	Accordin use of p (main F foldawa phone thousa	FPC of ay cell		Measure instrument Bend angle : 150° C in the casement when customer supply

9 Handling Precautions

9.1 Mounting method

The LCD panel of Daxian LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

9.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl), Salfur (S)

If goods were sent without being sili8con coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Salfur (S) from customer, Responsibility is on customer.

9.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

9.4 packing

- Module employ LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

9.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean

malfunction or out of order with LCD's, which will come back in the specified operation temperature.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
 - Usage under the maximum operating temperature, 50%Rh or less is required.

9.6 storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

9.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

10 Precaution for use

10.1

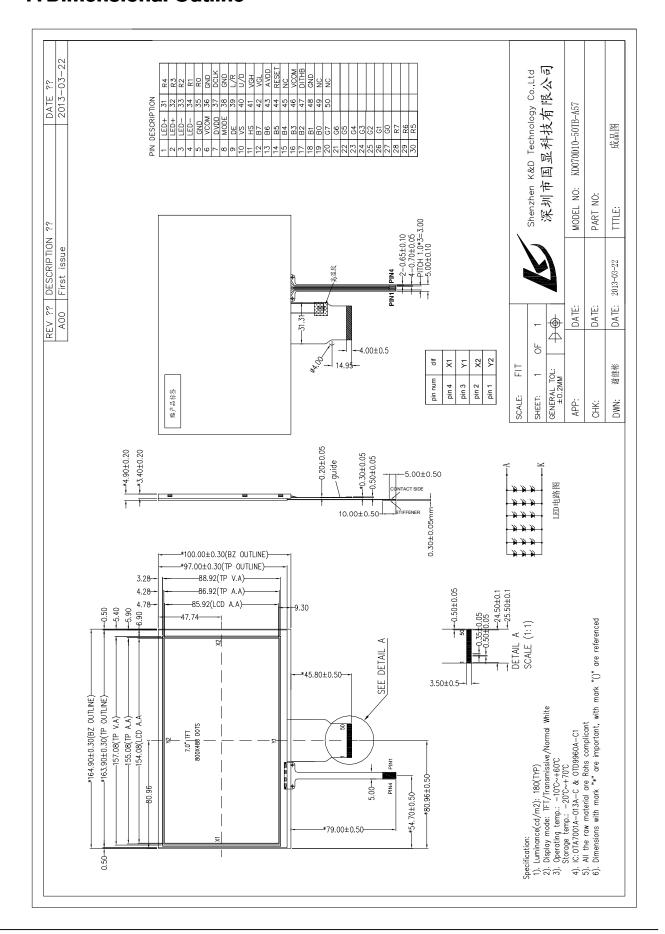
A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

10.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported, and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

11 Dimensional Outline



12. Package Drawing

TBD