



PORT	AF0 SYS	AF1 TIM1 TIM2	AF2 TIM3 TIM4 TIM5	AF3 TIM9 TIM10 TIM11	AF4 I2C1 I2C2 I2C3	AF5 SPI1 SPI2 SPI4	AF6 SPI3 SPI4 SPI5	AF7 SPI3 USART1 USART2	AF8 USART6	AF9 I2C2 I2C3	AF10 OTG_FS	AF12 SDIO	ADC	配置										
PA	0	MCO1	CH1 / ETR	CH1	CH1 CH2		MOSI	NSS	CTS RTS TX RX CK			CMD	ADC1_IN0	X										
	1		CH2	CH2									ADC1_IN1	X										
	2		CH3	CH3									ADC1_IN2	X										
	3		CH4	CH4									ADC1_IN3	X										
	4		CH1 / ETR	CH1 CH2									ADC1_IN4	X										
	5												ADC1_IN5	X										
	6												ADC1_IN6	X										
	7												ADC1_IN7	X										
	8												SCL SMBA	CK TX RX CTS RTS	SOF VBUS ID DM DP	D1 D2	X							
	9																X							
	10																X							
	11																X							
	12																X							
	13																X							
	14																X							
15	JTDI	CH1 / ETR	NSS	NSS	TX	X																		
PB	0	BOOT JTDO JTRST	CH2N CH3N	CH3 CH4	CH1 CH2	SMBA SCL SDA SCL SDA	SCK MISO MOSI	SCK MISO MOSI	SCK NSS	RX TX RX	SDA SDA SDA	D0 D3 D0 D4 D5 D7	ADC1_IN8	X										
	1		CH2	CH1 CH2									CH1 CH2 CH3 CH4	CH1 CH1	SCL	NSS SCK	MOSI	TX RX	SDA SDA	D0 D3 D0 D4 D5 D7	ADC1_IN9	X		
	2																				X			
	3																				X			
	4																				X			
	5																				X			
	6																				X			
	7																				X			
	8																				X			
	9																				X			
	10																				X			
	12		RTC	BKIN									SMBA			NSS SCK MISO MOSI	NSS SCK	SCK				D6 CK		X
	13			CH1N																				X
	14			CH2N																				X
15	CH3N	X																						
PC	13													X	EXTON WAKE									
	14													X										
	15													X										