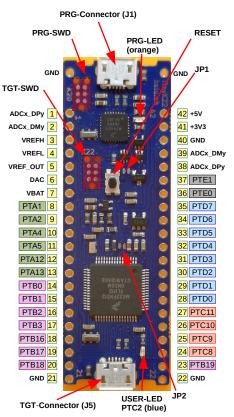
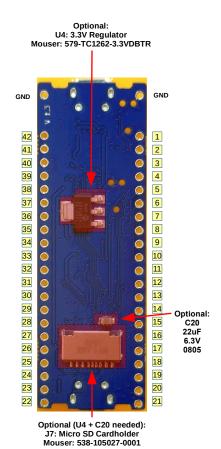
TinyK22 V 1.3

HOCHSCHULE LUZERN





PTA1 R 6 PTA2 PTA4 PTA5 PTA12 0 PTA13 PTB0 0 8 8 PTB1 9 9 1 D С PTB2 12 12 PTB3 13 13 0 PTB16 R PTB17 Т Τ PTB18 0 PTB19 1 4b 2 PTC8 4 PTC9 5b 3 5 6b PTC10 6 PTC11 7b 7 D PTD0 0 PTD1 1 5b PTD2 R 2 C То PTD3 3 D Т 1 PTD4 4 PTD5 5 C 6b PTD6 6 O 7b R PTD7 Т 7 Т PTE0 ח 4a PTE1 ΙR С I/O 5a

UART FTM R:RxD, T:TxD 0..7: Channel C:SCL, D:SDA C:Clock, O:SOUT, I:SIN 4..13:Channel 2..3: Input

UART2

R

Т

I2C SPI

PTA1

PTA2

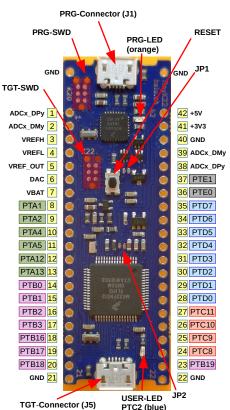
UART to Programmer UART1 or LPUART0 (PTC3 & PTC4)

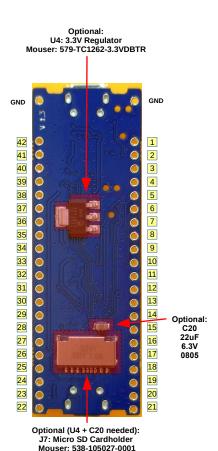
ADC0 SP11

27102020

TinyK22 V 1.3

Lucerne University of Applied Sciences and Arts HOCHSCHULE LUZERN





PTA4					Т										
PTA5					2										
PTA12						0									
PTA13						1									
PTB0						0			С				8	8	
PTB1						1			D				9	9	
PTB2									С				12	12	
PTB3									D				13	13	
PTB16		R										0			
PTB17		Т										Ι			
PTB18							0								
PTB19							1								
PTC8								4						4b	2
PTC9								5						5b	3
PTC10								6		С				6b	
PTC11								7		D				7b	
PTD0								0							
PTD1								1			С		5b		
PTD2	R			R				2	С		0				
PTD3	Т			Т				3	D		Ι				
PTD4					4										
PTD5					5								6b		
PTD6		R			6							0	7b		
PTD7		Т			7							1			
PTE0			Т							D				4a	
PTE1			R							С		I/O		5a	

R:RxD, T:TxD 0..7: Channel C:SCL, D:SDA C:Clock, O:SOUT, I:SIN 4..13:Channel 2..3: Input UART FTM I2C SPI ADC CMP

UART to Programme UART1 or LPUART0 (PTC3 & PTC4)

