BSEC Binary Size Information

BSEC version: 2.2.0.0

1. Platform Supported Currently

Platform	Compiler	TYPE
Cortex-ARM	Keil5 ARMCC	Cortex-M0, M0+, M3, M4, M4F, M7
Cortex-ARM	arm-none-eabi-gcc	Cortex-M0, M0+, M3, M33, M33F, M4, M4F, M7, ARMv8-m
Cortex-A*	arm-none-eabi-gcc	Cortex-A7, A73
AVR_8bit	Atmel Studio AVR-GCC	MegaAVR, XMEGA
AVR_32bit	Atmel Studio AVR-GCC	32-bit AVR UC3
ESP	xtensa-lx106-elf-gcc	ESP8266, ESP32
MSP430	msp430-elf-gcc	MSP430
IAR	IAR compiler	Cortex-M0, M0+, M3, M4, M4F, M7
Raspberry pi	Arm-linux-gnueabihf-gcc	armv6, armv8-a
Windows	TDM-GCC	x86, x64
Linux	GCC	x86, x64

2. Binary Size on different platform

Platform Type	Compiler	ROM(.text+.data) in bytes	RAM(.data+.bss) in bytes
Cortex-M0	Keil5 ARMCC	27522	4176
Cortex-M0+		27522	4176
Cortex-M3		26138	4176
Cortex-M4		26138	4176

Cortex-M4F		27896	4176
Cortex-M7		26156	4176
Cortex-M0	arm-none-eabi-gcc	34021	4176
Cortex-M0+		34021	4176
Cortex-M3		30597	4176
Cortex-M33		30617	4176
Cortex-M33F		30750	4176
Cortex-M4		30613	4176
Cortex-M4F		31101	4176
Cortex-M7		30833	4176
ARMv8-m		33621	4176
Cortex-A7		31293	4176
Cortex-A73		36489	4192
AVR8bit-MegaAVR	Atmel Studio AVR-GCC	65758	4113
AVR8bit-XMEGA		64642	4113
AVR 32bit		34818	4652
ESP32	Elf-gcc	36405	4192
ESP8266		40969	4192
Msp430		51344	4126
Armv6	Arm-linux-gnueabihf-gcc	79280	4192
Armv8-a		79400	4192
Cortex-M0	IAR7	29924	4176
Cortex-M0+		29924	4176
Cortex-M3		29666	4176
Cortex-M4		29662	4176
Cortex-M4F		30152	4176
Cortex-M7		29662	4176
Cortex-M0	IAR8	30148	4176
Cortex-M0+		30148	4176

Cortex-M3		29414	4176
Cortex-M4		29414	4176
Cortex-M4F		30036	4176
Cortex-M7		29414	4176
Windows_x64	TDM-GCC	52036	4256
Windows_x86		48260	4256
Linux_m64	GCC	58499	4208
Linux_m32		71626	4180

*Note:

1. ROM/RAM size is basic requirement of BSEC. Static Lib File size doesn’t count.
2. M4F/M33F means the MCU with FPU.