

# Tong Xing

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INTERESTS	<b>Operating Systems, Virtualization, Heterogeneous Memory Systems, Heterogeneous-ISA CPU Systems</b>	
EDUCATION	<b>Ph.D.</b> at the School of Informatics, The University of Edinburgh, UK Topic: Operating System and Virtualization Environments Thesis: Redesigning Data-Center Supervisory Software for the New Generation of Peripheral Interconnects <b>M.S.</b> at the School of Engineering & Science, Stevens Institute of Technology, USA Program: Computer Science Thesis: Software Development Methods to Support Heterogeneous-ISA Application Migration <b>B.S.</b> at the School of Energy and Environment, Xihua University, China Program: Intelligent Building and Electrical Engineering	Jan 2021 – Nov 2024 Sep 2018 – Jun 2020 Sep 2013 – Jun 2017
PUBLICATIONS IN PROGRESS	<b>T. Xing</b> , A. Barbalace, “CXL shared memory: SMP or distributed OS? Inter-kernel IPC and namespaces!” <i>Under summition of HotOS’25</i> <b>T. Xing</b> , J. Yang, W. Boncela, J. Picorel, A. Barbalace, “Smart VM: The Art of Tiered Memory Virtual Machines Placement” <i>Plan for submission of Eurosys’26</i>	
PUBLICATIONS	<b>T. Xing</b> , C. Xiong, T. Wei, J. Balkind, B. Ravindran, A. Sanchez, A. Barbalace, “Stramash: A Fused Kernel Operating System For Cache-Coherent, Heterogeneous-ISA Systems,” <i>will be in Proceedings of the ACM International Conference on Architectural Support for Programming Languages and Operating Systems(Asplos’25)</i> , Neitherlands, April 2025 D. Mvondo, <b>T. Xing</b> , A. Barbalace, “UTwinVM: Reliable hints on the effects of hypervisor updates on VMs in the Cloud,” <i>in Proceedings of the 25th ACM/IFIP International Middleware Conference(Middleware’24)</i> , Hong Kong, Pages 103 - 116, Dec 2024. <b>T. Xing</b> , C. Xiong, J. Picorel, Y. Chuan, A. Barbalace “Maximizing VMs’ IO Performance on Overcommitted CPUs with Fairness”, <i>in Proceedings of the 2023 ACM Symposium on Cloud Computing(SoCC’23)</i> , Santa Cruz, Pages 93–108, Oct 2023. H. Chuang*, K. Manaouil*, <b>T. Xing*</b> , A. Barbalace, P. Olivier, B. Heerekar, B. Ravindran, “Aggregate VM: Why Reduce or Evict VM’s Resources When You Can Borrow Them From Other Nodes,” <i>in Proceedings of the Eighteenth European Conference on Computer Systems (EuroSys ’23)</i> , Rome, Pages 469–487, May 2023. <b>(Co-First Author)*</b> <b>T. Xing</b> , H. Tajbakhsh, I. Haque, M. Honda, A. Barbalace, “Towards Portable End-to-End Network Performance Characterization of SmartNICs,” <i>in Proceedings of 13th ACM Asia-Pacific Workshop on Systems (APSys’22)</i> , Online, Pages 46–52, Aug 2022. <b>T. Xing</b> , A. Barbalace, P. Olivier, ML. Karaoui, W. Wang, B. Ravindran, “H-Container: Enabling Heterogeneous-ISA Container Migration in Edge Computing,” <i>ACM Transactions on Computer Systems (TOCS)</i> , Volume 39, Issue 1-4, Mar 2022. A. Barbalace, ML. Karaoui, W. Wang, <b>T. Xing</b> , P. Olivier, B. Ravindran, “Edge computing: the case for heterogeneous-ISA container migration”, <i>in Proceedings of the 16th ACM SIGPLAN/SIGOPS International Conference on Virtual Execution Environments (VEE ’20)</i> , Pages 73–87, Online, Mar 2020.	
TUTORIALS & PRESENTATIONS	<b>UK Systems Research</b> , Presentation at the 6th and 7th UK Systems Research workshop, Durham, UK, Characterizing SmartNIC Architectures, <b>Tong Xing</b> , Michio Honda, and Antonio Barbalace (University of Edinburgh) Maximize the IO performance of overcommitted VM, <b>Tong Xing</b> , Antonio Barbalace (University of Edinburgh) <b>H-Container Tutorial</b> , <b>Tong Xing</b> , Antonio Barbalace, presentation and tutorial at the First Popcorn Summit, 8th of Nov 2019, Virginia Tech, Blacksburg, VA, USA, Extension the H-Container to support Docker on local testbed and AWS Prepared the tutorial slide, test case and demo, help the Office of Naval Research (ONR) team in the tutorial	

**Programming Heterogeneous-ISA Platforms with Popcorn Linux OS and Compiler Framework,**  
 Antonio Barbalace, Pierre Olivier, Binoy Ravindran, tutorial at SOSP 2019, 27th of October 2019,  
 Huntsville, ON, CAN, \*

Prepared tutorial slides material, test virtual machines, test experiments, and helped the audience in the tutorial

**WORK  
EXPERIENCE**

**Research Associate (Post-Doc) – University of Edinburgh**

Nov 2024 – present

New OS design for next generation data center

**Huawei Munich Research Center – PhD Intern**

Aug 2023 – Feb 2024

Tiered Memory management in virtualization

Supervised by Dr. Javier Picorel

**SERVICE**

**Shadow PC** for EuroSys’25

**Student Volunteer** for the EuroSys’21

**AWARDS &  
SCHOLARSHIPS**

**PhD Scholarship**

Jan 2021

School of Informatics, The University of Edinburgh

**Excellence in Graduate Research Award**

May 2020

Stevens Institute of Technology

**Student Travel Grant**

ASPLOS’20, SoCC’23

**Provost’s Masters’ Fellowship**

2018

Stevens Institute of Technology

**SKILLS**

**Areas of expertise**

Linux kernel, KVM, Container(LXC/Docker), SmartNICs(DPU), Emulator(Qemu), PIM(Upmem)

**Programming Language**

C, C++, Python, Assembler 8086, ARM Assembly, Makefile, Shell Script

**Spoken Language**

English, Chinese

**HOBBYS**

Swimming, Cooking, Eating, Gym, Sauna

**REFERENCES**

**Dr.Antonio Barbalace** Senior Lecturer – Institute for Computing Systems Architecture, School of Informatics, The University of Edinburgh

**Dr.Michio Honda** Reader – Institute for Computing Systems Architecture, School of Informatics, The University of Edinburgh

**Dr.Binoy Ravindran** Full Professor – Department of Computer Science, The Bradley Department of Electrical and Computer Engineering, Virginia Tech

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\*Tong Xing substantially contributed to this tutorial, but at the time of submitting the proposal for this tutorial Tong was not actively working on the project