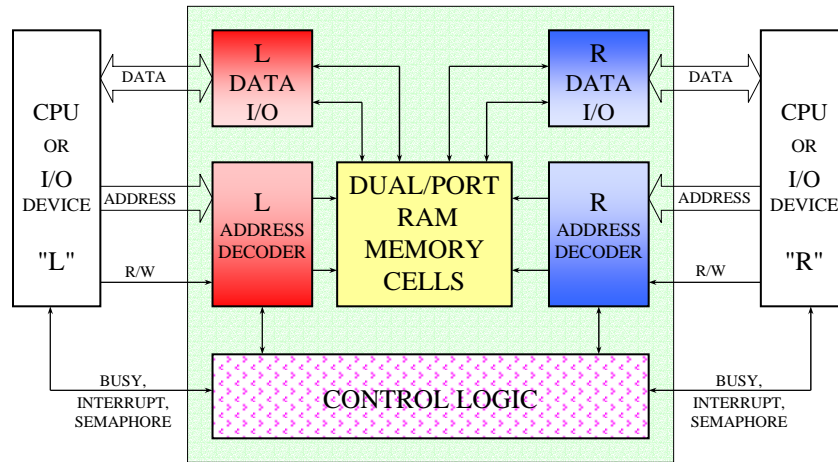
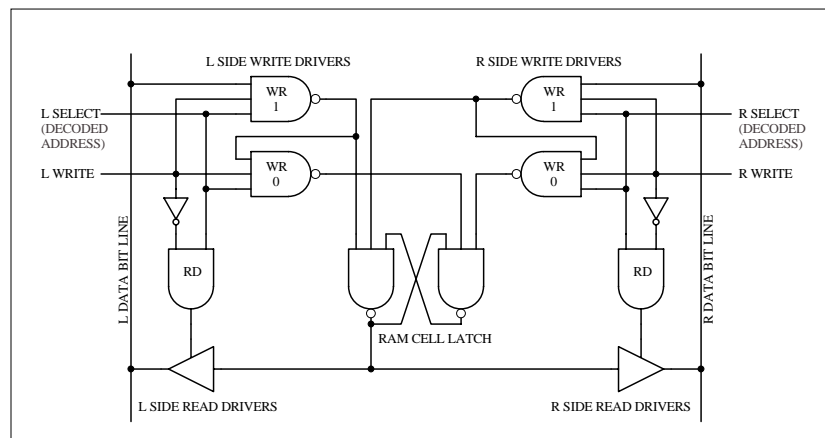


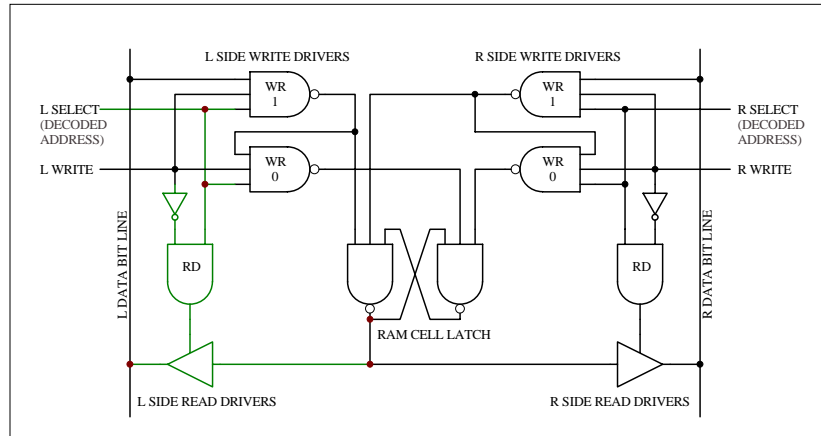
DUAL-PORT MEMORY BLOCK DIAGRAM



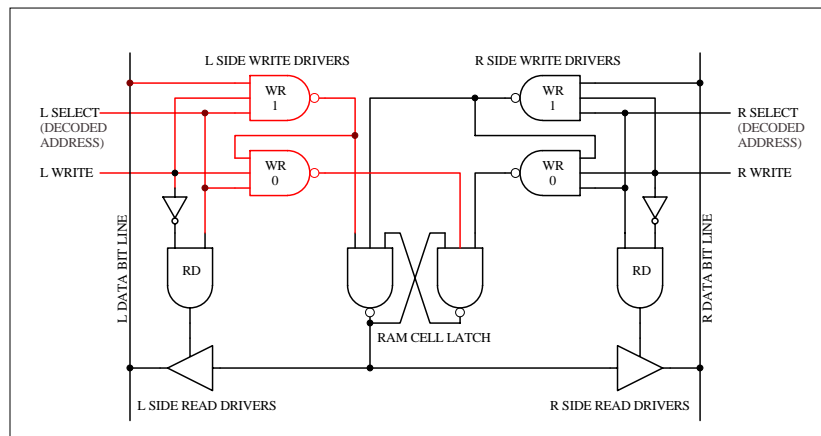
DUAL-PORT RAM CELL



DUALL-PORT RAM CELL - Read

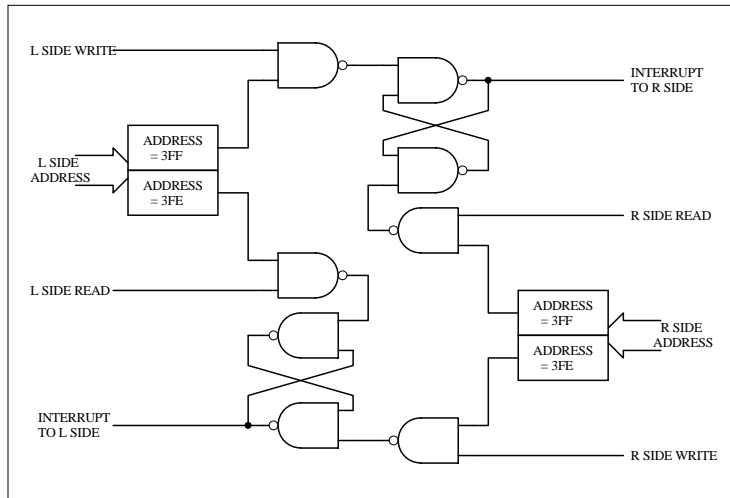


DUALL-PORT RAM CELL - Write



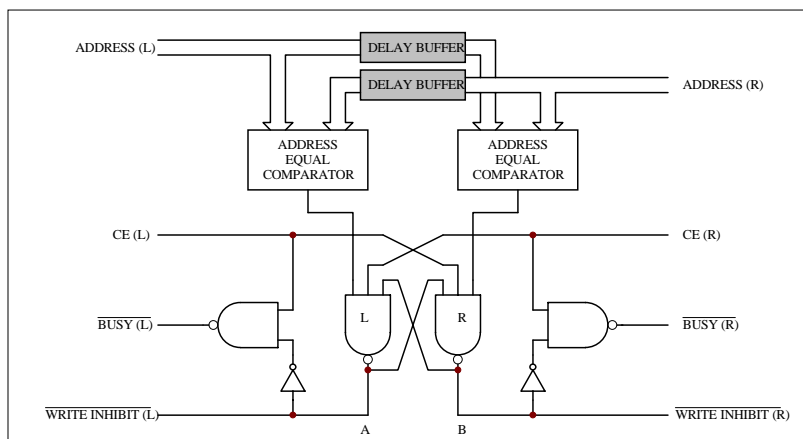
DUALL-PORT RAM

Interrupt Logic



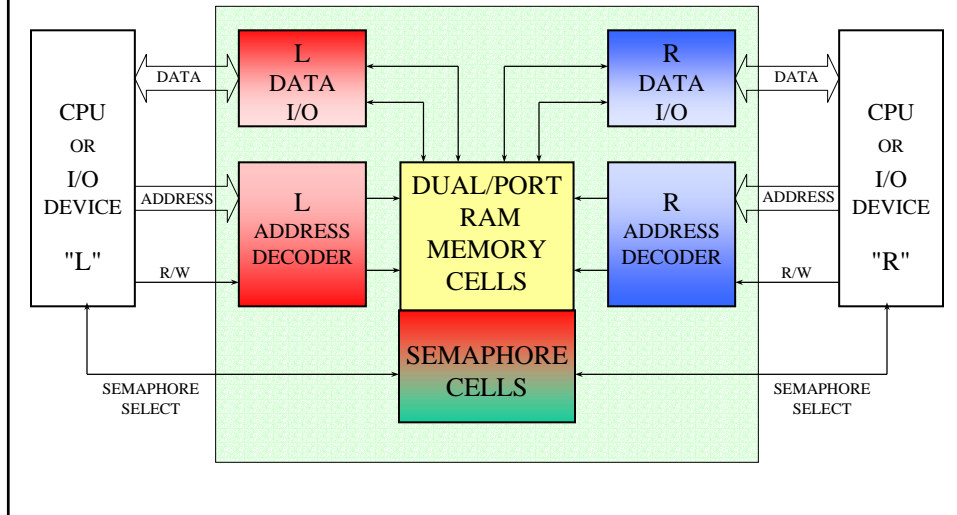
DUUALL-PORT RAM

Busy Logic



DUAL-PORT RAM

Semaphore Logic



DUAL-PORT RAM

Semaphore Logic cell

