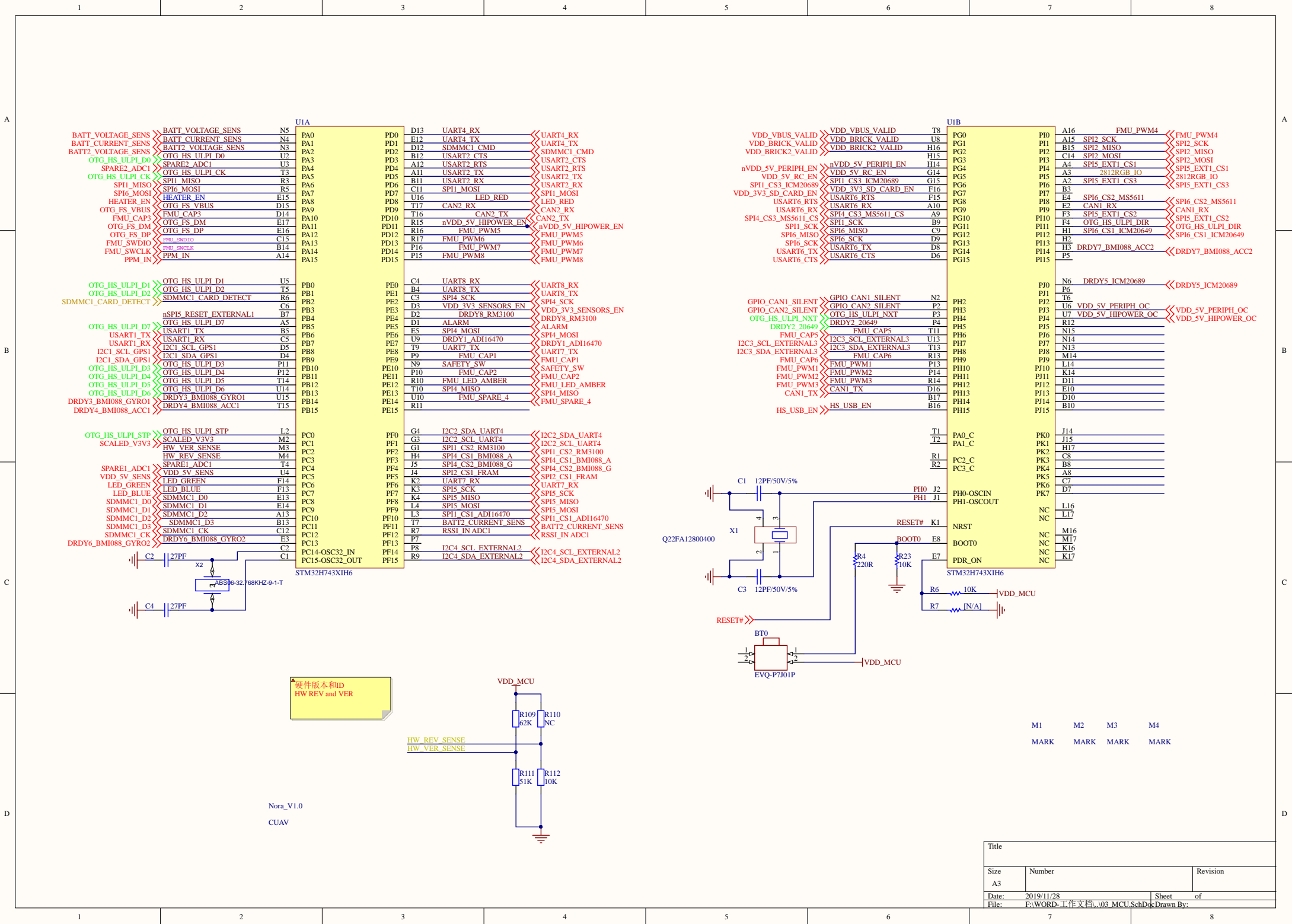
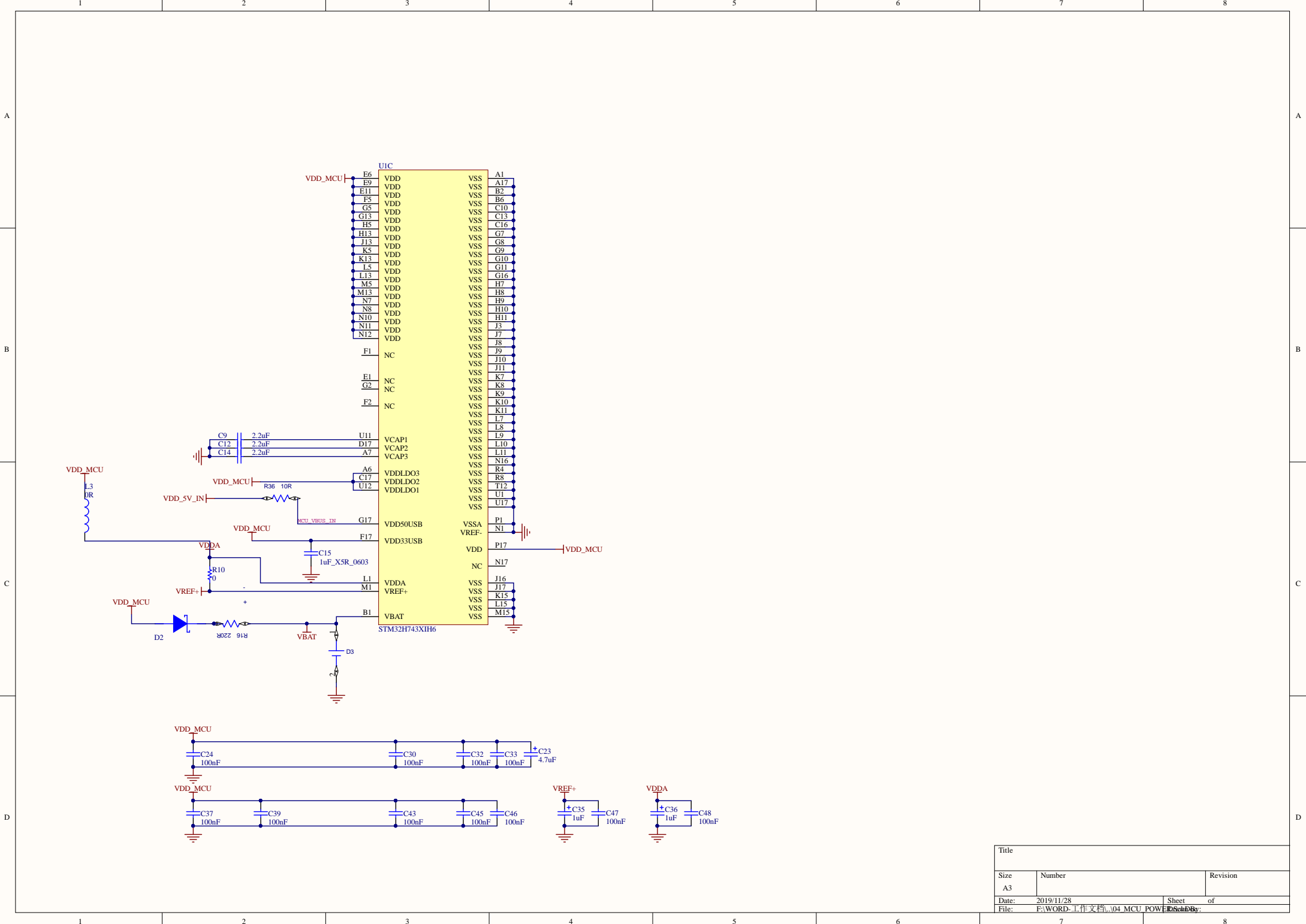


Title		
Size A3	Number	Revision
Date: 2019/11/28	Sheet of	
File: F:\WORD- 工作文档\02 LED.SchDoc Drawn By:		



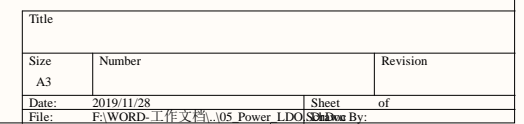


Title		
Size A3	Number	Revision
Date: 2019/11/28	Sheet of	
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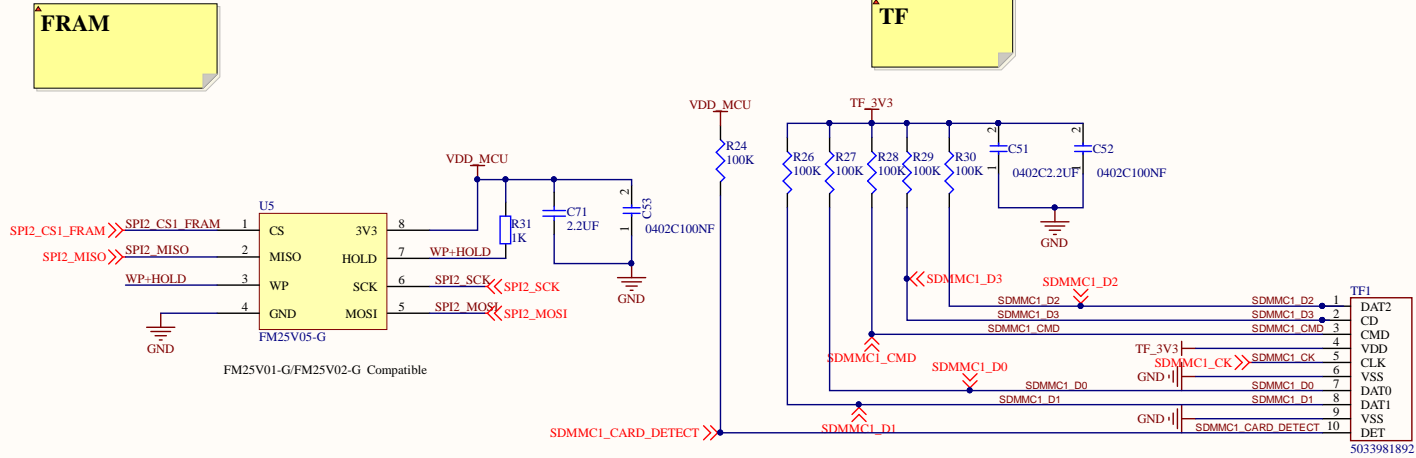
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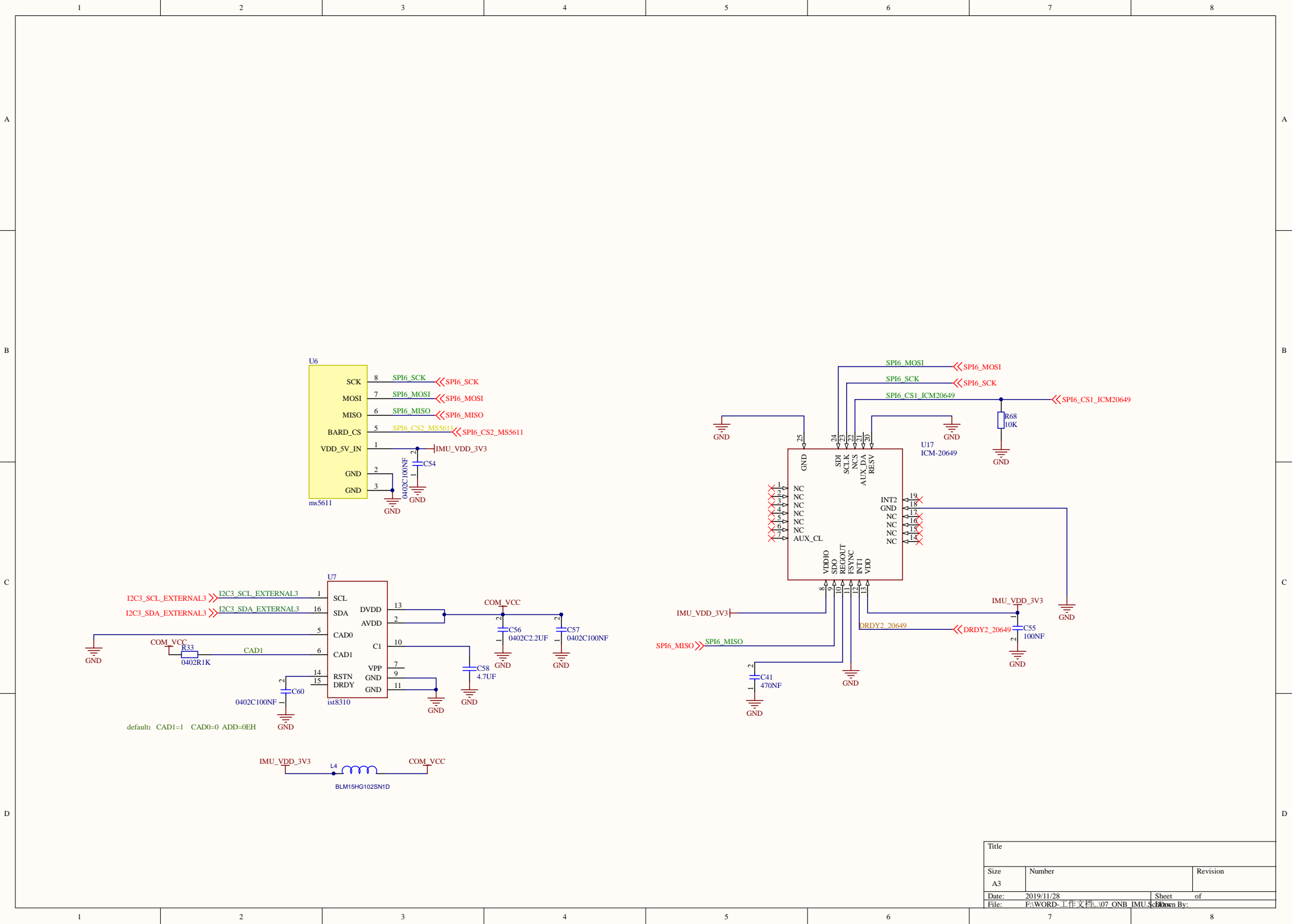
C



TF CARD + MTD of STM32H7



Title		
Size	Number	Revision
A3		
Date:	2019/11/28	Sheet of
File:	E:\WORD>工作文档\06 TF CARD+MTD of STM32H7	



Title		
Size	Number	Revision
A3		
Date:	2019/11/28	Sheet of
File:	E:\WORD> 工作文档\07 ONB IMU.Sch	
Drawn By:		

A

B

C

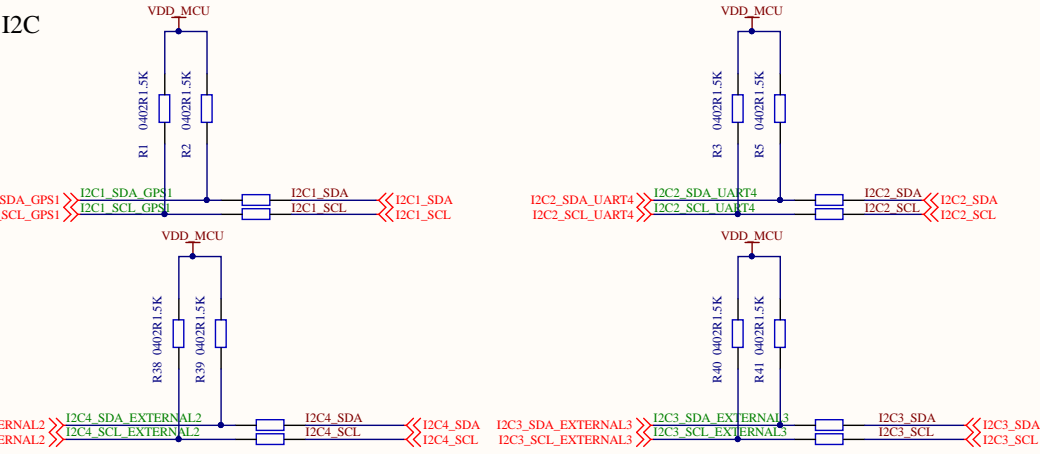
D

A

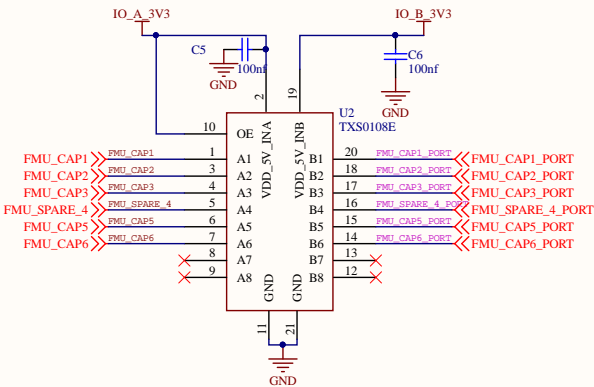
B

C

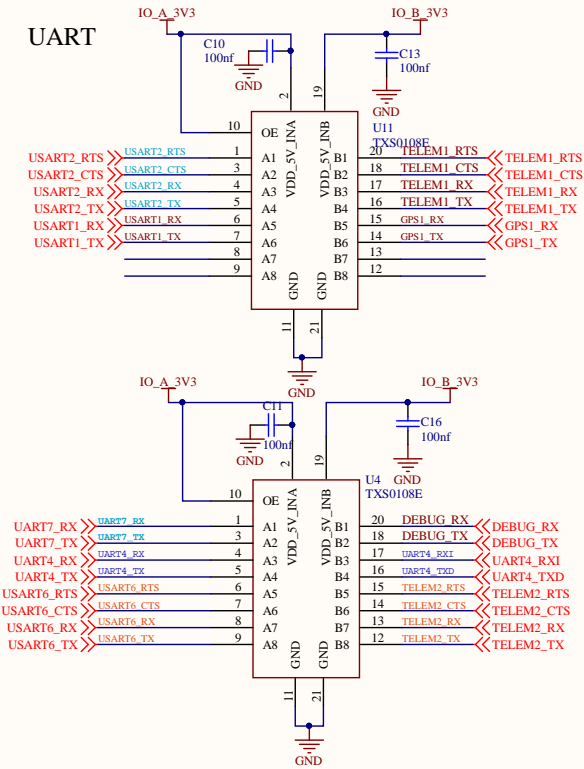
D



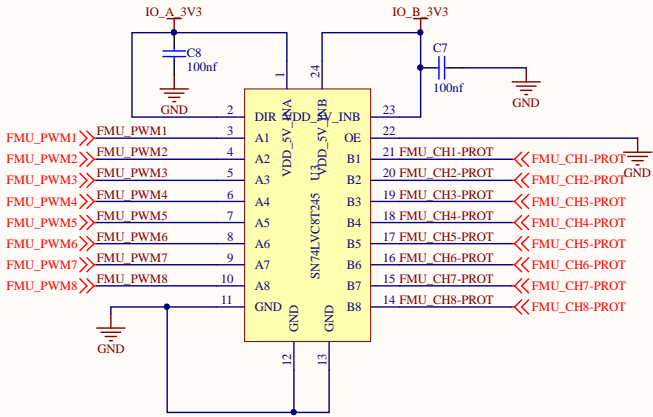
PWM



UART



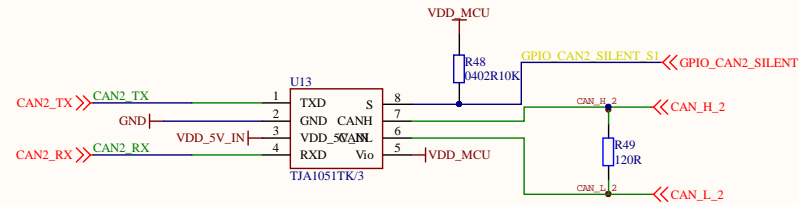
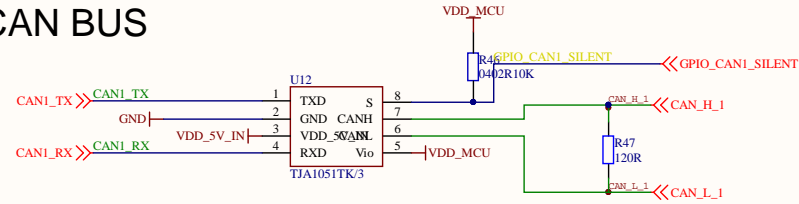
PWM



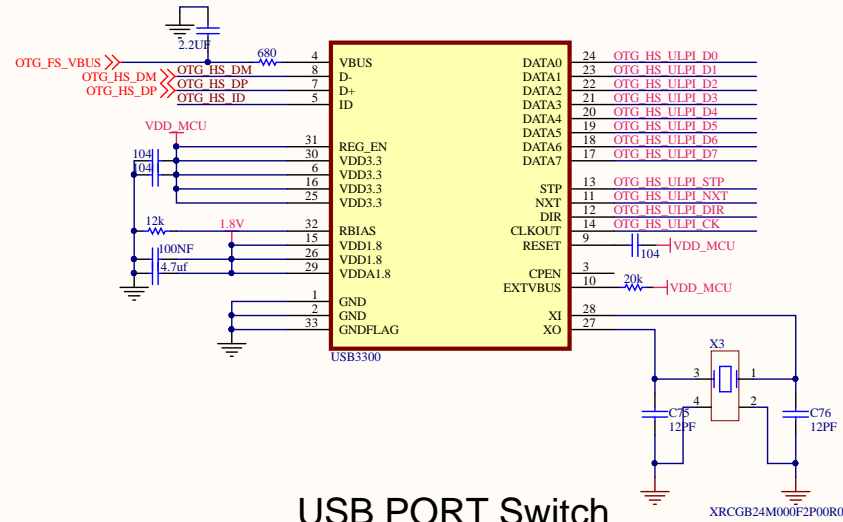
I/O RAIL

Title		
Size	Number	Revision
A3		
Date:	2019/11/28	Sheet of
File:	E:\WORD> 工作文档\09 IO bridge\IO Down By:	

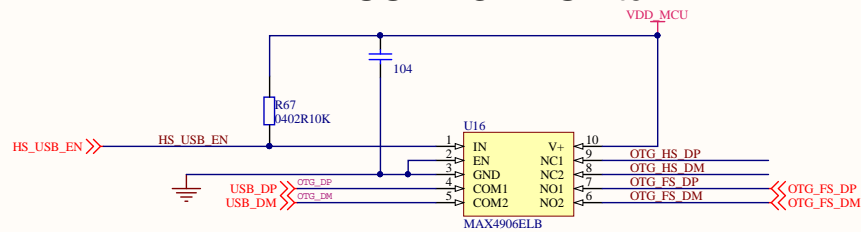
CAN BUS



USB HS PHY



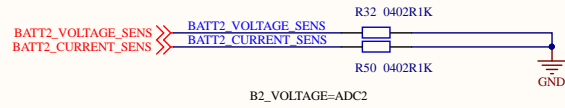
USB PORT Switch



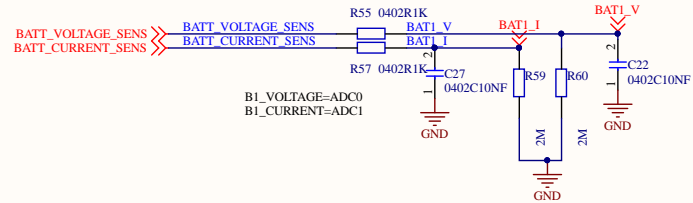
Port switching for USB HS and FS, high = USB_FS , LOW = USB_HS

Title		
Size A3	Revision	
Date: 2019/11/28	Sheet	of
File: F:\WORD-工作文档\10_CAN&USB	Drawn By:	

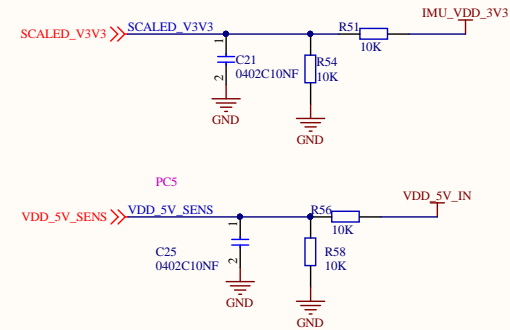
PM1 & PM2 voltage and current sensing



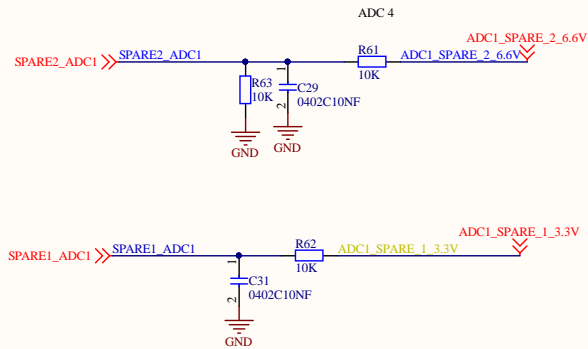
PM2 Disable



Core voltage detection



ADC of 3.3V + 6.6V



Title		
Size	Number	Revision
A3		
Date:	2019/11/28	Sheet of
File:	E:\WORD-工作文档\111_ADC_Rail_Split	Down By:

A

B

C

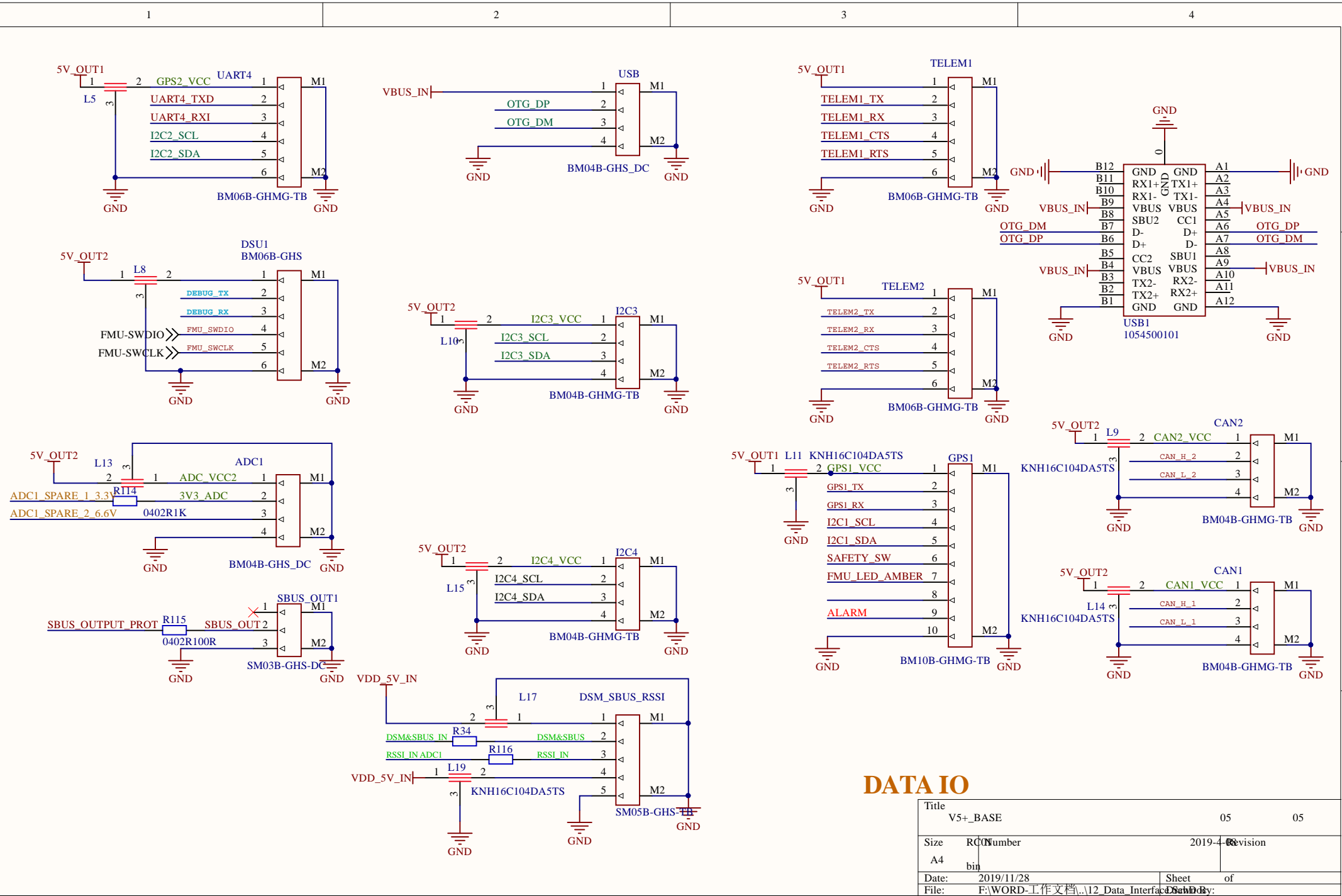
D

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B

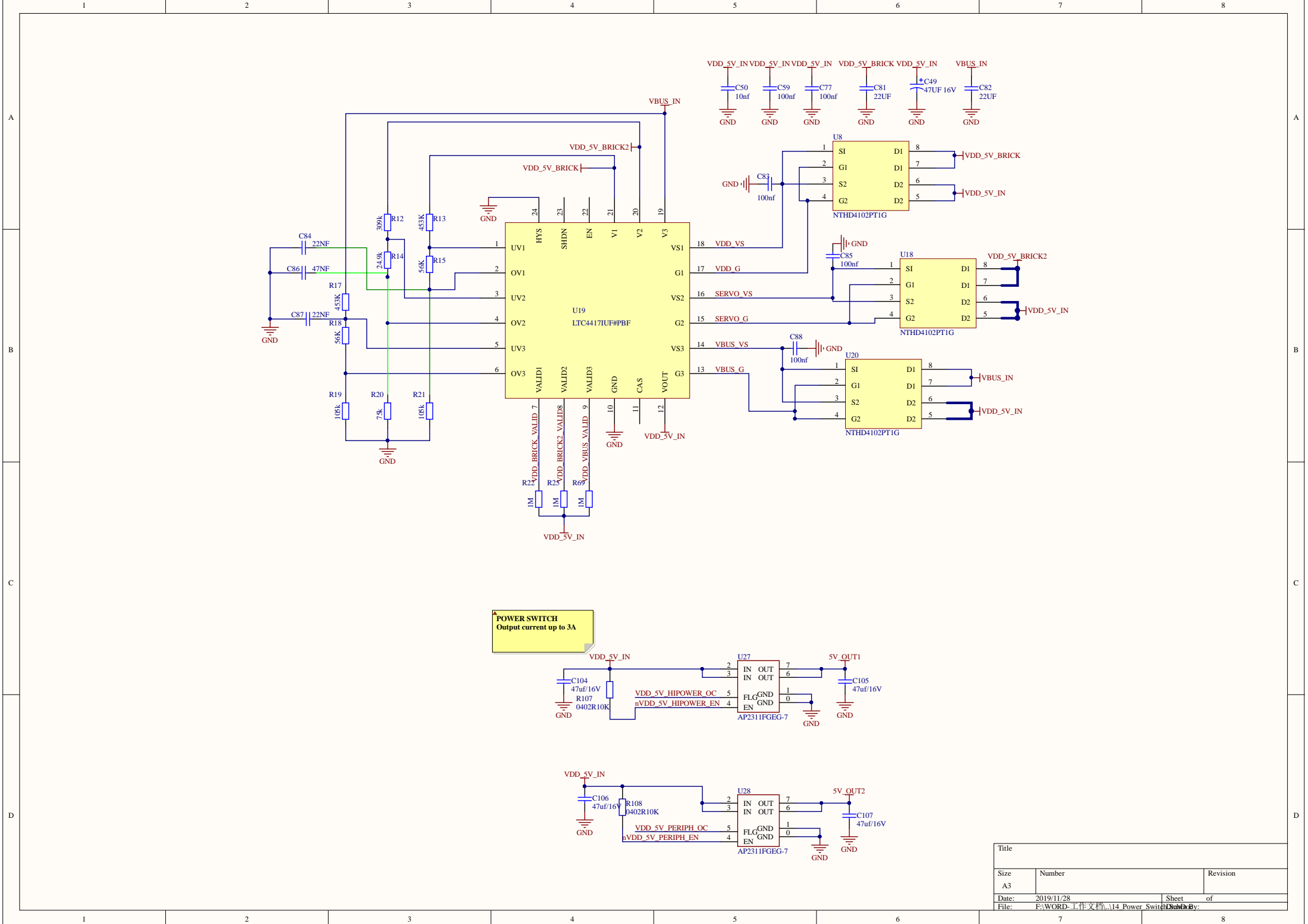
C

D



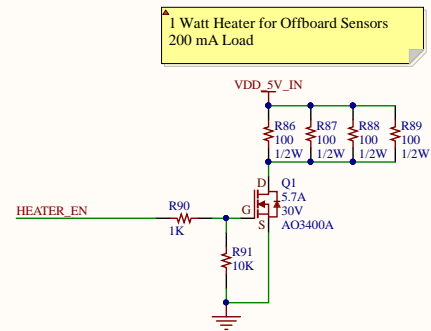
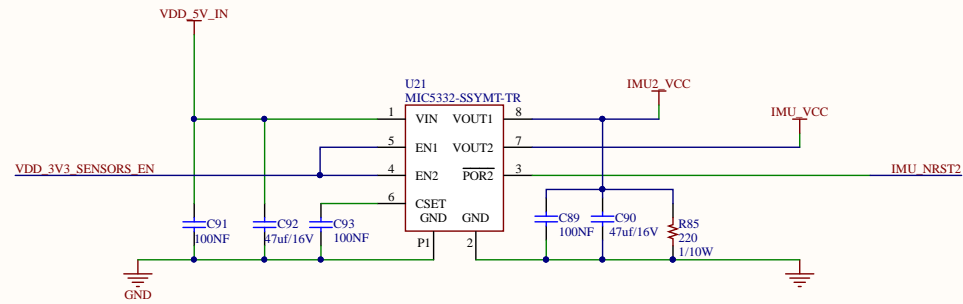
DATA IO

Title	V5+_BASE	05	05
Size	RCN	2019-4-26	Revision
A4	bin		
Date:	2019/11/28	Sheet	of
File:	F:\WORD-工作文档\12_Data_Interface\Subby		

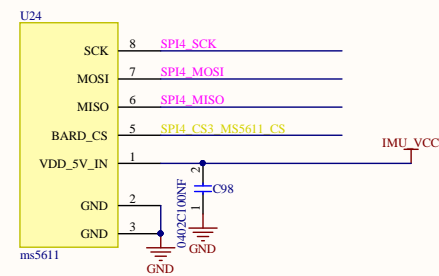
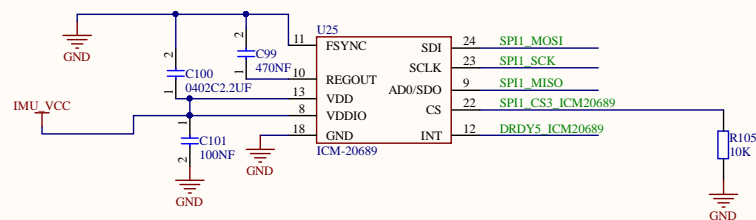


POWER SWITCH
Output current up to 3A

Title		
Size	Number	Revision
A3		
Date:	2019/11/28	Sheet of
File:	E:\WORD> 工作文档\14 Power Switch Design By:	



Title		
Size	Number	Revision
A3		
Date:	2019/11/28	Sheet of
File:	E:\WORD>工作文档\15 IMU POWER\15 IMU POWER.dwg	
Drawn By:		



Title		
Size A3	Number	Revision
Date: 2019/11/28	Sheet	of
File: F:\WORD-工作文档\16 IMU.SchDoc	Drawn By:	

