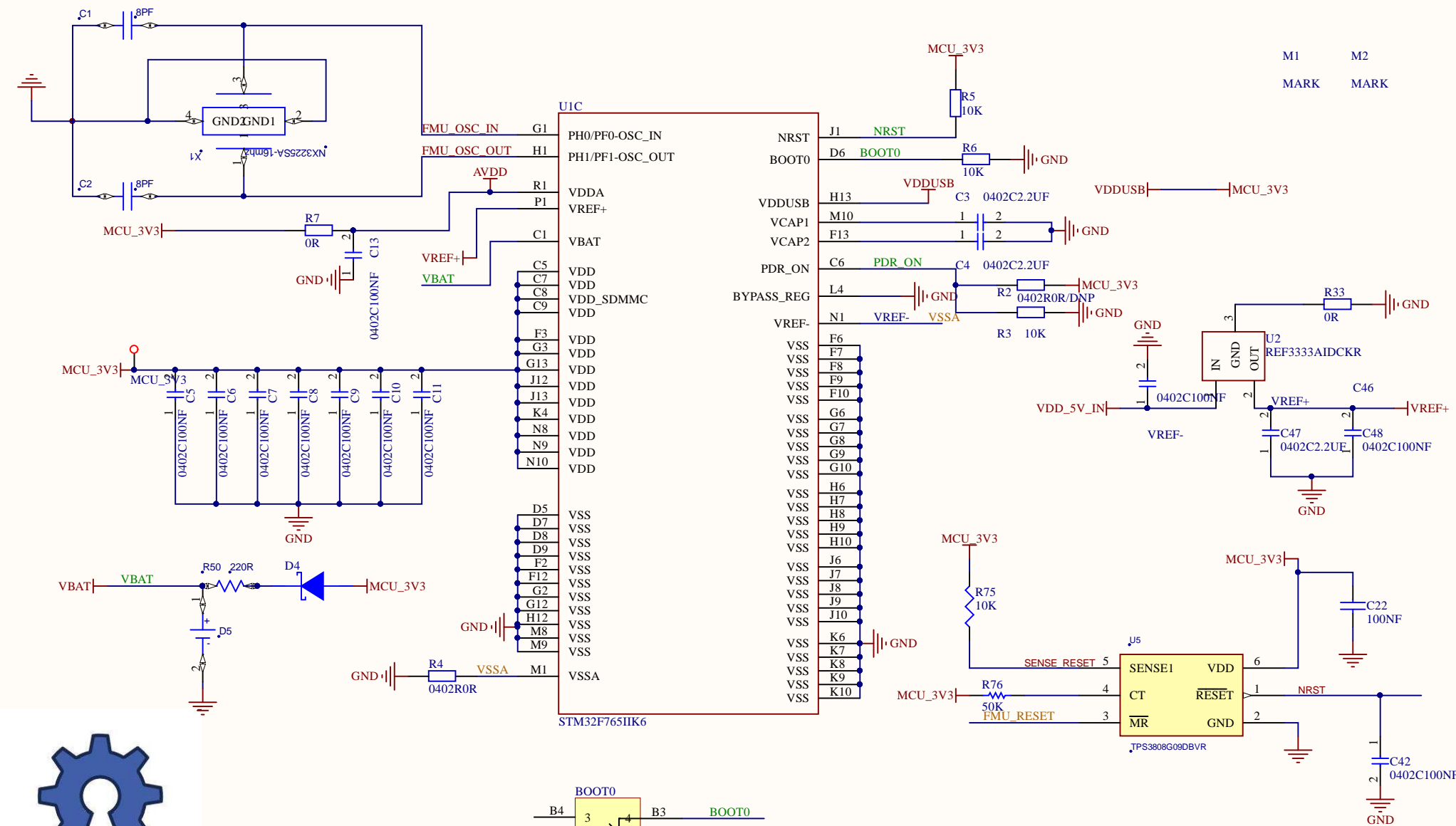
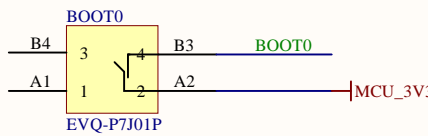


	1	2	3	4	
A					A
B	V2.2B				B
	2019-1-26 核对官方FMU5管理图，进行细节修改				
	1: IO修改 PB6/PB7 将这些IO释放为普通GPIO使用，原来是POWER切换芯片的ABC检测 PB6=IO_SPARE_GPIO1,PB7=IO_SPARE_GPIO2,PB12=IO_SPARE_GPIO6				
C	2019-1-28				C
	1: 修改板对板连接器，让定义分类更集中和规范				
	2: 移动80P连接器位置，让结构上与之前的不兼容				
D	2019-2-15				D
	1: 修改内部IMU扩展总线为SPI6，之前的为SPI5				
	2019-2-25				
1: 修改CAN芯片的S引脚上拉					



V5 Core X1
CUAV



Title F765_SYS			* DrawnBy: *	
Size: A4	Number: *	Revision: *		
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U1B

UART4_RX_I2C2	B12
UART4_TX_I2C2	C12
SDMMC1_CMD	D12
USART2_CTS_TELEM1	D11
USART2_RTS_TELEM1	D10
USART2_TX_TELEM1	C11
USART2_RX_TELEM1	B11
SPI1_MOSI_SENSOR1	A11
USART3_TX_TELEM2	P15
USART3_RX_TELEM2	P14
SPI1_DRDY6_BMI055_ACC	N15
USART3_CTS_TELEM2	N14
USART3_RTS_TELEM2	N13
FMU_CH5	M15
FMU_CH6	M14
SPI5_DRDY7_EXTERNAL1	L14

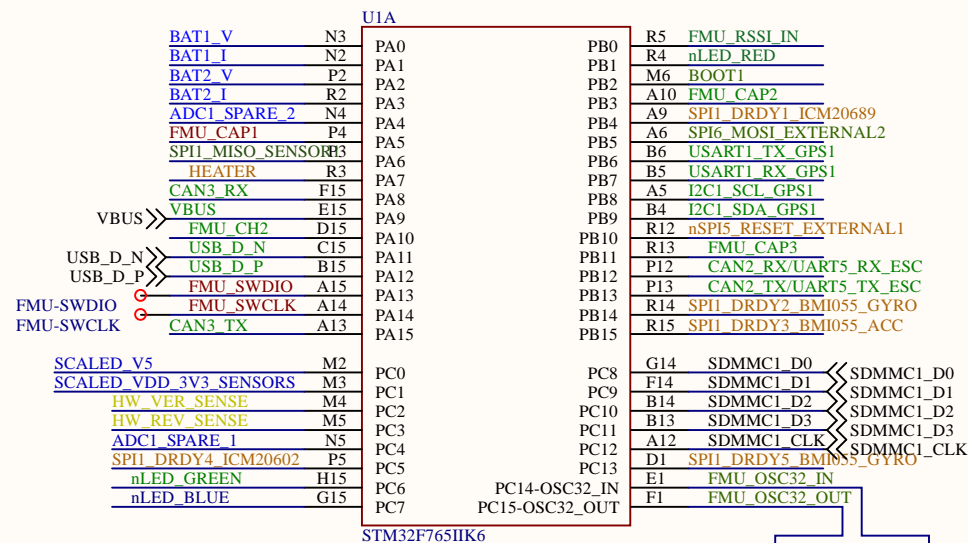
UART8_RX_FROM_IO	A4
UART8_TX_TO_IO	A3
SPI4_SCK_SENSOR2	A2
VDD_3V3_SENSORS_EN	A1
FMU_VDD_3V3_SPEKTRUM_EN	B1
BUZZER_1	B2
SPI4_MOSI_SENSOR2	B3
DRDY8	R8
UART7_TX_DEBUG	P8
FMU_CH4	P9
FMU SAFETY_SWITCH_IN	R9
FMU_CH3	P10
nFMU SAFETY_SWITCH_LED_OUT	R10
SPI4_MISO_SENSOR2	N11
FMU_CH1	P11
nVDD_5V_PERIPH_OC	R11

TIM5_SPARE_4	E14
SPI2_SCK	D14
SPI2_MISO	C14
SPI2_MOSI	C13
SPI5_CS1_EX1	D4
FMU_PPM_INPUT	C4
SPI6_CS1_EXTERNAL2	C3
SPI6_CS2_EXTERNAL2	C2
SPI6_CS3_EXTERNAL2	D2
CAN1_RX	D3
SPI5_CS2_EX1	E3
SPI5_CS3_EX1	E4

STM32F765IHK6

PF0	E2	I2C2_SDA_UART4
PF1	H3	I2C2_SCL_UART4
PF2	H2	SPI1_CS1_ICM20689
PF3	J2	SPI1_CS2_ICM20602
PF4	J3	SPI1_CS3_BMI055_GYRO
PF5	K3	SPI2_CS_FRAM
PF6	K2	UART7_RX_DEBUG
PF7	K1	SPI5_SCK_EXTERNAL1
PF8	L3	SPI5_MISO_EXTERNAL1
PF9	L2	SPI5_MOSI_EXTERNAL1
PF10	L1	SPI4_CS1_MS5611
PF11	R6	SPI4_CS2
PF12	P6	nVDD_5V_HIPOWER_EN
PF13	N6	nVDD_5V_HIPOWER_OC
PF14	R7	I2C4_SCL_EXTERNAL2
PF15	P7	I2C4_SDA_EXTERNAL2
PG0	N7	HW_VER_DRIVE
PG1	M7	nPOWER_IN_A
PG2	L15	nPOWER_IN_B
PG3	K15	nPOWER_IN_C
PG4	K14	nVDD_5V_PERIPH_EN
PG5	K13	VDD_5V_RC_EN
PG6	J15	VDD_5V_WIFI_EN
PG7	J14	VDD_3V3_SD_CARD_EN
PG8	H14	USART6_RTS_TELEM3
PG9	C10	USART6_RX_TELEM3
PG10	B10	SPI1_CS4_BMI055_ACC
PG11	B9	SPI1_SCK_SENSOR1
PG12	B8	SPI6_MISO_EXTERNAL2
PG13	A8	SPI6_SCK_EXTERNAL2
PG14	A7	USART6_TX_TELEM3/RC_INPUT
PG15	B7	USART6_CTS_TELEM3
PH2	F4	GPIO CAN1 SILENT_S0
PH3	G4	GPIO CAN2 SILENT_S1
PH4	H4	GPIO CAN3 SILENT_S2
PH5	J4	SPIAUX_CS_MEM
PH6	M11	FMU_CH7
PH7	N12	I2C3_SCL_EXTERNAL3
PH8	M12	I2C3_SDA_EXTERNAL3
PH9	M13	FMU_CH8
PH10	L13	UI_LED_RED_EXTERNAL
PH11	L12	UI_LED_GREEN_EXTERNAL
PH12	K12	UI_LED_BLUE_EXTERNAL
PH13	E12	CAN1_TX
PH14	E13	HW_REV_DRIVE

Brick 1 Is Chosen
Brick 2 Is Chosen
USB Is Chosen

Title **F765_IO**Size: **A4**

Number:*

Revision:*

Date: 2019/3/6

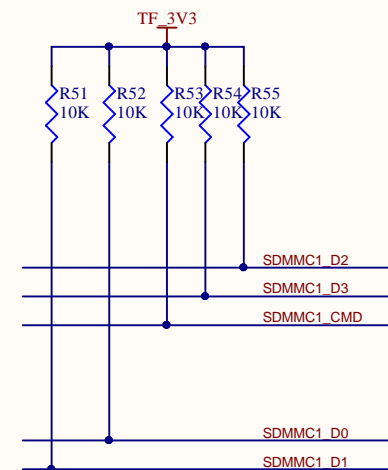
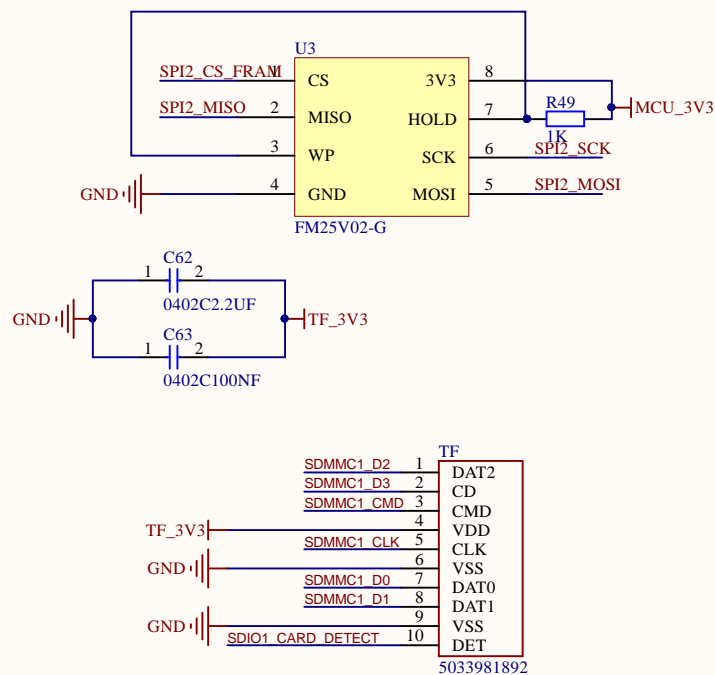
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* DrawnBy: *

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Title TF_CARD			* DrawnBy: *	
Size: A4	Number:*	Revision:*		
Date: 2019/3/6	Time: 14:52:11	Sheet* of *		

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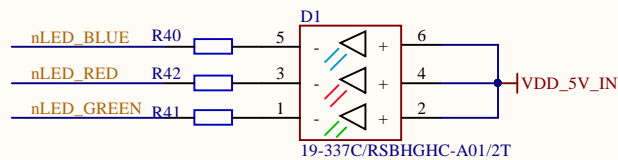
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Title <i>status_LED</i>			* DrawnBy: *	
Size: A4	Number:*	Revision:*		
Date: 2019/3/6	Time: 14:52:11	Sheet* of *		

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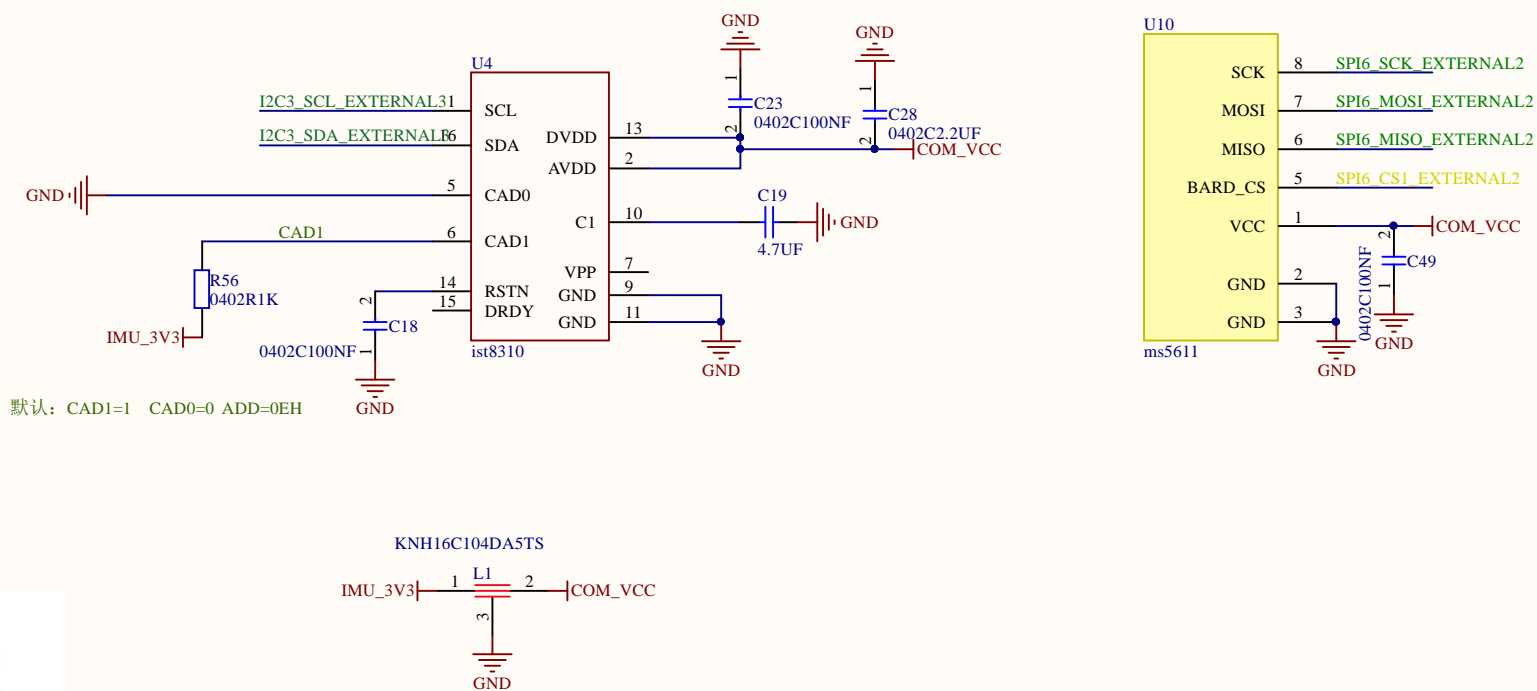
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Number:*

Revision:*

Date: 2019/3/6

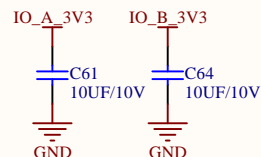
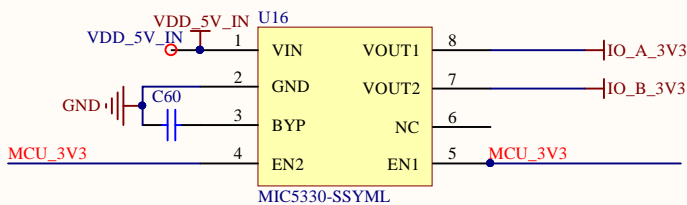
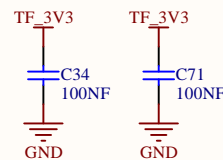
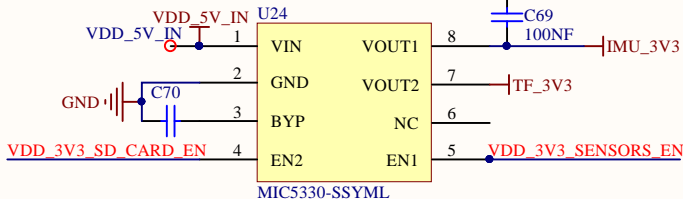
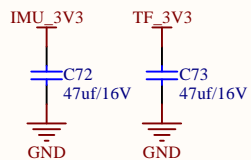
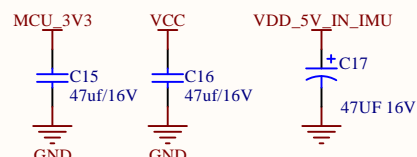
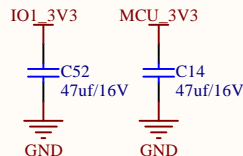
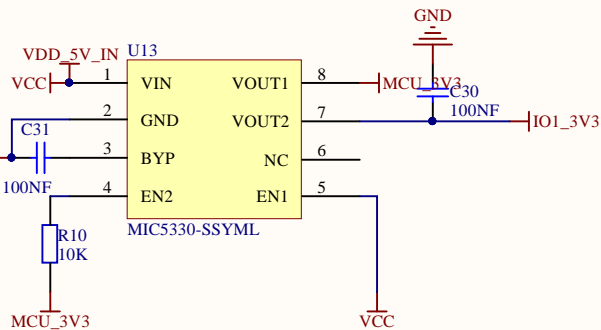
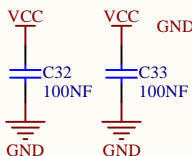
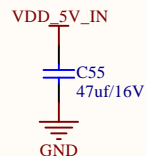
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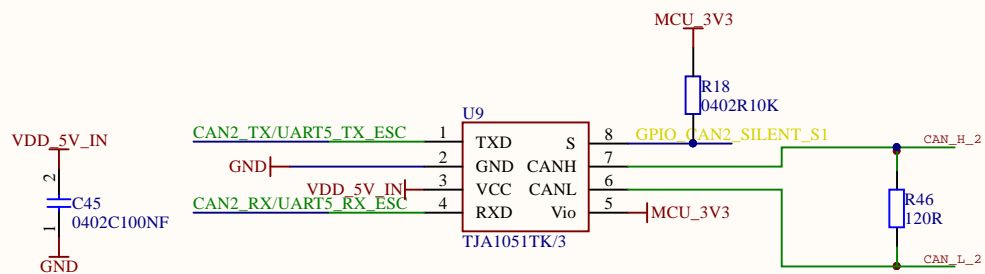
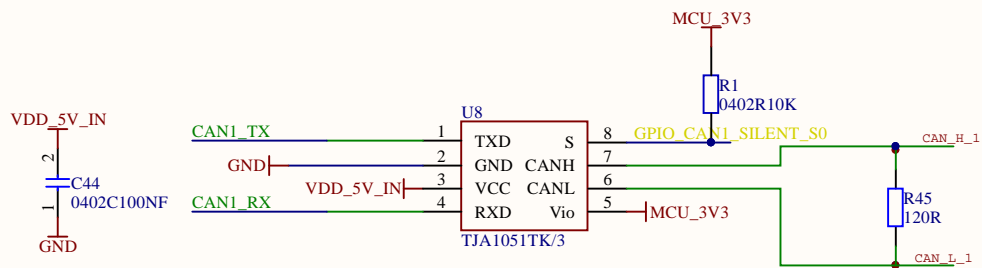
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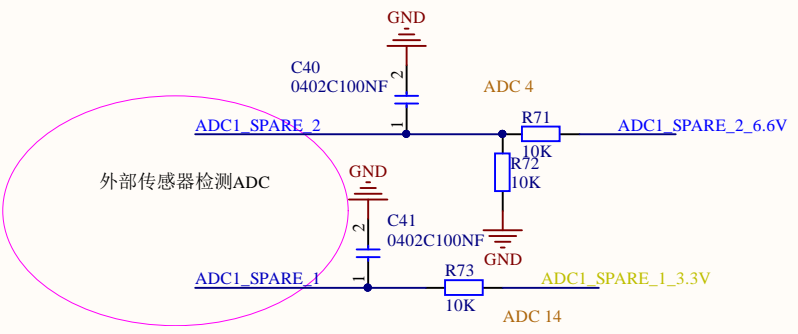
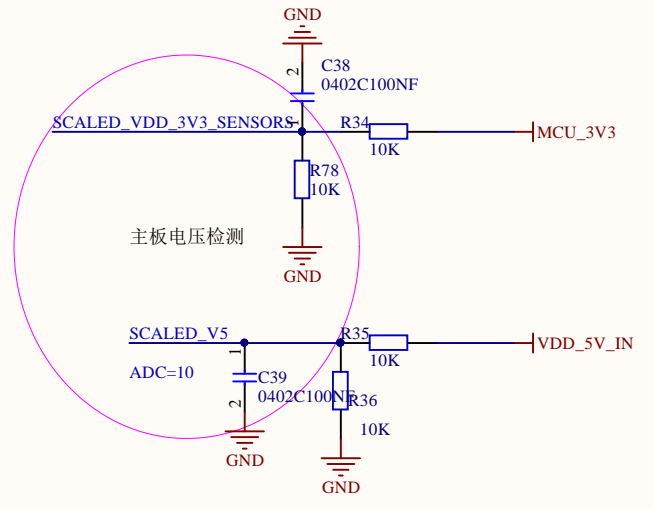
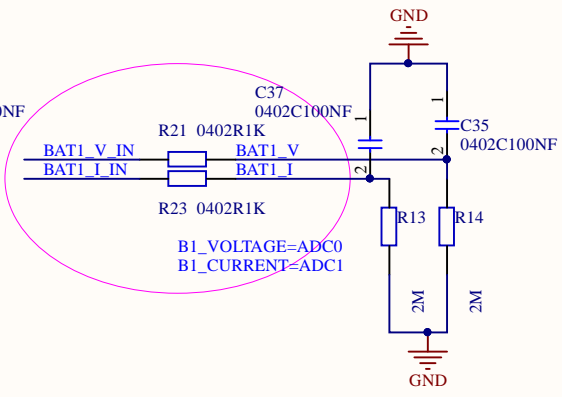
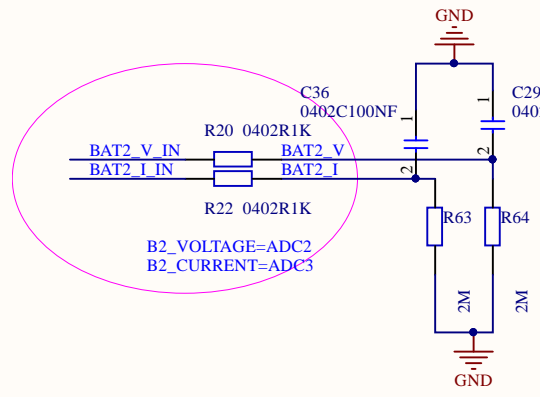
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Title LDO_Power			* DrawnBy: *	
Size: A4	Number:*	Revision:*		
Date: 2019/3/6	Time: 14:52:11	Sheet* of *		



Title CAN_BUS			* DrawnBy: *	
Size: A4	Number:*	Revision:*		
Date: 2019/3/6	Time: 14:52:11	Sheet* of *		

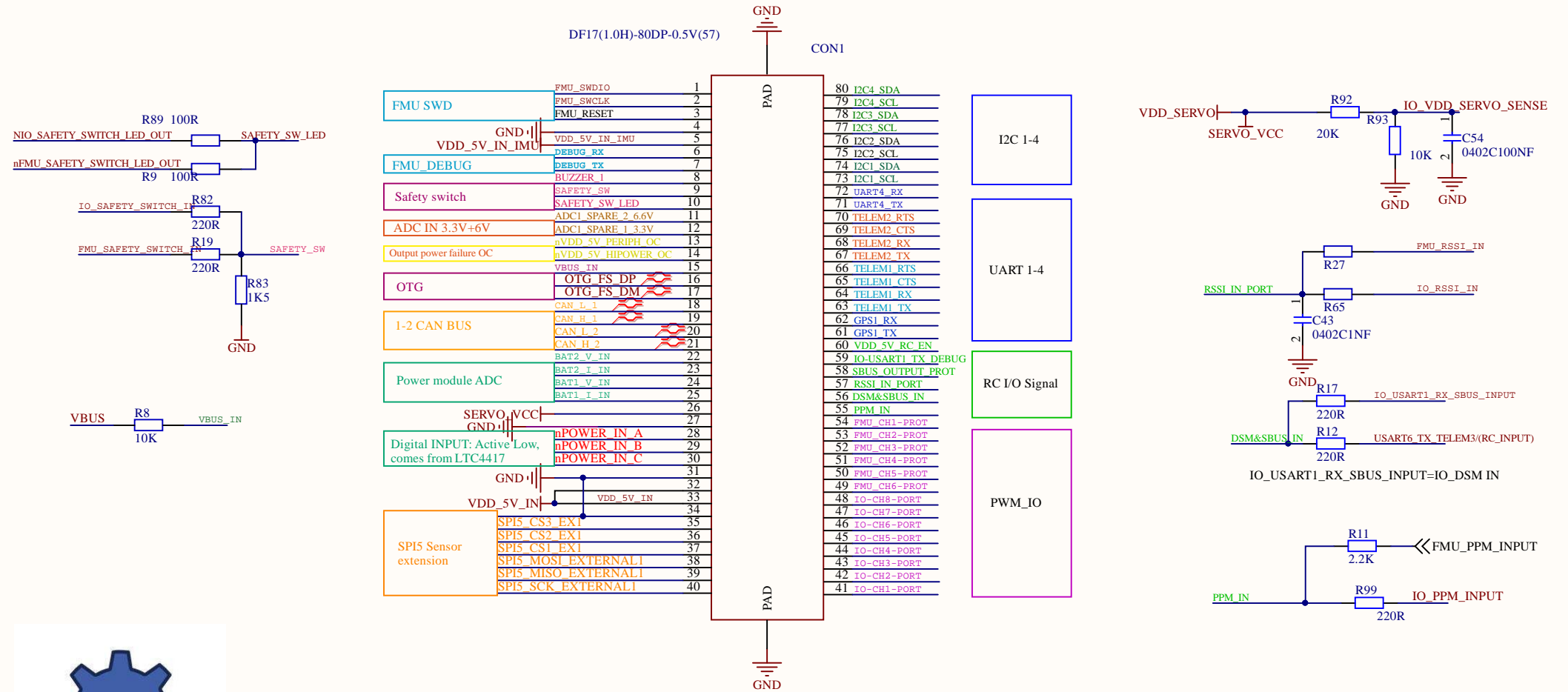


2019-1-3 在ADC检测添加滤波电容



Title 5V_RAIL_ADC			* DrawnBy: *	
Size: A4	Number:*	Revision:*		
Date: 2019/3/6	Time: 14:52:11	Sheet* of *		

Only compatible with CUAUV V5-CORE x module, other incompatible
80P板对板连接器定义只兼容CUAUV V5-CORE x标准, 其他类似模块不能兼容



Title V5_CORE_B_TO_B-CON			CUAUV Ltd	
Size: A4	Number:*	Revision:*		
Date: 2019/3/6	Time: 14:52:12	Sheet* of *		

A

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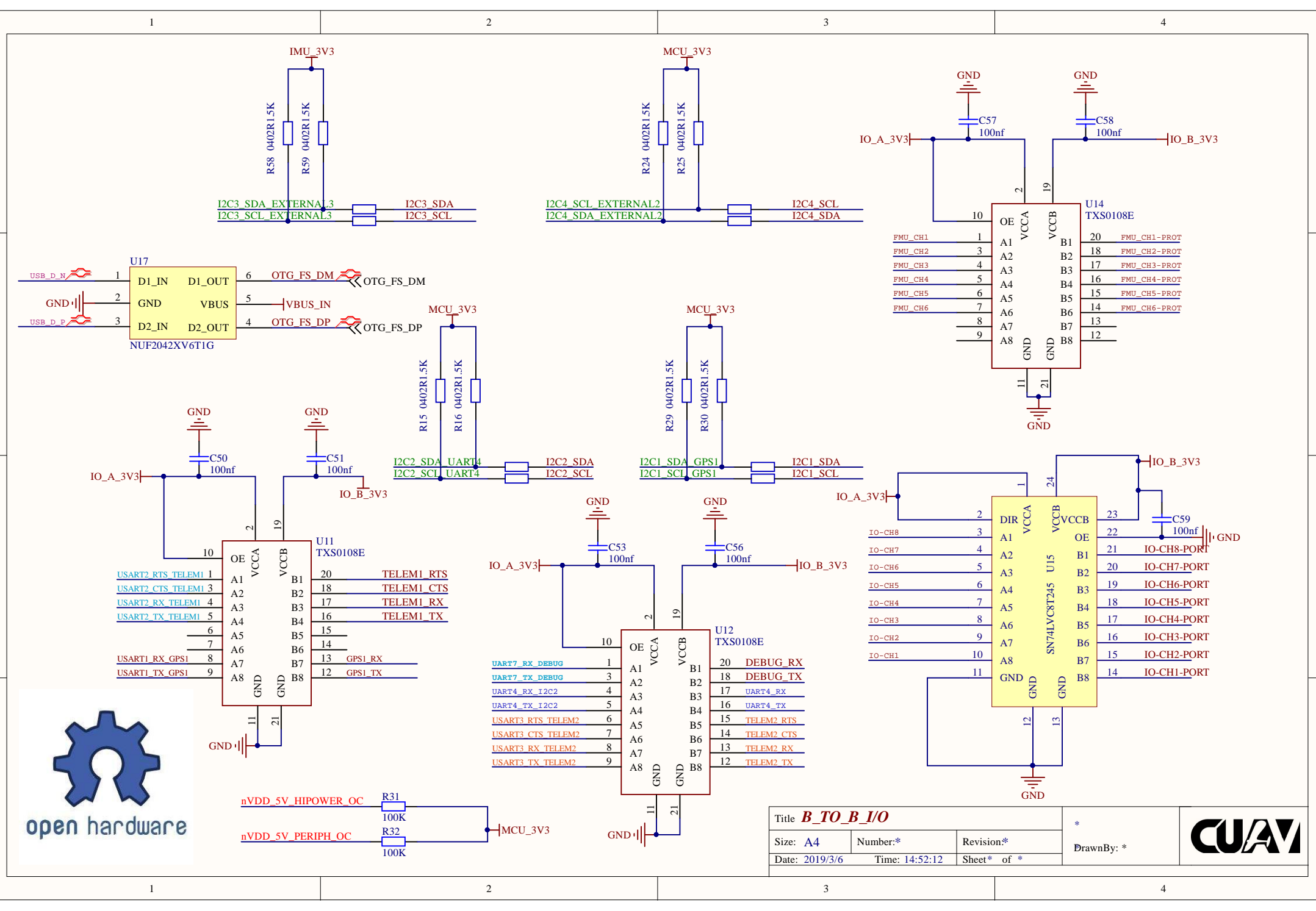
D

A

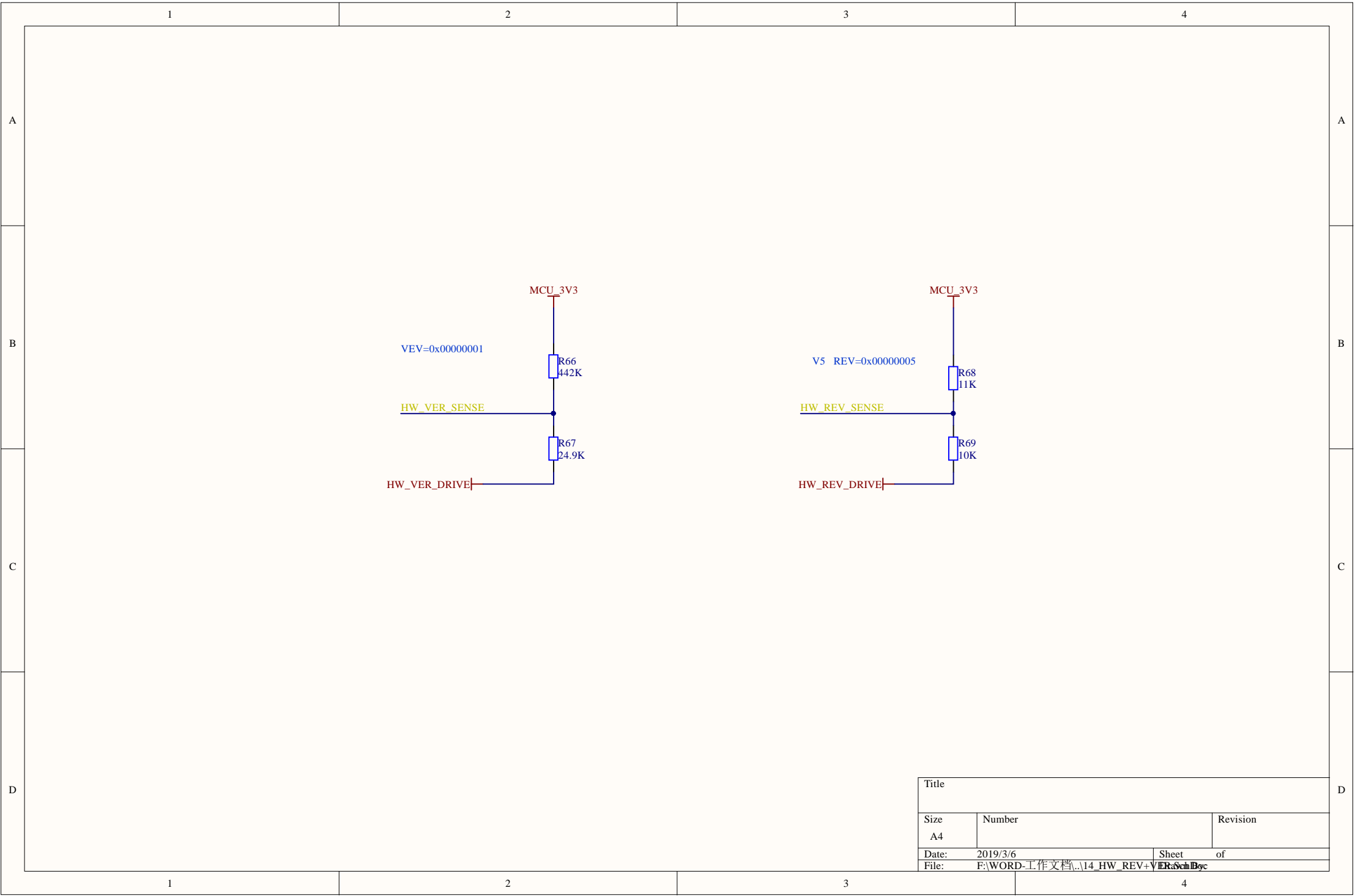
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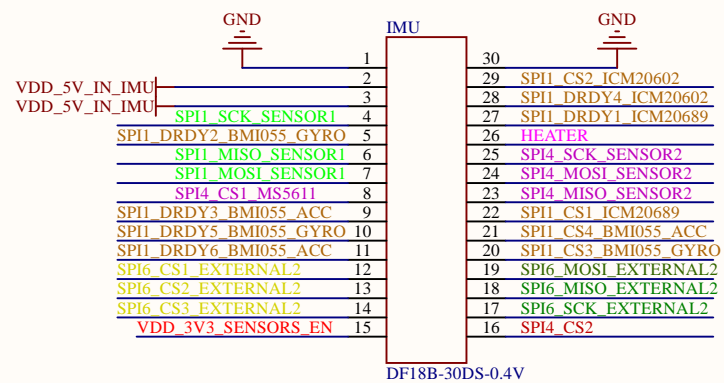
C


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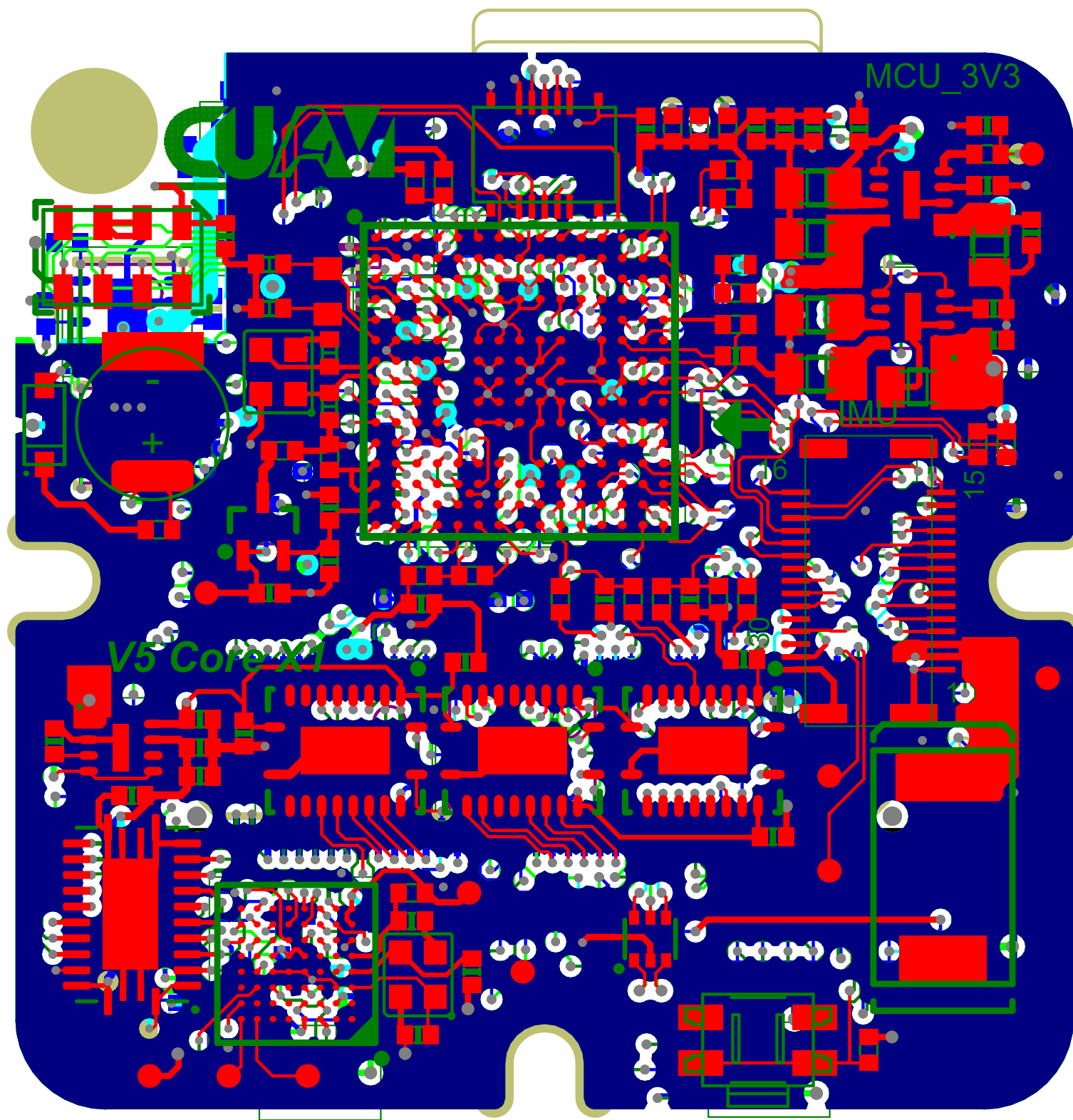


Title B_TO_B_I/O			* DrawnBy: *	
Size: A4	Number:*	Revision:*		
Date: 2019/3/6	Time: 14:52:12	Sheet* of *		





Title <i>IMU&LED_CON</i>			* DrawnBy: *	
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CUM

MCU_3V3

IMU

V5 Core X1

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