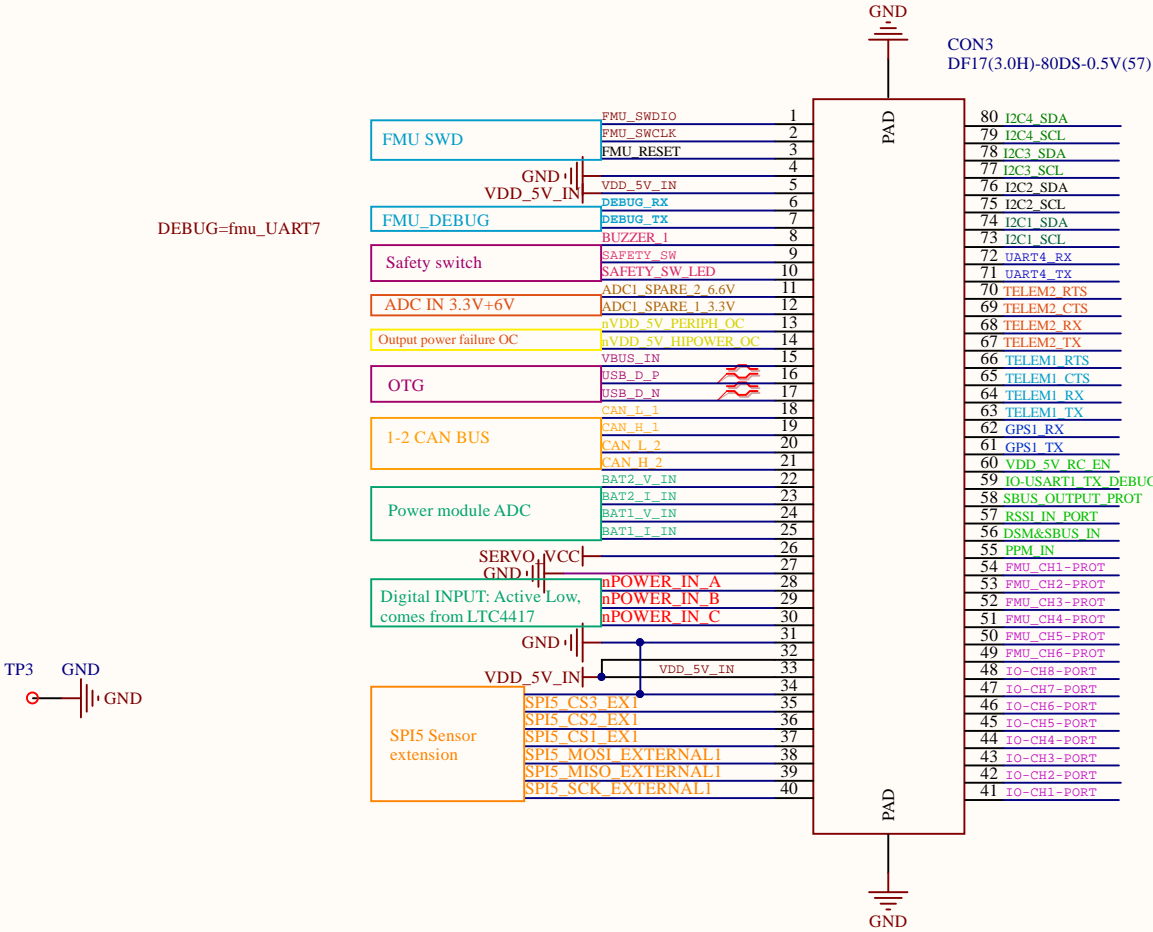


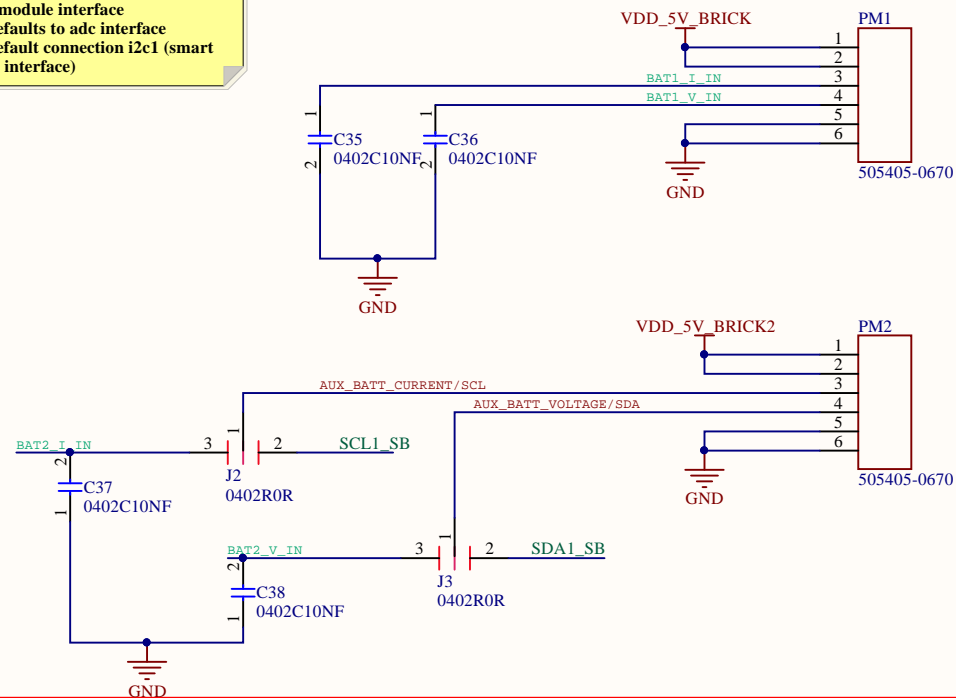
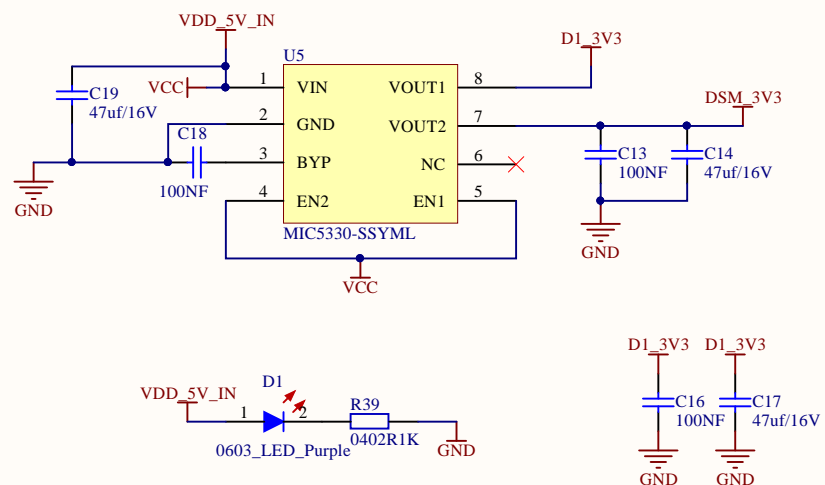
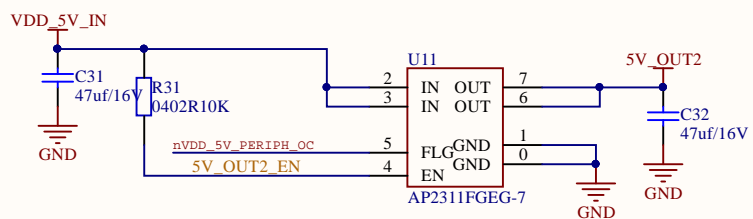
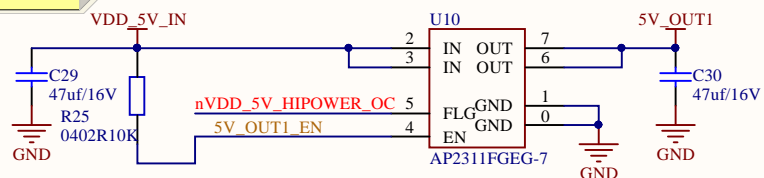
	1	2	3	4
A	<div>Modify list</div>			
B	<div><div>V5+_BASE_RC00(V5_X1)</div><div>2018-12-18 修改SBUS接口，增加一个DSM输入</div><div>2018-12-18 重构接口，使用dronecode标准定义</div><div>2019-1-12 修正I2C3_PORT定义反向错误</div><div>2019-1-12 重构V5X BASE底板</div></div>	<div><div>V5+_BASE_RC00</div><div>2018-12-18 Modify the SBUS interface and add a DSM input</div><div>2018-12-18 Refactoring interface, defined using the dronecode standard</div><div>2019-1-12 Fixed I2C3_PORT definition reverse error</div><div>2019-1-12 Reconstructing the V5X BASE</div></div>		
C	<div><div>V5+_BASE_RC01(V5_X11)</div><div>2019-3-18 修改GPS接口</div><div>2019-3-20 修改I2C1的信号反了</div><div>2019-3-28 修改智能电池接口SDA SCL定义</div><div>2019-3-28 修改SBUS OUT引脚排序错误</div></div>	<div><div>V5+_BASE_RC01</div><div>2019-3-18 Modify the GPS interface</div><div>2019-3-20 Modifying I2C1 error</div><div>2019-3-28 Modify Smart Battery Interface SDA SCL Definition</div><div>2019-3-28 Modify SBUS OUT pin error</div></div>		
D				
	1	2	3	4

Only compatible with CUAV V5+-CORE module, other incompatible
80P板对板连接器定义只兼容CUAV V5+-CORE 标准，其他类似模块不能兼容



B_TO_B CON

Tiele: V5+_BASE		SHEET 02 OF 05
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Designer	bin	
Reviewed		

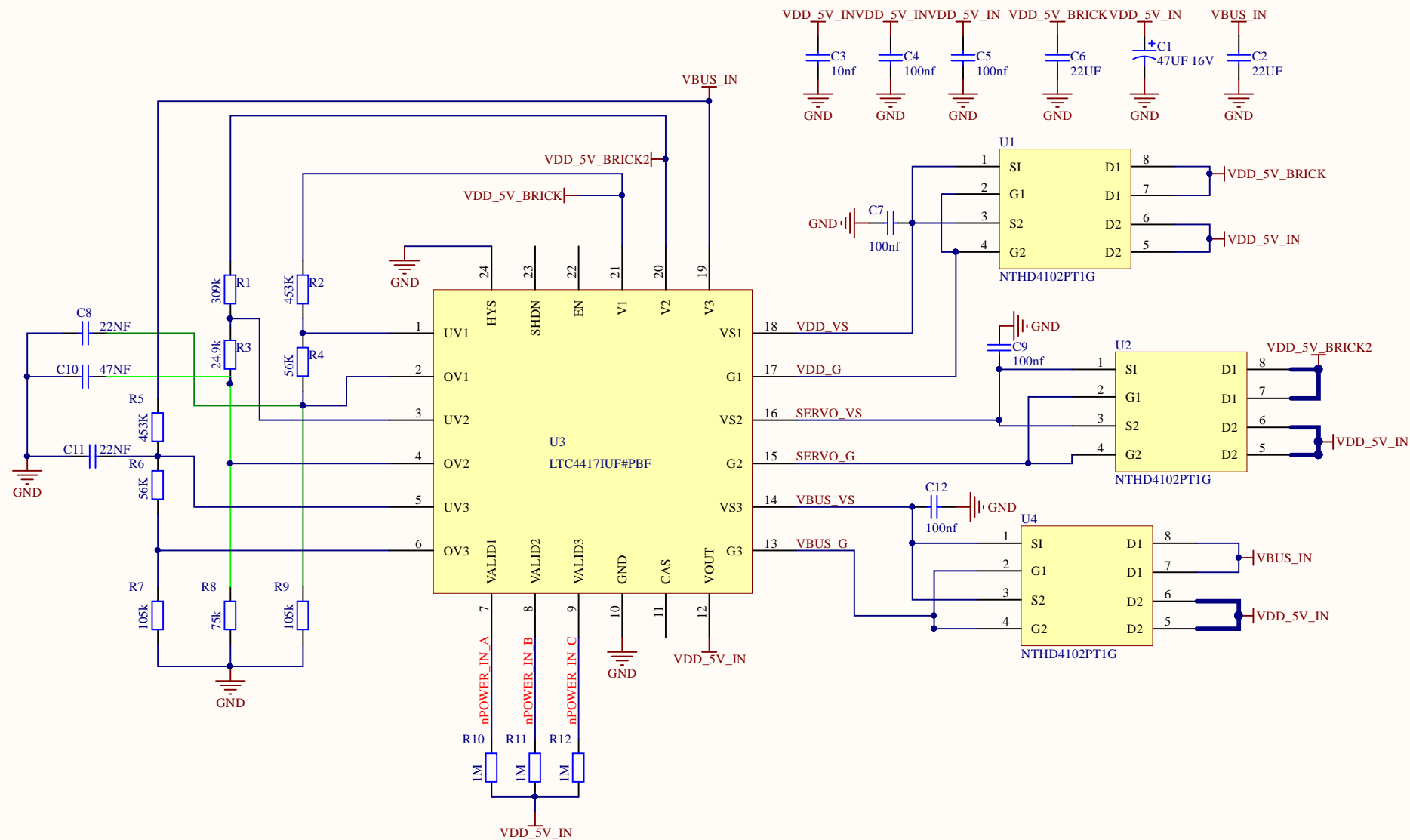


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POWER SWITCH

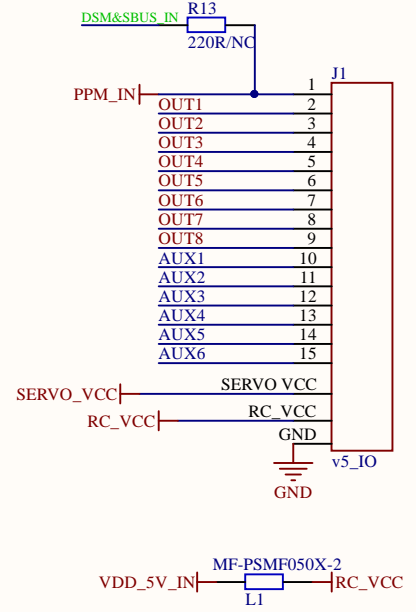
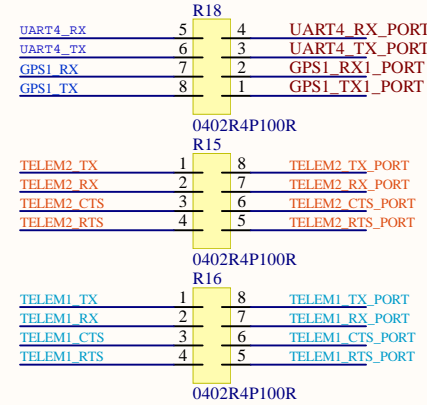
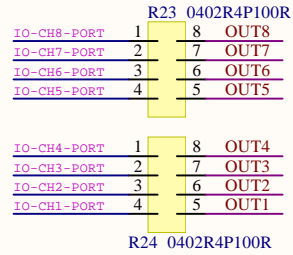
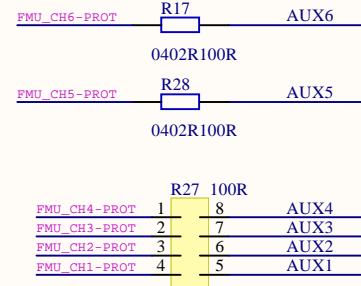
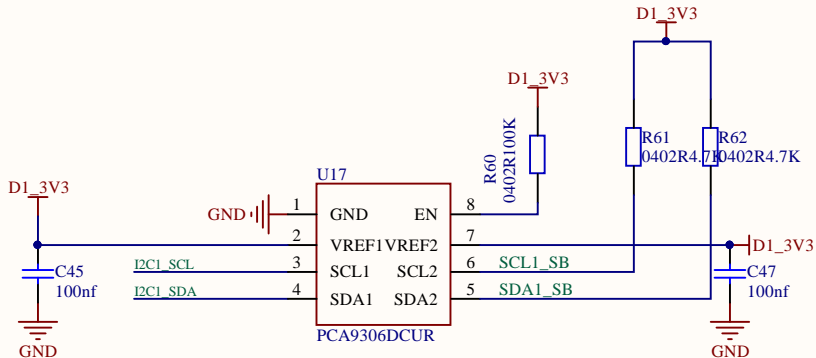
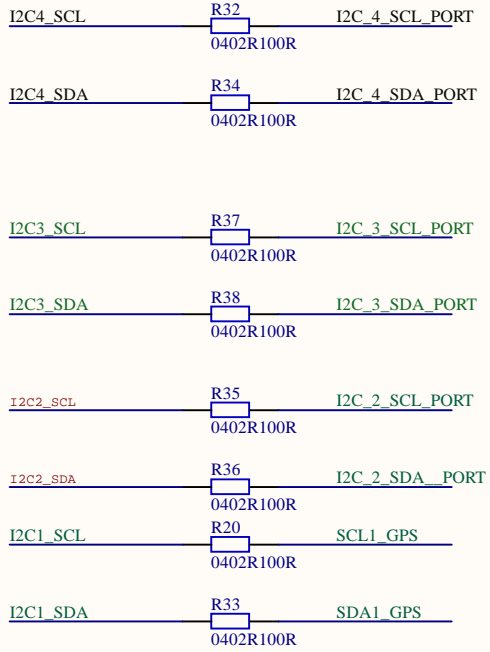
Tiele: V5+_BASE		SHEET 03 OF 05	
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Designer	bin		
Reviewed			

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I2C,UART,PWM IO

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A

B

C

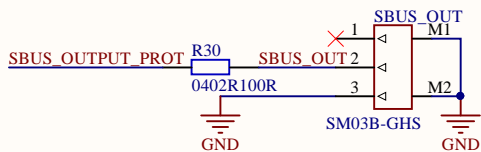
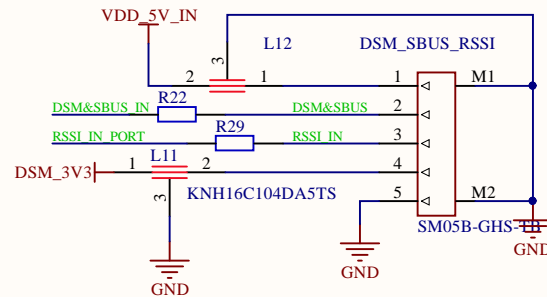
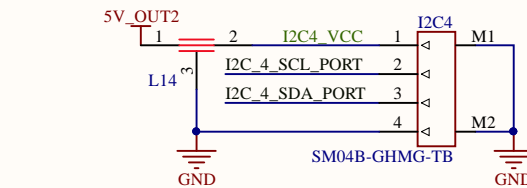
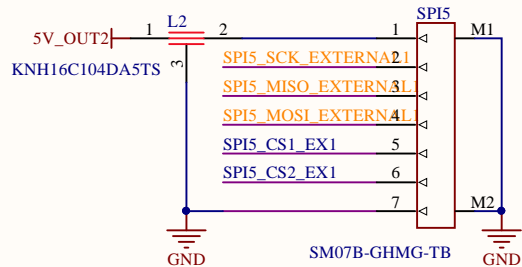
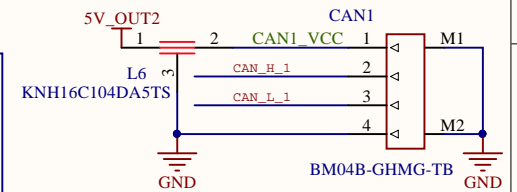
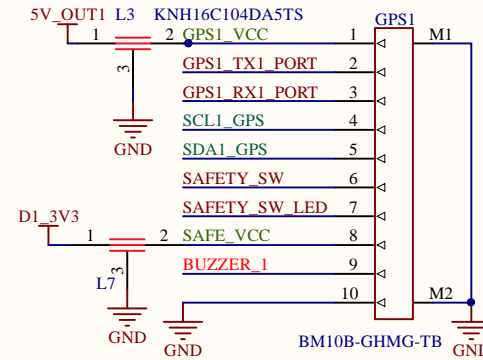
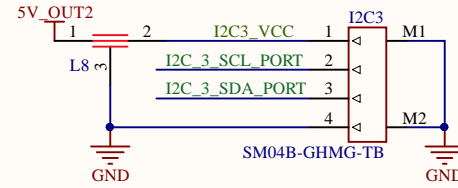
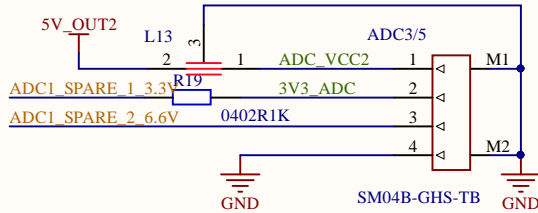
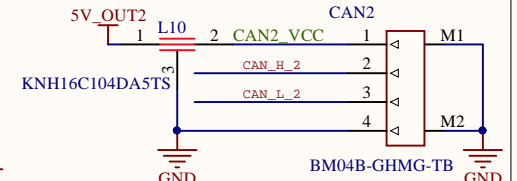
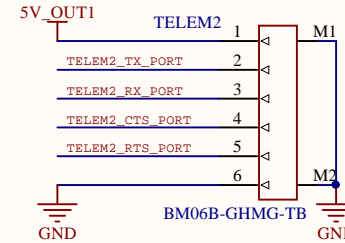
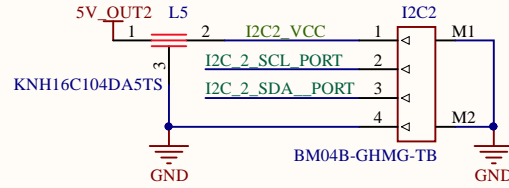
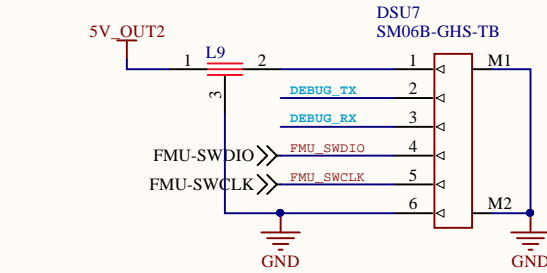
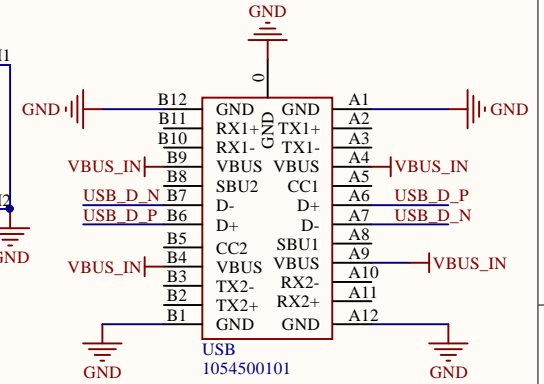
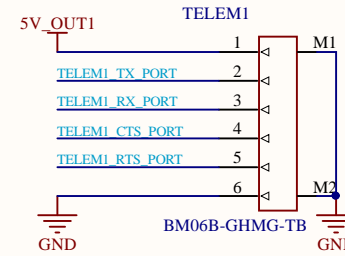
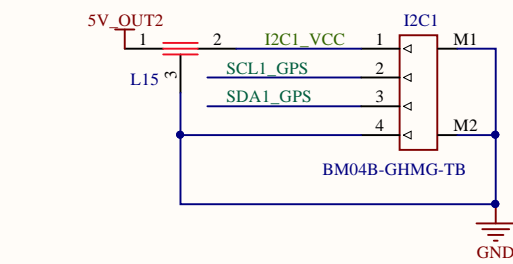
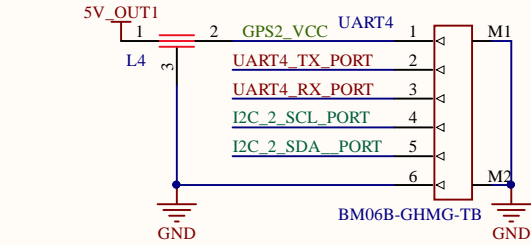
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A

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DATA IO

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