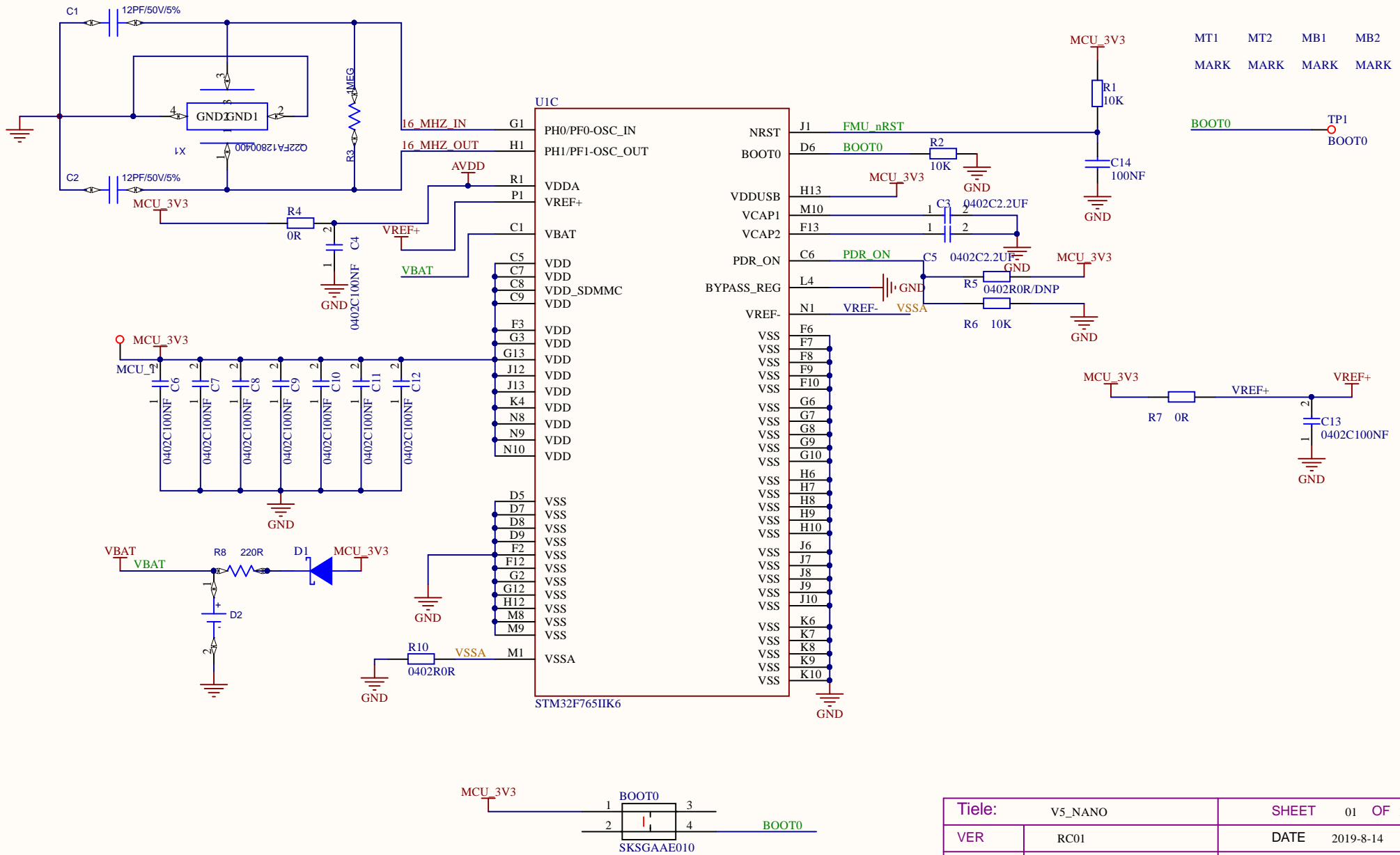
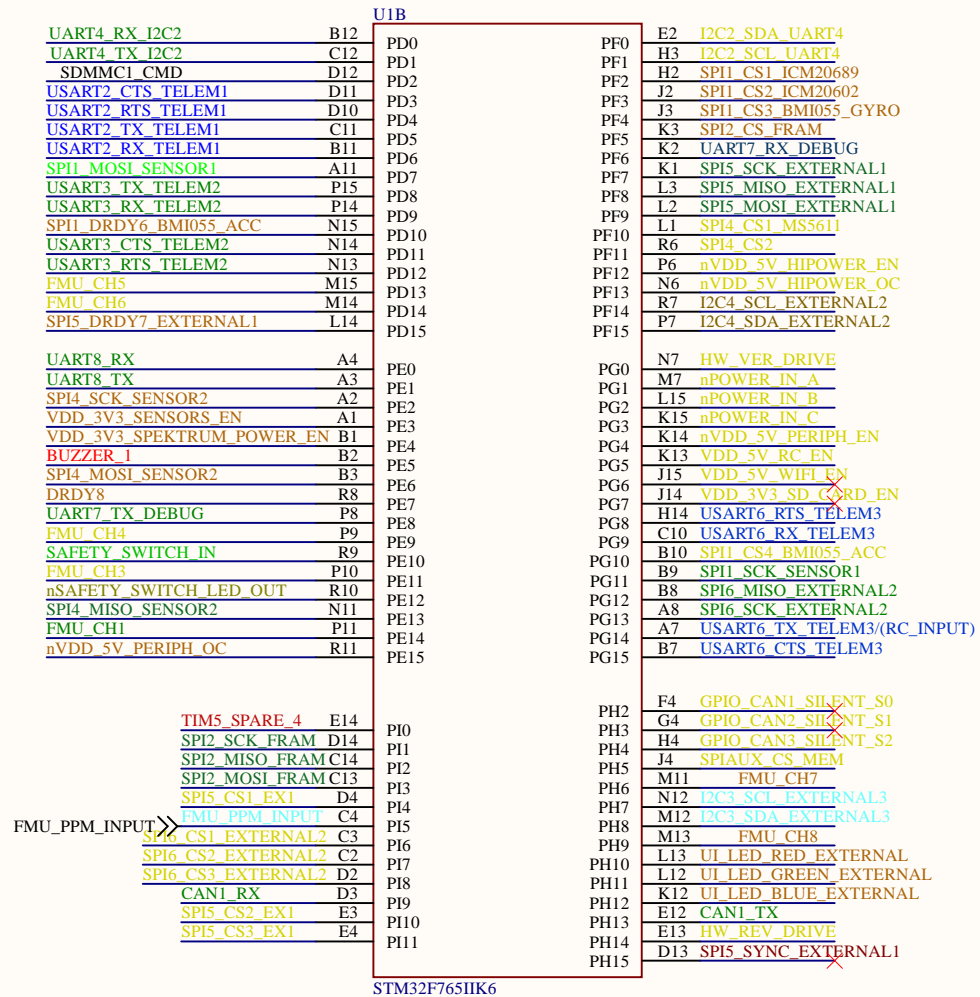


# System of STM32F765IHK6

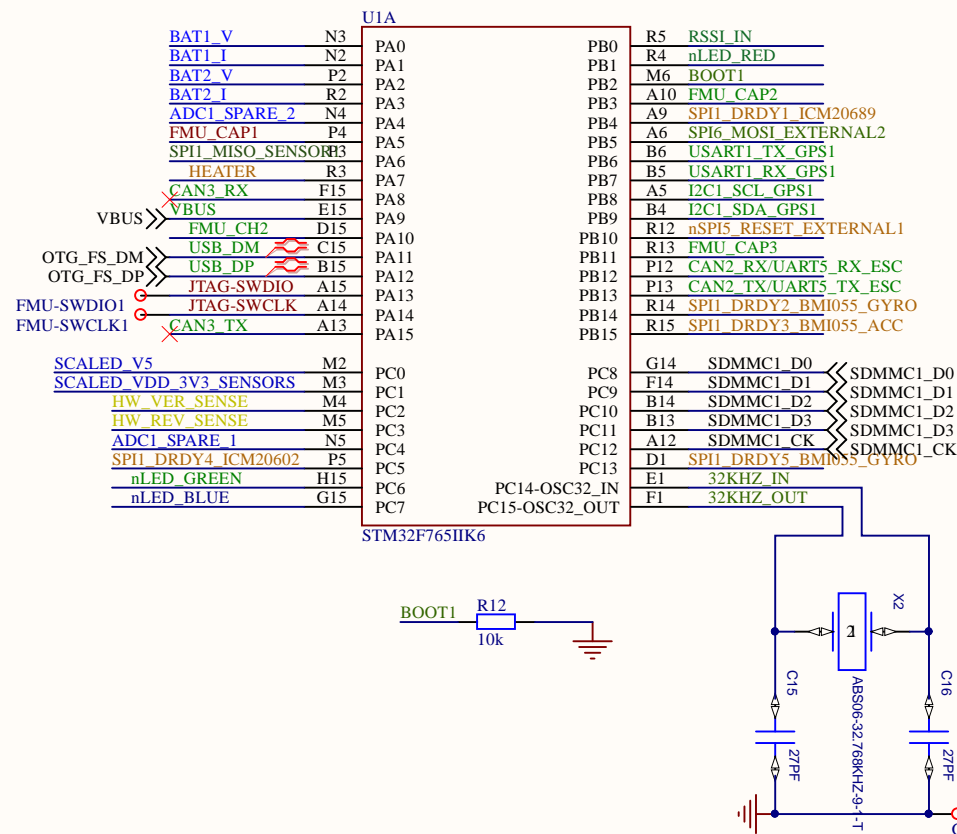



Tiele: V5_NANO		SHEET 01 OF 16	
VER	RC01	DATE	2019-8-14
Designer	bin	CUAV	
Reviewed			

# I/O of STM32F765IHK6



Brick 1 Is Chosen  
Brick 2 Is Chosen  
USB Is Chosen



Tiele: V5_NANO		SHEET 02 OF 16	
VER	RC01	DATE 2019-4-11	
Designer	bin		
Reviewed			

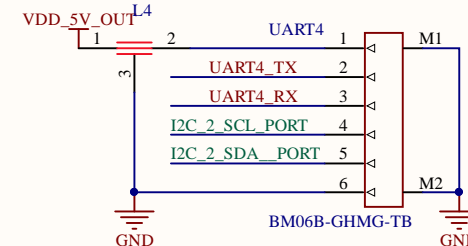
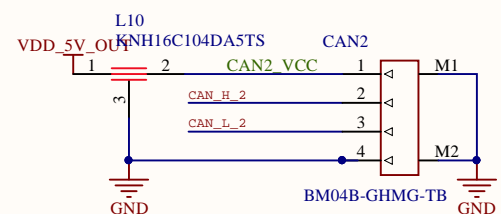
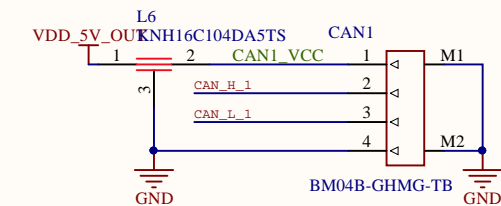
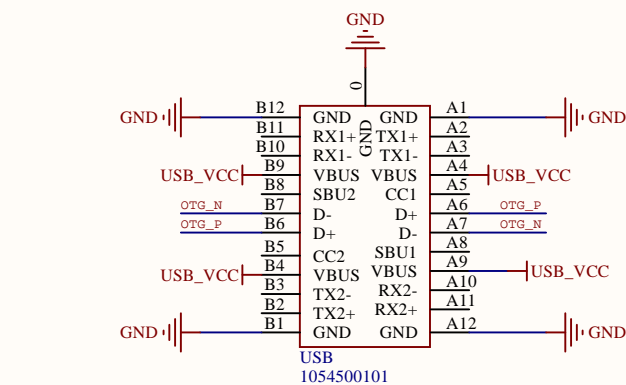
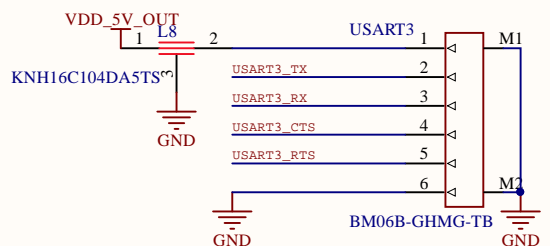
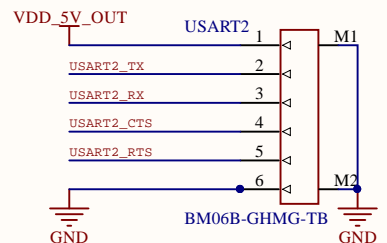
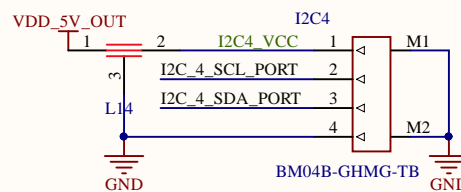
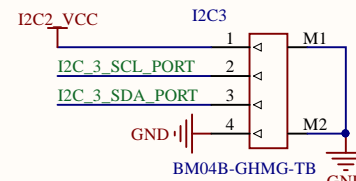
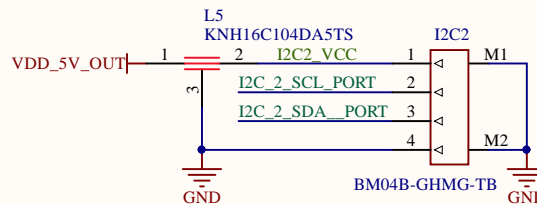
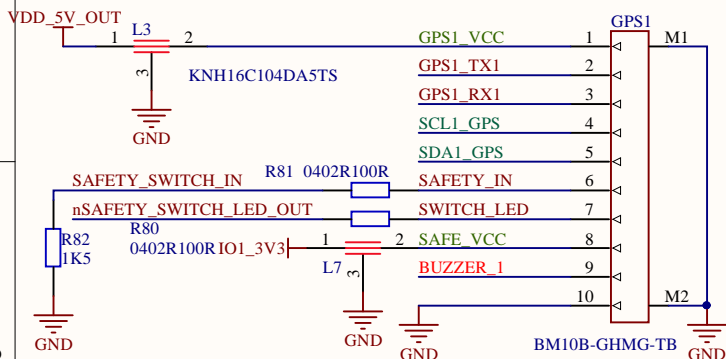
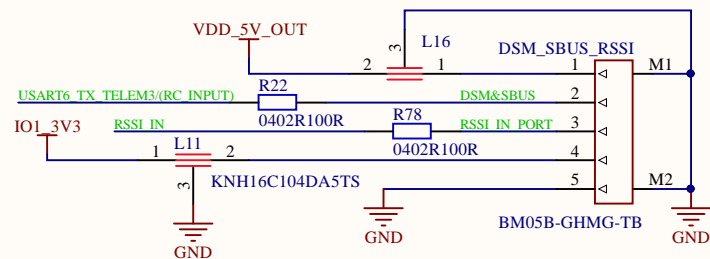
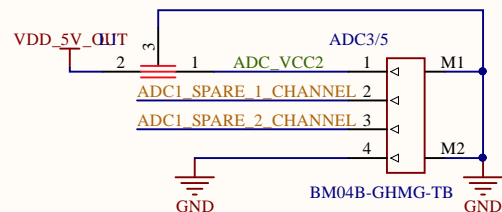
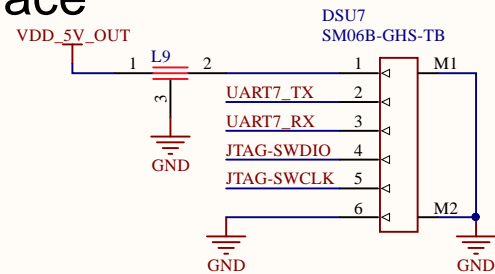
# I/O Interface

1

2

3

4



Tiele:	V5_NANO	SHEET	03 OF 16
VER	RC01	DATE:	2019-8-14
Designer	bin	CUAV	
Reviewed			

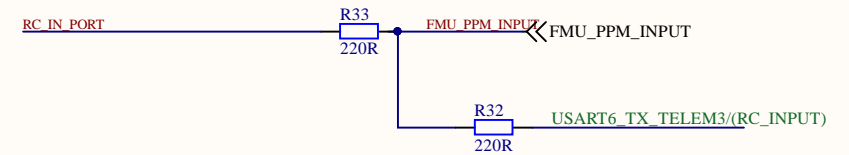
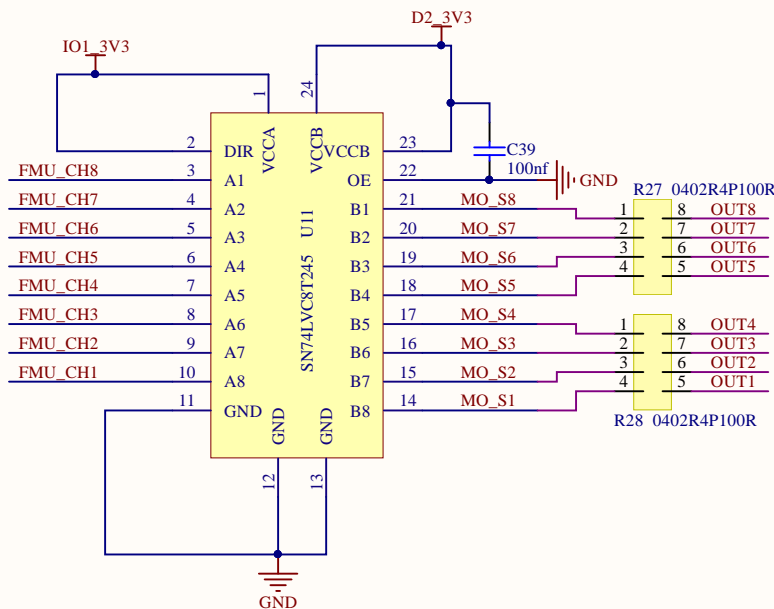
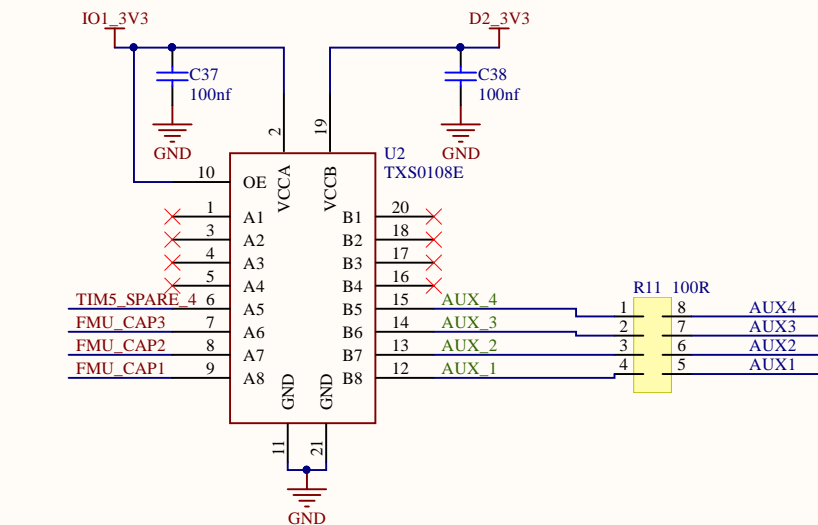
1

2

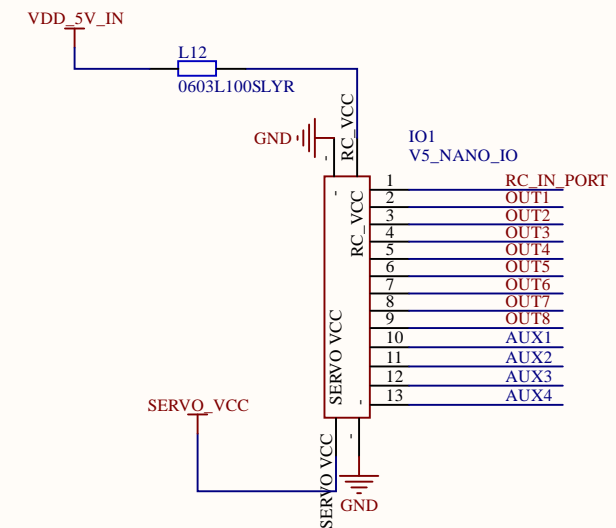
3

4

# PWM I/O



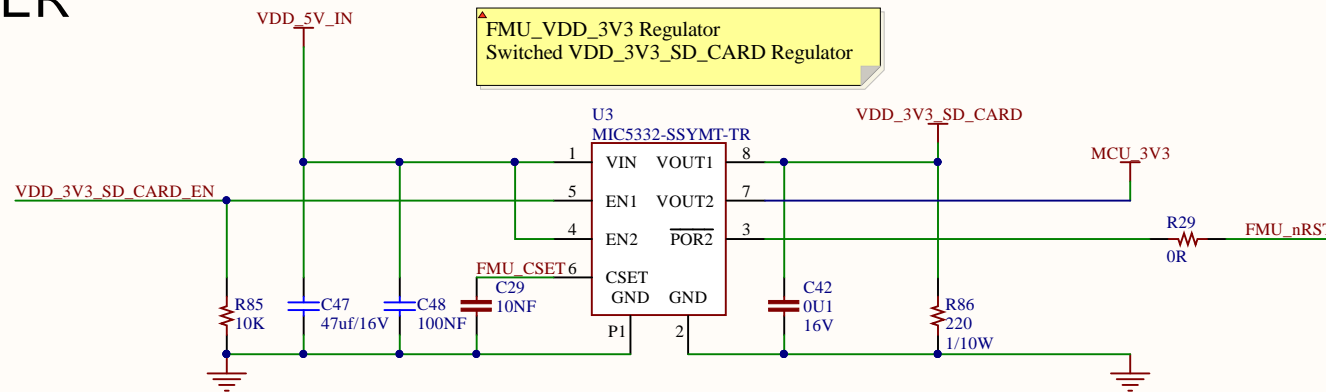
RC\_INPUT is for any of PPM/SBUS/SPEKTRUM



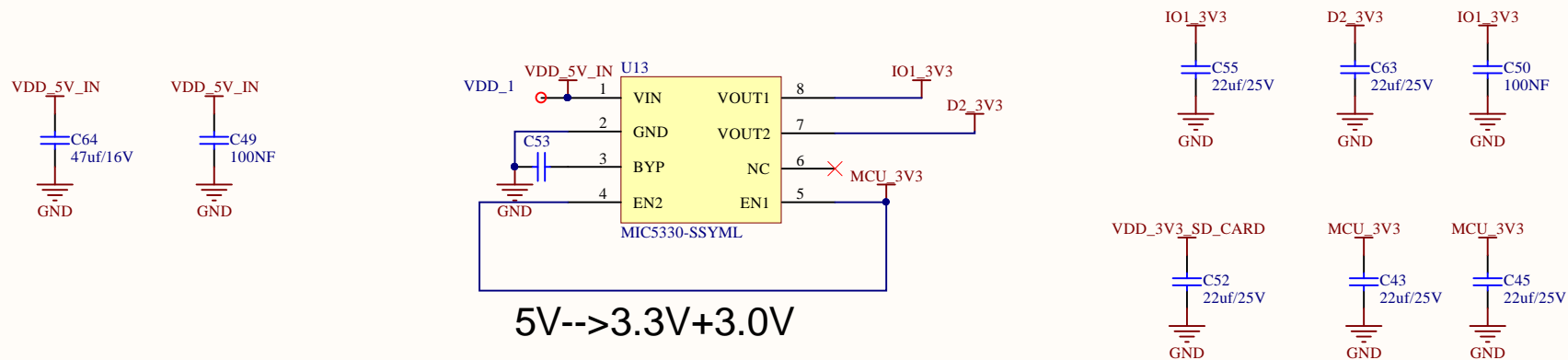
Tiele:	V5_NANO	SHEET	04 OF 16
VER	RC01	DATE:	2019-4-11
Designer	bin		
Reviewed			

# LDO POWER

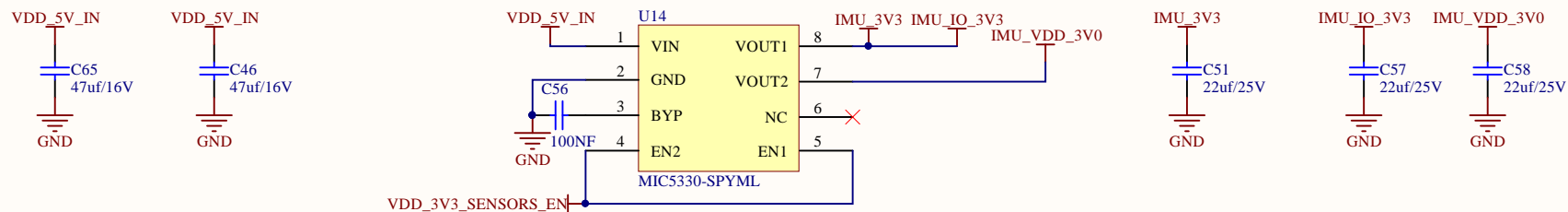
5V-->3.3V+3.3V MCU+TF CARD



5V-->3.3V+3.3V

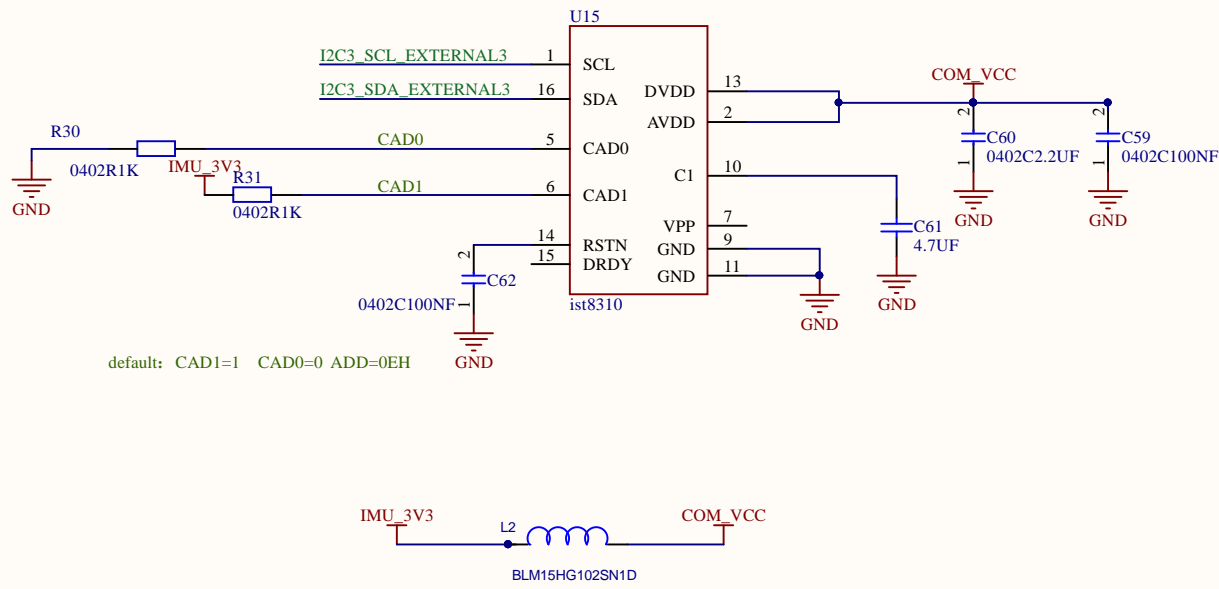


5V-->3.3V+3.0V



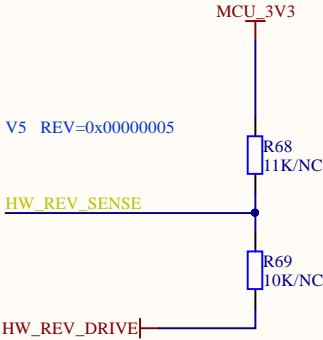
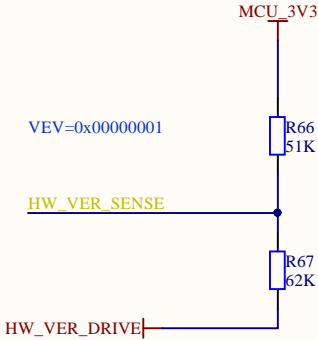
Tiele:	V5_NANO	SHEET	05 OF 16
VER	RC01	DATE:	2019-4-11
Designer	bin	CUAV	
Reviewed			

# Onboard Compass



Tiele: V5_NANO		SHEET 06 OF 16
VER	RC01	DATE: 2019-4-11
Designer	bin	
Reviewed		

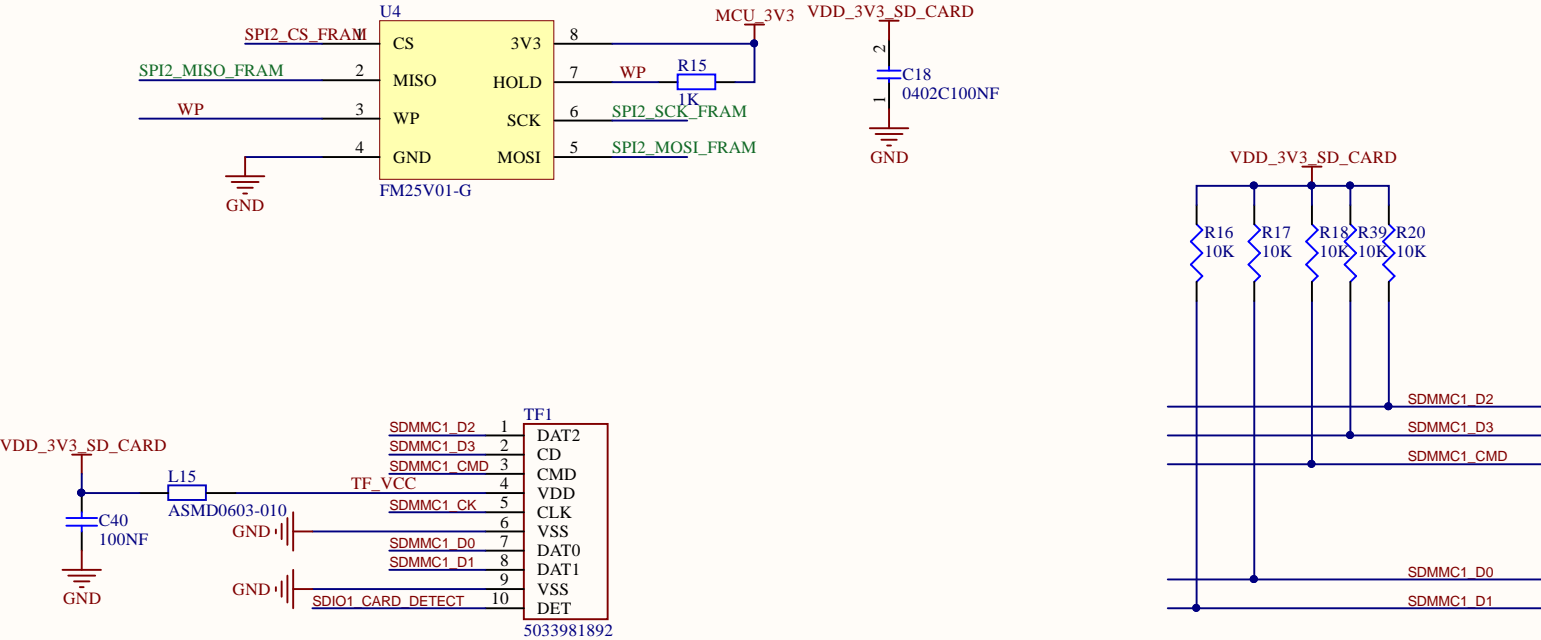
HW\_REV+VER



Currently ID has not been confirmed 2019-4-10

Tiele: V5_NANO		SHEET 7 OF 16
VER	RC01	DATE: 2019-4-11
Designer	bin	
Reviewed		

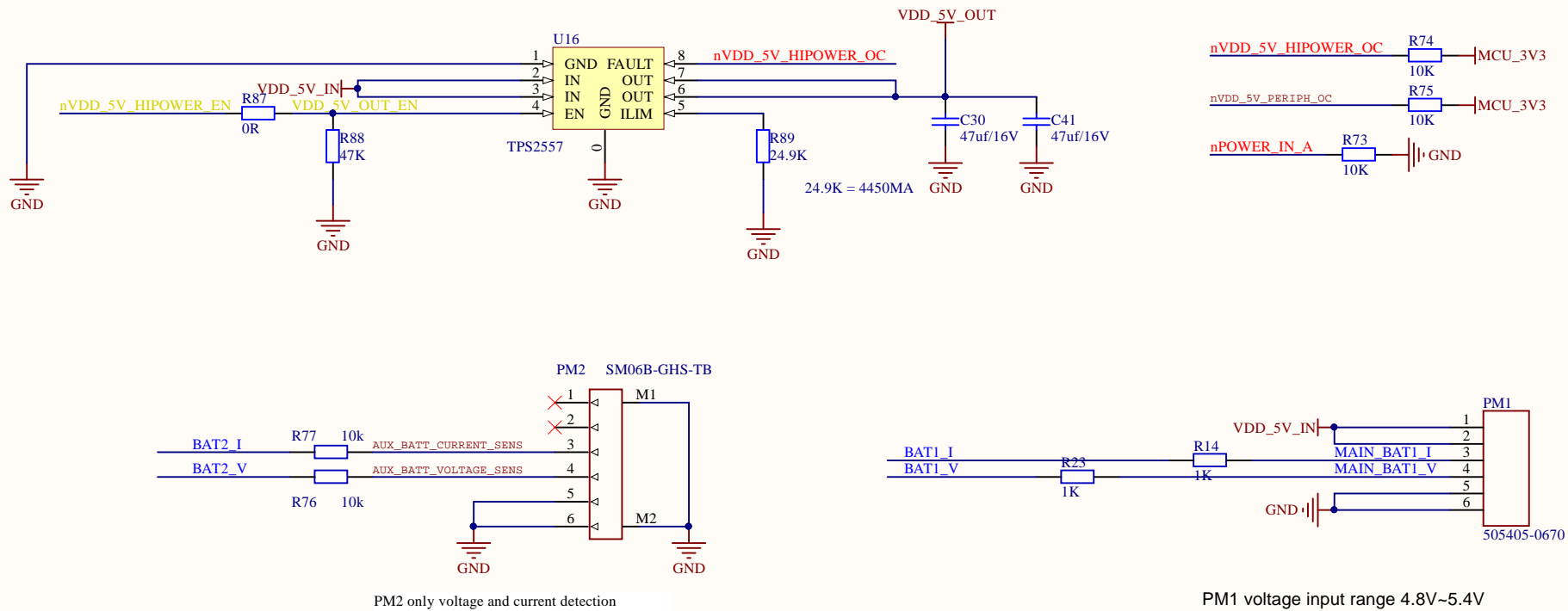
# TF CARD + MTD



Tiele: V5_NANO		SHEET 8 OF 16
VER	RC01	DATE: 2019-4-11
Designer	bin	
Reviewed		

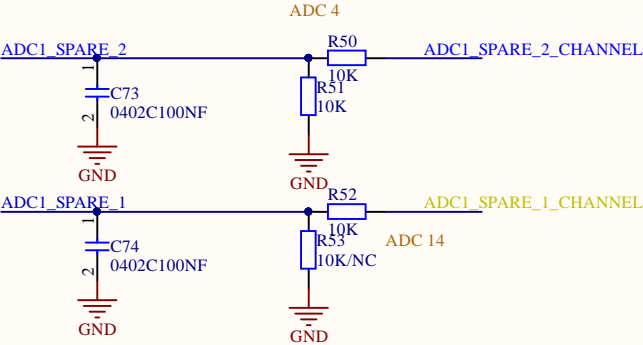
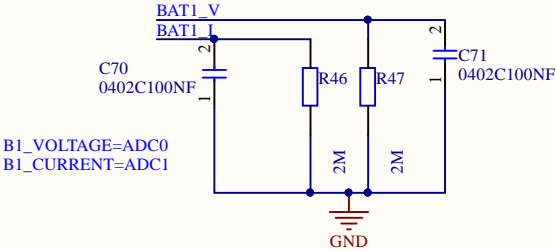
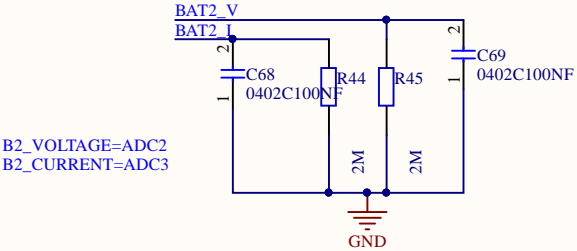


# Power IN



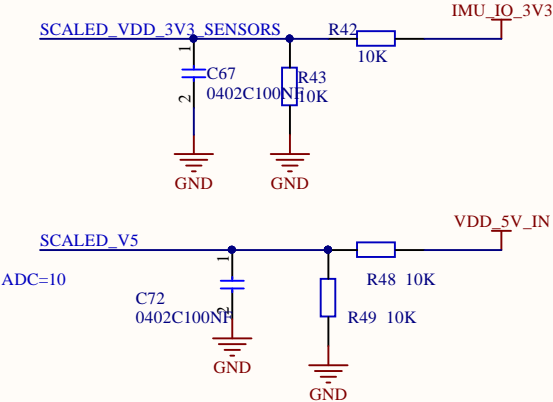
Tiele: V5_NANO		SHEET 09 OF 16
VER	RC01	DATE: 2019-8-14
Designer	bin	
Reviewed		

ADC RAIL




外部ad传感器检测ADC

IMU POWER SENSORS



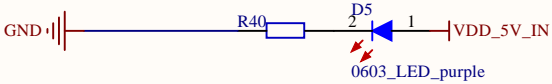
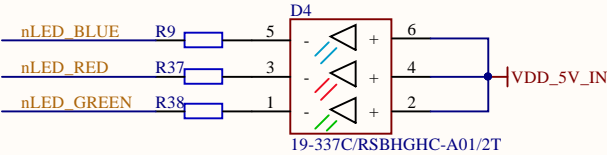
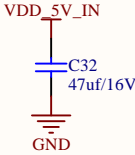
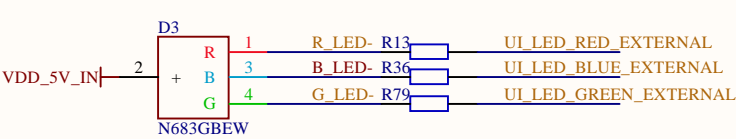
主板电压检测

Tiele: V5_NANO		SHEET 11 OF 16
VER	RC01	DATE: 2019-4-11
Designer	bin	
Reviewed		

## A

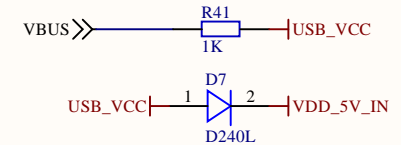
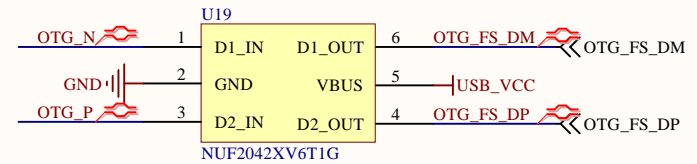
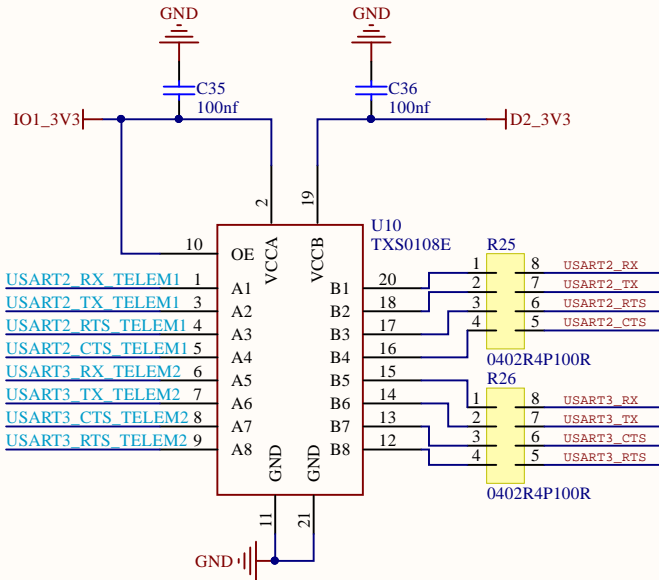
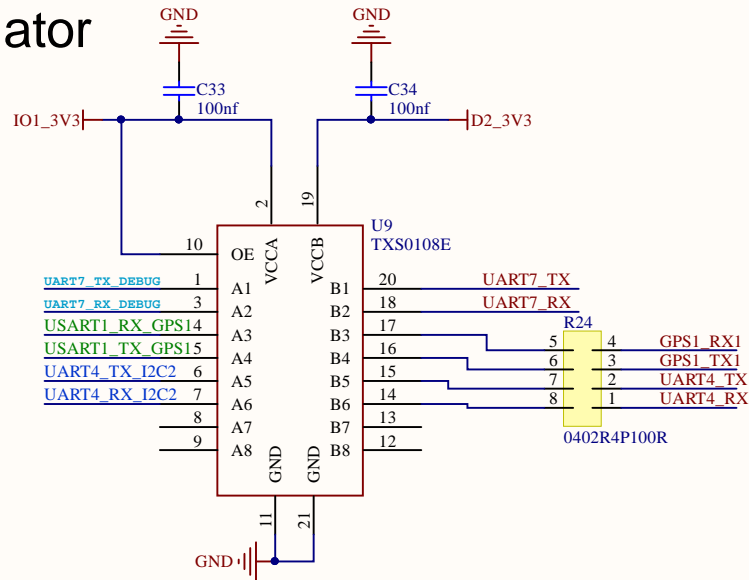
D

Status LED



Tiele: V5_NANO		SHEET 13 OF 16	
VER	RC01	DATE: 2019-4-11	
Designer	bin		
Reviewed			

Data\_Translator



Tiele: V5_NANO		SHEET 14 OF 16	
VER	RC01	DATE:	2019-4-11
Designer	bin		
Reviewed			

## A

D

## A

D

