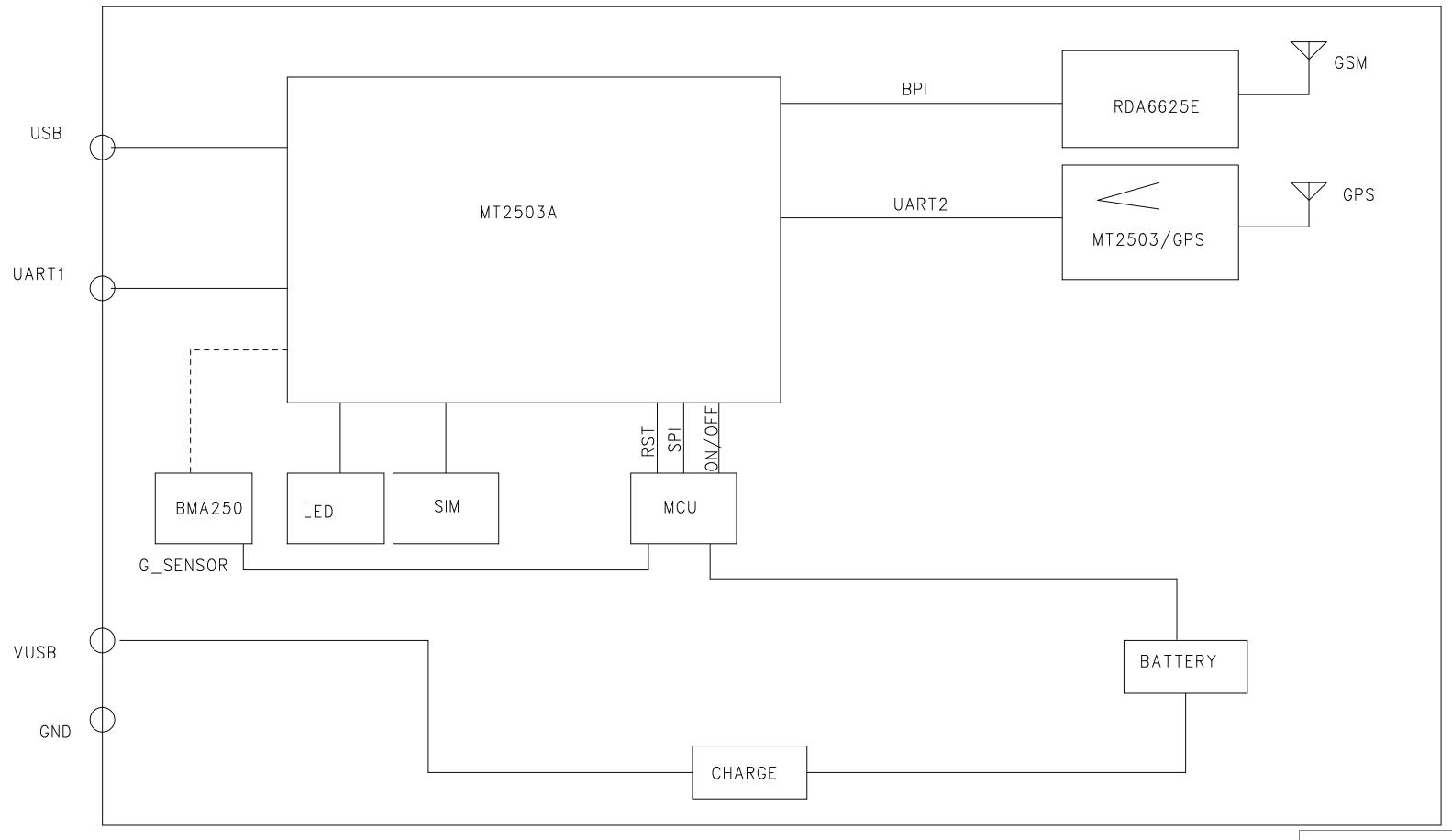
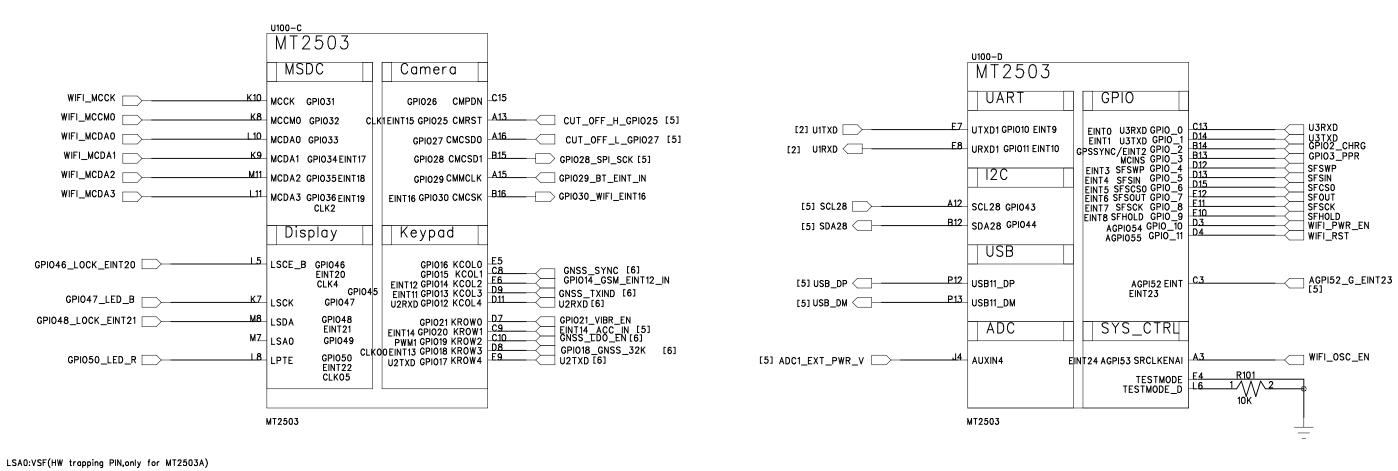
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PAGE02:	MT2503BB+SPI FLASH+WATCHDOG	
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	=	
		File NO:
MODEL:		Drawing NO:
TITLE:		STANDARD:
SCH:	PCB:	CHECK:
DESIGN:		APPROVE:



LSAO=L,VSF=1.8V LSAO=H,VSF=3V

[2] UITXD R8 100R TEST1
[2] UIRXD R9 100R TEST2
[3,4,5,6] VBAT TEST5

File NO:

MODEL:

MODEL:

Drawing NO:

TITLE:

STANDARD:

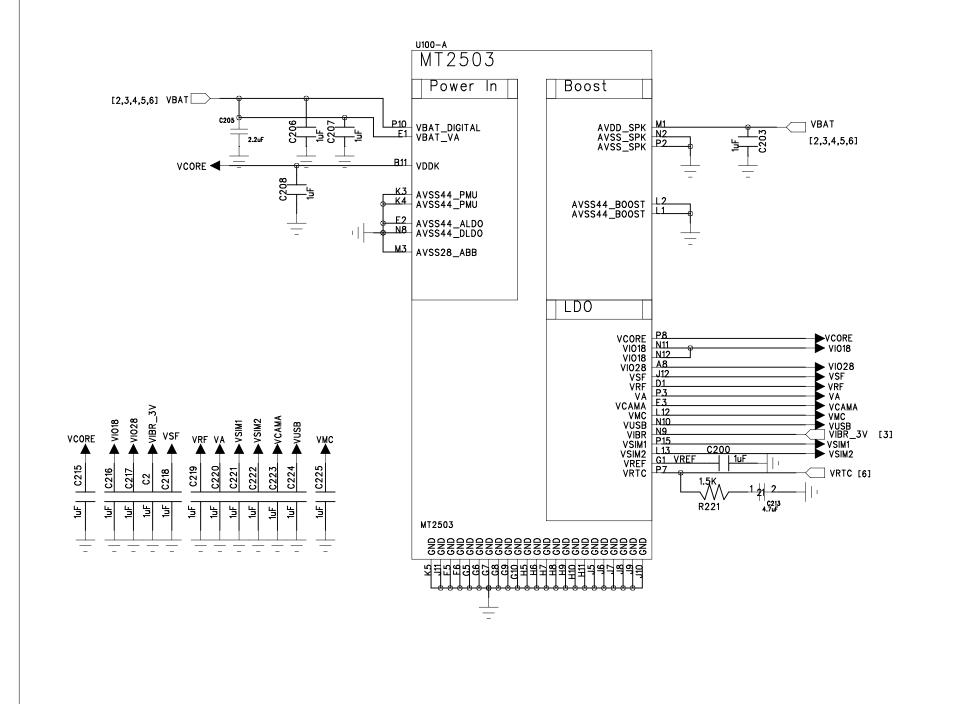
SCH:

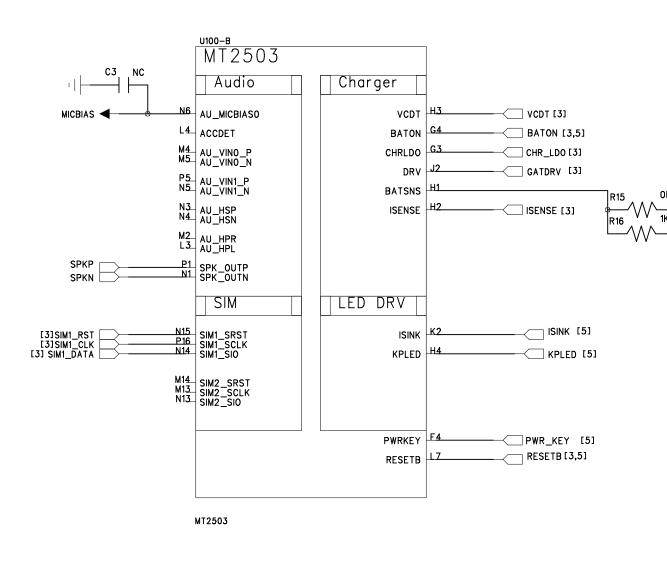
PCB:

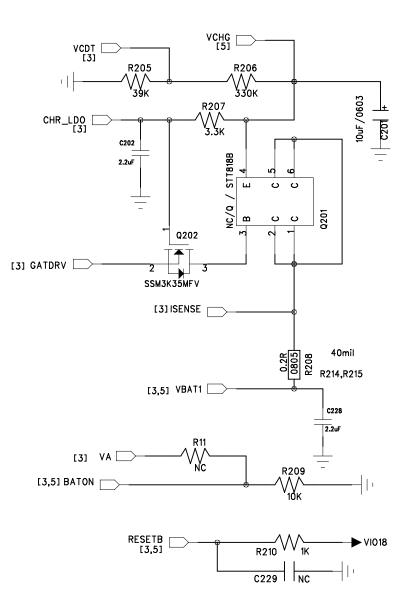
CHECK:

DESIGN:

APPROVE:





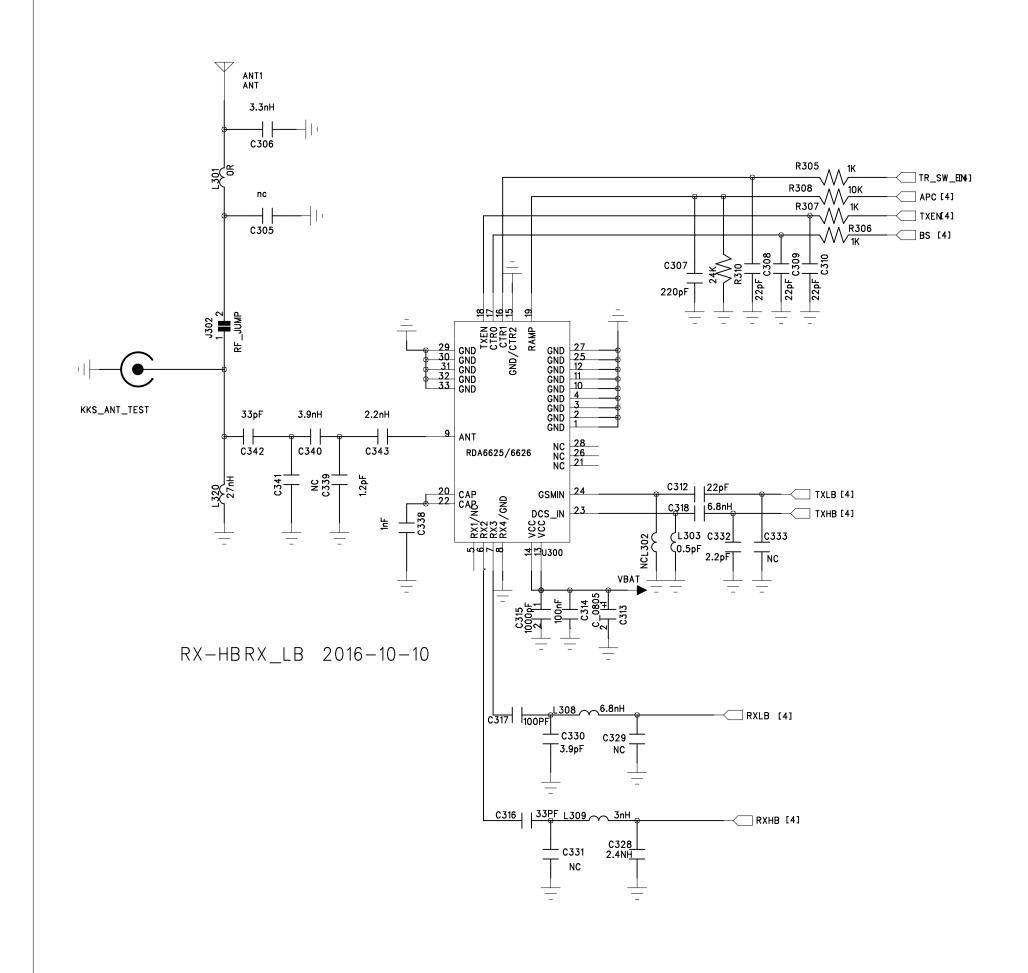


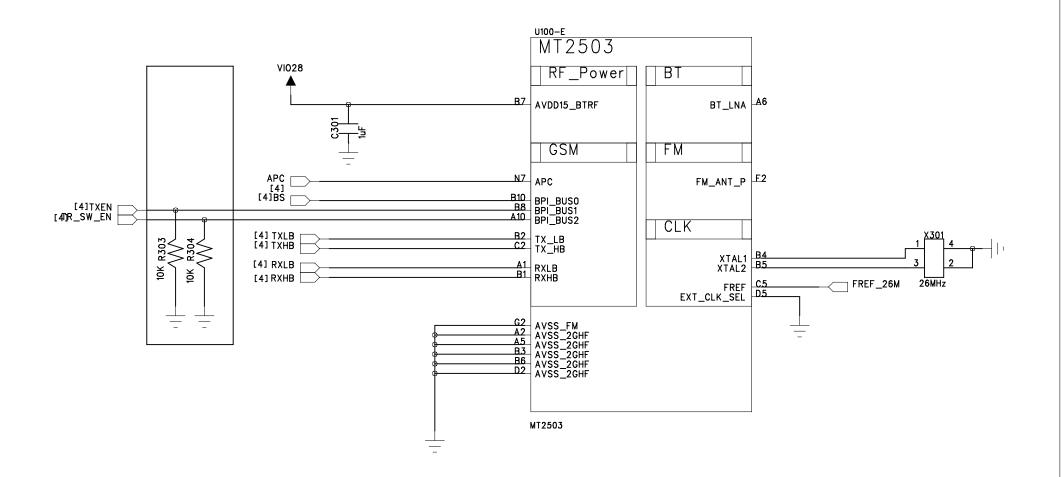
── VBAT [2,3,4,5,6]

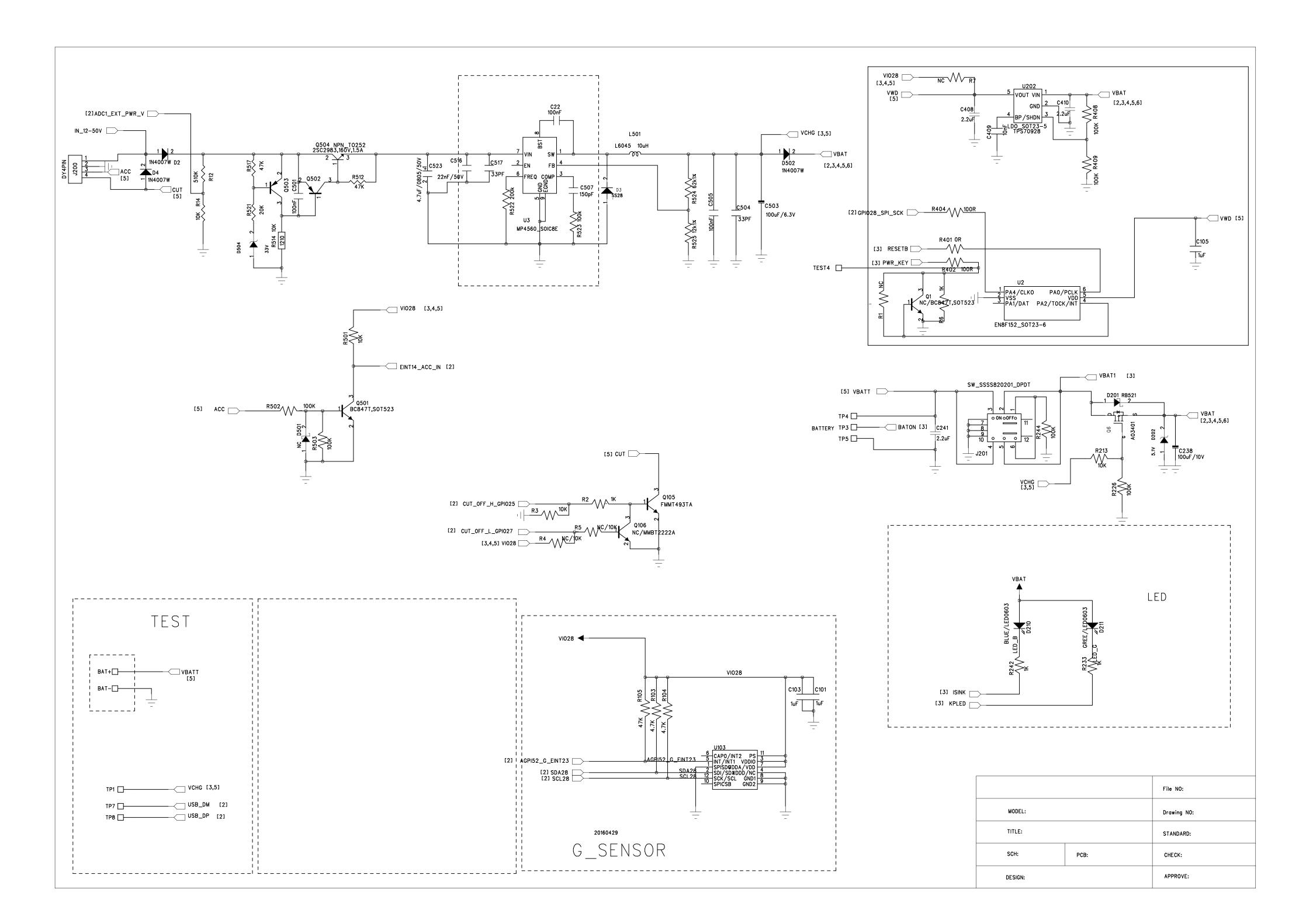
[3] VSIM1 ————————————————————————————————————	SIM1_RST SIM1_RST SIM1_DATA	J3 C791 1 VCC GND RST VPP CLK IO NC NC	

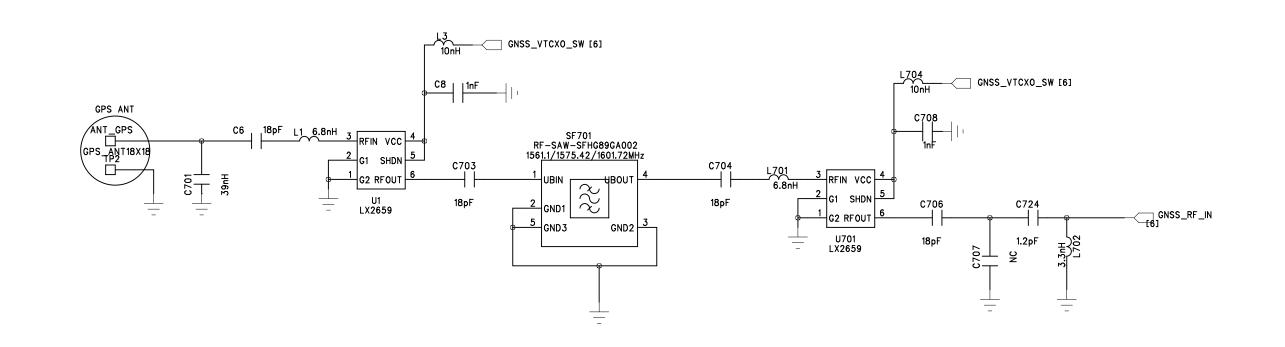
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MODEL:		Drawing NO:
TITLE:		STANDARD:
SCH:	PCB:	CHECK:
DESIGN:		APPROVE:

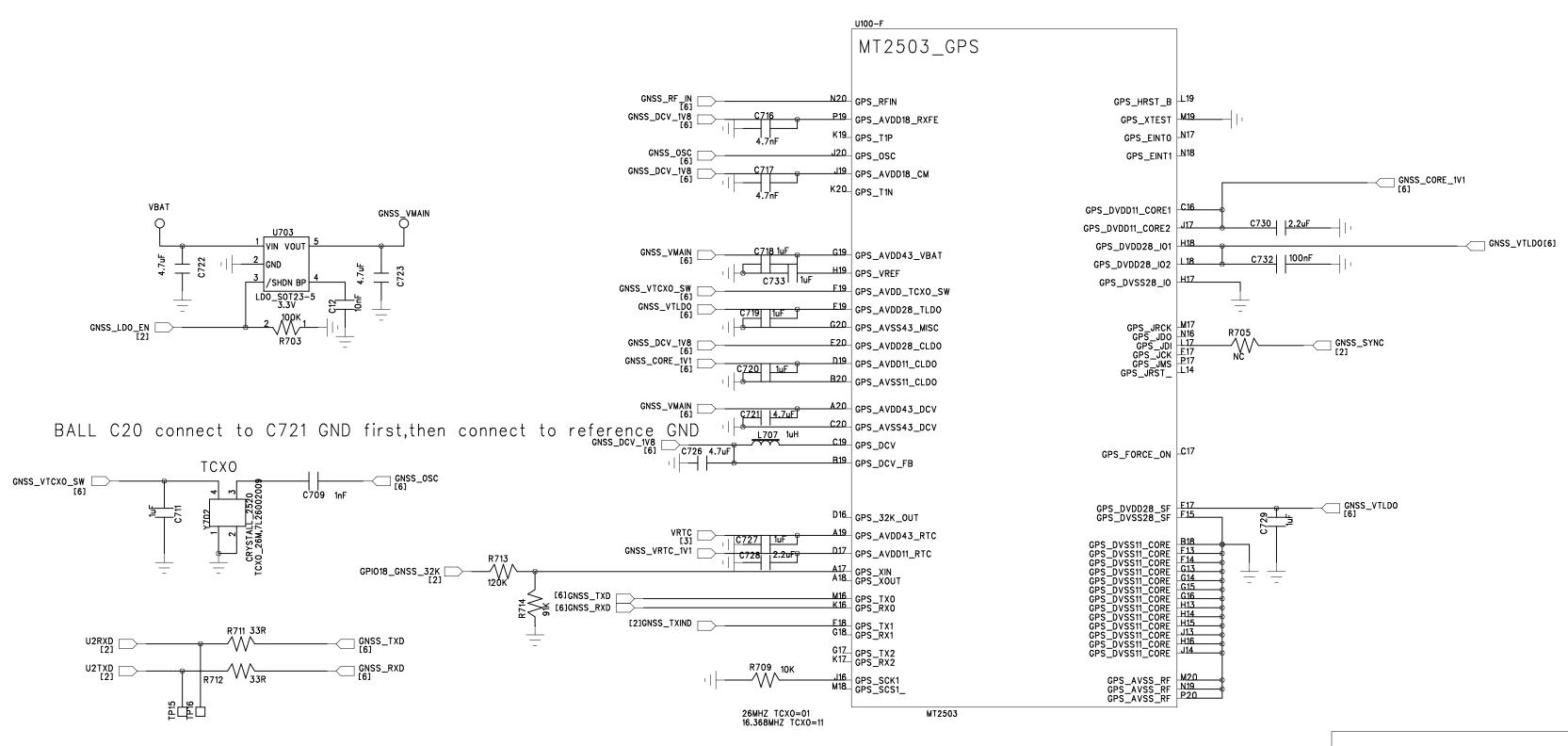
		File NO:
MODEL:		Drawing NO:
TITLE:		STANDARD:
SCH:	PCB:	CHECK:
DESIGN:		APPROVE:











		File NO:
MODEL:	MODEL:	
TITLE:		STANDARD:
SCH:	PCB:	CHECK:
DESIGN:	1	APPROVE: