

Ajay Kumar Garg Engineering College, Ghaziabad
Department of ECE
MODEL SOLUTION ST2

Course: B.Tech
 Session: 2016-17
 Subject: EDC
 Max.Marks: 50

Semester: III
 Section: EC-1, EC-2, EC-3, EI
 Sub.Code: REC-302
 Time: 3 Hour

Section - A

- 1) An abrupt Si p-n junction has $N_a = 10^{18} \text{ cm}^{-3}$ on one side and $N_d = 5 \times 10^{15} \text{ cm}^{-3}$ on the other side. Calculate the Fermi level position in p & n regions when $KT = 0.0259$ ($n_i = 1.5 \times 10^{10} \text{ cm}^{-3}$)

$$qV_o = KT \ln \frac{N_a N_d}{n_i^2} =$$

$$= 0.0259 \ln \left(\frac{5 \times 10^{33}}{2.25 \times 10^{20}} \right)$$

$$= 0.79 \text{ eV}$$

- 2) What is fill factor of solar cell? what is its importance?

The ratio $\frac{I_m V_m}{I_{sc} V_{oc}}$ is called fill factor.

where $I_m V_m$ is the maximum power delivered.

I_{sc} is the short circuit current

V_{oc} is the open circuit voltage.

Fill factor is the Figure of merit of solar cell so it is important to show efficiency.

Q3:- What is aspect ratio of MOSFET? on what factor it depends?
The ratio of width of channel to its length of ch is called the aspect ratio of MOSFET i.e. W/L ratio is aspect ratio.

It depends on the manufacturing of MOSFET.
Concentration of N_A acceptor and donor charge carriers.

Q4:- What is body effect in MOSFET?

When body and source are not short circuited incremental change in V_{SB} gives rise to increase in V_t that results in increase in i_D at constant V_{GS} i.e. Body Voltage controls i_D . Hence body/substrate acts as the another gate. This is called body effect. T

The body effect degrades the circuit performance.

Q5:- Define threshold voltage in MOSFET

The excess of V_{GS} over the V_t is called overdrive voltage.

The overdrive voltage determines the change in channel

$$V_{OV} = V_{GS} - V_t$$

where V_{GS} is Gate to source voltage

V_t is threshold voltage

V_{OV} is overdrive voltage

Threshold voltage is the minimum voltage required to form channel in MOSFET.

Section B

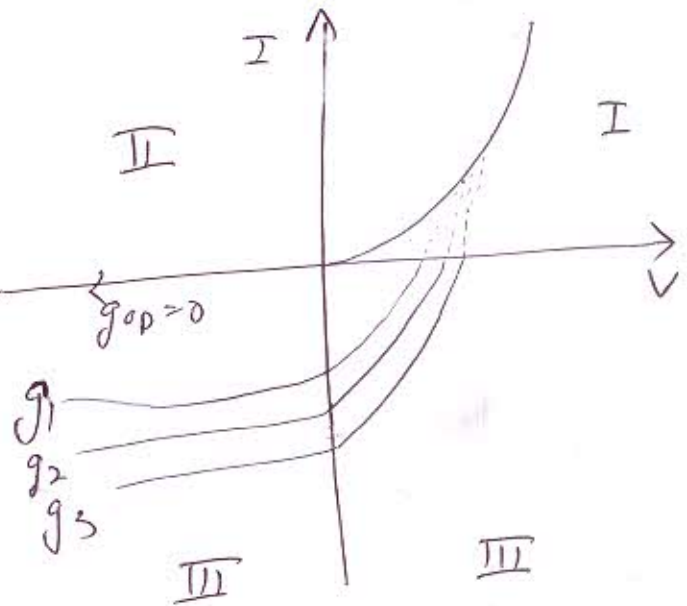
- 6) Draw V-I characteristics of illuminated Junction. Explain region of operation of diff. optoelectronic devices.
 For optoelectronic P-n junction devices the current equation is
- $$I = I_{th} (e^{V/KT} - 1) - I_{op}$$

where I_{th} is thermal current.

I_{op} is current generated due to optically generated carriers.
 So V-I characteristic curve is lowered by amount proportional to I_{op} generation rate.

Figure 1— V-I characteristic of optoelectronic device.

$g_1 < g_2 < g_3$ are the generation rate



Quadrant of operation:-

- ① The optoelectronic P-n junction acts as normal junction in 1st quadrant. But can be used as LED with special material.
- ② In 3rd quadrant V is -ve & I is also negative. The total power is VI which depends on g_{op} so the devices can be used as optical / photodetector.
- ③ In 4th quadrant I is -ve & power = $-VI$ -ve sign shows the power is delivered from the device to outer circuit e.g. in Solar cell.

Q7:- Derive equation of contact potential in terms of N_A & N_D for a p-n junction at equilibrium.

net current that flow across the junction at equilibrium is zero

$$\text{i.e. } J_p(\text{drift}) + J_p(\text{diffusion}) = 0$$

$$\text{i.e. } (q \mu_p p(x) E(x)) + (-D_p q \frac{dp(x)}{dx}) = 0$$

$$\Rightarrow q \left[\mu_p p(x) E(x) - D_p \frac{dp(x)}{dx} \right] = 0$$

$$\Rightarrow \frac{\mu_p}{D_p} E(x) = \frac{1}{p(x)} \frac{dp(x)}{dx}$$

using Einstein Relation $\frac{\mu_p}{D_p} = \frac{q}{kT}$

& By definition of electric field $E(x) = -\frac{dV(x)}{dx}$

$$\text{So } \frac{-q}{kT} \cdot \frac{dV(x)}{dx} = \frac{1}{p(x)} \frac{dp(x)}{dx}$$

Integrating on both sides,

$$\frac{-q}{kT} \int_{V_p}^{V_n} dV(x) = \int_{p_0}^{p_n} \frac{1}{p(x)} dp(x)$$

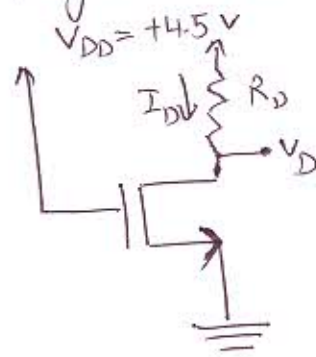
$$\frac{-q}{kT} (V_n - V_p) = \ln(p_n) - \ln(p_0) = \ln\left(\frac{p_n}{p_0}\right)$$

$$\Rightarrow V_0 = \frac{kT}{q} \ln\left(\frac{p_0}{p_n}\right) \quad \left| \begin{array}{l} \text{where } V_0 = V_p - V_n \\ \text{is contact potential} \end{array} \right.$$

as $p_p = N_A$ & $p_n = n_i^2 / N_D$

$$\Rightarrow V_0 = \frac{kT}{q} \ln\left(\frac{N_A N_D}{n_i^2}\right)$$

Q8:- Design The circuit given below to establish a drain Voltage of 0.2V. Calculate the effective resistance between drain and source at this operating point. Assume $V_t = 1V$ and $k_n = 1 \text{ mA/V}^2$. Neglect channel modulation effect.



for the given circuit $V_D < V_{GS}$ So operation of MOSFET in triode region

$$V_{GS} - V_D = (4.5 - 0.2) \text{ V} = 4.3 \text{ V}$$

In triode region drain current

$$I_D = k'_n \frac{W}{L} \left[(V_{GS} - V_t) V_{DS} - \frac{1}{2} V_{DS}^2 \right]$$

$$k'_n \frac{W}{L} = 1 \text{ mA/V}^2$$

$$V_{GS} = 4.5$$

$$V_{DS} = 0.2$$

$$V_t = 1$$

$$I_D = 1 \left[(4.5 - 1) \times 0.2 - \frac{1}{2} (0.2)^2 \right]$$

$$= 3.5 \times 0.2 - 0.02$$

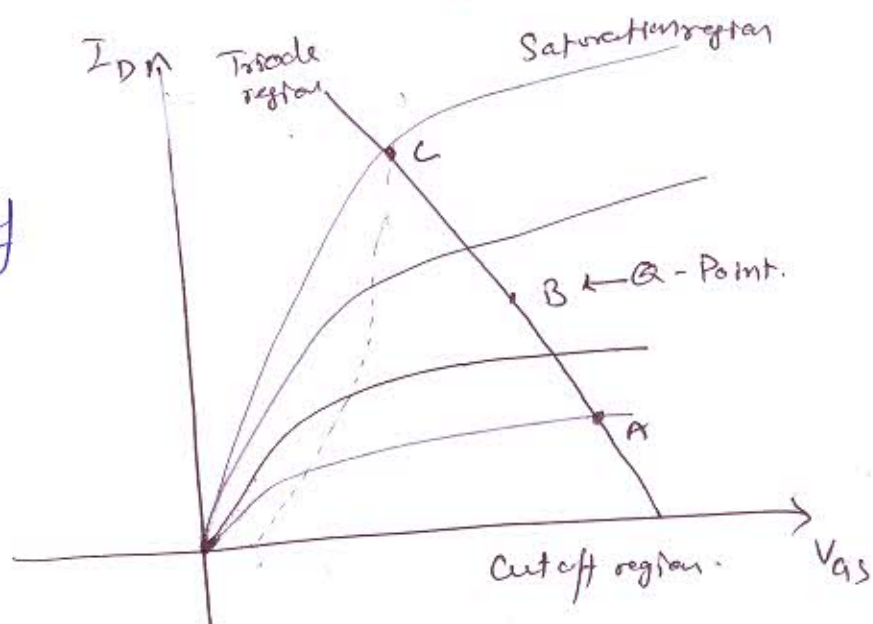
$$= 0.68 \text{ mA}$$

$$\text{Required value of } R_D = \frac{V_{DD} - V_D}{I_D} = \frac{4.5 - 0.2}{0.68} = 6.32 \text{ k}\Omega$$

$$R_D = 6.32 \text{ k}\Omega$$

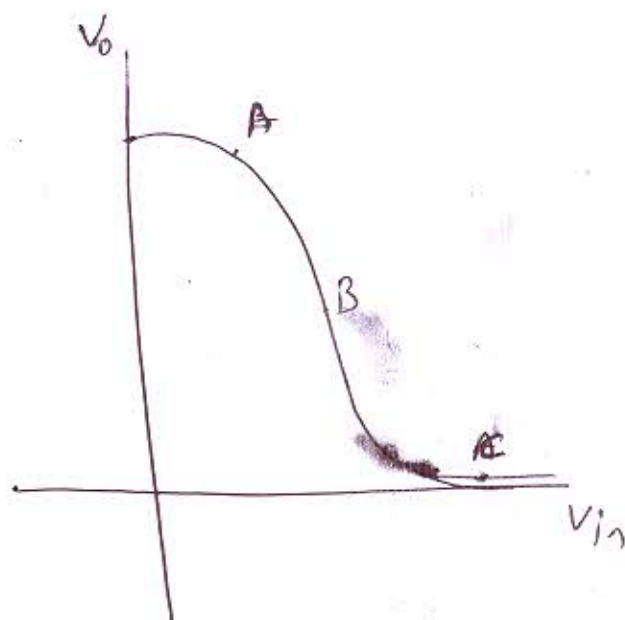
Q9. With the help of Voltage transfer curve Explain region of operation of MOSFET to work as amplifier as switch.

Figure V-I curve of MOSFET



Transfer

Figure:- Voltage Transfer Curve of MOSFET.



In the V-I curve. The line ABC is the load line. The transfer curve is graph between V_O & V_{in} .

The point B is called the Q point or operating point.

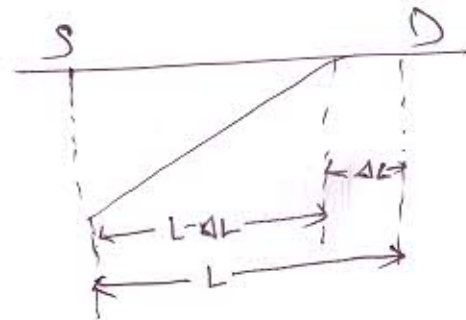
MOSFET as Amplifier:- MOSFET works as Amplifier, it should operate in the linear region around a point. So that signal should not be chopped off.

MOSFET as switch:- To work MOSFET as switch it should be operated at point A or at B i.e. in triode region or in cut off region.

Q10: What is channel modulation effect in MOSFET? Derive the expression of drain current in saturation region considering this effect.

In practice increasing V_{DS} beyond $V_{DS sat}$. the pinch off point moves slightly away from drain toward source. The voltage across ch. remains constant at $V_{GS} - V_t = V_{DS sat}$. and additional voltage applied to drain appears as voltage drop across narrow depletion region between the end of the ch. and drain region. This voltage accelerates the electrons that races to drain end of ch and sweeps them across depletion region. with widening of depletion layer the ~~width~~ channel length reduces. This effect is called channel modulation effect.

Drain current:-



at saturation the drain current

$$I_D = \frac{1}{2} k'_n \frac{W}{L} (V_{OV})^2$$

when channel modulation effect is considered.

$$I_D = \frac{1}{2} k'_n \frac{W}{L + \Delta L} (V_{GS} - V_t)^2$$

$$= \frac{1}{2} k'_n \frac{W}{L} \left(1 + \frac{\Delta L}{L}\right) (V_{OV})^2$$

assuming $\frac{\Delta L}{L} \ll 1$ also $\Delta L \propto V_{DS}$

$$\Delta L = \lambda' V_{DS}$$

where λ is constant of proportionality

$$\text{So } I_D = \frac{1}{2} k'_n \frac{W}{L} \left(1 + \frac{\lambda'}{L} V_{DS}\right) V_{OV}^2$$

$$\text{Let } \frac{\lambda'}{L} = \lambda$$

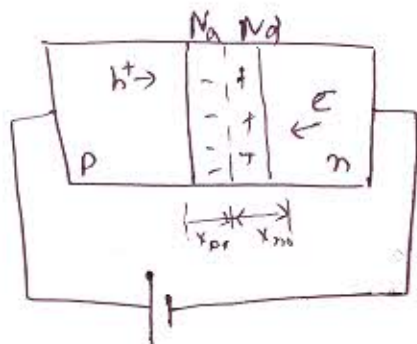
$$\text{So } d_D = \frac{1}{2} k_n \frac{w}{L} (V_{GS})^2 (1 + \lambda V_{DS})$$

The factor $(1 + \lambda V_{DS})$ is representing channel mod. effect.

Section C

- 1) Derive the equation for space charge region in both sides of p-n junction at equilibrium. Also draw the variation of charge density and electric field across the junction.

Space charge region is the depletion layer of P-n junction that can be shown as.



The space charge region
W is $x_{po} + x_{no}$

By the poissens equation

$$E = \int \frac{\rho}{\epsilon}$$

where ρ is charge density
 ϵ is permittivity.

For p-region

$$\epsilon_p = \frac{q N_a x_{po}}{\epsilon}$$

For n-region

$$\epsilon_n = \frac{q N_d x_{no}}{\epsilon}$$

If A be the area of cross section then at equilibrium

$$N_a x_{po} q A = N_d x_{no} q A$$

$$\Rightarrow \frac{x_{po}}{x_{no}} = \frac{N_d}{N_a}$$

Also from definition of electrical

$$E = -\frac{dv}{dx}$$

$$\Rightarrow E dx = -dv$$

integrating both sides.

$$\int_0^{V_0} dv = -E \int_{x_{p0}}^{x_{n0}} dx$$

$$\Rightarrow V_0 = \frac{1}{2} E (x_{p0} + x_{n0})$$

$$\Rightarrow V_0 = \frac{1}{2} \frac{1}{2} E w$$

only for holes

$$V_0 = \frac{1}{2} \frac{q N_d x_{p0}}{E} \cdot w \quad \text{using equation of } E.$$

$$\text{from above } x_{p0} = \frac{N_d x_{n0}}{N_a}$$

$$\text{Put in } x_{p0} + x_{n0} = w$$

$$w = x_{p0} \left[1 + \frac{N_d}{N_a} \right]$$

$$x_{p0} \Rightarrow w \left[\frac{N_a}{N_a + N_d} \right]$$

Put the value in V_0 expression

$$V_0 = \frac{1}{2} q \frac{N_d w}{C} \cdot w \left[\frac{N_d}{N_a + N_d} \right]$$

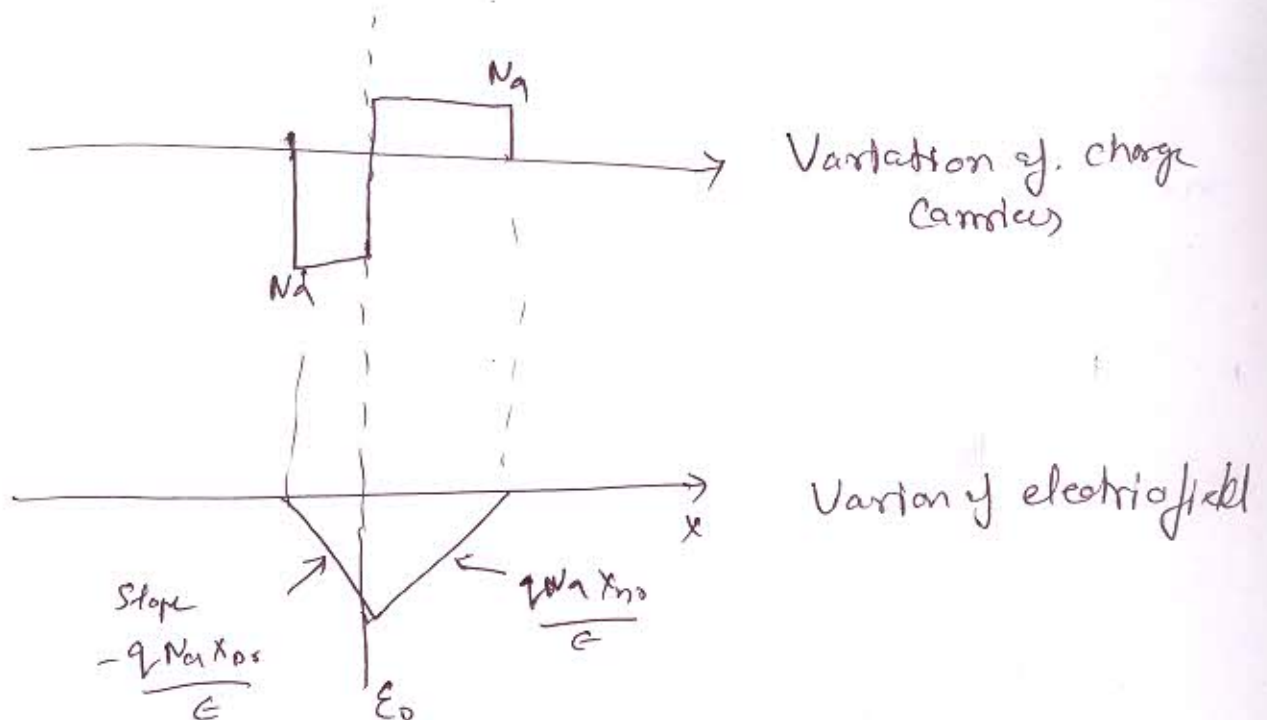
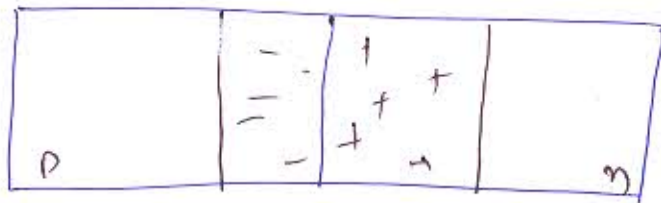
$$w^2 = \frac{2 \epsilon V_0}{q} \left[\frac{N_a N_d}{N_a + N_d} \right]$$

$$w = \sqrt{\frac{2 \epsilon V_0}{q} \left[\frac{1}{N_a} + \frac{1}{N_d} \right]}$$

This is space charge density

On p side $\rho_{po} = \sqrt{\frac{2\epsilon V_0}{q} \left[\frac{N_a}{N_a + N_d} + \frac{1}{N_d} \right]} \cdot \frac{N_d}{N_a + N_d}$

2 $\rho_{no} = \sqrt{\frac{2\epsilon V_0}{q} \left[\frac{1}{N_a} + \frac{1}{N_d} \right]} \cdot \frac{N_d}{N_a + N_d}$



Q12:- Draw the structure of MOSFET to explain its working. Derive the expression of Drain current of. for small V_{ps} .

