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# Chapter 1

# **AMD Debugger API Specification**

# 1.1 Introduction

The amd-dbgapi is a library that implements an AMD GPU debugger application programming interface (API). It provides the support necessary for a client of the library to control the execution and inspect the state of supported commercially available AMD GPU devices.

The term *client* is used to refer to the application that uses this API.

The term library is used to refer to the implementation of this interface being used by the client.

The term AMD GPU is used to refer to commercially available AMD GPU devices supported by the library.

The term *inferior* is used to refer to the process being debugged.

The library does not provide any operations to perform symbolic mappings, code object decoding, or stack unwinding. The client must use the AMD GPU code object ELF ABI defined in User Guide for AMDGPU Backend - Code Object, together with the AMD GPU debug information DWARF and call frame information CFI ABI define in User Guide for AMDGPU Backend - Code Object - DWARF to perform those tasks.

The library does not provide operations for inserting or managing breakpoints. The client must write the architecture specific breakpoint instruction provided by the AMD\_DBGAPI\_ARCHITECTURE\_INFO\_BREAKPOINT\_INSTRUCTION query into the loaded code object memory to set breakpoints. For resuming from breakpoints the client must use the displaced stepping mechanism provided by amd\_dbgapi\_displaced\_stepping\_start and amd\_dbgapi\_displaced\_stepping\_complete in conjunction with the amd\_dbgapi\_wave\_resume in single step mode. In order to determine the location of stopped waves the client must read the architecture specific program counter register available using the AMD\_DBGAPI\_ARCHITECTURE\_INFO\_PC\_REGISTER query and adjust it by the amount specified by the AMD\_DBGAPI\_ARCHITECTURE\_INFO\_BREAKPOINT\_INSTRUCTION\_PC\_ADJUST query.

The client is responsible for checking that only a single thread at a time invokes a function provided by the library. A callback (see Callbacks) invoked by the library must not itself invoke any function provided by the library.

The library implementation uses the native operating system to inspect and control the inferior. Therefore, the library must be executed on the same machine as the inferior.

The library uses opaque handles to refer to the entities that it manages. These should not be modified directly. See the handle definitions for information on the lifetime and scope of handles of that type. If a handle becomes invalidated it is undefined to use it with any library operations. A handle value is globally unique between a call to amd\_dbgapi\_initialize and a matching call to amd\_dbgapi\_finalize. This is true even if the handle becomes invalidated: handle values are not reused within a library instance. Every handle with handle of 0 is reserved to indicate the handle does not reference an entity.

When the library is first loaded it is in the uninitialized state with the logging level set to AMD\_DBGAPI\_LOG\_LEVEL\_NONE.

### 1.2 AMD GPU Execution Model

In this section the AMD GPU execution model is described to provide background to the reader if they are not familiar with this environment. The AMD GPU execution model is more complicated than that of a traditional CPU because of how GPU hardware is used to accelerate and schedule the very large number of threads of execution that are created on GPUs.

Chapter 2 of the [HSA Programmer's Reference Manual][hsa-prm] provides an introduction to this execution model. Note that the AMD ROCm compilers compile directly to ISA and do not use the HSAIL intermediate language. However, the ROCr low-level runtime and ROCgdb debugger use the same terminology.

In this model, a CPU process may interact with multiple AMD GPU devices, which are termed agents. A Process Address Space Identifier (PASID) is created for each process that interacts with agents. An agent can be executing code for multiple processes at once. This is achieved by mapping the PASID to one of a limited set of Virtual Memory Identifiers (VMIDs). Each VMID is associated with its own page table.

The AMD GPU device driver for Linux, termed the Kernel Mode Driver (KMD), manages the page tables used by each GPU so they correlate with the CPU page table for the corresponding process. The CPU and GPU page tables do not necessarily map all the same memory pages but pages they do have in common have the same virtual address. Therefore, the CPU and GPUs have a unified address space.

Each GPU includes one or more Microcode Engines (ME) that can execute microcode firmware. This firmware includes a Hardware Scheduler (HWS) that, in collaboration with the KMD, manages which processes, identified by a PASID, are mapped onto the GPU using one of the limited VMIDs. This mapping configures the VMID to use the GPU page table that corresponds to the PASID. In this way, the code executing on the GPU from different processes is isolated.

Multiple software submission queues may be created for each agent. The GPU hardware has a limited number of pipes, each of which has a fixed number of hardware queues. The HWS, in collaboration with the KMD, is responsible for mapping software queues onto hardware queues. This is done by multiplexing the software queues onto hardware queues using time slicing. The software queues provide a virtualized abstraction, allowing for more queues than are directly supported by the hardware. Each ME manages its own set of pipes and their associated hardware queues.

To execute code on the GPU, a packet must be created and placed in a software queue. This is achieved using regular user space atomic memory operations. No Linux kernel call is required. For this reason, the queues are termed user mode queues.

The AMD ROCm platform uses the Asynchronous Queuing Language (AQL) packet format defined in the [HSA Platform System Architecture Specification][hsa-sysarch]. Packets can request GPU management actions (for example, manage memory coherence) and the execution of kernel functions. The ME firmware includes the Command Processor (CP) which, together with fixed-function hardware support, is responsible for detecting when packets are added to software queues that are mapped to hardware queues. Once detected, CP is responsible for initiating actions requested by the packet, using the appropriate VMID when performing all memory operations.

Dispatch packets are used to request the execution of a kernel function. Each dispatch packet specifies the address of a kernel descriptor, the address of the kernel argument block holding the arguments to the kernel function, and the number of threads of execution to create to execute the kernel function. The kernel descriptor describes how the CP must configure the hardware to execute the kernel function and the starting address of the kernel function code. The compiler generates a kernel descriptor in the code object for each kernel function and determines the kernel argument block layout. The number of threads of execution is specified as a grid, such that each thread of execution can identify its position in the grid. Conceptually, each of these threads executes the same kernel code, with the same arguments.

The dispatch grid is organized as a three-dimensional collection of work-groups, where each work-group is the same size (except for potential boundary partial work-groups). The work-groups form a three-dimensional collection of work-items. The work-items are the threads of execution. The position of a work-item is its zero-based three-dimensional

position in a work-group, termed its work-item ID, plus its work-group's three-dimensional position in the dispatch grid, termed its work-group ID. These three-dimensional IDs can also be expressed as a zero-based one-dimensional ID, termed a flat ID, by simply numbering the elements in a natural manner akin to linearizing a multi-dimensional array.

Consecutive work-items, in flat work-item ID order, of a work-group are organized into fixed size wavefronts, or waves for short. Each work-item position in the wave is termed a lane, and has a zero-base lane ID. The hardware imposes an upper limit on the number of work-items in a work-group but does not limit the number of work-groups in a dispatch grid. The hardware executes instructions for waves independently. But the lanes of a wave all execute the same instruction jointly. This is termed Single Instruction Multiple Thread (SIMT) execution.

Each hardware wave has a set of registers that are shared by all lanes of the wave, termed scalar registers. There is only one set of scalar registers for the whole wave. Instructions that act on the whole wave, which typically use scalar registers, are termed scalar instructions.

Additionally, each wave also has a set of vector registers that are replicated so each lane has its own copy. A set of vector registers can be viewed as a vector with each element of the vector belonging to the corresponding lane of the wave. Instructions that act on vector registers, which produce independent results for each lane, are termed vector instructions.

Each hardware wave has an execution mask that controls if the execution of a vector instruction should change the state of a particular lane. If the lane is masked off, no changes are made for that lane and the instruction is effectively ignored. The compiler generates code to update the execution mask which emulates independent work-item execution. However, the lanes of a wave do not execute instructions independently. If two subsets of lanes in a wave need to execute different code, the compiler will generate code to set the execution mask to execute the subset of lanes for one path, then generate instructions for that path. The compiler will then generate code to change the execution mask to enable the other subset of lanes, then generate code for those lanes. If both subsets of lanes execute the same code, the compiler will generate code to set the execution mask to include both subsets of lanes, then generate code as usual. When only a subset of lanes is enabled, they are said to be executing divergent control flow. When all lanes are enabled, they are said to be executing wave uniform control flow.

Not all MEs have the hardware to execute kernel functions. One such ME is used to execute the HWS microcode and to execute microcode that manages a service queue that is used to update GPU state. If the ME does support kernel function execution it uses fixed-function hardware to initiate the creation of waves. This is accomplished by sending requests to create work-groups to one or more Compute Units (CUs). Requests are sent to create all the work-groups of a dispatch grid. Each CU has resources to hold a fixed number of waves and has fixed-function hardware to schedule execution of these waves. The scheduler may execute multiple waves concurrently and will hide latency by switching between the waves that are ready to execute. At any point of time, a subset of the waves belonging to work-groups in a dispatch may be actively executing. As waves complete, the waves of subsequent work-group requests are created.

Each CU has a fixed amount of memory from which it allocates vector and scalar registers. The kernel descriptor specifies how many registers to allocate for a wave. There is a tradeoff between how many waves can be created on a CU and the number of registers each can use.

The CU also has a fixed size Local Data Store (LDS). A dispatch packet specifies how much LDS each work-group is allocated. All waves in a work-group are created on the same CU. This allows the LDS to be used to share data between the waves of the same work-group. There is a tradeoff between how much LDS a work-group can allocate, and the number of work-groups that can fit on a CU. The address of a location in a work-group LDS allocation is zero-based and is a different address space than the global virtual memory. There are specific instructions that take an LDS address to access it. There are also flat address instructions that map the LDS address range into an unused fixed aperture range of the global virtual address range. An LDS address can be converted to or from a flat address by offsetting by the base of the aperture. Note that a flat address in the LDS aperture only accesses the LDS work-group allocation for the wave that uses it. The same address will access different LDS allocations if used by waves in different work-groups.

The dispatch packet specifies the amount of scratch memory that must be allocated for a work-item. This is used for work-item private memory. Fixed-function hardware in the CU manages per wave allocation of scratch memory from

pre-allocated global virtual memory mapped to GPU device memory. Like an LDS address, a scratch address is zero-based, but is per work-item instead of per work-group. It maps to an aperture in a flat address. The hardware swizzles this address so that adjacent lanes access adjacent DWORDs (4 bytes) in global memory for better cache performance.

For an AMD Radeon Instinct™ MI60 GPU the work-group size limit is 1,024 work-items, the wave size is 64, and the CU count is 64. A CU can hold up to 40 waves (this is limited to 32 if using scratch memory). Therefore, a work-group can comprise between 1 and 16 waves inclusive, and there can be up to 2,560 waves, making a maximum of 163,840 work-items. A CU is organized as 4 Execution Units (EUs) also referred to as Single Instruction Multiple Data units (SIMDs) that can each hold 10 waves. Each SIMD has 256 64-wide DWORD vector registers and each CU has 800 DWORD scalar registers. A single wave can access up to 256 64-wide vector registers and 112 scalar registers. A CU has 64KiB of LDS.

# 1.3 Supported AMD GPU Architectures

The following AMD GPU architectures are supported:

- gfx900 (AMD Vega 10)
- gfx906 (AMD Vega 7nm also referred to as AMD Vega 20)
- gfx908 (AMD Instinct™ MI100 accelerator)
- · gfx90a (Aldebaran)
- gfx1010 (Navi10)
- gfx1011 (Navi12)
- gfx1012 (Navi14)
- gfx1030 (Sienna Cichlid)
- gfx1031 (Navy Flounder)

For more information about the AMD ROCm ecosystem, please refer to:

• https://rocmdocs.amd.com/

### 1.4 Known Limitations and Restrictions

The AMD Debugger API library implementation is currently a prototype and has the following restrictions. Future releases aim to address these restrictions.

- 1. The following \*\_get\_info queries are not yet implemented:
  - · AMD DBGAPI QUEUE INFO ERROR REASON
  - AMD\_DBGAPI\_QUEUE\_INFO\_STATE
- 2. On a AMD\_DBGAPI\_STATUS\_FATAL error the library does fully reset the internal state and so subsequent functions may not operate correctly.
- 3. amd\_dbgapi\_process\_next\_pending\_event returns AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP events only for AQL queues. PM4 queues that launch wavefronts are not supported.
- 4. amd dbgapi queue packet list returns packets only for AQL queues.

1.5 References 5

# 1.5 References

Advanced Micro Devices: www.amd.com

2. AMD ROCm Ecosystem: rocmdocs.amd.com

3. Bus:Device.Function (BDF) Notation: wiki.xen.org/wiki/Bus:Device.Function\_(BDF)\_← Notation

**4. HSA Platform System Architecture Specification:** www.hsafoundation.com/html\_spec111/HSA← \_Library.htm::SysArch/Topics/SysArch\_title\_page.htm

5. HSA Programmer's Reference Manual: www.hsafoundation.com/html\_spec111/HSA\_← Library.htm::PRM/Topics/PRM\_title\_page.htm

6. Semantic Versioning: semver.org

7. The LLVM Compiler Infrastructure: llvm.org

8. User Guide for AMDGPU LLVM Backend: 11vm.org/docs/AMDGPUUsage.html

# 1.6 Disclaimer

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AMD Debugger	API Si	pecification
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# **Chapter 2**

# **Module Documentation**

# 2.1 Symbol Versions

The names used for the shared library versioned symbols.

#### **Macros**

- #define AMD DBGAPI VERSION 0 24
  - The function was introduced in version 0.24 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.24".
- #define AMD DBGAPI VERSION 0 30
  - The function was introduced in version 0.30 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.30".
- #define AMD\_DBGAPI\_VERSION\_0\_41
  - The function was introduced in version 0.41 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.41".
- #define AMD\_DBGAPI\_VERSION\_0\_42
  - The function was introduced in version 0.42 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.42".
- #define AMD\_DBGAPI\_VERSION\_0\_46
  - The function was introduced in version 0.46 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.46".
- #define AMD DBGAPI VERSION 0 47
  - The function was introduced in version 0.47 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.47".

# 2.1.1 Detailed Description

The names used for the shared library versioned symbols.

Every function is annotated with one of the version macros defined in this section. Each macro specifies a corresponding symbol version string. After dynamically loading the shared library with <code>dlopen</code>, the address of each function can be obtained using <code>dlvsym</code> with the name of the function and its corresponding symbol version string. An error will be reported by <code>dlvsym</code> if the installed library does not support the version for the function specified in this version of the interface.

### 2.1.2 Macro Definition Documentation

### 2.1.2.1 AMD\_DBGAPI\_VERSION\_0\_24

#define AMD\_DBGAPI\_VERSION\_0\_24

The function was introduced in version 0.24 of the interface and has the symbol version string of "AMD\_DBGAPI\_← 0.24".

### 2.1.2.2 AMD\_DBGAPI\_VERSION\_0\_30

#define AMD\_DBGAPI\_VERSION\_0\_30

The function was introduced in version 0.30 of the interface and has the symbol version string of "AMD\_DBGAPI\_ $\leftarrow$  0.30".

# 2.1.2.3 AMD\_DBGAPI\_VERSION\_0\_41

#define AMD\_DBGAPI\_VERSION\_0\_41

The function was introduced in version 0.41 of the interface and has the symbol version string of "AMD\_DBGAPI\_

0.41".

### 2.1.2.4 AMD DBGAPI VERSION 0 42

#define AMD\_DBGAPI\_VERSION\_0\_42

The function was introduced in version 0.42 of the interface and has the symbol version string of "AMD\_DBGAPI\_ $\leftarrow$  0 . 42 ".

### 2.1.2.5 AMD DBGAPI VERSION 0 46

#define AMD\_DBGAPI\_VERSION\_0\_46

The function was introduced in version 0.46 of the interface and has the symbol version string of "AMD\_DBGAPI\_ $\leftarrow$  0.46".

### 2.1.2.6 AMD\_DBGAPI\_VERSION\_0\_47

#define AMD\_DBGAPI\_VERSION\_0\_47

The function was introduced in version 0.47 of the interface and has the symbol version string of "AMD\_DBGAPI\_ $\leftarrow$  0.47".

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# 2.2 Basic Types

Types used for common properties.

# **Typedefs**

```
    typedef uint64_t amd_dbgapi_global_address_t
```

Integral type used for a global virtual memory address in the inferior process.

• typedef uint64\_t amd\_dbgapi\_size\_t

Integral type used for sizes, including memory allocations, in the inferior.

typedef pid\_t amd\_dbgapi\_os\_process\_id\_t

Native operating system process ID.

typedef int amd\_dbgapi\_notifier\_t

Type used to notify the client of the library that a process may have pending events.

• typedef uint64\_t amd\_dbgapi\_os\_agent\_id\_t

Native operating system agent ID.

typedef uint64\_t amd\_dbgapi\_os\_queue\_id\_t

Native operating system queue ID.

typedef uint64 t amd dbgapi os queue packet id t

Native operating system queue packet ID.

#### **Enumerations**

```
    enum amd_dbgapi_changed_t { AMD_DBGAPI_CHANGED_NO = 0, AMD_DBGAPI_CHANGED_YES = 1 }
    Indication of if a value has changed.
```

```
    enum amd_dbgapi_os_queue_type_t {
        AMD_DBGAPI_OS_QUEUE_TYPE_UNKNOWN = 0, AMD_DBGAPI_OS_QUEUE_TYPE_HSA_KERNEL_DISPATCH_MULTIPLE
        = 1, AMD_DBGAPI_OS_QUEUE_TYPE_HSA_KERNEL_DISPATCH_SINGLE_PRODUCER = 2, AMD_DBGAPI_OS_QUEUE_TY
        = 3,
        AMD_DBGAPI_OS_QUEUE_TYPE_AMD_PM4 = 257, AMD_DBGAPI_OS_QUEUE_TYPE_AMD_SDMA = 513,
        AMD_DBGAPI_OS_QUEUE_TYPE_AMD_SDMA_XGMI = 514 }
```

Native operating system queue type.

# 2.2.1 Detailed Description

Types used for common properties.

Note that in some cases enumeration types are used as output parameters for functions using pointers. The C language does not define the underlying type used for enumeration types. This interface requires that:

- For the enumeration types amd\_dbgapi\_wave\_stop\_reason\_t, amd\_dbgapi\_instruction\_← properties\_t, and amd\_dbgapi\_queue\_error\_reason\_t, the underlying type used by the client will be unsigned long long with a size of 64 bits.
- For all other enumeration types the underlying type used by the client will be int with a size of 32 bits.

In addition, it requires that enumeration types passed by value to functions, or returned as values from functions, will have the platform function ABI representation.

# 2.2.2 Typedef Documentation

### 2.2.2.1 amd dbgapi global address t

```
typedef uint64_t amd_dbgapi_global_address_t
```

Integral type used for a global virtual memory address in the inferior process.

### 2.2.2.2 amd\_dbgapi\_notifier\_t

```
typedef int amd_dbgapi_notifier_t
```

Type used to notify the client of the library that a process may have pending events.

A notifier is created when amd\_dbgapi\_process\_attach is used to successfully attach to a process. It is obtained using the AMD\_DBGAPI\_PROCESS\_INFO\_NOTIFIER query. If the notifier indicates there may be pending events, then amd\_dbgapi\_process\_next\_pending\_event can be used to retrieve them.

For Linux® this is a file descriptor number that can be used with the poll call to wait on events from multiple sources. The file descriptor is made to have data available when events may be added to the pending events. The client can flush the file descriptor and read the pending events until none are available. Note that the file descriptor may become ready spuriously when no pending events are available, in which case the client should simply wait again. If new pending events are added while reading the pending events, then the file descriptor will again have data available. The amount of data on the file descriptor is not an indication of the number of pending events as the file may become full and so no further data will be added. The file descriptor is simply a robust way to determine if there may be some pending events.

### 2.2.2.3 amd\_dbgapi\_os\_agent\_id\_t

```
typedef uint64_t amd_dbgapi_os_agent_id_t
```

Native operating system agent ID.

This is the agent ID used by the operating system AMD GPU device driver that is executing the library to specify the AMD GPU agents accessible to a process.

#### 2.2.2.4 amd\_dbgapi\_os\_process\_id\_t

```
typedef pid_t amd_dbgapi_os_process_id_t
```

Native operating system process ID.

This is the process ID used by the operating system that is executing the library. It is used in the implementation of the library to interact with the operating system AMD GPU device driver.

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### 2.2.2.5 amd\_dbgapi\_os\_queue\_id\_t

```
typedef uint64_t amd_dbgapi_os_queue_id_t
```

Native operating system queue ID.

This is the queue ID used by the operating system AMD GPU device driver that is executing the library to specify the AMD GPU queues of a process.

### 2.2.2.6 amd\_dbgapi\_os\_queue\_packet\_id\_t

```
typedef uint64_t amd_dbgapi_os_queue_packet_id_t
```

Native operating system queue packet ID.

# 2.2.2.7 amd\_dbgapi\_size\_t

```
typedef uint64_t amd_dbgapi_size_t
```

Integral type used for sizes, including memory allocations, in the inferior.

# 2.2.3 Enumeration Type Documentation

## 2.2.3.1 amd\_dbgapi\_changed\_t

```
enum amd_dbgapi_changed_t
```

Indication of if a value has changed.

#### Enumerator

AMD_DBGAPI_CHANGED_NO	The value has not changed.
AMD_DBGAPI_CHANGED_YES	The value has changed.

# 2.2.3.2 amd\_dbgapi\_os\_queue\_type\_t

enum amd\_dbgapi\_os\_queue\_type\_t

Native operating system queue type.

This is used by the operating system AMD GPU device driver that is executing the library to specify the AMD GPU queue mechanics supported by the queues of a process.

### Enumerator

AMD DBGAPI OS QUEUE TYPE UNKNOWN	Llakaowa guoue type	
	Unknown queue type.	
AMD_DBGAPI_OS_QUEUE_TYPE_HSA_KERNEL↔ (	Queue supports the HSA kernel dispatch with multiple	
_DISPATCH_MULTIPLE_PRODUCER   ;	producers protocol. This follows the multiple producers	
	mechanics described by HSA Platform System	
	Architecture Specification:	
	Requirement: User mode queuing and	
	uses the HSA Architected Queuing Language (AQL)	
l l	packet format described in HSA Platform	
	System Architecture Specification:	
	Requirement: Architected Queuing	
	Language (AQL)	
	For this queue type the AQL dispatch ID is used for	
	amd_dbgapi_os_queue_packet_id_t. It is only unique	
	within a single queue of a single process.	
	Queue supports the HSA kernel dispatch with single	
	producer protocol. This follows the single producer	
	mechanics described by HSA Platform System	
	Architecture Specification:	
	Requirement: User mode queuing and	
	uses the HSA Architected Queuing Language (AQL) packet format described in HSA Platform	
	System Architecture Specification:	
	Requirement: Architected Queuing	
	Language (AQL).	
	For this queue type the AQL dispatch ID is used for	
	amd_dbgapi_os_queue_packet_id_t. It is only unique	
	within a single queue of a single process.	
AMD_DBGAPI_OS_QUEUE_TYPE_HSA_KERNEL↔ (	Queue supports HSA kernel dispatch with multiple producers protocol that supports cooperative	
	dispatches. Queues of this type follow the same	
	protocol as	
	AMD_DBGAPI_OS_QUEUE_TYPE_HSA_KERNEL_DISF	PATCH_MULTIPL
	In addition, dispatches are able to use global wave	
	synchronization (GWS) operations.	
AMD_DBGAPI_OS_QUEUE_TYPE_AMD_PM4	Queue supports the AMD PM4 protocol.	
AMD_DBGAPI_OS_QUEUE_TYPE_AMD_SDMA	Queue supports the AMD SDMA protocol.	
AMD_DBGAPI_OS_QUEUE_TYPE_AMD_SDMA_X↔ GMI	Queue supports the AMD SDMA XGMI protocol.	

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AMD DBGAPI STATUS SUCCESS = 0, AMD DBGAPI STATUS ERROR = -1, AMD DBGAPI STATUS FATAL

# 2.3 Status Codes

enum amd\_dbgapi\_status\_t {

Most operations return a status code to indicate success or error.

### **Enumerations**

```
= -2, AMD DBGAPI STATUS ERROR UNIMPLEMENTED = -3,
AMD DBGAPI STATUS ERROR NOT SUPPORTED = -4, AMD DBGAPI STATUS ERROR INVALID ARGUMENT
= -5, AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMENT_COMPATIBILITY = -6, AMD_DBGAPI_STATUS_ERROR_ALREA
AMD DBGAPI STATUS ERROR NOT INITIALIZED = -8, AMD DBGAPI STATUS ERROR RESTRICTION
= -9, AMD DBGAPI STATUS ERROR ALREADY ATTACHED = -10, AMD DBGAPI STATUS ERROR INVALID ARCHITECT
AMD DBGAPI STATUS ERROR ILLEGAL INSTRUCTION = -12, AMD DBGAPI STATUS ERROR INVALID CODE OBJECT
= -13, AMD_DBGAPI_STATUS_ERROR_INVALID_ELF_AMDGPU_MACHINE = -14, AMD_DBGAPI_STATUS_ERROR_INVALID
= -15,
AMD_DBGAPI_STATUS_ERROR_INVALID_AGENT_ID = -16, AMD_DBGAPI_STATUS_ERROR_INVALID_QUEUE_ID
=-17, AMD DBGAPI STATUS ERROR INVALID DISPATCH ID =-18, AMD DBGAPI STATUS ERROR INVALID WAVE ID
AMD DBGAPI STATUS ERROR WAVE NOT STOPPED = -20, AMD DBGAPI STATUS ERROR WAVE STOPPED
= -21, AMD_DBGAPI_STATUS_ERROR_WAVE_OUTSTANDING_STOP = -22, AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_
AMD DBGAPI STATUS ERROR INVALID DISPLACED STEPPING ID = -24, AMD DBGAPI STATUS ERROR DISPLACED
= -25, AMD DBGAPI STATUS ERROR INVALID WATCHPOINT ID = -26, AMD DBGAPI STATUS ERROR NO WATCHPOI
AMD DBGAPI STATUS ERROR INVALID REGISTER CLASS ID = -28, AMD DBGAPI STATUS ERROR INVALID REGIST
= -29, AMD DBGAPI STATUS ERROR INVALID LANE ID = -30, AMD DBGAPI STATUS ERROR INVALID ADDRESS CLA
= -31.
AMD DBGAPI STATUS ERROR INVALID ADDRESS SPACE ID = -32, AMD DBGAPI STATUS ERROR MEMORY ACCES
= -33, AMD DBGAPI STATUS ERROR INVALID ADDRESS SPACE CONVERSION = -34, AMD DBGAPI STATUS ERROR
AMD_DBGAPI_STATUS_ERROR_INVALID_SHARED_LIBRARY_ID = -36, AMD_DBGAPI_STATUS_ERROR_INVALID_BREAKI
= -37, AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK = -38, AMD_DBGAPI_STATUS_ERROR_INVALID_CLIENT_PRO(
AMD DBGAPI STATUS ERROR PROCESS EXITED = -40, AMD DBGAPI STATUS ERROR LIBRARY NOT LOADED
= -41, AMD DBGAPI STATUS ERROR SYMBOL NOT FOUND = -42, AMD DBGAPI STATUS ERROR INVALID ADDRESS
AMD_DBGAPI_STATUS_ERROR_DISPLACED_STEPPING_ACTIVE = -44, AMD_DBGAPI_STATUS_ERROR_RESUME_DISPL
  AMD debugger API status codes.
```

## **Functions**

 amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_get\_status\_string (amd\_dbgapi\_status\_t status, const char \*\*status string) AMD DBGAPI VERSION 0 24

Query a textual description of a status code.

# 2.3.1 Detailed Description

Most operations return a status code to indicate success or error.

# 2.3.2 Enumeration Type Documentation

# 2.3.2.1 amd\_dbgapi\_status\_t

enum amd\_dbgapi\_status\_t

AMD debugger API status codes.

### Enumerator

AMD_DBGAPI_STATUS_SUCCESS	The function has executed successfully.
AMD_DBGAPI_STATUS_ERROR	A generic error has occurred.
AMD_DBGAPI_STATUS_FATAL	A fatal error has occurred. The library encountered an error from which it cannot recover. All processes are detached. All breakpoints inserted by amd_dbgapi_callbacks_s::insert_breakpoint are attempted to be removed. All handles are invalidated. The library is left in an uninitialized state. The logging level is reset to AMD_DBGAPI_LOG_LEVEL_NONE. To resume using the library the client must re-initialize the library; re-attach to any processes; re-fetch the list of code objects, agents, queues, dispatches, and waves; and update the state of all waves as appropriate. While in the uninitialized state the inferior processes will continue executing but any execution of a breakpoint instruction will put the queue into an error state, aborting any executing waves. Note that recovering from a fatal error most likely will require the user of the client to re-start their session.  The cause of possible fatal errors is that resources became exhausted or unique handle numbers became exhausted.
AMD_DBGAPI_STATUS_ERROR_UNIMPLEMENTED	The operation is not currently implemented. This error may be reported by any function. Check the Known Limitations and Restrictions section to determine the status of the library implementation of the interface.
	The operation is not supported.
AMD_DBGAPI_STATUS_ERROR_NOT_SUPPORTED	11
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGU← MENT	An invalid argument was given to the function.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGU↔ MENT_COMPATIBILITY	An invalid combination of arguments was given to the function.

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### Enumerator

AMD_DBGAPI_STATUS_ERROR_ALREADY_INITI↔	The library is already initialized.
ALIZED  AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized.
AMD DBGAPI STATUS ERROR RESTRICTION	There is a restriction error that prevents debugging the
AMD_DBGAFI_STATUS_ENDON_NESTRICTION	process. Reasons include:
	<ul> <li>The installed AMD GPU driver version is not compatible with the library.</li> </ul>
	The installed AMD GPU driver's debug support version is not compatible with the library.
	The AMD GPU runtime version is not compatible with the library.
	<ul> <li>One of the AMD GPU agents has an architecture not supported by the library.</li> </ul>
	The firmware version of one of the AMD GPU agents is not compatible with the library.
	<ul> <li>A limitation on the number of debuggers that can be active for an AMD GPU agent has been exceeded.</li> </ul>
AMD_DBGAPI_STATUS_ERROR_ALREADY_ATT↔ ACHED	The process is already attached to the given inferior process.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHI↔ TECTURE_ID	The architecture handle is invalid.
AMD_DBGAPI_STATUS_ERROR_ILLEGAL_INSTR ← UCTION	The bytes being disassembled are not a legal instruction.
AMD_DBGAPI_STATUS_ERROR_INVALID_CODE ← _OBJECT_ID	The code object handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_ELF_A↔ MDGPU_MACHINE	The ELF AMD GPU machine value is invalid or unsupported.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROC← ESS_ID	The process handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_AGEN↔ T_ID	The agent handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_QUEU↔ E_ID	The queue handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_DISPA↔ TCH_ID	The dispatch handle is invalid.
AMB DB04DL074TU0 55555 :::::::::::::::::::::::::::::::	The wave handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	The control is not shown and
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_ST OPPED	The wave is not stopped.
AMD_DBGAPI_STATUS_ERROR_WAVE_STOPPED	The wave is stopped.
AMD_DBGAPI_STATUS_ERROR_WAVE_OUTSTA   NDING_STOP	The wave has an outstanding stop request.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_RE ↔ SUMABLE	The wave cannot be resumed.

### Enumerator

AMD_DBGAPI_STATUS_ERROR_INVALID_DISPL↔ ACED_STEPPING_ID	The displaced stepping handle is invalid.
AMD_DBGAPI_STATUS_ERROR_DISPLACED_ST↔ EPPING_BUFFER_UNAVAILABLE	No more displaced stepping buffers are available that are suitable for the requested wave.
AMD_DBGAPI_STATUS_ERROR_INVALID_WATC↔ HPOINT ID	The watchpoint handle is invalid.
AMD_DBGAPI_STATUS_ERROR_NO_WATCHPOI↔ NT_AVAILABLE	No more watchpoints available.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGIS  TER_CLASS_ID	The register class handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGIS↔ TER_ID	The register handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_LANE_ID	The lane handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDR↔ ESS_CLASS_ID	The address class handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDR↔ ESS_SPACE_ID	The address space handle is invalid.
AMD_DBGAPI_STATUS_ERROR_MEMORY_ACC↔ ESS	An error occurred while trying to access memory in the inferior.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDR ← ESS_SPACE_CONVERSION	The segment address cannot be converted to the requested address space.
AMD_DBGAPI_STATUS_ERROR_INVALID_EVEN↔ T_ID	The event handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_SHAR↔ ED_LIBRARY_ID	The shared library handle is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_BREA↔ KPOINT_ID	The breakpoint handle is invalid.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLB↔ ACK	A callback to the client reported an error.
AMD_DBGAPI_STATUS_ERROR_INVALID_CLIEN↔ T_PROCESS_ID	The client process handle is invalid.
AMD_DBGAPI_STATUS_ERROR_PROCESS_EXIT↔ ED	The native operating system process associated with a client process has exited.
AMD_DBGAPI_STATUS_ERROR_LIBRARY_NOT_↔ LOADED	The shared library is not currently loaded.
AMD_DBGAPI_STATUS_ERROR_SYMBOL_NOT_↔ FOUND	The symbol was not found.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDR↔ ESS	The address is not within the shared library.
AMD_DBGAPI_STATUS_ERROR_DISPLACED_ST↔ EPPING_ACTIVE	The wave has an active displaced stepping buffer.
AMD_DBGAPI_STATUS_ERROR_RESUME_DISPL↔ ACED_STEPPING	The wave cannot be resumed in the manner requested due to displaced stepping restrictions.

# 2.3.3 Function Documentation

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# 2.3.3.1 amd\_dbgapi\_get\_status\_string()

Query a textual description of a status code.

This function can be used even when the library is uninitialized.

#### **Parameters**

in	status	Status code.
out	status_string	A NUL terminated string that describes the status code. The string is read only and owned
		by the library.

#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully. status_string has been updated.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tstatus is an invalid status code or status_string is NULL. status_string is unaltered.

# 2.4 Versioning

Version information about the interface and the associated installed library.

### **Macros**

• #define AMD\_DBGAPI\_VERSION\_MAJOR 0

The semantic version of the interface following [semver.org][semver] rules.

#define AMD\_DBGAPI\_VERSION\_MINOR 48

The minor version of the interface as a macro so it can be used by the preprocessor.

### **Functions**

- void AMD\_DBGAPI amd\_dbgapi\_get\_version (uint32\_t \*major, uint32\_t \*minor, uint32\_t \*patch) AMD\_DBGAPI\_VERSION\_0\_24

  Query the version of the installed library.
- const char AMD\_DBGAPI \* amd\_dbgapi\_get\_build\_name (void) AMD\_DBGAPI\_VERSION\_0\_24 Query the installed library build name.

## 2.4.1 Detailed Description

Version information about the interface and the associated installed library.

# 2.4.2 Macro Definition Documentation

### 2.4.2.1 AMD\_DBGAPI\_VERSION\_MAJOR

```
#define AMD_DBGAPI_VERSION_MAJOR 0
```

The semantic version of the interface following [semver.org][semver] rules.

A client that uses this interface is only compatible with the installed library if the major version numbers match and the interface minor version number is less than or equal to the installed library minor version number. The major version of the interface as a macro so it can be used by the preprocessor.

### 2.4.2.2 AMD DBGAPI VERSION MINOR

```
#define AMD_DBGAPI_VERSION_MINOR 48
```

The minor version of the interface as a macro so it can be used by the preprocessor.

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# 2.4.3 Function Documentation

### 2.4.3.1 amd\_dbgapi\_get\_build\_name()

Query the installed library build name.

This function can be used even when the library is not initialized.

### Returns

Returns a string describing the build version of the library. The string is owned by the library.

# 2.4.3.2 amd\_dbgapi\_get\_version()

Query the version of the installed library.

Return the version of the installed library. This can be used to check if it is compatible with this interface version. This function can be used even when the library is not initialized.

#### **Parameters**

out major The major version number is stor		The major version number is stored if non-NULL.	
C	out <i>minor</i>		The minor version number is stored if non-NULL.
C	out	patch	The patch version number is stored if non-NULL.

### 2.5 Initialization and Finalization

Operations to control initializing and finalizing the library.

### **Functions**

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_initialize (amd\_dbgapi\_callbacks\_t \*callbacks) AMD\_DBGAPI\_VERSION\_0\_30
   Initialize the library.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_finalize (void) AMD\_DBGAPI\_VERSION\_0\_24
   Finalize the library.

# 2.5.1 Detailed Description

Operations to control initializing and finalizing the library.

When the library is first loaded it is in the uninitialized state. Before any operation can be used, the library must be initialized. The exception is the status operation in Status Codes and the version operations in Versioning which can be used regardless of whether the library is initialized.

#### 2.5.2 Function Documentation

## 2.5.2.1 amd\_dbgapi\_finalize()

Finalize the library.

Finalizing the library invalidates all handles previously returned by any operation. It is undefined to use any such handle even if the library is subsequently initialized with amd\_dbgapi\_initialize. Finalizing the library implicitly detaches from any processes currently attached. It is allowed to initialize and finalize the library multiple times. Finalizing the library does not changed the logging level (see Logging).

### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the library is now uninitialized.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if any of the amd_dbgapi_callbacks_s callbacks used return an error. The library is still left uninitialized, but the client may be in an inconsistent state.

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# 2.5.2.2 amd\_dbgapi\_initialize()

Initialize the library.

Initialize the library so that the library functions can be used to control the AMD GPU devices accessed by processes.

Initializing the library does not change the logging level (see Logging).

### **Parameters**

i	callbacks	A set of callbacks must be provided. These are invoked by certain operations. They are	
		described in amd_dbgapi_callbacks_t.	

#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the library is now initialized.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library remains uninitialized.
AMD_DBGAPI_STATUS_ERROR_ALREADY_INITIALIZ	In the library is already initialized. The library is left initialized and the callbacks are not changed.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tcallbacks is NULL or has fields that are NULL. The library remains uninitialized.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if any of the amd_dbgapi_callbacks_s callbacks used return an error. The library remains uninitialized.

### 2.6 Architectures

Operations related to AMD GPU architectures.

### **Data Structures**

struct amd\_dbgapi\_architecture\_id\_t

Opaque architecture handle.

### **Macros**

#define AMD\_DBGAPI\_ARCHITECTURE\_NONE ((amd\_dbgapi\_architecture\_id\_t){ 0 })

The NULL architecture handle.

# **Typedefs**

typedef struct amd\_dbgapi\_symbolizer\_id\_s \* amd\_dbgapi\_symbolizer\_id\_t
 Opaque client symbolizer handle.

# **Enumerations**

```
    enum amd_dbgapi_architecture_info_t {
        AMD_DBGAPI_ARCHITECTURE_INFO_NAME = 1, AMD_DBGAPI_ARCHITECTURE_INFO_ELF_AMDGPU_MACHINE
        = 2, AMD_DBGAPI_ARCHITECTURE_INFO_LARGEST_INSTRUCTION_SIZE = 3, AMD_DBGAPI_ARCHITECTURE_INFO_MIN
        = 4,
        AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPOINT_INSTRUCTION_SIZE = 5, AMD_DBGAPI_ARCHITECTURE_INFO_BRE
        = 6, AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPOINT_INSTRUCTION_PC_ADJUST = 7, AMD_DBGAPI_ARCHITECTURE
        = 8 }
        Architecture queries that are supported by amd_dbgapi_architecture_get_info.
    enum amd_dbgapi_instruction_kind_t {
        AMD_DBGAPI_INSTRUCTION_KIND_LINKNOWN = 0, AMD_DBGAPI_INSTRUCTION_KIND_SEQUENTIAL = 1.0.1.
```

= 5, AMD\_DBGAPI\_INSTRUCTION\_KIND\_DIRECT\_CALL\_REGISTER\_PAIR = 6, AMD\_DBGAPI\_INSTRUCTION\_KIND\_INDIRE = 7,

AMD\_DBGAPI\_INSTRUCTION\_KIND\_TERMINATE = 8, AMD\_DBGAPI\_INSTRUCTION\_KIND\_TRAP = 9,

AMD\_DBGAPI\_INSTRUCTION\_KIND\_HALT = 10, AMD\_DBGAPI\_INSTRUCTION\_KIND\_BARRIER = 11

AMD\_DBGAPI\_INSTRUCTION\_KIND\_HALT = 10, AMD\_DBGAPI\_INSTRUCTION\_KIND\_BARRIER = 11, AMD\_DBGAPI\_INSTRUCTION\_KIND\_SPECIAL = 13 }

The kinds of instruction classifications.

 enum amd\_dbgapi\_instruction\_properties\_t { AMD\_DBGAPI\_INSTRUCTION\_PROPERTY\_NONE = 0ULL, AMD\_DBGAPI\_INSTRUCTION\_PROPERTY\_RESERVED = (1ULL << 63) }</li>

A bit mask of the properties of an instruction.

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### **Functions**

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_get\_info (amd\_dbgapi\_architecture\_id\_t architecture\_id, amd\_dbgapi\_architecture\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_30
 Query information about an architecture.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_get\_architecture (uint32\_t elf\_amdgpu\_machine, amd\_dbgapi\_architecture\_id\_t \*architecture\_id) AMD\_DBGAPI\_VERSION\_0\_24

Get an architecture from the AMD GPU ELF EF\_AMDGPU\_MACH value corresponding to the architecture.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_disassemble\_instruction (amd\_dbgapi\_architecture\_id\_t architecture\_id, amd\_dbgapi\_global\_address\_t address, amd\_dbgapi\_size\_t \*size, const void \*memory, char \*\*instruction\_text, amd\_dbgapi\_symbolizer\_id\_t symbolizer\_id, amd\_dbgapi\_status\_t(\*symbolizer)(amd\_dbgapi\_symbolizer\_id\_t symbolizer\_id, amd\_dbgapi\_global\_address\_t address, char \*\*symbol\_text)) AMD\_DBGAPI\_VERSION\_0\_30
   Disassemble a single instruction.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_classify\_instruction (amd\_dbgapi\_architecture\_id\_t architecture 
   \_id, amd\_dbgapi\_global\_address\_t address, amd\_dbgapi\_size\_t \*size, const void \*memory, amd\_dbgapi\_instruction\_kind\_t
   \*instruction\_kind, amd\_dbgapi\_instruction\_properties\_t \*instruction\_properties, void \*\*instruction\_information)
   AMD\_DBGAPI\_VERSION\_0\_46

Classify a single instruction.

### 2.6.1 Detailed Description

Operations related to AMD GPU architectures.

The library supports a family of AMD GPU devices. Each device has its own architectural properties. The operations in this section provide information about the supported architectures.

### 2.6.2 Macro Definition Documentation

### 2.6.2.1 AMD DBGAPI ARCHITECTURE NONE

```
#define AMD_DBGAPI_ARCHITECTURE_NONE ((amd_dbgapi_architecture_id_t){ 0 })
```

The NULL architecture handle.

### 2.6.3 Typedef Documentation

### 2.6.3.1 amd\_dbgapi\_symbolizer\_id\_t

```
typedef struct amd_dbgapi_symbolizer_id_s* amd_dbgapi_symbolizer_id_t
```

Opaque client symbolizer handle.

A pointer to client data associated with a symbolizer. This pointer is passed to the amd\_dbgapi\_disassemble\_instruction symbolizer callback.

# 2.6.4 Enumeration Type Documentation

# 2.6.4.1 amd\_dbgapi\_architecture\_info\_t

enum amd\_dbgapi\_architecture\_info\_t

Architecture queries that are supported by amd\_dbgapi\_architecture\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_architecture\_get\_info.

### Enumerator

		_
AMD_DBGAPI_ARCHITECTURE_INFO_NAME	Return the architecture name. The type of this attribute is a pointer to a NUL terminated char*. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.	
AMD_DBGAPI_ARCHITECTURE_INFO_ELF_AMD↔ GPU_MACHINE	Return the AMD GPU ELF EF_AMDGPU_MACH value corresponding to the architecture. This is defined as a bit field in the e_flags AMD GPU ELF header. See User Guide for AMDGPU Backend - Code Object - Header. The type of this attribute is uint32_t.	
AMD_DBGAPI_ARCHITECTURE_INFO_LARGEST ← _INSTRUCTION_SIZE	Return the largest instruction size in bytes for the architecture. The type of this attribute is amd_dbgapi_size_t.	
AMD_DBGAPI_ARCHITECTURE_INFO_MINIMUM_← INSTRUCTION_ALIGNMENT	Return the minimum instruction alignment in bytes for the architecture. The returned value will be a power of two. The type of this attribute is amd_dbgapi_size_t.	
AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPO ← INT_INSTRUCTION_SIZE	Return the breakpoint instruction size in bytes for the architecture. The type of this attribute is amd_dbgapi_size_t.	
AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPO↔ INT_INSTRUCTION	Return the breakpoint instruction for the architecture. The type of this attribute is pointer to N bytes where N is the value returned by the AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPOINT_query. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.	INSTRUCTION_S
AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPO← INT_INSTRUCTION_PC_ADJUST	Return the number of bytes to subtract from the PC after stopping due to a breakpoint instruction to get the address of the breakpoint instruction for the architecture. The type of this attribute is amd_dbgapi_size_t.	
AMD_DBGAPI_ARCHITECTURE_INFO_PC_REGIS↔ TER	Return the register handle for the PC for the architecture. The type of this attribute is amd_dbgapi_register_id_t.	

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# 2.6.4.2 amd\_dbgapi\_instruction\_kind\_t

enum amd\_dbgapi\_instruction\_kind\_t

The kinds of instruction classifications.

### Enumerator

AMD_DBGAPI_INSTRUCTION_KIND_UNKNOWN	The instruction classification is unknown. The instruction has no information.
AMD_DBGAPI_INSTRUCTION_KIND_SEQUENTIAL	The instruction executes sequentially. It performs no control flow and the next instruction executed is the following one. The instruction has no information.
AMD_DBGAPI_INSTRUCTION_KIND_DIRECT_BR→ ANCH	The instruction unconditionally branches to a literal address. The instruction information is of type amd_dbgapi_global_address_t with the value of the target address of the branch.
AMD_DBGAPI_INSTRUCTION_KIND_DIRECT_BR ↔ ANCH_CONDITIONAL	The instruction conditionally branches to a literal address. If the condition is not satisfied then the next instruction is the following one. The instruction information is of type amd_dbgapi_global_address_t with the value of the target address of the branch if taken.
AMD_DBGAPI_INSTRUCTION_KIND_INDIRECT_B↔ RANCH_REGISTER_PAIR	The instruction unconditionally branches to an address held in a pair of registers. The instruction information is of type amd_dbgapi_register_id_t[2] with the value of the register IDs for the registers. The first register holds the least significant address bits, and the second register holds the most significant address bits.
AMD_DBGAPI_INSTRUCTION_KIND_INDIRECT_B↔ RANCH_CONDITIONAL_REGISTER_PAIR	The instruction conditionally branches to an address held in a pair of registers. If the condition is not satisfied then the next instruction is the following one. The instruction information is of type amd_dbgapi_register_id_t[2] with the value of the register IDs for the registers holding the value of the target address of the branch if taken. The register with index 0 holds the least significant address bits, and the register with index 1 holds the most significant address bits.
AMD_DBGAPI_INSTRUCTION_KIND_DIRECT_CA↔ LL_REGISTER_PAIR	The instruction unconditionally branches to a literal address and the address of the following instruction is saved in a pair of registers. The instruction information is of type amd_dbgapi_direct_call_register_pair_information_t with the value of the target address of the call followed by the value of the saved return address register IDs. The saved return address register with index 0 holds the least significant address bits, and the register with index 1 holds the most significant address bits.

## Enumerator

reason, then the program counter will be at the instruction following the trap instruction, it will not be at the first instruction of the trap handler. It is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  The instruction unconditionally halts the wave. The instruction has no information.  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and	AMD_DBGAPI_INSTRUCTION_KIND_INDIRECT_C↔	The instruction unconditionally branches to an address
indices 2 and 3. The registers with indices 0 and 2 hold the least significant address bits, and the registers with indices 1 and 3 hold the most significant address bits.  AMD_DBGAPI_INSTRUCTION_KIND_TERMINATE  AMD_DBGAPI_INSTRUCTION_KIND_TRAP  The instruction terminates the wave execution. The instruction has no information.  The instruction enters the trap handler. The trap handler may return to resume execution, may halt the wave and create an event for amd_dbgapi_process_next_pending_event to report, or may terminate the wave. The library cannot report execution in the trap handler. If single stepping the trap instruction reports the  AMD_DBGAPI_WAVE_STOP_REASON_SINGLE_STEP reason, then the program counter will be at the instruction following the trap instruction, it will not be at the first instruction of the trap handler. It is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint 64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instruction include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL  AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction has some form of special behavior not covered by any of the other instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction sinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may	ALL_REGISTER_FAIRS	following instruction is saved in a pair of destination registers. The instruction information is of type amd_dbgapi_register_id_t[4] with the source register
the least significant address bits, and the registers with indices 1 and 3 hold the most significant address bits.  AMD_DBGAPI_INSTRUCTION_KIND_TERMINATE  The instruction terminates the wave execution. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_TRAP  The instruction enters the trap handler. The trap handler may return to resume execution, may halt the wave and create an event for amd_dbgapi_process_next_pending_event to report, or may terminate the wave. The library cannot report execution in the trap handler. It is single stepping the trap instruction reports the  AMD_DBGAPI_WAVE_STOP_REASON_SINGLE_STEP reason, then the program counter will be at the instruction following the trap instruction, it will not be at the first instruction of the trap handler, it is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  The instruction unconditionally halts the wave. The instruction has no information.  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		
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instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_TRAP  The instruction enters the trap handler. The trap handler may return to resume execution, may halt the wave and create an event for amd_dbgapi_process_next_pending_event to report, or may terminate the wave. The library cannot report execution in the trap handler. If single stepping the trap instruction reports the  AMD_DBGAPI_WAVE_STOP_REASON_SINGLE_STEP reason, then the program counter will be at the instruction following the trap instruction, it will not be at the first instruction of the trap handler. It is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, and		-
may return to resume execution, may halt the wave and create an event for amd_dbgapi_process_next_pending_event to report, or may terminate the wave. The library cannot report execution in the trap handler. If single stepping the trap instruction reports the AMD_DBGAPI_WAVE_STOP_REASON_SINGLE_STEP reason, then the program counter will be at the instruction following the trap instruction, it will not be at the first instruction of the trap handler. It is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  The instruction unconditionally halts the wave. The instruction has no information.  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  The instruction. The instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		instruction has no information.
may terminate the wave. The library cannot report execution in the trap handler. If single stepping the trap instruction reports the  AMD_DBGAPI_WAVE_STOP_REASON_SINGLE_STEP reason, then the program counter will be at the instruction following the trap instruction, it will not be at the first instruction of the trap handler. It is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT The instruction unconditionally halts the wave. The instruction has no information.  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and	AMD_DBGAPI_INSTRUCTION_KIND_TRAP	may return to resume execution, may halt the wave and create an event for
instruction reports the  AMD_DBGAPI_WAVE_STOP_REASON_SINGLE_STEP reason, then the program counter will be at the instruction following the trap instruction, it will not be at the first instruction of the trap handler. It is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT The instruction unconditionally halts the wave. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		may terminate the wave. The library cannot report
reason, then the program counter will be at the instruction following the trap instruction, it will not be at the first instruction of the trap handler. It is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  The instruction unconditionally halts the wave. The instruction has no information.  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		instruction reports the
the first instruction of the trap handler. It is undefined to set a breakpoint in the trap handler, and will likely cause the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  The instruction unconditionally halts the wave. The instruction has no information.  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		reason, then the program counter will be at the
the inferior to report errors and stop executing correctly. The instruction information is of type uint64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  The instruction unconditionally halts the wave. The instruction has no information.  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		
The instruction information is of type uint 64_t with the value of the trap code.  AMD_DBGAPI_INSTRUCTION_KIND_HALT  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  The instruction unconditionally halts the wave. The instruction has no information.  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		
instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_BARRIER  The instruction performs some kind of execution barrier which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP  The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		The instruction information is of type uint64_t with
which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SLEEP The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and	AMD_DBGAPI_INSTRUCTION_KIND_HALT	-
AMD_DBGAPI_INSTRUCTION_KIND_SLEEP The instruction causes the wave to stop executing for some period of time, before continuing execution with the next instruction. The instruction has no information.  AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and	AMD_DBGAPI_INSTRUCTION_KIND_BARRIER	which may result in the wave being halted until other waves allow it to continue. Such instructions include wave execution barriers, wave synchronization barriers, and wave semaphores. The instruction has no
AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL  The instruction has some form of special behavior not covered by any of the other instruction kinds. This likely makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and	AMD_DBGAPI_INSTRUCTION_KIND_SLEEP	some period of time, before continuing execution with
makes it unsuitable to assume it will execute sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and	AMD_DBGAPI_INSTRUCTION_KIND_SPECIAL	The instruction has some form of special behavior not
sequentially. This may include instructions that can affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		
affect the execution of other waves waiting at wave synchronization barriers, that may send interrupts, and		
		affect the execution of other waves waiting at wave
30 IOLUL TIO HIGH GOOD TO HIGH GUID.		synchronization barriers, that may send interrupts, and so forth. The instruction has no information.

# 2.6.4.3 amd\_dbgapi\_instruction\_properties\_t

 $\verb"enum" amd\_dbgapi_instruction\_properties\_t$ 

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A bit mask of the properties of an instruction.

#### Enumerator

AMD_DBGAPI_INSTRUCTION_PROPERTY_NONE	The instruction has no properties.
AMD_DBGAPI_INSTRUCTION_PROPERTY_RESE ←	A reserved value only present to ensure that the
RVED	underlying representation of this enumeration type is
	uint64_t.

## 2.6.5 Function Documentation

# 2.6.5.1 amd\_dbgapi\_architecture\_get\_info()

Query information about an architecture.

amd\_dbgapi\_architecture\_info\_t specifies the queries supported and the type returned using the value argument.

### **Parameters**

in	architecture←	The architecture being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.
out	value	Pointer to memory where the query result is stored.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	Tlane_nDtecture_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vGOMeA \$ IBUI⊲ Doves not match the size of the query
	result. value is unaltered.

### Return values

AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

# 2.6.5.2 amd\_dbgapi\_classify\_instruction()

Classify a single instruction.

### **Parameters**

in	architecture_id	The architecture to use to perform the classification.
in	address	The address of the first byte of the instruction.
in,out	size	Pass in the number of bytes available in memory which must be greater than 0. Return the number of bytes consumed to decode the instruction.
in	memory	The bytes to decode as an instruction. Must point to an array of at least size bytes. The AMD_DBGAPI_ARCHITECTURE_INFO_LARGEST_INSTRUCTION_SIZE query for architecture_id can be used to determine the number of bytes of the largest instruction. By making size at least this size ensures that the instruction can be decoded if legal. However, size may need to be smaller if no memory exists at the address of address plus size.
out	instruction_kind	The classification kind of the instruction.
out	instruction_properties	Pointer to the instruction properties. amd_dbgapi_instruction_properties_t defines the type of the instruction properties. If NULL, no value is returned.
out	instruction_information	Pointer to the instruction information that corresponds to the value of instruction_kind. amd_dbgapi_instruction_kind_t defines the type of the instruction information for each instruction kind value. If the instruction has no information then NULL is returned. The memory is allocated using the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client. If NULL, no value is returned.

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### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully; and the
	result is stored in instruction_kind,
	instruction_properties, and
	instruction_information.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	and size, instruction_kind,
	instruction_properties, and
	instruction_information are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	<pre>uninitialized; and size, instruction_kind,</pre>
	instruction_properties, and
	instruction_information are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	TUREND tecture_id is invalid. size,
	instruction_kind,
	instruction_properties, and
	instruction_information are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	size, memory, <b>or</b> instruction_kind <b>are NULL</b> ;
	or size is 0. size, instruction_kind,
	instruction_properties, and
	instruction_information are unaltered.
AMD_DBGAPI_STATUS_ERROR	Encountered an error disassembling the instruction.
	The bytes may or may not be a legal instruction. size,
	instruction_kind,
	instruction_properties, and
	instruction_information are unaltered.
AMD_DBGAPI_STATUS_ERROR_ILLEGAL_INSTRUCT	
	bytes are available, are not a legal instruction for the
	architecture. size, instruction_kind,
	instruction_properties, and
	instruction_information are unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate instruction_text and
	address_operands returns NULL. size,
	instruction_kind,
	instruction_properties, and
	instruction_information are unaltered.

# 2.6.5.3 amd\_dbgapi\_disassemble\_instruction()

```
amd_dbgapi_symbolizer_id_t symbolizer_id,
amd_dbgapi_status_t(*)(amd_dbgapi_symbolizer_id_t symbolizer_id, amd_dbgapi_global_address_t
address, char **symbol_text) symbolizer)
```

Disassemble a single instruction.

#### **Parameters**

in	architecture← _id	The architecture to use to perform the disassembly.
in	address	The address of the first byte of the instruction.
in,out	size	Pass in the number of bytes available in memory which must be greater than 0. Return the number of bytes consumed to decode the instruction.
in	memory	The bytes to decode as an instruction. Must point to an array of at least <code>size</code> bytes. The AMD_DBGAPI_ARCHITECTURE_INFO_LARGEST_INSTRUCTION_SIZE query for <code>architecture_id</code> can be used to determine the number of bytes of the largest instruction. By making <code>size</code> at least this size ensures that the instruction can be decoded if legal. However, <code>size</code> may need to be smaller if no memory exists at the address of <code>address</code> plus <code>size</code> .
out	instruction_text	If NULL then only the instruction size is returned.

If non-NULL then set to a pointer to a NUL terminated string that contains the disassembled textual representation of the instruction. The memory is allocated using the <a href="mailto:amd\_dbgapi\_callbacks\_s::allocate\_memory">amd\_dbgapi\_callbacks\_s::allocate\_memory</a> callback and is owned by the client.

#### **Parameters**

in	symbolizer↔ _id	The client handle that is passed to any invocation of the symbolizer callback made while disassembling the instruction.	
in	symbolizer		

If symbolizer is NULL, then no symbolization will be performed and any memory addresses will be shown as their numeric address.

If symbolizer is non-NULL, the symbolizer function will be called with symbolizer\_id having the value of the above symbolizer\_id operand, and with address having the value of the address of the disassembled instruction's operand.

If the symbolizer callback wishes to report a symbol text it must allocate and assign memory for a non-empty NUL terminated char\* string using a memory allocator that can be deallocated using the amd\_dbgapi\_callbacks\_s::deallocate\_memory callback. If must assign the pointer to symbol\_text, and return AMD DBGAPI STATUS SUCCESS.

If the symbolizer callback does not wish to report a symbolit must return AMD DBGAPI STATUS ERROR SYMBOL NOT FOUND.

Any symbol\_text strings returned by the symbolizer callbacks reporting AMD\_DBGAPI\_STATUS\_SUCCESS are deallocated using the amd\_dbgapi\_callbacks\_s::deallocate\_memory callback before amd\_dbgapi\_disassemble\_instruction returns.

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#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in size and instruction_text.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	size and instruction_text are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and size and instruction_text are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	TURENDtecture_id is invalid. size and
	instruction_text are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tsize or memory are NULL; or *size is 0. size and
	instruction_text are unaltered.
AMD_DBGAPI_STATUS_ERROR	Encountered an error disassembling the instruction, a
	symbolizer callback returned
	AMD_DBGAPI_STATUS_SUCCESS with a NULL or
	empty symbol_text string. The bytes may or may
	not be a legal instruction. size and
	instruction_text are unaltered.
AMD_DBGAPI_STATUS_ERROR_ILLEGAL_INSTRUCT	,
	bytes are available, are not a legal instruction for the
	architecture. size and instruction_text are
AMD DOORD STATUS EDDOD SUIDAT SALLDASK	unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate instruction_text returns NULL, or a symbolizer callback returns a status other than
	AMD DBGAPI STATUS SUCCESS and
	AMD_DBGAPI_STATUS_SUCCESS and AMD_DBGAPI_STATUS_ERROR_SYMBOL_NOT_FOUN
	size and instruction_text are unaltered.
	3120 and Instruction_cent are unallered.

## 2.6.5.4 amd\_dbgapi\_get\_architecture()

Get an architecture from the AMD GPU ELF  ${\tt EF\_AMDGPU\_MACH}$  value corresponding to the architecture.

This is defined as a bit field in the  $e_{\tt flags}$  AMD GPU ELF header. See [User Guide for AMDGPU Backend - Code Object

• Header] ( https://llvm.org/docs/AMDGPUUsage.html#header).

### **Parameters**

in	elf_amdgpu_machine	The AMD GPU ELF EF_AMDGPU_MACH value.	
out	architecture_id	The corresponding architecture.	1

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in architecture_id.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and architecture_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and architecture_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ELF_AMDG	Pel_tMAGMilypu_machine is invalid or unsupported. architecture_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tarchitecture_id is NULL. architecture_id is unaltered.

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## 2.7 Processes

Operations related to establishing AMD GPU debug control of a process.

### **Data Structures**

struct amd\_dbgapi\_process\_id\_t
 Opaque process handle.

#### **Macros**

#define AMD\_DBGAPI\_PROCESS\_NONE ((amd\_dbgapi\_process\_id\_t){ 0 })
 The NULL process handle.

## **Typedefs**

• typedef struct amd\_dbgapi\_client\_process\_s \* amd\_dbgapi\_client\_process\_id\_t Opaque client process handle.

#### **Enumerations**

```
    enum amd_dbgapi_process_info_t {
        AMD_DBGAPI_PROCESS_INFO_NOTIFIER = 1, AMD_DBGAPI_PROCESS_INFO_WATCHPOINT_COUNT =
        2, AMD_DBGAPI_PROCESS_INFO_WATCHPOINT_SHARE = 3, AMD_DBGAPI_PROCESS_INFO_PRECISE_MEMORY_SUPP = 4,
        AMD_DBGAPI_PROCESS_INFO_OS_ID = 5 }
```

Process queries that are supported by amd\_dbgapi\_process\_get\_info.

enum amd\_dbgapi\_progress\_t { AMD\_DBGAPI\_PROGRESS\_NORMAL = 0, AMD\_DBGAPI\_PROGRESS\_NO\_FORWARD = 1 }

The kinds of progress supported by the library.

enum amd\_dbgapi\_wave\_creation\_t { AMD\_DBGAPI\_WAVE\_CREATION\_NORMAL = 0, AMD\_DBGAPI\_WAVE\_CREATION\_STC
 = 1 }

The kinds of wave creation supported by the hardware.

## **Functions**

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_get\_info (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_process\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about a process.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_attach (amd\_dbgapi\_client\_process\_id\_t client\_
 process\_id, amd\_dbgapi\_process\_id\_t \*process\_id) AMD\_DBGAPI\_VERSION\_0\_30

Attach to a process in order to provide debug control of the AMD GPUs it uses.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_detach (amd\_dbgapi\_process\_id\_t process\_id)
 AMD\_DBGAPI\_VERSION\_0\_24

Detach from a process and no longer have debug control of the AMD GPU devices it uses.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_set\_progress (amd\_dbgapi\_process\_id\_t process\_id\_t process

Set the progress required for a process.

• amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_set\_wave\_creation (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_wave\_creation\_t creation) AMD\_DBGAPI\_VERSION\_0\_24

Set the wave creation mode for a process.

# 2.7.1 Detailed Description

Operations related to establishing AMD GPU debug control of a process.

The library supports AMD GPU debug control of multiple operating system processes. Each process can have access to multiple AMD GPU devices, but each process uses the AMD GPU devices independently of other processes.

#### 2.7.2 Macro Definition Documentation

## 2.7.2.1 AMD\_DBGAPI\_PROCESS\_NONE

```
#define AMD_DBGAPI_PROCESS_NONE ((amd_dbgapi_process_id_t) { 0 })
```

The NULL process handle.

# 2.7.3 Typedef Documentation

## 2.7.3.1 amd\_dbgapi\_client\_process\_id\_t

```
typedef struct amd_dbgapi_client_process_s* amd_dbgapi_client_process_id_t
```

Opaque client process handle.

A pointer to client data associated with a process. This pointer is passed to the process specific callbacks (see Callbacks) to allow the client of the library to identify the process. Each process must have a single unique value.

## 2.7.4 Enumeration Type Documentation

### 2.7.4.1 amd\_dbgapi\_process\_info\_t

```
enum amd_dbgapi_process_info_t
```

Process queries that are supported by amd dbgapi process get info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_process\_get\_info.

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#### Enumerator

AMD_DBGAPI_PROCESS_INFO_NOTIFIER	The notifier for the process that indicates if pending events are available. The type of this attributes is amd_dbgapi_notifier_t.
AMD_DBGAPI_PROCESS_INFO_WATCHPOINT_C→ OUNT	Return the number of data watchpoints supported by the process. Zero is returned if data watchpoints are not supported. The type of this attribute is size_t.
AMD_DBGAPI_PROCESS_INFO_WATCHPOINT_S↔ HARE	Return how watchpoints are shared between processes.  The type of this attribute is uint32_t with the values defined by amd_dbgapi_watchpoint_share_kind_t.
AMD_DBGAPI_PROCESS_INFO_PRECISE_MEM ORY_SUPPORTED	Return if the architectures of all the agents of a process support controlling memory precision. The type of this attribute is uint32_t with the values defined by amd_dbgapi_memory_precision_t.
AMD_DBGAPI_PROCESS_INFO_OS_ID	Native operating system process ID. The type of this attribute is amd_dbgapi_os_process_id_t. AMD_DBG← API_STATUS_ERROR_PROCESS_EXITED is returned if the native operating system process was exited when attaching.

## 2.7.4.2 amd\_dbgapi\_progress\_t

enum amd\_dbgapi\_progress\_t

The kinds of progress supported by the library.

In performing operations, the library may make both waves it needs to access, as well as other waves, unavailable for hardware execution. After completing the operation, it will make all waves available for hardware execution. This is termed pausing and unpausing wave execution respectively. Pausing and unpausing waves for each command separately works but can result in longer latency than if several commands could be performed while the waves are paused. Debugging the very large number of waves that can exist on an AMD GPU can involve many operations, making batching commands even more beneficial. The progress setting allows controlling this behavior.

### Enumerator

AMD_DBGAPI_PROGRESS_NORMAL	Normal progress is needed. Commands are issued immediately.
	After completing each command all non-stopped waves will be
	unpaused. Switching from another progress mode to this will
	unpause any waves that are paused.

#### Enumerator

AMB BROADI BROODESS NO ESPINABR	
AMD_DBGAPI_PROGRESS_NO_FORWARD	No forward progress is needed. Commands are issued
	immediately. After completing each command, non-stopped
	waves may be left paused. The waves left paused may include
	both the wave(s) the command operates on, as well as other
	waves. While in AMD_DBGAPI_PROGRESS_NO_FORWARD
	mode, paused waves may remain paused, or may be unpaused
	at any point. Only by leaving
	AMD_DBGAPI_PROGRESS_NO_FORWARD mode will the
	library not leave any waves paused after completing a command.
	Note that the events that amd_dbgapi_wave_stop causes to be
	reported will occur when in
	AMD DBGAPI PROGRESS NO FORWARD mode. It is not
	necessary to change the progress mode to
	AMD DBGAPI PROGRESS NORMAL for those events to be
	reported.
	This can result in a series of commands completing far faster
	than in AMD_DBGAPI_PROGRESS_NORMAL mode. Also, any
	queries for lists such as amd_dbgapi_process_wave_list may
	return unchanged as true more often, reducing the work
	needed to parse the lists to determine what has changed. With
	large lists this can be significant. If the client needs a wave to
	complete a single step resume, then it must leave
	AMD_DBGAPI_PROGRESS_NO_FORWARD mode in order to
	prevent that wave from remaining paused.

### 2.7.4.3 amd\_dbgapi\_wave\_creation\_t

enum amd\_dbgapi\_wave\_creation\_t

The kinds of wave creation supported by the hardware.

The hardware creates new waves asynchronously as it executes dispatch packets. If the client requires that all waves are stopped, it needs to first request that the hardware stops creating new waves, followed by halting all already created waves. The wave creation setting allows controlling how the hardware creates new waves for dispatch packets on queues associated with agents belonging to a specific process. It has no affect on waves that have already been created.

## Enumerator

AMD_DBGAPI_WAVE_CREATION_NORMAL	Normal wave creation allows new waves to be created.
AMD_DBGAPI_WAVE_CREATION_STOP	Stop wave creation prevents new waves from being created.

# 2.7.5 Function Documentation

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### 2.7.5.1 amd\_dbgapi\_process\_attach()

Attach to a process in order to provide debug control of the AMD GPUs it uses.

Attaching can be performed on processes that have not started executing, as well as those that are already executing.

The process progress is initialized to AMD\_DBGAPI\_PROGRESS\_NORMAL. All agents accessed by the process are configured to AMD\_DBGAPI\_MEMORY\_PRECISION\_NONE.

The client process handle must have been associated with a native operating system process, and the amd\_dbgapi\_callbacks\_s::get\_os\_pid callback is used to obtain it.

It is the client's responsibility to fetch the current code object list using amd\_dbgapi\_process\_code\_object\_list as the AMD\_DBGAPI\_EVENT\_KIND\_CODE\_OBJECT\_LIST\_UPDATED event is only reported when a thread is in the process of changing the code object list.

The AMD\_DBGAPI\_EVENT\_KIND\_RUNTIME event will be reported if the runtime support is already enabled in process.

If the associated native operating system process exits while the library is attached to it, appropriate actions are taken to reflect that the inferior process no longer has any state. For example, pending events are created for wave command termination if there are pending wave stop or wave single step requests; a pending code object list updated event is created if there were codes objects previously loaded; a pending runtime event is created to indicate the runtime support has been unloaded if previously loaded; and queries on agents, queues, dispatches, waves, and code objects will report none exist. The process handle remains valid until amd\_dbgapi\_process\_detach is used to detach from the client process.

If the associated native operating system process has already exited when attaching, then the attach is still successful, but any queries on agents, queues, dispatches, waves, and code objects will report none exist.

If the associated native operating system process exits while a library operation is being executed, then the operation behaves as if the process exited before it was invoked. For example, a wave operation will report an invalid wave handle, a list query will report an empty list, and so forth.

It is undefined to use any library operation except amd\_dbgapi\_process\_detach on a process that has its virtual address space replaced. After detach, the same process can be attached again to continue accessing the process if desired. For example, in Linux an <code>exec</code> system call replaces the virtual address space which causes all information about agents, queues, dispatches, and waves to become invalid, and the ability to read and write memory may also no longer be allowed by the operating system.

If after attaching to a process it spawns another process, the library continues to be attached to the parent process. If desired, the client can always use <a href="mailto:amd\_dbgapi\_process\_attach">amd\_dbgapi\_process\_attach</a> to attach to the child process and <a href="mailto:amd\_dbgapi\_process\_detach">amd\_dbgapi\_process\_detach</a> to detach from the parent process.

#### **Parameters**

in	client_process⇔	The client handle for the process. It is passed as an argument to any callbacks	
	_id	performed to indicate the process being requested.	
out	process_id	ocess_id The process handle to use for all operations related to this process.	

#### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the process is now attached returning process_id.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and process_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and process_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_ALREADY_ATTACHE	DThe process is already attached. The process remains attached and process_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_RESTRICTION	There is a restriction error that prevents debugging process client_process_id. See AMD_DBGAPI_STATUS_ERROR_RESTRICTION for possible reasons. The process is not attached and process_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tclient_process_id or process_id are NULL.  The process is not attached and process_id is unaltered.
AMD_DBGAPI_STATUS_ERROR	Encountered some other error while attaching to the process. The process is not attached and process_id is unaltered.

## 2.7.5.2 amd\_dbgapi\_process\_detach()

Detach from a process and no longer have debug control of the AMD GPU devices it uses.

If the associated native operating system process has already exited, or exits while being detached, then the process is trivially detached.

Otherwise, detaching causes execution of the associated native operating system process to continue unaffected by the library. Any waves with a displaced stepping buffer are stopped and the displaced stepping buffer completed. Any data watchpoints are removed. All agents are configured to AMD\_DBGAPI\_MEMORY\_PRECISION\_NONE. Any waves in the stopped or single step state are resumed in non-single step mode. Any pending events for the process are discarded, and no further events will be generated for the process.

After detaching, the process handle, and all handles associated with entities relating to the process, become invalid.

A native operating system process can be attached and detached multiple times. Each attach returns a unique process handle even for the same native operating system process.

The client is responsible for removing any inserted breakpoints before detaching. Failing to do so will cause execution of a breakpoint instruction to put the queue into an error state, aborting any executing waves for dispatches on that queue.

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## **Parameters**

process⇔	The process handle that is being detached.
_id	

#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the process has been detached from the associated native operating system process, or the associated native operating system process has already exited.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/The process_id is invalid. No process is detached.

# 2.7.5.3 amd\_dbgapi\_process\_get\_info()

Query information about a process.

amd\_dbgapi\_process\_info\_t specifies the queries supported and the type returned using the value argument.

### **Parameters**

in	process⇔	The process being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.
out	value	Pointer to memory where the query result is stored.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/process_id is invalid. value is unaltered.

### Return values

AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_x@OM@A\$Bbble does not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_PROCESS_EXITED	query is AMD_DBGAPI_PROCESS_INFO_OS_ID and the native operating system process was exited when attaching. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the amd_dbgapi_callbacks_s::allocate_memory callback used to allocate value returns NULL. value is unaltered.

# 2.7.5.4 amd\_dbgapi\_process\_set\_progress()

Set the progress required for a process.

#### **Parameters**

in	process⇔	If AMD_DBGAPI_PROCESS_NONE then set the progress for all processes currently	
	_id	attached. Otherwise, set the progress for the process process_id.	
in	progress	The progress being set.	

## Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the progress has been set.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	Iprocess_id is invalid. The progress setting is not changed.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tprogress is invalid. The progress setting is not changed.

# 2.7.5.5 amd\_dbgapi\_process\_set\_wave\_creation()

```
\verb|amd_dbgapi_status_t| \verb|AMD_DBGAPI| amd_dbgapi_process_set_wave\_creation| (
```

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```
amd_dbgapi_process_id_t process_id,
amd_dbgapi_wave_creation_t creation )
```

Set the wave creation mode for a process.

The setting applies to all agents of the specified process.

## **Parameters**

in	process↔ _id	The process being controlled.
in	creation	The wave creation mode being set.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the wave creation mode has been set.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	Iprocess_id is invalid. The wave creation mode setting is not changed.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tcreation is invalid. The wave creation setting is not changed.

# 2.8 Code Objects

Operations related to AMD GPU code objects loaded into a process.

### **Data Structures**

struct amd\_dbgapi\_code\_object\_id\_t

The NULL code object handle.

Opaque code object handle.

#### **Macros**

#define AMD\_DBGAPI\_CODE\_OBJECT\_NONE ((amd\_dbgapi\_code\_object\_id\_t){ 0 })

## **Enumerations**

 enum amd\_dbgapi\_code\_object\_info\_t { AMD\_DBGAPI\_CODE\_OBJECT\_INFO\_PROCESS = 1, AMD\_DBGAPI\_CODE\_OBJECT\_ = 2, AMD\_DBGAPI\_CODE\_OBJECT\_INFO\_LOAD\_ADDRESS = 3 }

Code object queries that are supported by amd\_dbgapi\_code\_object\_get\_info.

#### **Functions**

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_code\_object\_list (amd\_dbgapi\_process\_id\_t process\_id, size\_t \*code\_object\_count, amd\_dbgapi\_code\_object\_id\_t \*\*code\_objects, amd\_dbgapi\_changed\_t \*changed) AMD\_DBGAPI\_VERSION\_0\_41

Return the list of loaded code objects.

## 2.8.1 Detailed Description

Operations related to AMD GPU code objects loaded into a process.

AMD GPU code objects are standard ELF shared libraries defined in User Guide for AMDGPU Backend - Code Object.

AMD GPU code objects can be embedded in the host executable code object that is loaded into memory or be in a separate file in the file system. The AMD GPU loader supports loading either from memory or from files. The loader selects the segments to put into memory that contain the code and data necessary for AMD GPU code execution. It allocates global memory to map these segments and performs necessary relocations to create the loaded code object.

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# 2.8.2 Macro Definition Documentation

# 2.8.2.1 AMD\_DBGAPI\_CODE\_OBJECT\_NONE

```
#define AMD_DBGAPI_CODE_OBJECT_NONE ((amd_dbgapi_code_object_id_t){ 0 })
```

The NULL code object handle.

# 2.8.3 Enumeration Type Documentation

# 2.8.3.1 amd\_dbgapi\_code\_object\_info\_t

```
enum amd_dbgapi_code_object_info_t
```

Code object queries that are supported by amd\_dbgapi\_code\_object\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_code\_object\_get\_info.

### **Enumerator**

AMD_DBGAPI_CODE_OBJECT_INFO_PROCESS	Return the process to which this code object belongs.
	The type of this attribute is amd_dbgapi_process_id_t.

## Enumerator

AMD DROADLOODE ODIECT INEC LIDENIAME	TI UDI CU ELE L. LLI: (C. LI:LU	
AMD_DBGAPI_CODE_OBJECT_INFO_URI_NAME	The URI name of the ELF shared object from which the code object was loaded. Note that the code object is the	
	in memory loaded relocated form of the ELF shared	
	object. Multiple code objects may be loaded at different	
	memory addresses in the same process from the same	
	ELF shared object.	
	The type of this attribute is a NUL terminated char*. It	
	is allocated by the	
	amd_dbgapi_callbacks_s::allocate_memory callback	
	and is owned by the client.	
	The URI name syntax is defined by the following BNF syntax:	
	code_object_uri ::== file_uri   memory_uri	
	file_uri ::== "file://" file_path [ range_:	
	<pre>memory_uri ::== "memory://" process_id range range_specifier ::== [ "##"   "?" ] "offset=" numl</pre>	
	file_path ::== URI_ENCODED_OS_FILE_PATH	υστ α 512e-
	process_id ::== DECIMAL_NUMBER	
	number ::== HEX_NUMBER   DECIMAL_NUMBER	OCTAL_NUMBER
	number is a C integral literal where hexadecimal	
	values are prefixed by "0x" or "0X", and octal values by	
	"0".	
	file_path is the file's path specified as a URI	
	encoded UTF-8 string. In URI encoding, every character	
	that is not in the regular expression	
	$[a-zA-Z0-9/\\sim-]$ is encoded as two uppercase	
	hexidecimal digits proceeded by "%". Directories in the	
	path are separated by "/".	
	offset is a 0-based byte offset to the start of the code	
	object. For a file URI, it is from the start of the file	
	specified by the file_path, and if omitted defaults to	
	0. For a memory URI, it is the memory address and is	
	required.	
	size is the number of bytes in the code object. For a	
	file URI, if omitted it defaults to the size of the file. It is	
	required for a memory URI.	
	process_id is the identity of the process owning the	
	memory. For Linux it is the C unsigned integral decimal	
	literal for the process ID (PID).	
	For example:	
	file:///dir1/dir2/file1	
	file:///dir3/dir4/file2##offset=0x2000&size=3000	
	memory://1234##offset=0x20000&size=3000	
AMD DBGAPI CODE OBJECT INFO LOAD ADD↔	The difference between the address in the ELF shared	
RESS	object and the address the code object is loaded in	
TIE55	memory. The type of this attributes is ptrdiff_t.	
	memory. The type of this attributes is perutified.	

# 2.8.4 Function Documentation

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## 2.8.4.1 amd\_dbgapi\_code\_object\_get\_info()

Query information about a code object.

amd\_dbgapi\_code\_object\_info\_t specifies the queries supported and the type returned using the value argument.

#### **Parameters**

in	code_object⊷	The handle of the code object being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query
		result.
out	value	Pointer to memory where the query result is stored.

#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_CODE_OBJ	<b>ECJ</b> d <b>₽</b> _object_id <b>is invalid</b> . value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vCOMEA \$1841 does not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

# 2.8.4.2 amd\_dbgapi\_process\_code\_object\_list()

```
amd_dbgapi_status_t AMD_DBGAPI amd_dbgapi_process_code_object_list (
    amd_dbgapi_process_id_t process_id,
    size_t * code_object_count,
    amd_dbgapi_code_object_id_t ** code_objects,
    amd_dbgapi_changed_t * changed )
```

Return the list of loaded code objects.

The order of the code object handles in the list is unspecified and can vary between calls.

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# **Parameters**

in	process_id	If AMD_DBGAPI_PROCESS_NONE then the code object list for all processes is requested. Otherwise, the code object list for process process_id is requested.
out	code_object_count	The number of code objects currently loaded.
out	code_objects	If changed is not NULL and the code object list of all of the processes requested have not changed since the last call(s) to amd_dbgapi_process_code_object_list for each of them, then return NULL. Otherwise, return a pointer to an array of amd_dbgapi_code_object_id_t with code_object_count elements. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.
in,out	changed	If NULL then left unaltered. If non-NULL, set to AMD_DBGAPI_CHANGED_NO if the list of code objects for each requested process is the same as when amd_dbgapi_process_code_object_list was last called for them. Otherwise, set to AMD_DBGAPI_CHANGED_YES.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in changed, code_object_count, and code_objects.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	and code_object_count, code_objects, and
	changed <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	<pre>uninitialized; and code_object_count,</pre>
	code_objects, <b>and</b> changed <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/process_id is invalid. code_object_count,
	code_objects, and changed are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	code_object_count <b>or</b> code_objects <b>are</b>
	NULL, or changed is invalid.
	code_object_count,code_objects,and
	changed <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate code_objects returns NULL.
	code_object_count,code_objects,and
	changed <b>are unaltered</b> .

# 2.9 Agents

Operations related to AMD GPU agents accessible to a process.

### **Data Structures**

struct amd\_dbgapi\_agent\_id\_t
 Opaque agent handle.

#### **Macros**

#define AMD\_DBGAPI\_AGENT\_NONE ((amd\_dbgapi\_agent\_id\_t){ 0 })
 The NULL agent handle.

### **Enumerations**

enum amd\_dbgapi\_agent\_info\_t {
 AMD\_DBGAPI\_AGENT\_INFO\_PROCESS = 1, AMD\_DBGAPI\_AGENT\_INFO\_NAME = 2, AMD\_DBGAPI\_AGENT\_INFO\_ARCHIT
 = 3, AMD\_DBGAPI\_AGENT\_INFO\_PCI\_SLOT = 4,
 AMD\_DBGAPI\_AGENT\_INFO\_PCI\_VENDOR\_ID = 5, AMD\_DBGAPI\_AGENT\_INFO\_PCI\_DEVICE\_ID = 6,
 AMD\_DBGAPI\_AGENT\_INFO\_EXECUTION\_UNIT\_COUNT = 7, AMD\_DBGAPI\_AGENT\_INFO\_MAX\_WAVES\_PER\_EXECUTION = 8,
 AMD\_DBGAPI\_AGENT\_INFO\_OS\_ID = 9 }

Agent queries that are supported by amd\_dbgapi\_agent\_get\_info.

### **Functions**

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_agent\_get\_info (amd\_dbgapi\_agent\_id\_t agent\_id, amd\_dbgapi\_agent\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41
  - Query information about an agent.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_agent\_list (amd\_dbgapi\_process\_id\_t process\_id, size\_t \*agent\_count, amd\_dbgapi\_agent\_id\_t \*\*agents, amd\_dbgapi\_changed\_t \*changed) AMD\_DBGAPI\_VERSION\_0\_41
   Return the list of agents.

## 2.9.1 Detailed Description

Operations related to AMD GPU agents accessible to a process.

Agent is the term for AMD GPU devices that can be accessed by the process.

## 2.9.2 Macro Definition Documentation

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# 2.9.2.1 AMD\_DBGAPI\_AGENT\_NONE

```
#define AMD_DBGAPI_AGENT_NONE ((amd_dbgapi_agent_id_t) { 0 })
```

The NULL agent handle.

# 2.9.3 Enumeration Type Documentation

# 2.9.3.1 amd\_dbgapi\_agent\_info\_t

```
enum amd_dbgapi_agent_info_t
```

Agent queries that are supported by amd\_dbgapi\_agent\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_agent\_get\_info.

#### Enumerator

AMD_DBGAPI_AGENT_INFO_PROCESS	Return the process to which this agent belongs. The type of this attribute is amd_dbgapi_process_id_t.
AMD_DBGAPI_AGENT_INFO_NAME	Agent name. The type of this attribute is a pointer to a NUL terminated char*. It is allocated by amd_dbgapi_callbacks_s::allocate_memory and is owned by the client.
AMD_DBGAPI_AGENT_INFO_ARCHITECTURE	Return the architecture of this agent. The type of this attribute is amd_dbgapi_architecture_id_t.
AMD_DBGAPI_AGENT_INFO_PCI_SLOT	PCI slot of the agent in BDF format (see [Bus:Device.Function (BDF) Notation][bfd]. The type of this attribute is uint16_t.
AMD_DBGAPI_AGENT_INFO_PCI_VENDOR_ID	PCI vendor ID of the agent. The type of this attribute is uint32_t.
AMD_DBGAPI_AGENT_INFO_PCI_DEVICE_ID	PCI device ID of the agent. The type of this attribute is uint32_t.
AMD_DBGAPI_AGENT_INFO_EXECUTION_UNIT_← COUNT	Total number of Execution Units (EUs) available in the agent. The type of this attribute is size_t.
AMD_DBGAPI_AGENT_INFO_MAX_WAVES_PER↔ _EXECUTION_UNIT	Maximum number of waves supported by an execution unit. The type of this attribute is size_t.
AMD_DBGAPI_AGENT_INFO_OS_ID	Native operating system agent ID. The type of this attribute is amd_dbgapi_os_agent_id_t.

# 2.9.4 Function Documentation

## 2.9.4.1 amd\_dbgapi\_agent\_get\_info()

Query information about an agent.

amd\_dbgapi\_agent\_info\_t specifies the queries supported and the type returned using the value argument.

#### **Parameters**

in	agent_id	The handle of the agent being queried.	
in	query	The query being requested.	
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.	
out	value	Pointer to memory where the query result is stored.	

#### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_AGENT_ID	agent_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_cCOMPA TIBLE Does not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

# 2.9.4.2 amd\_dbgapi\_process\_agent\_list()

Return the list of agents.

The order of the agent handles in the list is unspecified and can vary between calls.

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# **Parameters**

in	process_id	If AMD_DBGAPI_PROCESS_NONE then the agent list for all processes is requested.  Otherwise, the agent list of process process_id is requested.
out	agent_count	The number of agents accessed by the process.
out	agents	If changed is not NULL and the agent list of all of the processes requested have not changed since the last call(s) to amd_dbgapi_process_agent_list for each of them, then return NULL. Otherwise, return a pointer to an array of amd_dbgapi_agent_id_t with agent_count elements. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.
in,out	changed	If NULL then left unaltered. If non-NULL, set to AMD_DBGAPI_CHANGED_NO if the list of agents for each requested process is the same as when amd_dbgapi_process_agent_list was last called for them. Otherwise, set to AMD_DBGAPI_CHANGED_YES.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in changed, agent_count, and agents.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized; and agent_count, agents, and changed are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized; and agent_count, agents, and changed are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	Iprocess_id is invalid. agent_count, agents, and changed are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tagent_count or agents are NULL, or changed is invalid. agent_count, agents, and changed are unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the amd_dbgapi_callbacks_s::allocate_memory callback used to allocate agents returns NULL. agent_count, agents, and changed are unaltered.

### 2.10 Queues

Operations related to AMD GPU queues.

#### **Data Structures**

struct amd\_dbgapi\_queue\_id\_t
 Opaque queue handle.

### **Macros**

#define AMD\_DBGAPI\_QUEUE\_NONE ((amd\_dbgapi\_queue\_id\_t){ 0 })
 The NULL queue handle.

#### **Enumerations**

```
enum amd_dbgapi_queue_info_t {
 AMD_DBGAPI_QUEUE_INFO_AGENT = 1, AMD_DBGAPI_QUEUE_INFO_PROCESS = 2, AMD_DBGAPI_QUEUE_INFO_ARCH
 = 3, AMD_DBGAPI_QUEUE_INFO_TYPE = 4,
 AMD DBGAPI QUEUE INFO STATE = 5, AMD DBGAPI QUEUE INFO ERROR REASON = 6, AMD DBGAPI QUEUE INFO
 = 7, AMD DBGAPI QUEUE INFO SIZE = 8,
 AMD_DBGAPI_QUEUE_INFO_OS_ID = 9 }
    Queue queries that are supported by amd_dbgapi_queue_get_info.

    enum amd dbgapi queue state t{AMD DBGAPI QUEUE STATE VALID = 1, AMD DBGAPI QUEUE STATE ERROR

 = 2 }
    Queue state.

    enum amd dbgapi queue error reason t {

 = (1ULL << 0), AMD DBGAPI QUEUE ERROR REASON MEMORY VIOLATION = (1ULL << 1),
 AMD_DBGAPI_QUEUE_ERROR_REASON_ASSERT_TRAP = (1ULL << 2),
 AMD DBGAPI QUEUE ERROR REASON WAVE ERROR = (1ULL << 3), AMD DBGAPI QUEUE ERROR REASON RESE
 = (1ULL << 63)
```

#### **Functions**

```
    amd_dbgapi_status_t AMD_DBGAPI amd_dbgapi_queue_get_info (amd_dbgapi_queue_id_t queue_id,
amd_dbgapi_queue_info_t query, size_t value_size, void *value) AMD_DBGAPI_VERSION_0_41
```

Query information about a queue.

A bit mask of the reasons that a queue is in error.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_queue\_list (amd\_dbgapi\_process\_id\_t process\_id, size\_t \*queue\_count, amd\_dbgapi\_queue\_id\_t \*\*queues, amd\_dbgapi\_changed\_t \*changed) AMD\_DBGAPI\_VERSION\_0\_41
   Return the list of queues.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_queue\_packet\_list (amd\_dbgapi\_queue\_id\_t queue\_id, amd\_dbgapi\_os\_queue\_packet\_id\_t \*read\_packet\_id, amd\_dbgapi\_os\_queue\_packet\_id\_t \*write\_packet\_id, size\_t \*packets\_byte\_size, void \*\*packets\_bytes) AMD\_DBGAPI\_VERSION\_0\_41

Return the packets for a queue.

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# 2.10.1 Detailed Description

Operations related to AMD GPU queues.

Queues are user mode data structures that allow packets to be inserted that control the AMD GPU agents. The dispatch packet is used to initiate the execution of a grid of waves.

## 2.10.2 Macro Definition Documentation

## 2.10.2.1 AMD\_DBGAPI\_QUEUE\_NONE

```
#define AMD_DBGAPI_QUEUE_NONE ((amd_dbgapi_queue_id_t) { 0 })
```

The NULL queue handle.

# 2.10.3 Enumeration Type Documentation

## 2.10.3.1 amd\_dbgapi\_queue\_error\_reason\_t

```
enum amd_dbgapi_queue_error_reason_t
```

A bit mask of the reasons that a queue is in error.

### **Enumerator**

AMD_DBGAPI_QUEUE_ERROR_REASON_NONE	If none of the bits are set, then the queue is not in the error state.
AMD_DBGAPI_QUEUE_ERROR_REASON_INVALI↔ D_PACKET	A packet on the queue is invalid.
AMD_DBGAPI_QUEUE_ERROR_REASON_MEMO↔ RY_VIOLATION	A wave on the queue had a memory violation.
AMD_DBGAPI_QUEUE_ERROR_REASON_ASSE↔ RT_TRAP	A wave on the queue had an assert trap.
AMD_DBGAPI_QUEUE_ERROR_REASON_WAVE↔ERROR	A wave on the queue executed an instruction that caused an error. The AMD_DBGAPI_WAVE_INFO_STOP_REASON query can be used on the waves of the queue to determine the exact reason.
AMD_DBGAPI_QUEUE_ERROR_REASON_RESE↔ RVED	A reserved value only present to ensure that the underlying representation of this enumeration type is uint64_t.

# 2.10.3.2 amd\_dbgapi\_queue\_info\_t

enum amd\_dbgapi\_queue\_info\_t

Queue queries that are supported by amd\_dbgapi\_queue\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_queue\_get\_info.

#### Enumerator

AMD_DBGAPI_QUEUE_INFO_AGENT	Return the agent to which this queue belongs. The type of this attribute is amd_dbgapi_agent_id_t.
AMD_DBGAPI_QUEUE_INFO_PROCESS	Return the process to which this queue belongs. The type of this attribute is amd_dbgapi_process_id_t.
AMD_DBGAPI_QUEUE_INFO_ARCHITECTURE	Return the architecture of this queue. The type of this attribute is amd_dbgapi_architecture_id_t.
AMD_DBGAPI_QUEUE_INFO_TYPE	Return the queue type. The type of this attribute is uint32_t with values from amd_dbgapi_os_queue_type_t.
AMD_DBGAPI_QUEUE_INFO_STATE	Return the queue state. The type of this attribute is uint32_t with values from amd_dbgapi_queue_state_t.
AMD_DBGAPI_QUEUE_INFO_ERROR_REASON	Return the reason the queue is in error as a bit set. If the queue is not in the error state then AMD_DBGAPI_QUEUE_ERROR_REASON_NONE is returned. The type of this attribute is uint64_t with values defined by amd_dbgapi_queue_error_reason_t.
AMD_DBGAPI_QUEUE_INFO_ADDRESS	Return the base address of the memory holding the queue packets. The type of this attribute is amd_dbgapi_global_address_t.
AMD_DBGAPI_QUEUE_INFO_SIZE	Return the size in bytes of the memory holding the queue packets. The type of this attribute is amd_dbgapi_size_t.
AMD_DBGAPI_QUEUE_INFO_OS_ID	Native operating system queue ID. The type of this attribute is amd_dbgapi_os_queue_id_t.

# 2.10.3.3 amd\_dbgapi\_queue\_state\_t

enum amd\_dbgapi\_queue\_state\_t

Queue state.

Enumerator

AMD_DBGAPI_QUEUE_STATE_VALID	Queue is in a valid state.

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### Enumerator

AMD_DBGAPI_QUEUE_STATE_ERROR	Queue is in an error state. When a queue enters the error state, a
	wave stop event will be created for all non-stopped waves. All waves
	of the queue will include the
	AMD_DBGAPI_WAVE_STOP_REASON_QUEUE_ERROR stop
	reason.

# 2.10.4 Function Documentation

# 2.10.4.1 amd\_dbgapi\_process\_queue\_list()

Return the list of queues.

The order of the queue handles in the list is unspecified and can vary between calls.

### **Parameters**

in	process_id	If AMD_DBGAPI_PROCESS_NONE then the queue list for all processes is requested. Otherwise, the queue list of process process_id is requested.
out	queue_count	The number of queues accessed by the process.
out	queues	If changed is not NULL and the queues list of all of the processes requested have not changed since the last call(s) to amd_dbgapi_process_queue_list for each of them, then return NULL. Otherwise, return a pointer to an array of amd_dbgapi_queue_id_t with queue_count elements. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.
in,out	changed	If NULL then left unaltered. If non-NULL, set to AMD_DBGAPI_CHANGED_NO if the list of queues for each requested process is the same as when amd_dbgapi_process_queue_list was last called for them. Otherwise set to AMD_DBGAPI_CHANGED_YES.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in changed, queue_count, and queues.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized; and queue_count, queues, and changed are unaltered.

### **Return values**

AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and queue_count, queues, and
	changed <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/process_id is invalid. queue_count, queues,
	and changed are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	queue_count <b>or</b> queues <b>are NULL, or</b> changed <b>is</b>
	invalid. queue_count, queues, and changed are
	unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate queues returns NULL.
	queue_count, queues, <b>and</b> changed <b>are</b>
	unaltered.

# 2.10.4.2 amd\_dbgapi\_queue\_get\_info()

Query information about a queue.

amd\_dbgapi\_queue\_info\_t specifies the queries supported and the type returned using the value argument.

# **Parameters**

in	queue_id	The handle of the queue being queried.
in	query	The query being requested.
out	value	Pointer to memory where the query result is stored.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_QUEUE_ID	queue_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	value is NULL or query is invalid. value is
	unaltered.

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#### **Return values**

AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_v@OM@A\$IBUId does not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

## 2.10.4.3 amd\_dbgapi\_queue\_packet\_list()

Return the packets for a queue.

Since the AMD GPU is asynchronously reading the packets this is only a snapshot of the packets present in the queue, and only includes the packets that the producer has made available to the queue. In obtaining the snapshot the library may pause the queue processing in order to get a consistent snapshot.

The queue packets are returned as a byte block that the client must interpret according to the packet ABI determined by the queue type available using the AMD\_DBGAPI\_QUEUE\_INFO\_TYPE query. See amd\_dbgapi\_os\_queue\_type\_t.

# **Parameters**

in	queue_id	The queue for which the packet list is requested.
out	read_packet_id	The packet ID for the next packet to be read from the queue. It corresponds to the first packet in packets_bytes. If packets_byte_size is zero, then the packet ID for the next packet added to the queue.
out	write_packet_id	The packet ID for the next packet to be written to the queue. It corresponds to the next packet after the last packet in packets_bytes. If packets_byte_size is zero, then the packet ID for the next packet added to the queue.
out	packets_byte_size	The number of bytes of packets on the queue.
out	packets_bytes	If non-NULL, it references a pointer to an array of packets_byte_size bytes which is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client. If NULL, the packet bytes are not returned, just packets_byte_size.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in read_packet_id, write_packet_id, packets_byte_size and packets_bytes.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized; and read_packet_id, write_packet_id, packets_byte_size and packets_bytes are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized; and read_packet_id, write_packet_id, packets_byte_size and packets_bytes are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	<pre>/ read_packet_id, write_packet_id, or packets_byte_size are NULL. read_packet_id, write_packet_id, packets_byte_size and packets_bytes are unaltered.</pre>
AMD_DBGAPI_STATUS_ERROR_NOT_SUPPORTED	queue_id has a queue type that is not supported. read_packet_id, write_packet_id, packets_byte_size and packets_bytes are unaltered.
AMD_DBGAPI_STATUS_ERROR	An error was encountered when attempting to access the queue queue_id. For example, the queue may be corrupted. read_packet_id, write_packet_id, packets_byte_size and packets_bytes are unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the amd_dbgapi_callbacks_s::allocate_memory callback used to allocate packets_bytes returns NULL. read_packet_id, write_packet_id, packets_byte_size and packets_bytes are unaltered.

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# 2.11 Dispatches

Operations related to AMD GPU dispatches.

#### **Data Structures**

struct amd\_dbgapi\_dispatch\_id\_t
 Opaque dispatch handle.

#### **Macros**

#define AMD\_DBGAPI\_DISPATCH\_NONE ((amd\_dbgapi\_dispatch\_id\_t){ 0 })
 The NULL dispatch handle.

### **Enumerations**

```
enum amd_dbgapi_dispatch_info_t {
 AMD_DBGAPI_DISPATCH_INFO_QUEUE = 1, AMD_DBGAPI_DISPATCH_INFO_AGENT = 2, AMD_DBGAPI_DISPATCH_INFO
 = 3, AMD_DBGAPI_DISPATCH_INFO_ARCHITECTURE = 4,
 AMD DBGAPI DISPATCH INFO OS QUEUE PACKET ID = 5, AMD DBGAPI DISPATCH INFO BARRIER
 = 6, AMD_DBGAPI_DISPATCH_INFO_ACQUIRE_FENCE = 7, AMD_DBGAPI_DISPATCH_INFO_RELEASE_FENCE
 = 8.
 AMD_DBGAPI_DISPATCH_INFO_GRID_DIMENSIONS = 9, AMD_DBGAPI_DISPATCH_INFO_WORK_GROUP_SIZES
 = 10, AMD_DBGAPI_DISPATCH_INFO_GRID_SIZES = 11, AMD_DBGAPI_DISPATCH_INFO_PRIVATE_SEGMENT_SIZE
 = 12,
 AMD DBGAPI DISPATCH INFO GROUP SEGMENT SIZE = 13, AMD DBGAPI DISPATCH INFO KERNEL ARGUMENT S
 = 14, AMD DBGAPI DISPATCH INFO KERNEL DESCRIPTOR ADDRESS = 15, AMD DBGAPI DISPATCH INFO KERNEL
 AMD DBGAPI DISPATCH INFO KERNEL COMPLETION ADDRESS = 17 }
    Dispatch queries that are supported by amd_dbgapi_dispatch_get_info.

    enum amd_dbgapi_dispatch_barrier_t { AMD_DBGAPI_DISPATCH_BARRIER_NONE = 0, AMD_DBGAPI_DISPATCH_BARRIER_

 = 1
    Dispatch barrier.
• enum amd_dbgapi_dispatch_fence_scope_t { AMD_DBGAPI_DISPATCH_FENCE_SCOPE_NONE = 0,
 AMD_DBGAPI_DISPATCH_FENCE_SCOPE_AGENT = 1, AMD_DBGAPI_DISPATCH_FENCE_SCOPE_SYSTEM
 = 2 }
    Dispatch memory fence scope.
```

### **Functions**

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_dispatch\_get\_info (amd\_dbgapi\_dispatch\_id\_t dispatch\_id, amd\_dbgapi\_dispatch\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41
   Query information about a dispatch.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_dispatch\_list (amd\_dbgapi\_process\_id\_t process
   \_id, size\_t \*dispatch\_count, amd\_dbgapi\_dispatch\_id\_t \*\*dispatches, amd\_dbgapi\_changed\_t \*changed)
   AMD\_DBGAPI\_VERSION\_0\_41

Return the list of dispatches.

# 2.11.1 Detailed Description

Operations related to AMD GPU dispatches.

Dispatches are initiated by queue dispatch packets in the format supported by the queue. See amd\_dbgapi\_os\_queue\_type\_t. Dispatches are the means that waves are created on the AMD GPU.

### 2.11.2 Macro Definition Documentation

## 2.11.2.1 AMD\_DBGAPI\_DISPATCH\_NONE

```
#define AMD_DBGAPI_DISPATCH_NONE ((amd_dbgapi_dispatch_id_t) { 0 })
```

The NULL dispatch handle.

# 2.11.3 Enumeration Type Documentation

### 2.11.3.1 amd\_dbgapi\_dispatch\_barrier\_t

```
enum amd_dbgapi_dispatch_barrier_t
```

Dispatch barrier.

Controls when the dispatch will start being executed relative to previous packets on the queue.

#### Enumerator

AMD_DBGAPI_DISPATCH_BARRIER_NONE	Dispatch has no barrier.
AMD_DBGAPI_DISPATCH_BARRIER_PRESENT	Dispatch has a barrier. The dispatch will not be executed until all proceeding packets on the queue have completed.

### 2.11.3.2 amd\_dbgapi\_dispatch\_fence\_scope\_t

enum amd\_dbgapi\_dispatch\_fence\_scope\_t

Dispatch memory fence scope.

2.11 Dispatches 61 Controls how memory is acquired before a dispatch starts executing and released after the dispatch completes execution.

## Enumerator

AMD_DBGAPI_DISPATCH_FENCE_SCOPE_NONE	There is no fence.
AMD_DBGAPI_DISPATCH_FENCE_SCOPE_AGENT	There is a fence with agent memory scope.
AMD_DBGAPI_DISPATCH_FENCE_SCOPE_SYSTEM	There is a fence with system memory scope.

# 2.11.3.3 amd\_dbgapi\_dispatch\_info\_t

enum amd\_dbgapi\_dispatch\_info\_t

Dispatch queries that are supported by amd\_dbgapi\_dispatch\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_queue\_get\_info.

AMD_DBGAPI_DISPATCH_INFO_QUEUE	Return the queue to which this dispatch belongs. The type of this attribute is amd_dbgapi_queue_id_t.
AMD_DBGAPI_DISPATCH_INFO_AGENT	Return the agent to which this dispatch belongs. The type of this attribute is amd_dbgapi_agent_id_t.
AMD_DBGAPI_DISPATCH_INFO_PROCESS	Return the process to which this dispatch belongs. The type of this attribute is amd_dbgapi_process_id_t.
AMD_DBGAPI_DISPATCH_INFO_ARCHITECTURE	Return the architecture of this dispatch. The type of this attribute is amd_dbgapi_architecture_id_t.
AMD_DBGAPI_DISPATCH_INFO_OS_QUEUE_PA↔ CKET_ID	Return the queue packet ID of the dispatch packet that initiated the dispatch. The type of this attribute is amd_dbgapi_os_queue_packet_id_t.
AMD_DBGAPI_DISPATCH_INFO_BARRIER	Return the dispatch barrier setting. The type of this attribute is uint32_t with values defined by amd_dbgapi_dispatch_barrier_t.
AMD_DBGAPI_DISPATCH_INFO_ACQUIRE_FENCE	Return the dispatch acquire fence. The type of this attribute is uint32_t with values defined by amd_dbgapi_dispatch_fence_scope_t.
AMD_DBGAPI_DISPATCH_INFO_RELEASE_FENCE	Return the dispatch release fence. The type of this attribute is uint32_t with values defined by amd_dbgapi_dispatch_fence_scope_t.
AMD_DBGAPI_DISPATCH_INFO_GRID_DIMENSI↔ ONS	Return the dispatch grid dimensionality. The type of this attribute is uint32 with a value of 1, 2, or 3.
AMD_DBGAPI_DISPATCH_INFO_WORK_GROUP↔ _SIZES	Return the dispatch workgroup size (work-items) in the X, Y, and Z dimensions. The type of this attribute is uint16_t[3].
AMD_DBGAPI_DISPATCH_INFO_GRID_SIZES	Return the dispatch grid size (work-items) in the X, Y, and Z dimensions. The type of this attribute is uint32_t[3].
AMD_DBGAPI_DISPATCH_INFO_PRIVATE_SEGM↔ ENT_SIZE	Return the dispatch private segment size in bytes. The type of this attribute is amd_dbgapi_size_t.

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## Enumerator

AMD_DBGAPI_DISPATCH_INFO_GROUP_SEGME↔ NT_SIZE	Return the dispatch group segment size in bytes. The type of this attribute is amd_dbgapi_size_t.
AMD_DBGAPI_DISPATCH_INFO_KERNEL_ARGU↔ MENT_SEGMENT_ADDRESS	Return the dispatch kernel argument segment address. The type of this attribute is amd_dbgapi_global_address_t.
AMD_DBGAPI_DISPATCH_INFO_KERNEL_DESC↔ RIPTOR_ADDRESS	Return the dispatch kernel descriptor address. The type of this attribute is amd_dbgapi_global_address_t.
AMD_DBGAPI_DISPATCH_INFO_KERNEL_CODE↔ _ENTRY_ADDRESS	Return the dispatch kernel code entry address. The type of this attribute is amd_dbgapi_global_address_t.
AMD_DBGAPI_DISPATCH_INFO_KERNEL_COMP↔ LETION_ADDRESS	Return the dispatch completion event address. The type of this attribute is amd_dbgapi_global_address_t. The ABI of the completion event varies depending on the queue type available using the AMD_DBGAPI_QUEUE_INFO_TYPE query. See amd_dbgapi_os_queue_type_t. If the queue type does not use completion events, or the dispatch packet does not define a completion event, then amd_dbgapi_dispatch_get_info will return AMD_DBGAPI_STATUS_ERROR_NOT_SUPPORTED.

# 2.11.4 Function Documentation

# 2.11.4.1 amd\_dbgapi\_dispatch\_get\_info()

Query information about a dispatch.

 $\underline{\text{amd\_dbgapi\_dispatch\_info\_t specifies the queries supported and the type returned using the } \\ \text{value argument.}$ 

## **Parameters**

in	dispatch← _id	The handle of the dispatch being queried.
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.
out	value	Pointer to memory where the query result is stored.

## Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_DISPATCH	/Bueue_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_SUPPORTED	The requested query is not supported for the specified
	dispatch_id. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	value is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vCOMPA TIBLE Does not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

# 2.11.4.2 amd\_dbgapi\_process\_dispatch\_list()

Return the list of dispatches.

The order of the dispatch handles in the list is unspecified and can vary between calls.

## **Parameters**

in	process_id	If AMD_DBGAPI_PROCESS_NONE then the dispatch list for all processes is requested. Otherwise, the dispatch list of process process_id is requested.
out	dispatch_count	The number of dispatches active for a process.
out	dispatches	If changed is not NULL and the dispatch list of all of the processes requested have not changed since the last call(s) to amd_dbgapi_process_dispatch_list for each of them, then return NULL. Otherwise, return a pointer to an array of amd_dbgapi_dispatch_id_t with dispatch_count elements. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.
in,out	changed	If NULL then left unaltered. If non-NULL, set to AMD_DBGAPI_CHANGED_NO if the list of dispatches for each requested process is the same as when amd_dbgapi_process_dispatch_list was last called for them. Otherwise, set to AMD_DBGAPI_CHANGED_YES.

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AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in changed, dispatch_count, and dispatches.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized; and changed, dispatch_count, and
	dispatches are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and changed, dispatch_count, and
	dispatches are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	
	dispatches, <b>and</b> changed <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	dispatch_count or dispatches are NULL, or
	changed is invalid. dispatch_count,
	dispatches, and changed are unaltered.

## 2.12 Wave

Operations related to AMD GPU waves.

## **Data Structures**

struct amd\_dbgapi\_wave\_id\_t
 Opaque wave handle.

enum amd\_dbgapi\_wave\_info\_t {

#### **Macros**

#define AMD\_DBGAPI\_WAVE\_NONE ((amd\_dbgapi\_wave\_id\_t){ 0 })
 The NULL wave handle.

### **Enumerations**

= 1 }

```
AMD DBGAPI WAVE INFO STATE=1, AMD DBGAPI WAVE INFO STOP REASON=2, AMD DBGAPI WAVE INFO WAT
   = 3, AMD_DBGAPI_WAVE_INFO_DISPATCH = 4,
   AMD DBGAPI WAVE INFO QUEUE = 5, AMD DBGAPI WAVE INFO AGENT = 6, AMD DBGAPI WAVE INFO PROCESS
   = 7, AMD DBGAPI WAVE INFO ARCHITECTURE = 8,
   AMD_DBGAPI_WAVE_INFO_PC = 9, AMD_DBGAPI_WAVE_INFO_EXEC_MASK = 10, AMD_DBGAPI_WAVE_INFO_WORK_GI
   = 11, AMD_DBGAPI_WAVE_INFO_WAVE_NUMBER_IN_WORK_GROUP = 12,
   AMD_DBGAPI_WAVE_INFO_LANE_COUNT = 13 }
         Wave queries that are supported by amd_dbgapi_wave_get_info.
• enum amd_dbgapi_wave_state_t { AMD_DBGAPI_WAVE_STATE_RUN = 1, AMD_DBGAPI_WAVE_STATE_SINGLE_STEP
   = 2, AMD DBGAPI WAVE STATE STOP = 3 }
         The execution state of a wave.
enum amd_dbgapi_wave_stop_reason_t {
   AMD DBGAPI WAVE STOP REASON NONE = OULL, AMD DBGAPI WAVE STOP REASON BREAKPOINT
   = (1ULL << 0), AMD DBGAPI WAVE STOP REASON WATCHPOINT = (1ULL << 1), AMD DBGAPI WAVE STOP REASON
   = (1ULL << 2),
   AMD_DBGAPI_WAVE_STOP_REASON_QUEUE_ERROR = (1ULL << 3), AMD_DBGAPI_WAVE_STOP_REASON_FP_INPUT_
   = (1ULL << 4), AMD_DBGAPI_WAVE_STOP_REASON_FP_DIVIDE_BY_0 = (1ULL << 5), AMD_DBGAPI_WAVE_STOP_PREASON_FP_DIVIDE_BY_0 = (1U
   = (1ULL << 6),
   AMD DBGAPI WAVE STOP REASON FP UNDERFLOW = (1ULL << 7), AMD DBGAPI WAVE STOP REASON FP INEXA
   = (1ULL << 8), AMD_DBGAPI_WAVE_STOP_REASON_FP_INVALID_OPERATION = (1ULL << 9),
   AMD DBGAPI WAVE STOP REASON INT DIVIDE BY 0 = (1ULL << 10),
   AMD_DBGAPI_WAVE_STOP_REASON_DEBUG_TRAP = (1ULL << 11), AMD_DBGAPI_WAVE_STOP_REASON_ASSERT_TF
   = (1ULL << 12), AMD_DBGAPI_WAVE_STOP_REASON_TRAP = (1ULL << 13), AMD_DBGAPI_WAVE_STOP_REASON_MEN
   = (1ULL << 14),
   AMD DBGAPI WAVE STOP REASON ILLEGAL INSTRUCTION = (1ULL << 15), AMD DBGAPI WAVE STOP REASON EC
   = (1ULL << 16), AMD_DBGAPI_WAVE_STOP_REASON_FATAL_HALT = (1ULL << 17), AMD_DBGAPI_WAVE_STOP_REASO
   = (1ULL << 18),
   AMD_DBGAPI_WAVE_STOP_REASON_RESERVED = (1ULL << 63) }
```

enum amd\_dbgapi\_resume\_mode\_t{AMD\_DBGAPI\_RESUME\_MODE\_NORMAL = 0, AMD\_DBGAPI\_RESUME\_MODE\_SINGLE

The mode in which to resuming the execution of a wave.

A bit mask of the reasons that a wave stopped.

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## **Functions**

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_get\_info (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_wave\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about a wave.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_wave\_list (amd\_dbgapi\_process\_id\_t process\_id, size\_t \*wave\_count, amd\_dbgapi\_wave\_id\_t \*\*waves, amd\_dbgapi\_changed\_t \*changed) AMD\_DBGAPI\_VERSION\_0\_41
   Return the list of existing waves.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_stop (amd\_dbgapi\_wave\_id\_t wave\_id) AMD\_DBGAPI\_VERSION\_0\_41 Request a wave to stop executing.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_resume (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_resume\_mode\_t resume\_mode) AMD\_DBGAPI\_VERSION\_0\_46

Resume execution of a stopped wave.

## 2.12.1 Detailed Description

Operations related to AMD GPU waves.

## 2.12.2 Macro Definition Documentation

### 2.12.2.1 AMD DBGAPI WAVE NONE

```
#define AMD_DBGAPI_WAVE_NONE ((amd_dbgapi_wave_id_t) { 0 })
```

The NULL wave handle.

## 2.12.3 Enumeration Type Documentation

## 2.12.3.1 amd\_dbgapi\_resume\_mode\_t

```
enum amd_dbgapi_resume_mode_t
```

The mode in which to resuming the execution of a wave.

AMD_DBGAPI_RESUME_MODE_NORMAL	Resume normal execution.
AMD_DBGAPI_RESUME_MODE_SINGLE_STEP	Resume execution in in single step mode.

# 2.12.3.2 amd\_dbgapi\_wave\_info\_t

enum amd\_dbgapi\_wave\_info\_t

Wave queries that are supported by amd\_dbgapi\_wave\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_wave\_get\_info.

AMD DBGAPI WAVE INFO STATE	Return the wave's state. The type of this attribute is
,b_bbd/ii	uint32_t with values define by
	amd_dbgapi_wave_state_t.
AMD_DBGAPI_WAVE_INFO_STOP_REASON	Return the reason the wave stopped as a bit set. The type of this attribute is uint64_t with values defined by amd_dbgapi_wave_stop_reason_t. The wave must be stopped to make this query.
AMD_DBGAPI_WAVE_INFO_WATCHPOINTS	Return the watchpoint(s) the wave triggered. The type of this attribute is amd_dbgapi_watchpoint_list_t. The amd_dbgapi_watchpoint_list_t::count field is set to the number of watchpoints that were triggered. The amd_dbgapi_watchpoint_list_t::watchpoint_ids field is set to a pointer to an array of amd_dbgapi_watchpoint_id_t with amd_dbgapi_watchpoint_list_t::count elements comprising the triggered watchpoint handles. The array is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client. The wave must be stopped to make this query.
AMD_DBGAPI_WAVE_INFO_DISPATCH	Return the dispatch to which this wave belongs. The type of this attribute is amd_dbgapi_dispatch_id_t.  If the dispatch associated with a wave is not available then AMD_DBGAPI_DISPATCH_NONE is returned. If a wave has no associated dispatch then the the AMD_DBGAPI_WAVE_INFO_WORK_GROUP_COORD query may return incorrect information. Note that a wave may not have an associated dispatch if attaching to a process with already existing waves.
AMD_DBGAPI_WAVE_INFO_QUEUE	Return the queue to which this wave belongs. The type of this attribute is amd_dbgapi_queue_id_t.
AMD_DBGAPI_WAVE_INFO_AGENT	Return the agent to which this wave belongs. The type of this attribute is amd_dbgapi_agent_id_t.
AMD_DBGAPI_WAVE_INFO_PROCESS	Return the process to which this wave belongs. The type of this attribute is amd_dbgapi_process_id_t.
AMD_DBGAPI_WAVE_INFO_ARCHITECTURE	Return the architecture of this wave. The type of this attribute is amd_dbgapi_architecture_id_t.

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## Enumerator

AMD_DBGAPI_WAVE_INFO_PC	Return the current program counter value of the wave. The type of this attribute is amd_dbgapi_global_address_t. The wave must be stopped to make this query.
AMD_DBGAPI_WAVE_INFO_EXEC_MASK	Return the current execution mask of the wave. Each bit of the mask maps to a lane with the least significant bit corresponding to the lane with a amd_dbgapi_lane_id_t value of 0 and so forth. If the bit is 1 then the lane is active, otherwise the lane is not active. The type of this attribute is uint64_t. The wave must be stopped to make this query.
AMD_DBGAPI_WAVE_INFO_WORK_GROUP_CO↔ ORD	The wave workgroup coordinate in the dispatch grid dimensions. The type of this attribute is uint32_t[3] with elements 1, 2, and 3 corresponding to the X, Y, and Z coordinates respectively.
AMD_DBGAPI_WAVE_INFO_WAVE_NUMBER_IN_← WORK_GROUP	The wave's number in the workgroup. The type of this attribute is uint32_t. The work-items of a workgroup are mapped to the lanes of the waves of the workgroup in flattened work-item ID order, with the first work-item corresponding to lane 0 of wave 0, and so forth.
AMD_DBGAPI_WAVE_INFO_LANE_COUNT	The number of lanes supported by the wave. The type of this attribute is size_t.

# 2.12.3.3 amd\_dbgapi\_wave\_state\_t

enum amd\_dbgapi\_wave\_state\_t

The execution state of a wave.

AMD_DBGAPI_WAVE_STATE_RUN	The wave is running.
AMD_DBGAPI_WAVE_STATE_SINGLE_STEP	The wave is running in single-step mode. It will execute a single instruction and then stop.
AMD_DBGAPI_WAVE_STATE_STOP	The wave is stopped. Note that a wave may stop at any time due to the instructions it executes or because the queue it is executing on enters the error state. This will cause a AMD_DBGAPI_EVENT_KIND_WAVE_STOP event to be created. However, until amd_dbgapi_process_next_pending_event returns the event, the wave will continue to be reported as in the AMD_DBGAPI_WAVE_STATE_RUN state. Only when the AMD_DBGAPI_EVENT_KIND_WAVE_STOP event is returned by amd_dbgapi_process_next_pending_event will the wave be reported in the AMD_DBGAPI_WAVE_STATE_STOP state.

# 2.12.3.4 amd\_dbgapi\_wave\_stop\_reason\_t

enum amd\_dbgapi\_wave\_stop\_reason\_t

A bit mask of the reasons that a wave stopped.

The stop reason of a wave is available using the AMD\_DBGAPI\_WAVE\_INFO\_STOP\_REASON query.

		٦
AMD_DBGAPI_WAVE_STOP_REASON_NONE	If none of the bits are set, then amd_dbgapi_wave_stop stopped the wave.	
AMD_DBGAPI_WAVE_STOP_REASON_BREAKPO↔ INT	The wave stopped due to executing a breakpoint instruction. Use the AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPOINT_query to determine the address of the breakpoint instruction.	INSTRUCTION_P
AMD_DBGAPI_WAVE_STOP_REASON_WATCHP↔ OINT	The wave stopped due to triggering a data watchpoint. The AMD_DBGAPI_WAVE_INFO_WATCHPOINTS query can be used to determine which watchpoint(s) were triggered. The program counter may not be positioned at the instruction that caused the watchpoint(s) to be triggered as the AMD GPU can continue executing instructions after initiating a memory operation. If the architecture supports it, the amd_dbgapi_set_memory_precision can be used to control the precision, but may significantly reduce performance.	
AMD_DBGAPI_WAVE_STOP_REASON_SINGLE_← STEP	The wave stopped due to completing an instruction single-step.	
AMD_DBGAPI_WAVE_STOP_REASON_QUEUE_E↔ RROR	The wave belongs to a queue that is in the error state. This is set in both waves that were stopped due to a queue error, as well as waves that were already stopped when the queue went into the queue error state. A wave that includes this stop reason cannot be resumed using amd_dbgapi_wave_resume. The wave's queue will be in the queue error state.	
AMD_DBGAPI_WAVE_STOP_REASON_FP_INPUT↔DENORMAL	The wave stopped due to triggering an enabled floating point input denormal exception.	
AMD_DBGAPI_WAVE_STOP_REASON_FP_DIVID↔ E_BY_0	The wave stopped due to triggering an enabled floating point divide by zero exception.	
AMD_DBGAPI_WAVE_STOP_REASON_FP_OVER← FLOW	The wave stopped due to triggering an enabled floating point overflow exception.	
AMD_DBGAPI_WAVE_STOP_REASON_FP_UNDE↔ RFLOW	The wave stopped due to triggering an enabled floating point underflow exception.	
AMD_DBGAPI_WAVE_STOP_REASON_FP_INEXA← CT	The wave stopped due to triggering an enabled floating point inexact exception.	
AMD_DBGAPI_WAVE_STOP_REASON_FP_INVAL↔ ID_OPERATION	The wave stopped due to triggering an enabled floating point invalid operation exception.	

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AMD_DBGAPI_WAVE_STOP_REASON_INT_DIVID↔ E_BY_0	The wave stopped due to triggering an enabled integer divide by zero exception.
AMD_DBGAPI_WAVE_STOP_REASON_DEBUG_T↔ RAP	The wave stopped due to executing a debug trap instruction. The program counter is left positioned after the trap instruction. The wave can be resumed using amd_dbgapi_wave_resume.  The debug trap instruction can be generated using the llvm.debugtrap compiler intrinsic. See User Guide for AMDGPU Backend - Code Conventions - AMDHSA - Trap Handler ABI.
	A debug trap can be used to explicitly insert stop points in a program to help debugging. They behave as no operations if a debugger is not connected and stop the wave if executed with the debugger attached.
AMD_DBGAPI_WAVE_STOP_REASON_ASSERT_↔ TRAP	The wave stopped due to executing an assert trap instruction. The program counter is left positioned at the assert trap instruction.  The trap instruction can be generated using the llvm.trap compiler intrinsic. See User Guide for AMDGPU Backend - Code Conventions - AMDHSA - Trap Handler ABI.  An assert trap can be used to abort the execution of the dispatches executing on a queue.  A wave that includes this stop reason cannot be resumed using amd_dbgapi_wave_resume. The wave's queue will enter the queue error state and include the AMD_DBGAPI_QUEUE_ERROR_REASON_ASSERT_TRAF queue error reason.
AMD_DBGAPI_WAVE_STOP_REASON_TRAP	The wave stopped due to executing an trap instruction other than the AMD_DBGAPI_WAVE_STOP_REASON_DEBUG_TRAP or AMD_DBGAPI_WAVE_STOP_REASON_ASSERT_TRAP trap instruction. The program counter is left positioned at the trap instruction.  A wave that includes this stop reason cannot be resumed using amd_dbgapi_wave_resume. The wave's queue will enter the queue error state and include the AMD_DBGAPI_QUEUE_ERROR_REASON_WAVE_ERROR queue error reason.

AMD_DBGAPI_WAVE_STOP_REASON_MEMORY VIOLATION	The wave stopped due to triggering a memory violation. The program counter may not be positioned at the instruction that caused the memory violation as the AMD GPU can continue executing instructions after initiating a memory operation. If the architecture supports it, the amd_dbgapi_set_memory_precision can be used to control the precision, but may significantly reduce performance.  A wave that includes this stop reason cannot be resumed using amd_dbgapi_wave_resume. The wave's queue will enter the queue error state and include the AMD_DBGAPI_QUEUE_ERROR_REASON_MEMORY_VIOLATION queue error reason.
AMD_DBGAPI_WAVE_STOP_REASON_ILLEGAL_← INSTRUCTION	The wave stopped due to executing an illegal instruction. The program counter is left positioned at the illegal instruction.  A wave that includes this stop reason cannot be resumed using amd_dbgapi_wave_resume. The wave's queue will enter the queue error state and include the AMD_DBGAPI_QUEUE_ERROR_REASON_WAVE_ERROR queue error reason.
AMD_DBGAPI_WAVE_STOP_REASON_ECC_ERR↔ OR	The wave stopped due to detecting an unrecoverable ECC error. The program counter may not be positioned at the instruction that caused the memory violation as the AMD GPU can continue executing instructions after initiating a memory operation. If the architecture supports it, the amd_dbgapi_set_memory_precision can be used to control the precision, but may significantly reduce performance.  A wave that includes this stop reason cannot be resumed using amd_dbgapi_wave_resume. The wave's queue will enter the queue error state and include the AMD_DBGAPI_QUEUE_ERROR_REASON_WAVE_ERROR queue error reason.
AMD_DBGAPI_WAVE_STOP_REASON_FATAL_HALT	The wave stopped after causing a hardware fatal halt. A wave that includes this stop reason cannot be resumed using amd_dbgapi_wave_resume. The wave's queue will enter the queue error state and include the AMD_DBGAPI_QUEUE_ERROR_REASON_WAVE_ERROR queue error reason.
AMD_DBGAPI_WAVE_STOP_REASON_XNACK_E↔ RROR	The wave stopped with an XNACK error. A wave that includes this stop reason cannot be resumed using amd_dbgapi_wave_resume. The wave's queue will enter the queue error state and include the AMD_DBGAPI_QUEUE_ERROR_REASON_WAVE_ERROR queue error reason.
AMD_DBGAPI_WAVE_STOP_REASON_RESERVED	A reserved value only present to ensure that the underlying representation of this enumeration type is uint64_t.

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# 2.12.4 Function Documentation

## 2.12.4.1 amd\_dbgapi\_process\_wave\_list()

```
amd_dbgapi_status_t AMD_DBGAPI amd_dbgapi_process_wave_list (
    amd_dbgapi_process_id_t process_id,
    size_t * wave_count,
    amd_dbgapi_wave_id_t ** waves,
    amd_dbgapi_changed_t * changed )
```

Return the list of existing waves.

The order of the wave handles in the list is unspecified and can vary between calls.

## **Parameters**

in	process_id	If AMD_DBGAPI_PROCESS_NONE then the wave list for all processes is requested.  Otherwise, the wave list of process process_id is requested.	
out	wave_count	The number of waves executing in the process.	
out	waves	If changed is not NULL and the wave list of all of the processes requested have not changed since the last call(s) to amd_dbgapi_process_wave_list for each of them, then return NULL. Otherwise, return a pointer to an array of amd_dbgapi_wave_id_t with wave_count elements. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.	
in,out	changed	If NULL then left unaltered. If non-NULL, set to AMD_DBGAPI_CHANGED_NO if the list of waves for each requested process is the same as when amd_dbgapi_process_wave_list was last called for them. Otherwise, set to AMD_DBGAPI_CHANGED_YES.	

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in changed, wave_count, and
	waves.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	and changed, wave_count, and waves are
	unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and wave_count, waves, and
	changed <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/process_id is invalid. wave_count, waves, and
	unchanged <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	wave_count or waves are NULL, or changed is
	invalid. wave_count, waves, and changed are
	unaltered.

## **Return values**

AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate waves returns NULL. wave_count,
	waves, and changed are unaltered.

## 2.12.4.2 amd\_dbgapi\_wave\_get\_info()

Query information about a wave.

amd\_dbgapi\_wave\_info\_t specifies the queries supported and the type returned using the value argument.

## **Parameters**

in	wave_id	The handle of the wave being queried.	
in	query	The query being requested.	
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.	
out	value	Pointer to memory where the query result is stored.	

AMD DBGAPI STATUS SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vGQM€A\$IBLE 706es not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	EQuery has a value of amd_dbgapi_wave_info_t that
	requires the wave to be stopped, but the wave is not
	stopped.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

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#### 2.12.4.3 amd\_dbgapi\_wave\_resume()

Resume execution of a stopped wave.

The wave can be resumed normally in which case it will be in the AMD\_DBGAPI\_WAVE\_STATE\_RUN state and be available for the hardware to execute instructions. Just because it is in the run state does not mean the hardware will start executing instructions immediately as that depends on the AMD GPU hardware scheduler.

If while in the AMD\_DBGAPI\_WAVE\_STATE\_RUN state, the wave encounters something that stops its execution, or amd\_dbgapi\_wave\_stop is used to stop the wave execution, then a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event will be created.

If while in the AMD\_DBGAPI\_WAVE\_STATE\_RUN state the wave terminates, no event is created.

The wave can be resumed in single step mode in which case it will be in the AMD\_DBGAPI\_WAVE\_STATE\_SINGLE\_STEP state. It is available for the hardware to execute one instruction. After completing execution of a regular instruction, a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event will be created that indicates the wave has stopped. The stop reason of the wave will include AMD\_DBGAPI\_WAVE\_STOP\_REASON\_SINGLE\_STEP. After completing execution of a wave termination instruction, a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_COMMAND\_TERMINATED event will be created that indicates that the wave has terminated.

Resuming a wave in single step mode does not necessarily cause it to execute any instructions as it is up to the AMD GPU hardware scheduler to decide what waves to execute. For example, the AMD GPU hardware scheduler may not execute any instructions of a wave until other waves have terminated. If the client has stopped other waves this can prevent a wave from ever performing a single step. The client should handle this gracefully and not rely on a single step request always resulting in a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event. If necessary, the client should respond to the stop events of other waves to allow them to make forward progress, and handle the single step stop request when it finally arrives. If necessary, the client can cancel the single step request by using amd\_dbgapi\_wave\_stop and allow the user to attempt it again later when other waves have terminated.

It is an error to resume a wave that has terminated. The wave handle will be reported as invalid. It is up to the client to use <a href="mailto:amd\_dbgapi\_process\_wave\_list">amd\_dbgapi\_process\_wave\_list</a> to determine what waves have been created and terminated. No event is reported when a wave is created or terminates.

It is an error to request a wave to resume that is not in the AMD\_DBGAPI\_WAVE\_STATE\_STOP state, or is in the AMD\_DBGAPI\_WAVE\_STATE\_STOP state but the AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event that put it in the stop state has not yet been completed using the amd\_dbgapi\_event\_processed operation. Therefore, it is not allowed to execute multiple resume requests as all but the first one will give an error.

It also means it is an error to resume a wave that has already stopped, but whose AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event has not yet been returned by amd\_dbgapi\_process\_next\_pending\_event, since the wave is still in the AMD\_DBGAPI\_WAVE\_STATE\_RUN state. The AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP must be processed first.

Since a resume request can only be sent to a wave that has stopped, there is no issue of the wave terminating while making the request. However, the wave may terminate after being resumed. Except for single stepping the wave termination instruction described above, no event is reported when the wave terminates.

Sending a resume request to a wave that includes a stop reason that cannot be resumed will report an error. See amd dbgapi wave\_stop\_reason\_t.

#### **Parameters**

in	wave_id	The wave being requested to resume.
in	resume_mode	If AMD_DBGAPI_RESUME_MODE_NORMAL, then resume normal execution of the wave. If AMD_DBGAPI_RESUME_MODE_SINGLE_STEP, then resume the wave in single step mode.

#### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the wave will either terminate or be stopped. In either case a AMD_DBGAPI_EVENT_KIND_WAVE_STOP event will be reported.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and no wave is resumed.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. No wave is resumed.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tresume_mode is invalid. No wave is resumed.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	<b>E</b> wave_id is not stopped. The wave remains running.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_RESUM	Aសង់តែe_id is stopped with a reason that includes one that cannot be resumed, or the event that put it in the stop state has not yet been completed using the amd_dbgapi_event_processed operation.
AMD_DBGAPI_STATUS_ERROR_RESUME_DISPLACE	Dwstephins stopped and has an associated displaced stepping buffer. The resume_mode is is either not AMD_DBGAPI_RESUME_MODE_SINGLE_STEP, or the wave_id has already been single stepped by one instruction and so amd_dbgapi_displaced_stepping_complete must be used before the wave can be resumed.

## 2.12.4.4 amd\_dbgapi\_wave\_stop()

Request a wave to stop executing.

The wave may or may not immediately stop. If the wave does not immediately stop, the stop request is termed outstanding until the wave does stop or the wave terminates before stopping. When the wave does stop it will create a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event. If the wave terminates before stopping it will create a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_COMMAND\_TERMINATED event.

A process in the AMD\_DBGAPI\_PROGRESS\_NO\_FORWARD progress mode will report the AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STO or AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_COMMAND\_TERMINATED event. It is not necessary to change the progress mode to AMD\_DBGAPI\_PROGRESS\_NORMAL for these events to be reported.

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It is not necessary for the process AMD\_DBGAPI\_PROGRESS\_NO\_FORWARD

It is an error to request a wave to stop that has terminated. The wave handle will be reported as invalid. It is up to the client to use <a href="mailto:amd\_dbgapi\_process\_wave\_list">amd\_dbgapi\_process\_wave\_list</a> to determine what waves have been created and terminated. No event is reported when a wave is created or terminates.

It is an error to request a wave to stop that is already in the AMD\_DBGAPI\_WAVE\_STATE\_STOP state.

It is an error to request a wave to stop for which there is an outstanding amd\_dbgapi\_wave\_stop request.

Sending a stop request to a wave that has already stopped, but whose AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event has not yet been returned by amd\_dbgapi\_process\_next\_pending\_event, is allowed since the wave is still in the AMD\_DBGAPI\_WAVE\_STATE\_RUN state. In this case the wave is not affected and the already existing AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP will notify the client that the stop request has completed. The client must be prepared that a wave may stop for other reasons in response to a stop request. It can use the AMD\_DBGAPI\_WAVE\_INFO\_STOP\_REASON query to determine if there are other reason(s). See AMD\_DBGAPI\_WAVE\_STATE\_STOP for more information.

Sending a stop request to a wave that is in the AMD\_DBGAPI\_WAVE\_STATE\_SINGLE\_STEP state will attempt to stop the wave and either report a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP or AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_COMMAND\_TERMI event. If the wave did stop, the setting of the AMD\_DBGAPI\_WAVE\_STOP\_REASON\_SINGLE\_STEP stop reason will indicate whether the wave completed the single step. If the single step does complete, but terminates the wave, then AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_COMMAND\_TERMINATED will be reported.

Sending a stop request to a wave that is present at the time of the request, and does stop, will result in a AMD DBGAPI EVENT KIND WAVE STOP event.

Sending a stop request to a wave that is present at the time of the request, but terminates before completing the stop request, will result in a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_COMMAND\_TERMINATED event.

## **Parameters**

in	wave←	The wave being requested to stop.
	_id	

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the wave will either report a AMD_DBGAPI_EVENT_KIND_WAVE_STOP or AMD_DBGAPI_EVENT_KIND_WAVE_COMMAND_TERMINATES event.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and no wave is stopped.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. No wave is stopped.
AMD_DBGAPI_STATUS_ERROR_WAVE_STOPPED	wave_id is already stopped. The wave remains stopped.
AMD_DBGAPI_STATUS_ERROR_WAVE_OUTSTANDII	VI he wave already has an outstanding stop request. This stop request is ignored and the previous stop request continues to stop the wave.

# 2.13 Displaced Stepping

Operations related to AMD GPU breakpoint displaced stepping.

#### **Data Structures**

struct amd\_dbgapi\_displaced\_stepping\_id\_t

Opaque displaced stepping handle.

### **Macros**

#define AMD\_DBGAPI\_DISPLACED\_STEPPING\_NONE (amd\_dbgapi\_displaced\_stepping\_id\_t{ 0 })
 The NULL displaced stepping handle.

#### **Enumerations**

enum amd\_dbgapi\_displaced\_stepping\_info\_t { AMD\_DBGAPI\_DISPLACED\_STEPPING\_INFO\_PROCESS = 1 }

Displaced stepping queries that are supported by amd\_dbgapi\_displaced\_stepping\_id\_t.

## **Functions**

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_displaced\_stepping\_get\_info (amd\_dbgapi\_displaced\_stepping\_id\_t displaced\_stepping\_id, amd\_dbgapi\_displaced\_stepping\_info\_t query, size\_t value\_size, void \*value)
 AMD\_DBGAPI\_VERSION\_0\_41

Query information about a displaced stepping buffer.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_displaced\_stepping\_start (amd\_dbgapi\_wave\_id\_t wave
 \_id, const void \*saved\_instruction\_bytes, amd\_dbgapi\_displaced\_stepping\_id\_t \*displaced\_stepping)
 AMD\_DBGAPI\_VERSION\_0\_46

Associate an active displaced stepping buffer with a wave.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_displaced\_stepping\_complete (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_displaced\_stepping\_id\_t displaced\_stepping) AMD\_DBGAPI\_VERSION\_0\_42

Complete a displaced stepping buffer for a wave.

# 2.13.1 Detailed Description

Operations related to AMD GPU breakpoint displaced stepping.

The library supports displaced stepping buffers. These allow an instruction that is overwritten by a breakpoint instruction to be copied to a buffer and single stepped in that buffer. This avoids needing to remove the breakpoint instruction by replacing it with the original instruction bytes, single stepping the original instruction, and finally restoring the breakpoint instruction.

This allows a client to support non-stop debugging where waves are left executing while others are halted after hitting a breakpoint. If resuming from a breakpoint involved removing the breakpoint, it could result in the running waves missing the removed breakpoint.

When an instruction is copied into a displaced stepping buffer, it may be necessary to modify the instruction, or its register inputs to account for the fact that it is executing at a different address. Similarly, after single stepping it, registers and program counter may need adjusting. It may also be possible to know the effect of an instruction and avoid single stepping it at all and simply update the wave state directly. For example, branches can be trivial to emulate this way.

The operations in this section allow displaced stepping buffers to be allocated and used. They will take care of all the architecture specific details described above.

The number of displaced stepping buffers supported by the library is unspecified, but there is always at least one. It may be possible for the library to share the same displaced stepping buffer with multiple waves. For example, if the waves are at the same breakpoint. The library will determine when this is possible, but the client should not rely on this. Some waves at the same breakpoint may be able to share while others may not. In general, it is best for the client to single step as many waves as possible to minimize the time to get all waves stepped over the breakpoints.

The client may be able to maximize the number of waves it can single step at once by requesting displaced stepping buffers for all waves at the same breakpoint. Just because there is no displaced stepping buffer for one wave, does not mean another wave cannot be assigned to a displaced stepping buffer through sharing, or through buffers being associated with specific agents or queues.

If allocating a displaced stepping buffer (amd\_dbgapi\_displaced\_stepping\_start) is successful, then the client must resume the wave (amd\_dbgapi\_wave\_resume) in single step mode. When the single step is reported as completed (AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP), the buffer can be released (amd\_dbgapi\_displaced\_stepping\_complete), and the wave resumed normally (amd\_dbgapi\_wave\_resume).

If the single step is reported as terminated (AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_COMMAND\_TERMINATED), then that indicates that the wave has exited. When a wave exits, any associated displaced stepping buffer is automatically released.

If the wave does not report the single step as complete (AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP) or terminated (AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_COMMAND\_TERMINATED), then the wave can be stopped (amd\_dbgapi\_wave\_stop), and the buffer released (amd\_dbgapi\_displaced\_stepping\_complete). This will leave the wave still at the breakpoint, and the client can retry stepping over the breakpoint later (amd\_dbgapi\_displaced\_stepping\_start).

If allocating a displaced stepping buffer indicates no more are available, the client must complete using the previously allocated buffers. It can do that by ensuring the allocated waves are resumed in single step mode, ensure that the waves will make forward progress, and process any reported pending events. This allows waves to perform the single step, report the single step has completed by an event, and the client's processing of the event will complete the displaced stepping buffer. That may free up a displaced stepping buffer for use by the client for other waves. Since there is always at least one displaced stepping buffer, in general, the worst case is that one wave at a time can be single stepped over a breakpoint using a displaced stepping buffer.

However, the weak forward progress of AMD GPU execution can result in no waves that have successfully been allocated a displaced stepping buffer from actually reporting completion of the single step. For example, this can happen if the waves being single stepped are prevented from becoming resident on the hardware due to other waves that are halted. The waves being single stepped can be stopped before completing the single step to release the displaced stepping buffer for use by a different set of waves. In the worst case, the user may have to continue halted waves and allow them to terminate before other waves can make forward progress to complete the single step using a displaced stepping buffer.

See also

amd\_dbgapi\_wave\_resume, amd\_dbgapi\_wave\_stop, amd\_dbgapi\_process\_set\_progress, amd\_dbgapi\_process\_next\_pending\_evaluations.

## 2.13.2 Macro Definition Documentation

## 2.13.2.1 AMD\_DBGAPI\_DISPLACED\_STEPPING\_NONE

```
#define AMD_DBGAPI_DISPLACED_STEPPING_NONE (amd_dbgapi_displaced_stepping_id_t{ 0 })
```

The NULL displaced stepping handle.

# 2.13.3 Enumeration Type Documentation

## 2.13.3.1 amd\_dbgapi\_displaced\_stepping\_info\_t

```
enum amd_dbgapi_displaced_stepping_info_t
```

Displaced stepping queries that are supported by amd\_dbgapi\_displaced\_stepping\_id\_t.

Each query specifies the type of data returned in the value argument to amd dbgapi displaced stepping id t.

## Enumerator

OCESS	belongs. The type of this attribute is
	amd_dbgapi_process_id_t.

## 2.13.4 Function Documentation

## 2.13.4.1 amd\_dbgapi\_displaced\_stepping\_complete()

Complete a displaced stepping buffer for a wave.

The wave must be stopped and have an associated displaced stepping buffer by using amd\_dbgapi\_displaced\_stepping\_start.

If the wave single step has not completed the wave state is reset to what it was before amd\_dbgapi\_displaced\_stepping\_start. The wave is left stopped and the client can retry stepping over the breakpoint again later.

If the single step has completed, then the wave state is updated to be after the instruction at which the breakpoint instruction is placed.

Completing a displaced stepping buffer may read and write the wave program counter and other registers so the client should invalidate any cached register values after completing a displaced stepping buffer. The wave is left stopped and can be resumed normally by the client.

If the wave is the last one using the displaced stepping buffer, the buffer is freed and the handle invalidated.

#### **Parameters**

in	wave_id	The wave using the displaced stepping buffer.
in	displaced_stepping	The displaced stepping buffer to complete.

#### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully. The displaced stepping buffer is completed, and the wave is either stepped over the breakpoint, or still at the breakpoint.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized, and no displaced stepping buffer is completed.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized, no displaced stepping buffer completed.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. No displaced stepping buffer is completed.
AMD_DBGAPI_STATUS_ERROR_INVALID_DISPLACED	<u>STEPPANGUL</u> stepping is invalid. No displaced stepping buffer is completed.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	EMave_id is not stopped. No displaced stepping buffer is completed.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_GGMPATEML[\$Yepping is not in use by wave_id (which includes that the wave has already competed the displaced stepping buffer). No displaced stepping buffer is completed.

## 2.13.4.2 amd\_dbgapi\_displaced\_stepping\_get\_info()

```
amd_dbgapi_displaced_stepping_info_t query,
size_t value_size,
void * value )
```

Query information about a displaced stepping buffer.

amd\_dbgapi\_displaced\_stepping\_info\_t specifies the queries supported and the type returned using the value argument.

#### **Parameters**

in	displaced_stepping←	The handle of the displaced stepping buffer being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.
out	value	Pointer to memory where the query result is stored.

#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_DISPLACEI	_d <b>iJspPlMed<u>lD</u>stepping_id is invalid.</b> value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	value is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vGQM@AstBld 706es not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

## 2.13.4.3 amd\_dbgapi\_displaced\_stepping\_start()

Associate an active displaced stepping buffer with a wave.

The wave must be stopped and not already have an active displaced stepping buffer.

Displaced stepping buffers are intended to be used to step over breakpoints. In that case, the wave will be stopped with a program counter set to a breakpoint instruction that was placed by the client overwriting all or part of the original instruction where the breakpoint was placed. The client must provide the overwritten bytes of the original instruction.

The wave program counter and other registers may be read and written as part of creating a displaced stepping buffer. Therefore, the client should flush any dirty cached register values before creating a displaced stepping buffer.

If a displaced stepping handle is returned successfully, the wave is still stopped. The client should resume the wave in single step mode using amd\_dbgapi\_wave\_resume. Once the single step is complete as indicated by the AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event with a stop reason that includes AMD\_DBGAPI\_WAVE\_STOP\_REASON\_SINGLE\_STEP, the client should use amd\_dbgapi\_displaced\_stepping\_complete to release the displaced stepping buffer. The wave can then be resumed normally using amd\_dbgapi\_wave\_resume.

If the single step is cancelled by stopping the wave, the client must determine if the wave completed the single step to determine if the wave can be resumed or must retry the displaced stepping later. See amd dbgapi wave stop.

#### **Parameters**

in	wave_id	The wave to create a displaced stepping buffer.
in	saved_instruction_bytes	The original instruction bytes that the breakpoint instruction replaced. The number of bytes must be AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPOINT_INSTRUCTION_SIZE.
out	displaced_stepping	The displaced stepping handle.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and displaced_stepping is set to a valid displaced stepping handle.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized, no displaced stepping buffer is allocated, and displaced_stepping is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized, no displaced stepping buffer is allocated, and displaced_stepping is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. No displaced stepping buffer is allocated and displaced_stepping is unaltered.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	EMave_id is not stopped. No displaced stepping buffer is allocated and displaced_stepping is unaltered.
AMD_DBGAPI_STATUS_ERROR_DISPLACED_STEPPI	Manager Active displaced stepping buffer. No displaced stepping buffer is allocated and displaced_stepping is unaltered.
AMD_DBGAPI_STATUS_ERROR_DISPLACED_STEPPI	AND Brofe displaced stepping buffers are available that are suitable for use by wave_id. No displaced stepping buffer is allocated and displaced_stepping is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Toriginal_instruction or displaced_stepping are NULL. No displaced stepping buffer is allocated and displaced_stepping is unaltered.

AMD_DBGAPI_STATUS_ERROR_MEMORY_ACCESS	The memory at the wave's program counter could not
	be successfully read. No displaced stepping buffer is
	allocated and displaced_stepping is unaltered.
AMD_DBGAPI_STATUS_ERROR_ILLEGAL_INSTRUCT	/C7M/me instruction at the wave's program counter is not a
	legal instruction for the architecture. No displaced
	stepping buffer is allocated and
	displaced_stepping is unaltered.

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# 2.14 Watchpoints

Operations related to AMD GPU hardware data watchpoints.

#### **Data Structures**

struct amd\_dbgapi\_watchpoint\_id\_t

Opaque hardware data watchpoint handle.

struct amd\_dbgapi\_watchpoint\_list\_t

A set of watchpoints.

## **Macros**

#define AMD DBGAPI WATCHPOINT NONE ((amd dbgapi watchpoint id t){ 0 })

The NULL hardware data watchpoint handle.

#### **Enumerations**

- enum amd\_dbgapi\_watchpoint\_info\_t { AMD\_DBGAPI\_WATCHPOINT\_INFO\_PROCESS = 1 }
   Watchpoint queries that are supported by amd\_dbgapi\_watchpoint\_get\_info.
- enum amd\_dbgapi\_watchpoint\_share\_kind\_t { AMD\_DBGAPI\_WATCHPOINT\_SHARE\_KIND\_UNSUPPORTED
   = 0, AMD\_DBGAPI\_WATCHPOINT\_SHARE\_KIND\_UNSHARED = 1, AMD\_DBGAPI\_WATCHPOINT\_SHARE\_KIND\_SHARED
   = 2 }

The way watchpoints are shared between processes.

enum amd\_dbgapi\_watchpoint\_kind\_t { AMD\_DBGAPI\_WATCHPOINT\_KIND\_LOAD = 1, AMD\_DBGAPI\_WATCHPOINT\_KIND\_S
 = 2, AMD\_DBGAPI\_WATCHPOINT\_KIND\_RMW = 3, AMD\_DBGAPI\_WATCHPOINT\_KIND\_ALL = 4 }

Watchpoint memory access kinds.

## **Functions**

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_watchpoint\_get\_info (amd\_dbgapi\_watchpoint\_id\_t watchpoint \_id, amd\_dbgapi\_watchpoint\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41
  - Query information about a watchpoint.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_set\_watchpoint (amd\_dbgapi\_process\_id\_t process 
   \_id, amd\_dbgapi\_global\_address\_t address, amd\_dbgapi\_size\_t size, amd\_dbgapi\_watchpoint\_kind\_t 
   kind, amd\_dbgapi\_watchpoint\_id\_t \*watchpoint\_id, amd\_dbgapi\_global\_address\_t \*watchpoint\_address, 
   amd\_dbgapi size t \*watchpoint size) AMD\_DBGAPI\_VERSION\_0\_41

Set a hardware data watchpoint.

 amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_remove\_watchpoint (amd\_dbgapi\_process\_id\_t process\_id, amd dbgapi watchpoint id t watchpoint id) AMD DBGAPI VERSION 0 24

Remove a hardware data watchpoint previously set by amd\_dbgapi\_set\_watchpoint.

# 2.14.1 Detailed Description

Operations related to AMD GPU hardware data watchpoints.

A data watchpoint is a hardware supported mechanism to generate wave stop events after a wave accesses memory in a certain way in a certain address range. The memory access will have been completed before the event is reported.

The number of watchpoints, the granularity of base address, and the address range is process specific. If a process has multiple agents, then the values are the lowest common denominator of the capabilities of the architectures of all the agents of a process.

The number of watchpoints supported by a process is available using the AMD\_DBGAPI\_PROCESS\_INFO\_WATCHPOINT\_COUNT query and may be 0. The AMD\_DBGAPI\_PROCESS\_INFO\_WATCHPOINT\_SHARE query can be used to determine if watchpoints are shared between processes.

When a wave stops due to a data watchpoint the stop reason will include AMD\_DBGAPI\_WAVE\_STOP\_REASON\_WATCHPOINT. The set of watchpoints triggered can be queried using AMD\_DBGAPI\_WAVE\_INFO\_WATCHPOINTS.

### 2.14.2 Macro Definition Documentation

## 2.14.2.1 AMD\_DBGAPI\_WATCHPOINT\_NONE

```
#define AMD_DBGAPI_WATCHPOINT_NONE ((amd_dbgapi_watchpoint_id_t) { 0 })
```

The NULL hardware data watchpoint handle.

# 2.14.3 Enumeration Type Documentation

## 2.14.3.1 amd\_dbgapi\_watchpoint\_info\_t

```
enum amd_dbgapi_watchpoint_info_t
```

Watchpoint queries that are supported by amd\_dbgapi\_watchpoint\_get\_info.

Each query specifies the type of data returned in the value argument to amd dbgapi watchpoint get info.

AMD_DBGAPI_WATCHPOINT_INFO_PROCESS	Return the process to which this watchpoint belongs. The
	type of this attribute is amd_dbgapi_process_id_t.

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## 2.14.3.2 amd\_dbgapi\_watchpoint\_kind\_t

enum amd\_dbgapi\_watchpoint\_kind\_t

Watchpoint memory access kinds.

The watchpoint is triggered only when the memory instruction is of the specified kind.

#### Enumerator

AMD_DBGAPI_WATCHPOINT_KIND_LOAD	Read access by load instructions.
AMD_DBGAPI_WATCHPOINT_KIND_STORE_AND↔	Write access by store instructions or read-modify-write
_RMW	access by atomic instructions.
AMD_DBGAPI_WATCHPOINT_KIND_RMW	Read-modify-write access by atomic instructions.
AMD_DBGAPI_WATCHPOINT_KIND_ALL	Read, write, or read-modify-write access by load, store,
	or atomic instructions.

## 2.14.3.3 amd\_dbgapi\_watchpoint\_share\_kind\_t

enum amd\_dbgapi\_watchpoint\_share\_kind\_t

The way watchpoints are shared between processes.

The AMD\_DBGAPI\_PROCESS\_INFO\_WATCHPOINT\_SHARE query can be used to determine the watchpoint sharing for an architecture.

## Enumerator

Watchpoints are not supported.
The watchpoints are not shared across processes.
Every process can use all
AMD_DBGAPI_PROCESS_INFO_WATCHPOINT_COUN
watchpoints.
The watchpoints of a process are shared between all
processes. The number of watchpoints available to a
process may be reduced if watchpoints are used by
another process.

# 2.14.4 Function Documentation

## 2.14.4.1 amd\_dbgapi\_remove\_watchpoint()

Remove a hardware data watchpoint previously set by amd\_dbgapi\_set\_watchpoint.

#### **Parameters**

in	process_id	The process that owns the watchpoint.
in	watchpoint⇔	The watchpoint to remove.
	_id	

#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the watchpoint has been removed.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and no watchpoint is removed.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/process_id is invalid. No watchpoint is removed.
AMD_DBGAPI_STATUS_ERROR_INVALID_WATCHPO	/WatDchpoint_id is invalid. No watchpoint is
	removed.

## 2.14.4.2 amd dbgapi set watchpoint()

Set a hardware data watchpoint.

The AMD GPU has limitations on the base address and size of hardware data watchpoints that can be set, and the limitations may vary by architecture. A watchpoint is created with the smallest range, supported by the architectures of all the agents of a process, that covers the requested range specified by address and size.

If the requested range is larger than is supported by the architectures of all the agents of a process, then a watchpoint is created with the smallest range that includes address and covers as much of the requested range as possible.

The range of the created watchpoint is returned in watchpoint\_address and watchpoint\_size. The client is responsible for determining if the created watchpoint completely covers the requested range. If it does not, the client can attempt to create additional watchpoints for the uncovered portion of the requested range.

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When a watchpoint is triggered, the client is responsible for determining if the access was to the requested range. For example, for writes the client can compare the original value with the current value to determine if it changed.

Each process has its own set of watchpoints. Only waves executing on the agents of a process will trigger the watchpoints set on that process.

## **Parameters**

in	process_id	The process on which to set the watchpoint.
in	address	The base address of memory area to set a watchpoint.
in	size	The non-zero number of bytes that the watchpoint should cover.
in	kind	The kind of memory access that should trigger the watchpoint.
out	watchpoint_id	The watchpoint created.
out	watchpoint_address	The base address of the created watchpoint.
out	watchpoint_size	The byte size of the created watchpoint.

#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the watchpoint has been created with handle watchpoint_id that covers the range specified by watchpoint_address and watchpoint_size.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized; and watchpoint_id, watchpoint_address, and watchpoint_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized; and watchpoint_id, watchpoint_address, and watchpoint_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	##Process_id is invalid. No watchpoint is set and watchpoint_id, watchpoint_address, and watchpoint_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_NO_WATCHPOINT_/	WWolnAcherwatchpoints are available. No watchpoint is set and watchpoint_id, watchpoint_address, and watchpoint_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_SUPPORTED	Watchpoints are not supported for the architectures of all the agents. No watchpoint is set and watchpoint_id, watchpoint_address, and watchpoint_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tkind is invalid; size is 0; or watchpoint_id, watchpoint_address, or watchpoint_size are NULL. No watchpoint is set and watchpoint_id, watchpoint_address, and watchpoint_size are unaltered.

# 2.14.4.3 amd\_dbgapi\_watchpoint\_get\_info()

 $\verb|amd_dbgapi_status_t| \verb|AMD_DBGAPI| amd_dbgapi_watchpoint_get_info| ($ 

```
amd_dbgapi_watchpoint_id_t watchpoint_id,
amd_dbgapi_watchpoint_info_t query,
size_t value_size,
void * value )
```

Query information about a watchpoint.

amd\_dbgapi\_watchpoint\_info\_t specifies the queries supported and the type returned using the value argument.

## **Parameters**

in	watchpoint←	The handle of the watchpoint being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.
out	value	Pointer to memory where the query result is stored.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WATCHPO	N√atDchpoint_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	value is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vGQM@AstBulled 70 bes not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

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# 2.15 Registers

Operations related to AMD GPU register access.

## **Data Structures**

```
    struct amd_dbgapi_register_class_id_t
```

Opaque register class handle.

struct amd\_dbgapi\_register\_id\_t

Opaque register handle.

struct amd\_dbgapi\_direct\_call\_register\_pair\_information\_t

Instruction information for direct call instructions.

#### **Macros**

#define AMD\_DBGAPI\_REGISTER\_CLASS\_NONE ((amd\_dbgapi\_register\_class\_id\_t){ 0 })

The NULL register class handle.

• #define AMD\_DBGAPI\_REGISTER\_NONE ((amd\_dbgapi\_register\_id\_t){ 0 })

The NULL register handle.

## **Enumerations**

 enum amd\_dbgapi\_register\_class\_info\_t { AMD\_DBGAPI\_REGISTER\_CLASS\_INFO\_ARCHITECTURE = 1, AMD\_DBGAPI\_REGISTER\_CLASS\_INFO\_NAME = 2}

Register class queries that are supported by amd\_dbgapi\_architecture\_register\_class\_get\_info.

```
    enum amd_dbgapi_register_info_t {
        AMD_DBGAPI_REGISTER_INFO_ARCHITECTURE = 1, AMD_DBGAPI_REGISTER_INFO_NAME = 2,
        AMD_DBGAPI_REGISTER_INFO_SIZE = 3, AMD_DBGAPI_REGISTER_INFO_TYPE = 4,
        AMD_DBGAPI_REGISTER_INFO_DWARF = 5 }
```

Register queries that are supported by amd\_dbgapi\_register\_get\_info.

enum amd\_dbgapi\_register\_exists\_t { AMD\_DBGAPI\_REGISTER\_ABSENT = 0, AMD\_DBGAPI\_REGISTER\_PRESENT = 1 }

Indication of if a wave has a register.

 enum amd\_dbgapi\_register\_class\_state\_t { AMD\_DBGAPI\_REGISTER\_CLASS\_STATE\_NOT\_MEMBER = 0, AMD\_DBGAPI\_REGISTER\_CLASS\_STATE\_MEMBER = 1 }

Indication of whether a register is a member of a register class.

## **Functions**

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_register\_class\_get\_info (amd\_dbgapi\_register\_class\_id\_t register\_class\_id, amd\_dbgapi\_register\_class\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41
 Query information about a register class of an architecture.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_register\_class\_list (amd\_dbgapi\_architecture\_id\_t architecture\_id, size\_t \*register\_class\_count, amd\_dbgapi\_register\_class\_id\_t \*\*register\_classes) AMD\_DBGAPI\_VERSION\_0\_2.
   Report the list of register classes supported by the architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_register\_get\_info (amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_register\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_46

Query information about a register.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_register\_exists (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_register\_exists\_t \*exists) AMD\_DBGAPI\_VERSION\_0\_41
   Query if a register exists for a wave.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_register\_list (amd\_dbgapi\_architecture\_id\_t architecture\_id, size\_t \*register\_count, amd\_dbgapi\_register\_id\_t \*\*registers) AMD\_DBGAPI\_VERSION\_0\_24
   Report the list of registers supported by the architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_register\_list (amd\_dbgapi\_wave\_id\_t wave\_id, size\_
   t \*register\_count, amd\_dbgapi\_register\_id\_t \*\*registers) AMD\_DBGAPI\_VERSION\_0\_41

Report the list of registers supported by a wave.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_dwarf\_register\_to\_register (amd\_dbgapi\_architecture\_id\_t architecture\_id, uint64\_t dwarf\_register, amd\_dbgapi\_register\_id\_t \*register\_id) AMD\_DBGAPI\_VERSION\_0\_41
   Return a register handle from an AMD GPU DWARF register number for an architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_register\_is\_in\_register\_class (amd\_dbgapi\_register\_class\_id\_t register\_class\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_register\_class\_state\_t \*register\_class\_ state\_) AMD\_DBGAPI\_VERSION\_0\_41

Determine if a register is a member of a register class.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_read\_register (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_size\_t offset, amd\_dbgapi\_size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41
   Read a register.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_write\_register (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_size\_t offset, amd\_dbgapi\_size\_t value\_size, const void \*value) AMD\_DBGAPI\_VERSION\_0\_42
   Write a register.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_prefetch\_register (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_size\_t register\_count) AMD\_DBGAPI\_VERSION\_0\_41
   Prefetch register values.

# 2.15.1 Detailed Description

Operations related to AMD GPU register access.

## 2.15.2 Macro Definition Documentation

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## 2.15.2.1 AMD\_DBGAPI\_REGISTER\_CLASS\_NONE

```
#define AMD_DBGAPI_REGISTER_CLASS_NONE ((amd_dbgapi_register_class_id_t) { 0 })
```

The NULL register class handle.

## 2.15.2.2 AMD\_DBGAPI\_REGISTER\_NONE

```
#define AMD_DBGAPI_REGISTER_NONE ((amd_dbgapi_register_id_t){ 0 })
```

The NULL register handle.

## 2.15.3 Enumeration Type Documentation

## 2.15.3.1 amd\_dbgapi\_register\_class\_info\_t

```
enum amd_dbgapi_register_class_info_t
```

Register class queries that are supported by amd\_dbgapi\_architecture\_register\_class\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_architecture\_register\_class\_get\_info.

## **Enumerator**

Return the architecture to which this register class
belongs. The type of this attribute is
amd_dbgapi_architecture_id_t.
Return the register class name. The type of this attribute is a pointer to a NUL terminated char. It is allocated by
the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.

## 2.15.3.2 amd\_dbgapi\_register\_class\_state\_t

```
enum amd_dbgapi_register_class_state_t
```

Indication of whether a register is a member of a register class.

## Enumerator

AMD_DBGAPI_REGISTER_CLASS_STATE_NOT_MEMBER	The register is not a member of the register class.
AMD_DBGAPI_REGISTER_CLASS_STATE_MEMBER	The register is a member of the register class.

# 2.15.3.3 amd\_dbgapi\_register\_exists\_t

enum amd\_dbgapi\_register\_exists\_t

Indication of if a wave has a register.

#### Enumerator

AMD_DBGAPI_REGISTER_ABSENT	The wave does not have the register.
AMD_DBGAPI_REGISTER_PRESENT	The wave has the register.

# 2.15.3.4 amd\_dbgapi\_register\_info\_t

enum amd\_dbgapi\_register\_info\_t

Register queries that are supported by amd\_dbgapi\_register\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_register\_get\_info.

AMD_DBGAPI_REGISTER_INFO_ARCHITECTURE	Return the architecture to which this register belongs.  The type of this attribute is  amd_dbgapi_architecture_id_t.
AMD_DBGAPI_REGISTER_INFO_NAME	Return the register name. The type of this attribute is a pointer to a NUL terminated char. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.
AMD_DBGAPI_REGISTER_INFO_SIZE	Return the size of the register in bytes. The type of this attribute is amd_dbgapi_size_t.

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## Enumerator

AMD_DBGAPI_REGISTER_INFO_TYP	E Return the register type as a C style type string. This can be used as the default type to use when displaying values of the register. The type string syntax is defined by the following BNF syntax:	
	<pre>type ::= integer_type   float_type   array_type   function_t integer_type ::= "uint32"   "uint64" float_type ::= "float"   "double"</pre>	уре
	<pre>array_type ::= ( integer_type   float_type ) "[" integer "]" function_type ::= "void(void)" integer ::= digit   ( digit integer )</pre>	
	digit ::= "0"   "1"   "2"   "3"   "4"   "5"   "6"   "7"   "8"	'
	The type size matches the size of the register. uint32	
	and float types are 4 bytes. unit64 and double	
	types are 8 bytes. void (void) is the size of a global address.	
	The type of this attribute is a pointer to a NUL	
	terminated char. It is allocated by the	
	amd_dbgapi_callbacks_s::allocate_memory callback	
	and is owned by the client.	
AMD_DBGAPI_REGISTER_INFO_DWAR	F Return the AMD GPU DWARF register number for the register's architecture. The type of this attribute is	
	uint64_t.	

## 2.15.4 Function Documentation

# 2.15.4.1 amd\_dbgapi\_architecture\_register\_class\_get\_info()

Query information about a register class of an architecture.

amd\_dbgapi\_register\_class\_info\_t specifies the queries supported and the type returned using the value argument.

#### **Parameters**

in	register_class⊷	The handle of the register class being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query
		result.
out	value	Pointer to memory where the query result is stored.

## Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER	<u>@leAgSSstt@</u> r_class_id is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vGQM€AstBuld toles not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

# 2.15.4.2 amd\_dbgapi\_architecture\_register\_class\_list()

Report the list of register classes supported by the architecture.

The order of the register handles in the list is stable between calls.

# **Parameters**

in	architecture_id	The architecture being queried.
out	register_class_count	The number of architecture register classes.
out	register_classes	A pointer to an array of amd_dbgapi_register_class_id_t with
		register_class_count elements. It is allocated by the
		amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in register_class_count and
	register_classes.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	and register_class_count and
	register_classes <b>are unaltered</b> .

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### **Return values**

AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and register_class_count and
	register_classes <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	TldREnDtecture_id is invalid.
	register_class_count and
	register_classes <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	register_class_count <b>or</b>
	register_classes <b>are NULL</b> .
	register_class_count and
	register_classes <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate register_classes returns NULL.
	register_class_count and
	register_classes <b>are unaltered</b> .

# 2.15.4.3 amd\_dbgapi\_architecture\_register\_list()

Report the list of registers supported by the architecture.

This list is all the registers the architecture can support, but a specific wave may not have all these registers. For example, AMD GPU architectures can specify the number of vector and scalar registers when a wave is created. Use the <a href="mailto:amd\_dbgapi\_wave\_register\_list">amd\_dbgapi\_wave\_register\_list</a> operation to determine the registers supported by a specific wave.

The order of the register handles in the list is stable between calls and registers on the same major class are contiguous in ascending hardware number order.

#### **Parameters**

in	architecture←	The architecture being queried.	
	_id		
out	register_count	The number of architecture registers.	
out	registers	A pointer to an array of amd_dbgapi_register_id_t with register_count elements. It	
		is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by	
		the client.	

### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in register_count and registers.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	<pre>and register_count and registers are</pre>
	unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and register_count and
	registers <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	TIARENDtecture_id is invalid. register_count
	and registers are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	register_count <b>or</b> registers <b>are NULL</b> .
	register_count and registers are unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate registers returns NULL.
	register_count <b>and</b> registers <b>are unaltered</b> .

# 2.15.4.4 amd\_dbgapi\_dwarf\_register\_to\_register()

Return a register handle from an AMD GPU DWARF register number for an architecture.

The AMD GPU DWARF register number must be valid for the architecture.

```
See User Guide for AMDGPU Backend - Code Object - DWARF - Register Mapping.
```

# Parameters

	in	architecture↔ _id	The architecture of the DWARF register.
Ī	in	dwarf_register	
Ī	out <i>register_id</i>		The register handle that corresponds to the DWARF register ID.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in register_id.
	A fatal error occurred. The library is left uninitialized and register_id is unaltered.

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### Return values

AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and register_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	Tlane_nlDtecture_id is invalid. register_id is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tregister_id is NULL. register_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_ <b>c©@M₽<u>A</u>ŦĿ</b> ijIJ₹¥er is not valid for the
	architecture_id.register_id is unaltered.

### 2.15.4.5 amd dbgapi prefetch register()

# Prefetch register values.

A hint to indicate that a range of registers may be read using amd\_dbgapi\_read\_register in the future. This can improve the performance of reading registers as the library may be able to batch the prefetch requests into one request.

The wave must be stopped. The register and wave must both belong to the same architecture, and the wave must have allocated that register.

If the wave is resumed, then any prefetch requests for registers that were not subsequently read may be discarded and so provide no performance benefit. Prefetch requests for registers that are never subsequently read may in fact reduce performance.

The registers to prefetch are specified as the first register and the number of registers. The first register can be any register supported by the wave. The number of registers is in terms of the wave register order returned by amd\_dbgapi\_wave\_register\_list. If the number exceeds the number of wave registers, then only up to the last wave register is prefetched.

#### **Parameters**

in	wave_id	The wave being queried for the register.
in	register_id	The first register being requested.
in	register_count	The number of registers being requested.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully. Registers may be prefetched.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized.

### Return values

AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. No registers are prefetched.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER	//Degister_id is invalid. No registers are prefetched.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	E₩ave_id is not stopped. No registers are prefetched.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	/_TCh@lancAiteStures of wave_id and register_id
	are not the same, or register_id is not allocated for
	wave_id. No registers are prefetched.

# 2.15.4.6 amd\_dbgapi\_read\_register()

```
amd_dbgapi_status_t AMD_DBGAPI amd_dbgapi_read_register (
    amd_dbgapi_wave_id_t wave_id,
    amd_dbgapi_register_id_t register_id,
    amd_dbgapi_size_t offset,
    amd_dbgapi_size_t value_size,
    void * value )
```

# Read a register.

value\_size bytes are read from the register starting at offset into value.

The wave must be stopped. The register and wave must both belong to the same architecture, and the wave must have allocated that register.

The register size can be obtained using amd\_dbgapi\_register\_get\_info with the AMD\_DBGAPI\_REGISTER\_INFO\_SIZE query.

# Parameters

in	wave_id	The wave to being queried for the register.
in	register⊷ _id	The register being requested.
in	offset	The first byte to start reading the register. The offset is zero based starting from the least significant byte of the register.
in	value_size	The number of bytes to read from the register which must be greater than 0 and less than the size of the register minus offset.
out	value	The bytes read from the register. Must point to an array of at least value_size bytes.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and value is set to value_size bytes starting at offset from the contents of the register.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and value is unaltered.

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## **Return values**

AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER	/Degister_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	<b>E</b> wave_id is not stopped. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	value is NULL or value_size is 0. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_ <b>COME</b> A <b>₹IB</b> Le/T <b>s</b> ∕greater than the size of the
	register_id minus offset, the architectures of
	wave_id and register_id are not the same, or
	register_id is not allocated for wave_id. value
	is unaltered.

# 2.15.4.7 amd\_dbgapi\_register\_get\_info()

Query information about a register.

amd\_dbgapi\_register\_info\_t specifies the queries supported and the type returned using the value argument.

# **Parameters**

in	register⊷ _id	The handle of the register being queried.
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.
out	value	Pointer to memory where the query result is stored.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER	<b>/D</b> egister_id <b>is invalid for</b> architecture_id.
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	value is NULL, or query is invalid or not supported
	for an architecture. value is unaltered.

## Return values

AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_xCOMEA \$ IBUI⊲ Tobes not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

# 2.15.4.8 amd\_dbgapi\_register\_is\_in\_register\_class()

Determine if a register is a member of a register class.

The register and register class must both belong to the same architecture.

### **Parameters**

in	register_class_id	The handle of the register class being queried.
in	register_id	The handle of the register being queried.
out	register_class_state	AMD_DBGAPI_REGISTER_CLASS_STATE_NOT_MEMBER if the register is not in the register class. AMD_DBGAPI_REGISTER_CLASS_STATE_MEMBER if the register is in the register class.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in register_class_state.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	register_class_state is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and register_class_state is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER	<b>/D</b> egister_id <b>is invalid</b> .
	register_class_state is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER	<u>@le∕tsiSs_tl@</u> r_class_id is invalid.
	register_class_state is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tregister_class_state is NULL.
	register_class_state is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	7_TCA⊖/ancAiTeCturveSyof register_class_id and
	register_id are not the same.
	register_class_state is unaltered.

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## 2.15.4.9 amd\_dbgapi\_wave\_register\_exists()

Query if a register exists for a wave.

The register and wave must both belong to the same architecture.

### **Parameters**

in	wave_id	The wave being queried.
in	register↔ _id	The register being queried.
out	exists	<pre>Indication of whether wave_id has register_id.</pre>

### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in exists.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	exists is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and exists is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. exists is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER	/Degister_id is invalid. exists is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	rexists is NULL. exists is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	/_TCA@/a/ccAiTcEtU/cES of wave_id and register_id
	are not the same. exists is unaltered.

### 2.15.4.10 amd dbgapi wave register list()

Report the list of registers supported by a wave.

This list is the registers allocated for a specific wave and may not be all the registers supported by the architecture. For example, AMD GPU architectures can specify the number of vector and scalar registers when a wave is created. Use the amd\_dbgapi\_architecture\_register\_list operation to determine the full set of registers supported by the architecture.

The order of the register handles in the list is stable between calls. It is equal to, or a subset of, those returned by <a href="mailto:amd\_dbgapi\_architecture\_register\_list">amd\_dbgapi\_architecture\_register\_list</a> and in the same order.

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### **Parameters**

in	wave_id	The wave being queried.
out	register_count	The number of wave registers.
out	registers	A pointer to an array of amd_dbgapi_register_id_t with register_count elements. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.

#### **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in register_count and registers.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized; and register_count and registers are
	unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and register_count and
	registers <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. register_count and
	registers <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	register_count <b>or</b> registers <b>are NULL</b> .
	register_count and registers are unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate registers returns NULL.
	register_count and registers are unaltered.

# 2.15.4.11 amd\_dbgapi\_write\_register()

## Write a register.

value\_size bytes are written into the register starting at offset.

The wave must be stopped. The register and wave must both belong to the same architecture, and the wave must have allocated that register. The wave must not have an active displaced stepping buffer (see amd\_dbgapi\_displaced\_stepping\_start) as the program counter and other registers may be changed as part of creating the displaced stepping buffer.

The register size can be obtained using amd\_dbgapi\_register\_get\_info with the AMD\_DBGAPI\_REGISTER\_INFO\_SIZE query.

# **Parameters**

in	wave_id	The wave to being queried for the register.
in	register <i>⊷</i> id	The register being requested.
in	offset	The first byte to start writing the register. The offset is zero based starting from the least significant byte of the register.
in	value_size	The number of bytes to write to the register which must be greater than 0 and less than the size of the register minus offset.
in	value	The bytes to write to the register. Must point to an array of at least value_size bytes.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and value_size bytes have been written to the contents of the register starting at offset.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and the register is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized. The register is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. The register is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER	/Degister_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	EMave_id is not stopped. The register is unaltered.
AMD_DBGAPI_STATUS_ERROR_DISPLACED_STEPP	Noa A€_T11/dEhas an active displaced stepping buffer.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	value is NULL or value_size is 0. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_√COMEAstBulle Ts greater than the size of the
	register_id minus offset, the architectures of
	wave_id and register_id are not the same, or
	register_id is not allocated for wave_id. value
	is unaltered.

# 2.16 Memory

Operations related to AMD GPU memory access.

### **Data Structures**

```
• struct amd_dbgapi_address_class_id_t
```

Opaque source language address class handle.

struct amd\_dbgapi\_address\_space\_id\_t

Opaque address space handle.

### **Macros**

```
#define AMD_DBGAPI_LANE_NONE ((amd_dbgapi_lane_id_t) (-1))
```

The NULL lane handle.

#define AMD\_DBGAPI\_ADDRESS\_CLASS\_NONE ((amd\_dbgapi\_address\_class\_id\_t){ 0 })

The NULL address class handle.

#define AMD\_DBGAPI\_ADDRESS\_SPACE\_NONE ((amd\_dbgapi\_address\_space\_id\_t){ 0 })

The NULL address space handle.

• #define AMD\_DBGAPI\_ADDRESS\_SPACE\_GLOBAL ((amd\_dbgapi\_address\_space\_id\_t){ 1 })

The global address space handle.

# **Typedefs**

typedef uint32\_t amd\_dbgapi\_lane\_id\_t

A wave lane handle.

typedef uint64\_t amd\_dbgapi\_segment\_address\_t

Each address space has its own linear address to access it termed a segment address.

### **Enumerations**

 enum amd\_dbgapi\_address\_class\_info\_t { AMD\_DBGAPI\_ADDRESS\_CLASS\_INFO\_ARCHITECTURE = 1, AMD\_DBGAPI\_ADDRESS\_CLASS\_INFO\_NAME = 2, AMD\_DBGAPI\_ADDRESS\_CLASS\_INFO\_ADDRESS\_SPACE = 3, AMD\_DBGAPI\_ADDRESS\_CLASS\_INFO\_DWARF = 4 }

Source language address class queries that are supported by amd\_dbgapi\_address\_class\_get\_info.

 enum amd\_dbgapi\_address\_space\_access\_t { AMD\_DBGAPI\_ADDRESS\_SPACE\_ACCESS\_ALL = 1, AMD\_DBGAPI\_ADDRESS\_SPACE\_ACCESS\_PROGRAM\_CONSTANT = 2, AMD\_DBGAPI\_ADDRESS\_SPACE\_ACCESS\_DISF = 3 }

Indication of how the address space is accessed.

enum amd\_dbgapi\_address\_space\_info\_t {
 AMD\_DBGAPI\_ADDRESS\_SPACE\_INFO\_ARCHITECTURE = 1, AMD\_DBGAPI\_ADDRESS\_SPACE\_INFO\_NAME
 = 2, AMD\_DBGAPI\_ADDRESS\_SPACE\_INFO\_ADDRESS\_SIZE = 3, AMD\_DBGAPI\_ADDRESS\_SPACE\_INFO\_NULL\_ADDRES
 = 4,
 AMD\_DBGAPI\_ADDRESS\_SPACE\_INFO\_ACCESS = 5, AMD\_DBGAPI\_ADDRESS\_SPACE\_INFO\_DWARF =
 6 }

Address space queries that are supported by amd\_dbgapi\_address\_space\_get\_info.

enum amd\_dbgapi\_address\_space\_alias\_t { AMD\_DBGAPI\_ADDRESS\_SPACE\_ALIAS\_NONE = 0, AMD\_DBGAPI\_ADDRESS\_S
 = 1 }

Indication of whether addresses in two address spaces may alias.

 enum amd\_dbgapi\_address\_class\_state\_t { AMD\_DBGAPI\_ADDRESS\_CLASS\_STATE\_NOT\_MEMBER = 0, AMD\_DBGAPI\_ADDRESS\_CLASS\_STATE\_MEMBER = 1 }

Indication of whether a segment address in an address space is a member of an source language address class.

enum amd\_dbgapi\_memory\_precision\_t { AMD\_DBGAPI\_MEMORY\_PRECISION\_NONE = 0, AMD\_DBGAPI\_MEMORY\_PRECIS
 = 1 }

Memory access precision.

# **Functions**

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_address\_class\_get\_info (amd\_dbgapi\_address\_class\_id\_t address\_class\_id, amd\_dbgapi\_address\_class\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_46
   Query information about a source language address class of an architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_address\_class\_list (amd\_dbgapi\_architecture\_id\_t architecture\_id, size\_t \*address\_class\_count, amd\_dbgapi\_address\_class\_id\_t \*\*address\_classes) AMD\_DBGAPI\_VERSION\_0\_
   Report the list of source language address classes supported by the architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_dwarf\_address\_class\_to\_address\_class (amd\_dbgapi\_architecture\_id\_t architecture\_id, uint64\_t dwarf\_address\_class, amd\_dbgapi\_address\_class\_id\_t\*address\_class\_id) AMD\_DBGAPI\_VERSION\_0\_

Return the architecture source language address class from a DWARF address class number for an architecture.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_address\_space\_get\_info (amd\_dbgapi\_address\_space\_id\_t address\_space\_id, amd\_dbgapi\_address\_space\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_46
   Query information about an address space.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_address\_space\_list (amd\_dbgapi\_architecture\_id\_t architecture\_id, size\_t \*address\_space\_count, amd\_dbgapi\_address\_space\_id\_t \*\*address\_spaces) AMD\_DBGAPI\_VERSION\_0
   Report the list of address spaces supported by the architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_dwarf\_address\_space\_to\_address\_space (amd\_dbgapi\_architecture\_id\_t architecture\_id, uint64\_t dwarf\_address\_space, amd\_dbgapi\_address\_space\_id\_t \*address\_space\_id)
   AMD\_DBGAPI\_VERSION\_0\_41

Return the address space from an AMD GPU DWARF address space number for an architecture.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_address\_spaces\_may\_alias (amd\_dbgapi\_address\_space\_id\_t address\_space\_id1, amd\_dbgapi\_address\_space\_id\_t address\_space\_id2, amd\_dbgapi\_address\_space\_alias\_t \*address\_space\_alias) AMD\_DBGAPI\_VERSION\_0\_41

Determine if an address in one address space may alias an address in another address space.

 amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_convert\_address\_space (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_lane\_id\_t lane\_id, amd\_dbgapi\_address\_space\_id\_t source\_address\_space\_id, amd\_dbgapi\_segment\_address\_t source\_segment\_address, amd\_dbgapi\_address\_space\_id\_t destination\_address\_space\_id, amd\_dbgapi\_segment\_address\_t \*destination\_segment\_address) AMD\_DBGAPI\_VERSION\_0\_41

Convert a source segment address in the source address space into a destination segment address in the destination address space.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_address\_is\_in\_address\_class (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_lane\_id\_t lane\_id, amd\_dbgapi\_address\_space\_id\_t address\_space\_id, amd\_dbgapi\_segment\_address\_t segment\_address, amd\_dbgapi\_address\_class\_id\_t address\_class\_id, amd\_dbgapi\_address\_class\_state\_t \*address\_class\_state) AMD\_DBGAPI\_VERSION\_0\_41

Determine if a segment address in an address space is a member of a source language address class.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_read\_memory (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_lane\_id\_t lane\_id, amd\_dbgapi\_address\_space\_id\_t address
 \_space\_id, amd\_dbgapi\_segment\_address\_t segment\_address, amd\_dbgapi\_size\_t \*value\_size, void \*value)
 AMD\_DBGAPI\_VERSION\_0\_41

Read memory.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_write\_memory (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_lane\_id\_t lane\_id, amd\_dbgapi\_address\_space\_id\_t address
 \_space\_id, amd\_dbgapi\_segment\_address\_t segment\_address, amd\_dbgapi\_size\_t \*value\_size, const void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Write memory.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_set\_memory\_precision (amd\_dbgapi\_process\_id\_t process
id, amd\_dbgapi\_memory\_precision t memory\_precision) AMD\_DBGAPI\_VERSION\_0\_24

Control precision of memory access reporting.

# 2.16.1 Detailed Description

Operations related to AMD GPU memory access.

The AMD GPU supports allocating memory in different address spaces. See User Guide for AMDGPU Backend - LLVM - Address Spaces.

## 2.16.2 Macro Definition Documentation

### 2.16.2.1 AMD\_DBGAPI\_ADDRESS\_CLASS\_NONE

```
#define AMD_DBGAPI_ADDRESS_CLASS_NONE ((amd_dbgapi_address_class_id_t){ 0 })
```

The NULL address class handle.

# 2.16.2.2 AMD\_DBGAPI\_ADDRESS\_SPACE\_GLOBAL

```
#define AMD_DBGAPI_ADDRESS_SPACE_GLOBAL ((amd_dbgapi_address_space_id_t) { 1 })
```

The global address space handle.

Every architecture supports a global address space that uses the same address space ID.

## 2.16.2.3 AMD\_DBGAPI\_ADDRESS\_SPACE\_NONE

```
#define AMD_DBGAPI_ADDRESS_SPACE_NONE ((amd_dbgapi_address_space_id_t) { 0 })
```

The NULL address space handle.

# 2.16.2.4 AMD\_DBGAPI\_LANE\_NONE

```
#define AMD_DBGAPI_LANE_NONE ((amd_dbgapi_lane_id_t) (-1))
```

The NULL lane handle.

# 2.16.3 Typedef Documentation

## 2.16.3.1 amd\_dbgapi\_lane\_id\_t

```
typedef uint32_t amd_dbgapi_lane_id_t
```

A wave lane handle.

A wave can have one or more lanes controlled by an execution mask. Vector instructions will be performed for each lane of the wave that the execution mask has enabled. Vector instructions can access registers that are vector registers. A vector register has a separate value for each lane, and vector instructions will access the corresponding component for each lane's evaluation of the instruction.

The number of lanes of a wave can be obtained with the AMD\_DBGAPI\_WAVE\_INFO\_LANE\_COUNT query. Different waves of the same architecture can have different lane counts.

The AMD GPU compiler may map source language threads of execution to lanes of a wave. The DWARF debug information which maps such source languages to the generated architecture specific code must include information about the lane mapping.

The DW\_ASPACE\_AMDGPU\_private\_lane DWARF address space supports memory allocated independently for each lane of a wave.

Lanes are numbered from 0 to AMD\_DBGAPI\_WAVE\_INFO\_LANE\_COUNT minus 1.

Only unique for a single wave of a single process.

## 2.16.3.2 amd\_dbgapi\_segment\_address\_t

```
typedef uint64_t amd_dbgapi_segment_address_t
```

Each address space has its own linear address to access it termed a segment address.

Different address spaces may have memory locations that alias each other, but the segment address for such memory locations may be different in each address space. Consequently a segment address is specific to an address space.

Some address spaces may access memory that is allocated independently for each work-group, for each wave, or for each lane of of a wave. Consequently a segment address may be specific to a wave or lane of a wave.

See User Guide for AMDGPU Backend - LLVM - Address Spaces.

# 2.16.4 Enumeration Type Documentation

# 2.16.4.1 amd\_dbgapi\_address\_class\_info\_t

enum amd\_dbgapi\_address\_class\_info\_t

Source language address class queries that are supported by amd\_dbgapi\_address\_class\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_address\_class\_get\_info.

## Enumerator

AMD_DBGAPI_ADDRESS_CLASS_INFO_ARCHIT← ECTURE	Return the architecture to which this address class belongs. The type of this attribute is amd_dbgapi_architecture_id_t.
AMD_DBGAPI_ADDRESS_CLASS_INFO_NAME	Return the source language address class name. The type of this attribute is a pointer to a NUL terminated char. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.
AMD_DBGAPI_ADDRESS_CLASS_INFO_ADDRES↔ S_SPACE	Return the architecture specific address space that is used to implement a pointer or reference to the source language address class. The type of this attribute is amd_dbgapi_address_class_id_t.  See User Guide for AMDGPU Backend - Code Object - DWARF - Address Class Mapping.
AMD_DBGAPI_ADDRESS_CLASS_INFO_DWARF	Return the AMD GPU DWARF address class number for the address class' architecture. The type of this attribute is uint64_t.

# 2.16.4.2 amd\_dbgapi\_address\_class\_state\_t

enum amd\_dbgapi\_address\_class\_state\_t

Indication of whether a segment address in an address space is a member of an source language address class.

## Enumerator

AMD_DBGAPI_ADDRESS_CLASS_STATE_NOT_← MEMBER	The segment address in the address space is not a member of the source language address class.
AMD_DBGAPI_ADDRESS_CLASS_STATE_MEMBER	The segment address in the address space is a member of the source language address class.

# 2.16.4.3 amd\_dbgapi\_address\_space\_access\_t

enum amd\_dbgapi\_address\_space\_access\_t

Indication of how the address space is accessed.

## Enumerator

AMD_DBGAPI_ADDRESS_SPACE_ACCESS_ALL	The address space supports all accesses. Values accessed can change during the lifetie of the program.
AMD_DBGAPI_ADDRESS_SPACE_ACCESS_PRO↔ GRAM_CONSTANT	The address space is read only. Values accessed are always the same value for the lifetime of the program execution.
AMD_DBGAPI_ADDRESS_SPACE_ACCESS_DISP↔ ATCH_CONSTANT	The address space is only read the waves of a kernel dispatch. Values accessed are always the same value for the lifetime of the dispatch.

# 2.16.4.4 amd\_dbgapi\_address\_space\_alias\_t

enum amd\_dbgapi\_address\_space\_alias\_t

Indication of whether addresses in two address spaces may alias.

## Enumerator

AMD_DBGAPI_ADDRESS_SPACE_ALIAS_NONE	No addresses in the address spaces can alias.
AMD_DBGAPI_ADDRESS_SPACE_ALIAS_MAY	Addresses in the address spaces may alias.

# 2.16.4.5 amd\_dbgapi\_address\_space\_info\_t

 $\verb"enum" amd_dbgapi_address_space_info_t"$ 

Address space queries that are supported by amd\_dbgapi\_address\_space\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_address\_space\_get\_info.

AMD_DBGAPI_ADDRESS_SPACE_INFO_ARCHIT↔ ECTURE	Return the architecture to which this address space belongs. The type of this attribute is amd_dbgapi_architecture_id_t.
AMD_DBGAPI_ADDRESS_SPACE_INFO_NAME	Return the address space name. The type of this attribute is a pointer to a NUL terminated char*. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.
AMD_DBGAPI_ADDRESS_SPACE_INFO_ADDRE↔ SS_SIZE	Return the byte size of an address in the address space. The type of this attribute is amd_dbgapi_size_t.

### Enumerator

AMD_DBGAPI_ADDRESS_SPACE_INFO_NULL_A↔ DDRESS	Return the NULL segment address value in the address space. The type of this attribute is amd_dbgapi_segment_address_t.
AMD_DBGAPI_ADDRESS_SPACE_INFO_ACCESS	Return the address space access. The type of this attribute is uint32_t with values defined by amd_dbgapi_address_space_access_t.
AMD_DBGAPI_ADDRESS_SPACE_INFO_DWARF	Return the AMD GPU DWARF address space number for the address space's architecture. The type of this attribute is uint64_t.

## 2.16.4.6 amd\_dbgapi\_memory\_precision\_t

enum amd\_dbgapi\_memory\_precision\_t

Memory access precision.

The AMD GPU can overlap the execution of memory instructions with other instructions. This can result in a wave stopping due to a memory violation or hardware data watchpoint hit with a program counter beyond the instruction that caused the wave to stop.

Some architectures allow the hardware to be configured to always wait for memory operations to complete before continuing. This will result in the wave stopping at the instruction immediately after the one that caused the stop event. Enabling this mode can make execution of waves significantly slower.

The AMD\_DBGAPI\_PROCESS\_INFO\_PRECISE\_MEMORY\_SUPPORTED query can be used to determine if the architectures of all the agents of a process support controlling precise memory accesses.

## Enumerator

AMD_DBGAPI_MEMORY_PRECISION_NONE	Memory instructions execute normally and a wave does not wait for the memory access to complete.
AMD_DBGAPI_MEMORY_PRECISION_PRECISE	A wave waits for memory instructions to complete before executing further instructions. This can cause a wave to execute significantly slower.

## 2.16.5 Function Documentation

### 2.16.5.1 amd\_dbgapi\_address\_class\_get\_info()

```
amd_dbgapi_address_class_info_t query,
size_t value_size,
void * value )
```

Query information about a source language address class of an architecture.

amd\_dbgapi\_address\_class\_info\_t specifies the queries supported and the type returned using the value argument.

#### **Parameters**

in	address_class⇔	The handle of the source language address class being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query
		result.
out	value	Pointer to memory where the query result is stored.

#### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	Cald 66 etcclass_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vCOMEA \$1841 does not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

# 2.16.5.2 amd\_dbgapi\_address\_is\_in\_address\_class()

Determine if a segment address in an address space is a member of a source language address class.

The address space and source language address class must both belong to the same architecture.

The address space, source language address class, and wave must all belong to the same architecture.

## **Parameters**

in	wave_id	The wave that is using the address.
in	lane_id	The lane of the wave_id that is using the address.
in	address_space_id	The address space of the <code>segment_address</code> . If the address space is dependent on: the active lane then the <code>lane_id</code> with in the <code>wave_id</code> is used; the active work-group then the work-group of <code>wave_id</code> is used; or the active wave then the <code>wave_id</code> is used.
in	segment_address	The integral value of the segment address. Only the bits corresponding to the address size for the address_space requested are used. The address size is provided by the AMD_DBGAPI_ADDRESS_SPACE_INFO_ADDRESS_SIZE query.
in	address_class_id	The handle of the source language address class.
out	address_class_state	AMD_DBGAPI_ADDRESS_CLASS_STATE_NOT_MEMBER if the address is not in the address class. AMD_DBGAPI_ADDRESS_CLASS_STATE_MEMBER if the address is in the address class.

## **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in address_class_state.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	address_class_state is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and address_class_state is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid. address_class_state is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_LANE_ID	lane_id is invalid. address_class_state is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	SRACE_LS_space_id is invalid.
	address_class_state is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	Calas_etDs_class_id is invalid.
	address_class_state is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Taddress_class_state is NULL.
	address_class_state is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	/_TCAO/anchiteByures/of wave_id,
	address_space_id, <b>and</b> address_class_id
	are not the same. address_class_state is
	unaltered.

# 2.16.5.3 amd\_dbgapi\_address\_space\_get\_info()

```
size_t value_size,
void * value )
```

Query information about an address space.

amd\_dbgapi\_address\_space\_info\_t specifies the queries supported and the type returned using the value argument.

### **Parameters**

in	address_space←	The address space.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the
		query result.
out	value	Pointer to memory where the query result is stored.

### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	1 0.2 0 0 0.0 0.0 0.0
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	S&ACE_BS_space_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tquery is invalid or value is NULL. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_xCOMEA \$ IBUI Tobes not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

## 2.16.5.4 amd\_dbgapi\_address\_spaces\_may\_alias()

Determine if an address in one address space may alias an address in another address space.

If addresses in one address space may alias the addresses in another, and if memory locations are updated using an address in one, then any cached information about values in the other needs to be invalidated.

The address spaces must belong to the same the architecture.

### **Parameters**

ſ	in	address_space_id1	An address space.
	in	address_space_id2	An address space.
	out	address_space_alias	AMD_DBGAPI_ADDRESS_SPACE_ALIAS_NONE if the address spaces do not alias. AMD_DBGAPI_ADDRESS_SPACE_ALIAS_MAY if the address spaces
			may alias.

## **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in address_space_alias.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and address_space_alias is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and address_space_alias is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	SPACE LS_space_id1 or address_space_id2 are invalid. address_space_alias is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Taddress_space_alias is NULL. address_space_alias is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_TDO MICHITEDIU/ES of address_space_id1 and address_space_id2 are not the same. address_space_alias is unaltered.

# 2.16.5.5 amd\_dbgapi\_architecture\_address\_class\_list()

Report the list of source language address classes supported by the architecture.

The order of the source language address class handles in the list is stable between calls.

## **Parameters**

i	.n	architecture_id	The architecture being queried.	
0	ut	address_class_count	The number of architecture source language address classes.	
0	ut	address_classes	A pointer to an array of amd_dbgapi_address_class_id_t with	
			address_class_count elements. It is allocated by the	
			amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.	

## **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in address_class_count and
	address_classes.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	<pre>and address_class_count and</pre>
	address_classes are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and address_class_count and
	address_classes <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	TlanenDtecture_id is invalid.
	address_class_count <b>and</b>
	address_classes <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Taddress_class_count <b>or</b> address_classes
	are NULL. address_class_count and
	address_classes are unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate address_classes returns NULL.
	address_class_count <b>and</b>
	address_classes are unaltered.

# 2.16.5.6 amd\_dbgapi\_architecture\_address\_space\_list()

Report the list of address spaces supported by the architecture.

The order of the address space handles in the list is stable between calls.

## **Parameters**

in	architecture_id	The architecture being queried.
out	address_space_count	The number of architecture address spaces.
out	address_spaces	A pointer to an array of amd_dbgapi_address_space_id_t with address_space_count elements. It is allocated by the amd_dbgapi_callbacks_s::allocate_memory callback and is owned by the client.

AMD_DBGAPI_STATUS_SUCCE	The function has been executed successfully and the result is stored in address_space_count and address_spaces.
-------------------------	---

### Return values

AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	and address_space_count and
	address_spaces are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and address_space_count and
	address_spaces are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	TURENDtecture_id is invalid.
	address_space_count <b>and</b> address_spaces
	are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Taddress_space_count <b>and</b> address_spaces
	are NULL. address_space_count and
	address_spaces are unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate address_spaces returns NULL.
	address_space_count <b>and</b> address_spaces
	are unaltered.

## 2.16.5.7 amd\_dbgapi\_convert\_address\_space()

Convert a source segment address in the source address space into a destination segment address in the destination address space.

The address spaces must belong to the same the architecture.

If the source segment address is the NULL value in the source address space then it is converted to the NULL value in the destination address space. The NULL address is provided by the AMD\_DBGAPI\_ADDRESS\_SPACE\_INFO\_NULL\_ADDRESS query.

An error is returned if the source segment address has no corresponding segment address in the destination address space. The source and destination address spaces must have the same linear ordering. For example, a swizzled address space is not the same linear ordering as an unswizzled address space. The source and destination address spaces must either both depend on the active lane, both depend on the same lane, or both not depend on the lane.

### **Parameters**

in	wave_id	The wave that is using the address.
in	lane_id	The lane of the wave_id that is using the address.

# **Parameters**

in	source_address_space_id	The address space of the source_segment_address.
in	source_segment_address	The integral value of the source segment address. Only the bits
		corresponding to the address size for the
		source_address_space_id requested are used. The address
		size is provided by the
		AMD_DBGAPI_ADDRESS_SPACE_INFO_ADDRESS_SIZE query.
in	destination_address_space←	The address space to which to convert source_segment_address
	_id	that is in source_address_space_id.
out	destination_segment_address	The integral value of the segment address in
		destination_address_space_id that corresponds to
		source_segment_address in source_address_space_id.
		The bits corresponding to the address size for the
		destination_address_space_id are updated, and any remaining bits are set to zero. The address size is provided by the AMD_DBGAPI_ADDRESS_SPACE_INFO_ADDRESS_SIZE query.

AMD DBGAPI STATUS SUCCESS	The function has been executed successfully and the
71101B_BB4711 1_07717 00_0000200	result is stored in
	destination_segment_address.
AMD DBGAPI STATUS FATAL	A fatal error occurred. The library is left uninitialized and
AINID_DDGAI I_STATOS_TATAL	destination_segment_address is unaltered.
AMD DROAD! STATUS EDDOR NOT INITIALIZED	
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and
	destination_segment_address is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid.
	destination_segment_address is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_LANE_ID	lane_id is invalid, or lane_id is
	AMD_DBGAPI_LANE_NONE and
	source_address_space_id depends on the
	active lane. destination_segment_address is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	SPACEc#D_address_space_id or
	destination_address_space_id are invalid.
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	SFMC & COM ESS MANT_address in the
	source_address_space_id is not an address
	that can be represented in the
	destination_address_space_id.
	destination_segment_address is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	/destination_segment_address is NULL.
	destination_segment_address is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	/_TBe/a/chitebyu/es/of wave_id,
	source_address_space_id, and
	destination_address_space_id are not the
	same. destination_segment_address is
	unaltered.

## 2.16.5.8 amd\_dbgapi\_dwarf\_address\_class\_to\_address\_class()

Return the architecture source language address class from a DWARF address class number for an architecture.

The AMD GPU DWARF address class number must be valid for the architecture.

See User Guide for AMDGPU Backend - Code Object - DWARF - Address Class Mapping.

#### **Parameters**

in	architecture_id	The architecture of the source language address class.	
in	dwarf_address_class	The DWARF source language address class.	
out	address_class_id	The source language address class that corresponds to the DWARF address class for the architecture.	

## **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in address_class_id.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and address_class_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and address_class_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	TlamenDtecture_id is invalid. address_class_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Taddress_class_id is NULL. address_class_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_@@MPA####de¥s_class is not valid for the architecture_id. address_class_id is unaltered.

# 2.16.5.9 amd\_dbgapi\_dwarf\_address\_space\_to\_address\_space()

Return the address space from an AMD GPU DWARF address space number for an architecture.

A DWARF address space describes the architecture specific address spaces. If is used in DWARF location expressions that calculate addresses. See User Guide for AMDGPU Backend - Code Object - DWARF - Address Space Mapping.

The AMD GPU DWARF address space number must be valid for the architecture.

#### **Parameters**

in	architecture_id	The architecture of the address space.
in	dwarf_address_space	The AMD GPU DWARF address space.
out	address_space_id	The address space that corresponds to the DWARF address space for the architecture_id.

#### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in address_space_id.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and address_space_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and address_space_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARCHITEC	TlanenDtecture_id is invalid. address_space_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Taddress_space_id is NULL. address_space_id is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_ccampatalless_space is not valid for architecture_id. address_class_id is unaltered.

## 2.16.5.10 amd\_dbgapi\_read\_memory()

## Read memory.

The memory bytes in address\_space are read for lane\_id of wave\_id starting at segment\_address sequentially into value until value\_size bytes have been read or an invalid memory address is reached.  $value\_ \Leftrightarrow size$  is set to the number of bytes read successfully.

If wave\_id is not AMD\_DBGAPI\_WAVE\_NONE then it must be stopped, must belong to process\_id, and its architecture must be the same as that of the address space.

The library performs all necessary hardware cache management so that the memory values read are coherent with the wave\_id.

# **Parameters**

in	process_id	The process to read memory from if wave_id is AMD_DBGAPI_WAVE_NONE the address_space is AMD_DBGAPI_ADDRESS_SPACE_GLOBAL.
in	wave_id	The wave that is accessing the memory. If the address_space is AMD_DBGAPI_ADDRESS_SPACE_GLOBAL then wave_id may be AMD_DBGAPI_WAVE_NONE, as the address space does not depend on the active wave, in which case process_id is used.
in	lane_id	The lane of wave_id that is accessing the memory. If the address_space does not depend on the active lane then this is ignored and may be AMD_DBGAPI_LANE_NONE. For example, the AMD_DBGAPI_ADDRESS_SPACE_GLOBAL address space does not depend on the lane.
in	address_space⊷ _id	The address space of the <code>segment_address</code> . If the address space is dependent on: the active lane then the <code>lane_id</code> with in the <code>wave_id</code> is used; the active work-group then the work-group of <code>wave_id</code> is used; or the active wave then the <code>wave_id</code> is used.
in	segment_address	The integral value of the segment address. Only the bits corresponding to the address size for the address_space requested are used. The address size is provided by the AMD_DBGAPI_ADDRESS_SPACE_INFO_ADDRESS_SIZE query.
in,out	value_size	Pass in the number of bytes to read from memory. Return the number of bytes successfully read from memory.
out	value	Pointer to memory where the result is stored. Must be an array of at least input value_size bytes.

AMD_DBGAPI_STATUS_SUCCESS	Either the input value_size was 0, or the input value_size was greater than 0 and one or more bytes have been read successfully. The output value_size is set to the number of bytes successfully read, which will be 0 if the input value_size was 0. The first output value_size bytes of value are set to the bytes successfully read, all other bytes in value are unaltered.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized; and value_size and value are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized; and value_size and value are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/process_id is invalid. value_size and value are unaltered.

### Return values

AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid, or wave_id is  AMD_DBGAPI_WAVE_NONE and address_space is not AMD_DBGAPI_ADDRESS_SPACE_GLOBAL.  value_size and value are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_LANE_ID	lane_id is invalid, or lane_id is
	AMD_DBGAPI_LANE_NONE and address_space
	depends on the active lane. value_size and value
	are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	SRACE_US_space_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	<b>E</b> wave_id is not stopped. value_size and value
	are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	<i>T</i> value <b>or</b> value_size <b>are NULL</b> . value_size
	and value are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_v@ <b>⊘Nd/P</b> AAT/ <b>BN/bot</b> tYAMD_DBGAPI_WAVE_NONE and
	does not belong to process_id or have the same the
	architecture as address_space_id. value_size
	and value are unaltered.
AMD_DBGAPI_STATUS_ERROR_MEMORY_ACCESS	The input value_size was greater than 0 and no
	bytes were successfully read. The output
	value_size is set to 0. All bytes in value are
	unaltered.

# 2.16.5.11 amd\_dbgapi\_set\_memory\_precision()

Control precision of memory access reporting.

A process can be set to AMD\_DBGAPI\_MEMORY\_PRECISION\_NONE to disable precise memory reporting. Use the AMD\_DBGAPI\_PROCESS\_INFO\_PRECISE\_MEMORY\_SUPPORTED query to determine if the architectures of all the agents of a process support another memory precision.

The memory precision is set independently for each process, and only affects the waves executing on the agents of that process. The setting may be changed at any time, including when waves are executing, and takes effect immediately.

## **Parameters**

in	process_id	The process being configured.
in	memory_precision	The memory precision to set.

### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the agents of the process have been configured.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and no configuration is changed.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/process_id is invalid. No configuration is changed.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tmemory_precision is an invalid value. No configuration is changed.
AMD_DBGAPI_STATUS_ERROR_NOT_SUPPORTED	The requested memory_precision is not supported by the architecture of all the agents of process_id.  No configuration is changed.

## 2.16.5.12 amd dbgapi write memory()

# Write memory.

The memory bytes in address\_space are written for lane\_id of wave\_id starting at segment\_address sequentially from value until value\_size bytes have been written or an invalid memory address is reached. value — \_size is set to the number of bytes written successfully.

If wave\_id is not AMD\_DBGAPI\_WAVE\_NONE then it must be stopped, must belong to process\_id, and its architecture must be the same as that of the address space.

The library performs all necessary hardware cache management so that the memory values written are coherent with the wave\_id.

## **Parameters**

in	process_id	The process to write memory to if wave_id is AMD_DBGAPI_WAVE_NONE the address_space is AMD_DBGAPI_ADDRESS_SPACE_GLOBAL.
in	wave_id	The wave that is accessing the memory. If the address_space is AMD_DBGAPI_ADDRESS_SPACE_GLOBAL then wave_id may be AMD_DBGAPI_WAVE_NONE, as the address space does not depend on the active wave, in which case process_id is used.
in	lane_id	The lane of wave_id that is accessing the memory. If the address_space does not depend on the active lane then this is ignored and may be AMD_DBGAPI_LANE_NONE. For example, the AMD_DBGAPI_ADDRESS_SPACE_GLOBAL address space does not depend
		On the lane. Generated on Sun Jul 25 2021 17:20:20 for AMD_DBGAPI by Doxygen

# **Parameters**

in	address_space↔ _id	The address space of the <code>segment_address</code> . If the address space is dependent on: the active lane then the <code>lane_id</code> with in the <code>wave_id</code> is used; the active work-group then the work-group of <code>wave_id</code> is used; or the active wave then the <code>wave_id</code> is used.
in	segment_address	The integral value of the segment address. Only the bits corresponding to the address size for the address_space requested are used. The address size is provided by the AMD_DBGAPI_ADDRESS_SPACE_INFO_ADDRESS_SIZE query.
in,out	value_size	Pass in the number of bytes to write to memory. Return the number of bytes successfully written to memory.
in	value	The bytes to write to memory. Must point to an array of at least input value_size bytes.

AMD_DBGAPI_STATUS_SUCCESS	Either the input value_size was 0, or the input value_size was greater than 0 and one or more bytes have been written successfully. The output
	value_size is set to the number of bytes
	successfully written, which will be 0 if the input
	value_size was 0. The first output value_size
	bytes of memory starting at segment_address are
	updated, all other memory is unaltered.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	and the memory and value_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; the memory and value_size are
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	/process_id is invalid. The memory and
	value_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_WAVE_ID	wave_id is invalid, or wave_id is
	AMD_DBGAPI_WAVE_NONE and address_space
	is AMD_DBGAPI_ADDRESS_SPACE_GLOBAL. The
	memory and value_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_LANE_ID	lane_id is invalid, or lane_id is
	AMD_DBGAPI_LANE_NONE and address_space
	depends on the active lane. The memory and
	value_size <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_	S&ACE_Space_id is invalid. The memory and
	value_size <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_STOPP	E⊌ave_id is not stopped. The memory and
	value_size <b>are unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	value or value_size are NULL. The memory and
	value_size are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_vGQNdP_ddT/BN/nh0ftYAMD_DBGAPI_WAVE_NONE and
	does not belong to process_id or have the same the
	architecture as address_space_id. The memory
	and value_size are unaltered.

AMD_DBGAPI_STATUS_ERROR_MEMORY_ACCESS	The input value_size was greater than 0 and no
	bytes were successfully written. The output
	value_size is set to 0. The memory is unaltered.

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# 2.17 Events

Asynchronous event management.

### **Data Structures**

struct amd\_dbgapi\_event\_id\_t

Opaque event handle.

### **Macros**

#define AMD\_DBGAPI\_EVENT\_NONE ((amd\_dbgapi\_event\_id\_t){ 0 })

The NULL event handle.

#### **Enumerations**

```
    enum amd_dbgapi_event_kind_t {
        AMD_DBGAPI_EVENT_KIND_NONE = 0, AMD_DBGAPI_EVENT_KIND_WAVE_STOP = 1, AMD_DBGAPI_EVENT_KIND_WAVE
        = 2, AMD_DBGAPI_EVENT_KIND_CODE_OBJECT_LIST_UPDATED = 3,
        AMD_DBGAPI_EVENT_KIND_BREAKPOINT_RESUME = 4, AMD_DBGAPI_EVENT_KIND_RUNTIME = 5,
        AMD_DBGAPI_EVENT_KIND_QUEUE_ERROR = 6 }
```

The event kinds.

enum amd\_dbgapi\_runtime\_state\_t { AMD\_DBGAPI\_RUNTIME\_STATE\_LOADED\_SUCCESS = 1, AMD\_DBGAPI\_RUNTIME\_ST = 2, AMD\_DBGAPI\_RUNTIME\_STATE\_LOADED\_ERROR\_RESTRICTION = 3 }

Inferior runtime state.

enum amd\_dbgapi\_event\_info\_t {
 AMD\_DBGAPI\_EVENT\_INFO\_PROCESS = 1, AMD\_DBGAPI\_EVENT\_INFO\_KIND = 2, AMD\_DBGAPI\_EVENT\_INFO\_WAVE
 = 3, AMD\_DBGAPI\_EVENT\_INFO\_BREAKPOINT = 4,
 AMD\_DBGAPI\_EVENT\_INFO\_CLIENT\_THREAD = 5, AMD\_DBGAPI\_EVENT\_INFO\_RUNTIME\_STATE = 6 }

Event queries that are supported by amd dbgapi event get info.

## **Functions**

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_next\_pending\_event (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_event\_id\_t \*event\_id, amd\_dbgapi\_event\_kind\_t \*kind) AMD\_DBGAPI\_VERSION\_0\_41
  - Obtain the next pending event.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_event\_get\_info (amd\_dbgapi\_event\_id\_t event\_id, amd\_dbgapi\_event\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about an event.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_event\_processed (amd\_dbgapi\_event\_id\_t event\_id)
 AMD\_DBGAPI\_VERSION\_0\_41

Report that an event has been processed.

# 2.17.1 Detailed Description

Asynchronous event management.

Events can occur asynchronously. The library maintains a list of pending events that have happened but not yet been reported to the client. Events are maintained independently for each process.

When amd\_dbgapi\_process\_attach successfully attaches to a process a amd\_dbgapi\_notifier\_t notifier is created that is available using the AMD\_DBGAPI\_PROCESS\_INFO\_NOTIFIER query. When this indicates there may be pending events for the process, amd\_dbgapi\_process\_next\_pending\_event can be used to retrieve the pending events.

The notifier must be reset before retrieving pending events so that the notifier will always conservatively indicate there may be pending events. After the client has processed an event it must report completion using amd\_dbgapi\_event\_processed.

See also

amd\_dbgapi\_notifier\_t

## 2.17.2 Macro Definition Documentation

### 2.17.2.1 AMD DBGAPI EVENT NONE

```
#define AMD_DBGAPI_EVENT_NONE ((amd_dbgapi_event_id_t) { 0 })
```

The NULL event handle.

# 2.17.3 Enumeration Type Documentation

## 2.17.3.1 amd\_dbgapi\_event\_info\_t

```
enum amd_dbgapi_event_info_t
```

Event queries that are supported by amd\_dbgapi\_event\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_event\_get\_info.

AMD_DBGAPI_EVENT_INFO_PROCESS	Return the process to which this event belongs. The type of
	this attribute is amd_dbgapi_process_id_t.

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# Enumerator

AMD_DBGAPI_EVENT_INFO_KIND	Return the event kind. The type of this attribute is amd_dbgapi_event_kind_t.
AMD_DBGAPI_EVENT_INFO_WAVE	Return the wave of a  AMD_DBGAPI_EVENT_KIND_WAVE_STOP or  AMD_DBGAPI_EVENT_KIND_WAVE_COMMAND_TERMINATE  event. The type of this attribute is a amd_dbgapi_wave_id_t.
AMD_DBGAPI_EVENT_INFO_BREAKPOINT	Return the breakpoint of a  AMD_DBGAPI_EVENT_KIND_BREAKPOINT_RESUME event. The type of this attribute is a  amd_dbgapi_breakpoint_id_t.
AMD_DBGAPI_EVENT_INFO_CLIENT_THREAD	Return the client thread of a  AMD_DBGAPI_EVENT_KIND_BREAKPOINT_RESUME event. The type of this attribute is a amd_dbgapi_client_thread_id_t.
AMD_DBGAPI_EVENT_INFO_RUNTIME_STATE	Return if the runtime loaded in the inferior is supported by the library for a AMD_DBGAPI_EVENT_KIND_RUNTIME event. The type of this attribute is uint32_t with a value defined by amd_dbgapi_runtime_state_t.

# 2.17.3.2 amd\_dbgapi\_event\_kind\_t

enum amd\_dbgapi\_event\_kind\_t

The event kinds.

AMD_DBGAPI_EVENT_KIND_NONE	No event.
AMD_DBGAPI_EVENT_KIND_WAVE_STOP	A wave has stopped.
AMD_DBGAPI_EVENT_KIND_WAVE_COMMAND_↔ TERMINATED	A command for a wave was not able to complete because the wave has terminated. Commands that can result in this event are amd_dbgapi_wave_stop and amd_dbgapi_wave_resume in single step mode. Since the wave terminated before stopping, this event will be reported instead of AMD_DBGAPI_EVENT_KIND_WAVE_STOP.  The wave that terminated is available by the AMD_DBGAPI_EVENT_INFO_WAVE query. However, the wave will be invalid since it has already terminated. It is the client's responsibility to know what command was being performed and was unable to complete due to the wave terminating.

AMD_DBGAPI_EVENT_KIND_CODE_OBJECT_LIS↔ T_UPDATED	The list of code objects has changed. This event is only reported when a thread is in the process of loading or unloading a code object. It is not reported when attaching to a process even if there are loaded code objects. It is the client's responsibility to fetch the current code object list using amd_dbgapi_process_code_object_list.  The thread that caused the code object list to change will be stopped until the event is reported as processed. Before reporting the event has been processed, the client must set any pending breakpoints for newly loaded code objects so that breakpoints will be set before any code in the code object is executed.  When the event is reported as complete, a  AMD_DBGAPI_EVENT_KIND_BREAKPOINT_RESUME event may be created which must be processed to resume the thread that caused the code object list to change. Leaving the thread stopped may prevent the inferior runtime from servicing requests from other threads.
AMD_DBGAPI_EVENT_KIND_BREAKPOINT_RES↔ UME	Request to resume a host breakpoint. If amd_dbgapi_report_breakpoint_hit returns with resume as false then it indicates that events must be processed before the thread hitting the breakpoint can be resumed. When the necessary event(s) are reported as processed, this event will be added to the pending events. The breakpoint and client thread can then be queried by amd_dbgapi_event_get_info using AMD_DBGAPI_EVENT_INFO_BREAKPOINT and AMD_DBGAPI_EVENT_INFO_CLIENT_THREAD respectively. The client must then resume execution of the thread.
AMD_DBGAPI_EVENT_KIND_RUNTIME	The runtime support in the inferior is enabled or disabled. Until it has been successfully enabled no code objects will be loaded and no waves will be created. The client can use this event to determine when to activate and deactivate AMD GPU debugging functionality. This event reports the enabled status, the version, and if it is compatible with this library. If it is not compatible, then no code objects or waves will be reported to exist.

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#### Enumerator

#### AMD\_DBGAPI\_EVENT\_KIND\_QUEUE\_ERROR

An event has occurred that is causing the queue to enter the error state. All non-stopped waves executing on the queue will have been stopped and a AMD\_DBGAPI\_EVENT\_KIND\_WAVE\_STOP event will proceed this event. All waves on the queue will include the

AMD\_DBGAPI\_WAVE\_STOP\_REASON\_QUEUE\_ERROR stop reason. No further waves will be started on the queue. The

AMD\_DBGAPI\_QUEUE\_INFO\_ERROR\_REASON query will include the union of the reasons that were reported. Some waves may be stopped before they were able to report a queue error condition. The wave stop reason will only include the reasons that were reported.

For example, if many waves encounter a memory violation at the same time, only some of the waves may report it before all the waves in the queue are stopped. Only the waves that were able to report the memory violation before all the waves were stopped will include the

AMD\_DBGAPI\_WAVE\_STOP\_REASON\_MEMORY\_VIOLATION stop reason.

The queue error will not be reported to the inferior runtime until this event is reported as complete by calling amd\_dbgapi\_event\_processed. Once reported to the inferior runtime, it may cause the application to be notified which may delete and re-create the queue in order to continue submitting dispatches to the AMD GPU. If the application deletes a queue then all information about the waves executing on the queue will be lost, preventing the user from determining if a wave caused the error.

Therefore, the client may choose to stop inferior threads before reporting the event as complete. This would prevent the queue error from causing the queue to be deleted, allowing the user to inspect all the waves in the queue. Alternatively, the client may not report the event as complete until the user explicitly requests the queue error to be passed on to the inferior runtime.

#### 2.17.3.3 amd\_dbgapi\_runtime\_state\_t

enum amd\_dbgapi\_runtime\_state\_t

Inferior runtime state.

#### Enumerator

AMD_DBGAPI_RUNTIME_STATE_LOADED_SUCC↔	The runtime has been loaded and debugging is
ESS	supported by the library.
AMD_DBGAPI_RUNTIME_STATE_UNLOADED	The runtime has been unloaded.
AMD_DBGAPI_RUNTIME_STATE_LOADED_ERR↔ OR_RESTRICTION	The runtime has been loaded but there is a restriction error that prevents debugging the process. See AMD_DBGAPI_STATUS_ERROR_RESTRICTION for possible reasons.

#### 2.17.4 Function Documentation

## 2.17.4.1 amd\_dbgapi\_event\_get\_info()

Query information about an event.

amd\_dbgapi\_event\_info\_t specifies the queries supported and the type returned using the value argument.

## **Parameters**

in	event_id	The event being queried.
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.
out	value	Pointer to memory where the query result is stored.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_EVENT_ID	event_id is invalid or the NULL event. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is for an attribute not present for the kind of the event. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_v&QM&A\$IBULA Too'es not match the size of the query result. value is unaltered.

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#### **Return values**

AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the	
	amd_dbgapi_callbacks_s::allocate_memory callback	
	used to allocate value returns NULL. value is	
	unaltered.	

## 2.17.4.2 amd\_dbgapi\_event\_processed()

Report that an event has been processed.

Every event returned by amd\_dbgapi\_process\_next\_pending\_event must be reported as processed exactly once. Events do not have to be reported completed in the same order they are retrieved.

#### **Parameters**

in	event←	The event that has been processed.
	_id	

## **Return values**

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the event has been reported as processed. The event_id is invalidated.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_INVALID_EVENT_ID	The event_id is invalid or the NULL event. No event is marked as processed.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tevent_id or kind are NULL. No event is marked as processed.

## 2.17.4.3 amd\_dbgapi\_process\_next\_pending\_event()

Obtain the next pending event.

The order events are returned is unspecified. If the client requires fairness then it can retrieve all pending events and randomize the order of processing.

## **Parameters**

in	process↔ _id	If AMD_DBGAPI_PROCESS_NONE then retrieve a pending event from any processes.  Otherwise, retrieve a pending event from process process_id.	
out	event_id	The event handle of the next pending event. Each event is only returned once. If there are no pending events the AMD_DBGAPI_EVENT_NONE handle is returned.	
out	kind	The kind of the returned event. If there are no pending events, then AMD_DBGAPI_EVENT_KIND_NONE is returned.	

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and an event or the NULL event has been returned.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized;
	and event_id and kind are unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized; and event_id and kind are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_PROCESS_	///The process_id is invalid. No event is retrieved and
	event_id and kind are unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	revent_id or kind are NULL. No event is retrieved
	and event_id and kind are unaltered.

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## 2.18 Logging

Control logging.

## **Enumerations**

enum amd\_dbgapi\_log\_level\_t {
 AMD\_DBGAPI\_LOG\_LEVEL\_NONE = 0, AMD\_DBGAPI\_LOG\_LEVEL\_FATAL\_ERROR = 1, AMD\_DBGAPI\_LOG\_LEVEL\_WARN
 = 2, AMD\_DBGAPI\_LOG\_LEVEL\_INFO = 3,
 AMD\_DBGAPI\_LOG\_LEVEL\_VERBOSE = 4 }

The logging levels supported.

#### **Functions**

void AMD\_DBGAPI amd\_dbgapi\_set\_log\_level (amd\_dbgapi\_log\_level\_t level) AMD\_DBGAPI\_VERSION\_0\_24
 Set the logging level.

## 2.18.1 Detailed Description

Control logging.

When the library is initially loaded the logging level is set to AMD\_DBGAPI\_LOG\_LEVEL\_NONE. The log level is not changed by amd\_dbgapi\_initialize or amd\_dbgapi\_finalize.

The log messages are delivered to the client using the amd\_dbgapi\_callbacks\_s::log\_message call back.

Note that logging can be helpful for debugging.

## 2.18.2 Enumeration Type Documentation

## 2.18.2.1 amd\_dbgapi\_log\_level\_t

```
enum amd_dbgapi_log_level_t
```

The logging levels supported.

### **Enumerator**

AMD_DBGAPI_LOG_LEVEL_NONE	Print no messages.
AMD_DBGAPI_LOG_LEVEL_FATAL_ERROR	Print fatal error messages. Any library function that returns the AMD_DBGAPI_STATUS_FATAL status code also logs a message with this level.
AMD DBGAPI LOG LEVEL WARNING Generated on Sun Jul 25 2021 17:20:20 for AMD DBGAPI by Doxyg	Print fatal error and warning messages.
AMD_DBGAPI_LOG_LEVEL_INFO	Print fatal error, warning, and info messages.
AMD_DBGAPI_LOG_LEVEL_VERBOSE	Print fatal error, warning, info, and verbose messages.

## 2.18.3 Function Documentation

## 2.18.3.1 amd\_dbgapi\_set\_log\_level()

Set the logging level.

Internal logging messages less than the set logging level will not be reported. If AMD\_DBGAPI\_LOG\_LEVEL\_NONE then no messages will be reported.

This function can be used even when the library is uninitialized. However, no messages will be reported until the library is initialized when the callbacks are provided.

#### **Parameters**

in level The log	gging level to set.
------------------	---------------------

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMENT	level is invalid. The logging level is ot changed.

2.19 Callbacks 139

## 2.19 Callbacks

The library requires the client to provide a number of services. These services are specified by providing callbacks when initializing the library using amd\_dbgapi\_initialize.

#### **Data Structures**

· struct amd\_dbgapi\_shared\_library\_id\_t

Opaque shared library handle.

struct amd\_dbgapi\_breakpoint\_id\_t

Opaque breakpoint handle.

struct amd\_dbgapi\_callbacks\_s

Callbacks that the client of the library must provide.

#### **Macros**

• #define AMD DBGAPI SHARED LIBRARY NONE ((amd dbgapi shared library id t){ 0 })

The NULL shared library handle.

#define AMD\_DBGAPI\_BREAKPOINT\_NONE ((amd\_dbgapi\_breakpoint\_id\_t){ 0 })

The NULL breakpoint handle.

## **Typedefs**

- typedef struct amd dbgapi callbacks s amd dbgapi callbacks t
- typedef struct amd\_dbgapi\_client\_thread\_s \* amd\_dbgapi\_client\_thread\_id\_t

Opaque client thread handle.

#### **Enumerations**

 enum amd\_dbgapi\_shared\_library\_state\_t { AMD\_DBGAPI\_SHARED\_LIBRARY\_STATE\_LOADED = 1, AMD\_DBGAPI\_SHARED\_LIBRARY\_STATE\_UNLOADED = 2}

The state of a shared library.

enum amd dbgapi shared library info t { AMD DBGAPI SHARED LIBRARY INFO PROCESS = 1 }

Shared library queries that are supported by amd\_dbgapi\_shared\_library\_get\_info.

 enum amd\_dbgapi\_breakpoint\_info\_t { AMD\_DBGAPI\_BREAKPOINT\_INFO\_SHARED\_LIBRARY = 1, AMD\_DBGAPI\_BREAKPOINT\_INFO\_PROCESS = 2}

Breakpoint queries that are supported by amd\_dbgapi\_breakpoint\_get\_info.

enum amd\_dbgapi\_breakpoint\_action\_t { AMD\_DBGAPI\_BREAKPOINT\_ACTION\_RESUME = 1, AMD\_DBGAPI\_BREAKPOINT\_ACTION\_RESUME = 2 }

The action to perform after reporting a breakpoint has been hit.

#### **Functions**

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_shared\_library\_get\_info (amd\_dbgapi\_shared\_library\_id\_t shared\_library\_id, amd\_dbgapi\_shared\_library\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41
 Query information about a shared library.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_report\_shared\_library (amd\_dbgapi\_shared\_library\_id\_t shared\_library\_id, amd\_dbgapi\_shared\_library\_state\_t shared\_library\_state) AMD\_DBGAPI\_VERSION\_0\_41

Report that a shared library enabled by the amd\_dbgapi\_callbacks\_s::enable\_notify\_shared\_library callback has been loaded or unloaded.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_report\_breakpoint\_hit (amd\_dbgapi\_breakpoint\_id\_t breakpoint\_id, amd\_dbgapi\_client\_thread\_id\_t client\_thread\_id, amd\_dbgapi\_breakpoint\_action\_t \*breakpoint → action) AMD\_DBGAPI\_VERSION\_0\_41

Report that a breakpoint inserted by the amd\_dbgapi\_callbacks\_s::insert\_breakpoint calback has been hit.

## 2.19.1 Detailed Description

The library requires the client to provide a number of services. These services are specified by providing callbacks when initializing the library using <a href="mailto:amd\_dbgapi\_initialize">amd\_dbgapi\_initialize</a>.

The callbacks defined in this section are invoked by the library and must not themselves invoke any function provided by the library before returning.

#### 2.19.2 Macro Definition Documentation

#### 2.19.2.1 AMD DBGAPI BREAKPOINT NONE

```
#define AMD_DBGAPI_BREAKPOINT_NONE ((amd_dbgapi_breakpoint_id_t){ 0 })
```

The NULL breakpoint handle.

#### 2.19.2.2 AMD DBGAPI SHARED LIBRARY NONE

```
#define AMD_DBGAPI_SHARED_LIBRARY_NONE ((amd_dbgapi_shared_library_id_t){ 0 })
```

The NULL shared library handle.

2.19 Callbacks

## 2.19.3 Typedef Documentation

#### 2.19.3.1 amd dbgapi callbacks t

```
typedef struct amd_dbgapi_callbacks_s amd_dbgapi_callbacks_t
```

Forward declaration of callbacks used to specify services that must be provided by the client.

## 2.19.3.2 amd\_dbgapi\_client\_thread\_id\_t

```
typedef struct amd_dbgapi_client_thread_s* amd_dbgapi_client_thread_id_t
```

Opaque client thread handle.

A pointer to client data associated with a thread. This pointer is passed in to the amd\_dbgapi\_report\_breakpoint\_hit so it can be passed out by the AMD\_DBGAPI\_EVENT\_KIND\_BREAKPOINT\_RESUME event to allow the client of the library to identify the thread that must be resumed.

## 2.19.4 Enumeration Type Documentation

## 2.19.4.1 amd\_dbgapi\_breakpoint\_action\_t

```
enum amd_dbgapi_breakpoint_action_t
```

The action to perform after reporting a breakpoint has been hit.

#### **Enumerator**

AMD_DBGAPI_BREAKPOINT_ACTION_RESUME	Resume execution.
AMD_DBGAPI_BREAKPOINT_ACTION_HALT	Leave execution halted.

#### 2.19.4.2 amd\_dbgapi\_breakpoint\_info\_t

```
enum amd_dbgapi_breakpoint_info_t
```

Breakpoint queries that are supported by amd\_dbgapi\_breakpoint\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_breakpoint\_get\_info.

#### Enumerator

AMD_DBGAPI_BREAKPOINT_INFO_SHARED_LIB↔ RARY	Return the shared library to which this breakpoint belongs. The type of this attribute is amd_dbgapi_shared_library_id_t.
AMD_DBGAPI_BREAKPOINT_INFO_PROCESS	Return the process to which this breakpoint belongs. The type of this attribute is amd_dbgapi_process_id_t.

## 2.19.4.3 amd\_dbgapi\_shared\_library\_info\_t

```
enum amd_dbgapi_shared_library_info_t
```

Shared library queries that are supported by amd\_dbgapi\_shared\_library\_get\_info.

Each query specifies the type of data returned in the value argument to amd\_dbgapi\_shared\_library\_get\_info.

#### Enumerator

AMD_DBGAPI_SHARED_LIBRARY_INFO_PROCESS	Return the process to which this shared library belongs.
	The type of this attribute is amd_dbgapi_process_id_t.

## 2.19.4.4 amd\_dbgapi\_shared\_library\_state\_t

```
enum amd_dbgapi_shared_library_state_t
```

The state of a shared library.

#### **Enumerator**

AMD_DBGAPI_SHARED_LIBRARY_STATE_LOADED	The shared library is loaded.
AMD_DBGAPI_SHARED_LIBRARY_STATE_UNLOADED	The shared library is unloaded.

## 2.19.5 Function Documentation

## 2.19.5.1 amd\_dbgapi\_breakpoint\_get\_info()

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```
amd_dbgapi_breakpoint_info_t query,
size_t value_size,
void * value )
```

Query information about a breakpoint.

amd\_dbgapi\_breakpoint\_info\_t specifies the queries supported and the type returned using the value argument.

#### **Parameters**

in	breakpoint⇔	The handle of the breakpoint being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query result.
out	value	Pointer to memory where the query result is stored.

#### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the
	result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and
	value <b>is unaltered</b> .
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left
	uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_BREAKPOI	Nัธิ <u>ช</u> อิakpoint_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is
	unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_COMPA TIBLE Does not match the size of the query
	result. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_CLIENT_CALLBACK	This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

#### 2.19.5.2 amd\_dbgapi\_report\_breakpoint\_hit()

Report that a breakpoint inserted by the amd\_dbgapi\_callbacks\_s::insert\_breakpoint calback has been hit.

The thread that hit the breakpoint must remain halted while this function executes, at which point it must be resumed if breakpoint\_action is AMD\_DBGAPI\_BREAKPOINT\_ACTION\_RESUME. If breakpoint\_action is :AMD\_DBGAPI\_BREAKPOINT\_ACTION\_HALT then the client should process pending events which will cause a AMD\_DBGAPI\_EVENT\_KIND\_BREAKPOINT\_RESUME event to be added which specifies that the thread should now be resumed.

#### **Parameters**

in	breakpoint_id	The breakpoint that has been hit.
in	client_thread_id	The client identification of the thread that hit the breakpoint.
out	breakpoint_action	Indicate if the thread hitting the breakpoint should be resumed or remain halted when
		this function returns.

#### Return values

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and breakpoint_action indicates if the thread hitting the breakpoint should be resumed.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and breakpoint_action is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and breakpoint_action is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_BREAKPOI	NTh⊕breakpoint_id is invalid. breakpoint_action is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tbreakpoint_action is NULL. breakpoint_action is unaltered.

## 2.19.5.3 amd\_dbgapi\_report\_shared\_library()

Report that a shared library enabled by the amd\_dbgapi\_callbacks\_s::enable\_notify\_shared\_library callback has been loaded or unloaded.

The thread that is performing the shared library load or unload must remain halted while this function executes. This allows the library to use the amd\_dbgapi\_callbacks\_s::get\_symbol\_address, amd\_dbgapi\_callbacks\_s::insert\_breakpoint and amd\_dbgapi\_callbacks\_s::remove\_breakpoint callbacks to add or remove breakpoints on library load or unload respectively. The breakpoints must be inserted before any code can execute in the shared library.

#### **Parameters**

in	shared_library_id	The shared library that has been loaded or unloaded.
in	shared_library_state	The shared library state.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The amd-dbgapi library is left uninitialized and resume is unaltered.

2.19 Callbacks

#### **Return values**

AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The amd-dbgapi library is not initialized. The
	amd-dbgapi library is left uninitialized.
AMD_DBGAPI_STATUS_ERROR_INVALID_SHARED_L	/ይፑሴቀቡያኒኒፈውed_library_id is invalid.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tshared_library_state is invalid.
AMD_DBGAPI_STATUS_ERROR	shared_library_state is not consistent with the
	previously reported load state. For example, it is
	reported as loaded when previously also reported as
	loaded.

## 2.19.5.4 amd\_dbgapi\_shared\_library\_get\_info()

Query information about a shared library.

amd\_dbgapi\_shared\_library\_info\_t specifies the queries supported and the type returned using the value argument.

#### **Parameters**

in	shared_library←	The handle of the shared_library being queried.
	_id	
in	query	The query being requested.
in	value_size	Size of the memory pointed to by value. Must be equal to the byte size of the query
		result.
out	value	Pointer to memory where the query result is stored.

AMD_DBGAPI_STATUS_SUCCESS	The function has been executed successfully and the result is stored in value.
AMD_DBGAPI_STATUS_FATAL	A fatal error occurred. The library is left uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_NOT_INITIALIZED	The library is not initialized. The library is left uninitialized and value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_SHARED_L	/BRAFY≦dDlibrary_id is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	Tvalue is NULL or query is invalid. value is unaltered.
AMD_DBGAPI_STATUS_ERROR_INVALID_ARGUMEN	T_COMEASIBLE Tools not match the size of the query result. value is unaltered.

AMD_DBGAPI_STATUS_ERROR_CLIENT	_CALLBACK This will be reported if the
	amd_dbgapi_callbacks_s::allocate_memory callback
	used to allocate value returns NULL. value is
	unaltered.

# **Chapter 3**

# **Data Structure Documentation**

## 3.1 amd dbgapi address class id t Struct Reference

Opaque source language address class handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

• uint64\_t handle

## 3.1.1 Detailed Description

Opaque source language address class handle.

A source language address class describes the source language address spaces. It is used to define source language pointer and reference types. Each architecture has its own mapping of them to the architecture specific address spaces.

The handle is only unique within a specific architecture.

```
See User Guide for AMDGPU Backend - Code Object - DWARF - Address Class Mapping.
```

#### 3.1.2 Field Documentation

## 3.1.2.1 handle

```
uint64_t amd_dbgapi_address_class_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.2 amd\_dbgapi\_address\_space\_id\_t Struct Reference

Opaque address space handle.

```
#include <amd-dbgapi.h>
```

#### **Data Fields**

• uint64\_t handle

## 3.2.1 Detailed Description

Opaque address space handle.

A handle that denotes the set of address spaces supported by an architecture.

The handle is only unique within a specific architecture.

```
See User Guide for AMDGPU Backend - LLVM - Address Spaces.
```

### 3.2.2 Field Documentation

#### 3.2.2.1 handle

```
\verb|uint64_t amd_dbgapi_address_space_id_t:: handle|\\
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.3 amd\_dbgapi\_agent\_id\_t Struct Reference

Opaque agent handle.

```
#include <amd-dbgapi.h>
```

#### **Data Fields**

• uint64\_t handle

## 3.3.1 Detailed Description

Opaque agent handle.

Only unique within a single process.

#### 3.3.2 Field Documentation

#### 3.3.2.1 handle

```
uint64_t amd_dbgapi_agent_id_t::handle
```

The documentation for this struct was generated from the following file:

• include/amd-dbgapi.h

## 3.4 amd\_dbgapi\_architecture\_id\_t Struct Reference

Opaque architecture handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

uint64\_t handle

## 3.4.1 Detailed Description

Opaque architecture handle.

An architecture handle is unique for each AMD GPU model supported by the library. They are only valid while the library is initialized and are invalidated when the library is uninitialized.

#### 3.4.2 Field Documentation

#### 3.4.2.1 handle

```
uint64_t amd_dbgapi_architecture_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.5 amd\_dbgapi\_breakpoint\_id\_t Struct Reference

Opaque breakpoint handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

uint64\_t handle

## 3.5.1 Detailed Description

Opaque breakpoint handle.

Every breakpoint added within a process will have a unique handle. Only unique within a single process.

The implementation of the library requests the client to insert breakpoints in certain functions so that it can be notified when certain actions are being performed, and to stop the thread performing the action. This allows the data to be retrieved and updated without conflicting with the thread. The library will resume the thread when it has completed the access.

## 3.5.2 Field Documentation

#### 3.5.2.1 handle

```
uint64_t amd_dbgapi_breakpoint_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.6 amd dbgapi callbacks s Struct Reference

Callbacks that the client of the library must provide.

```
#include <amd-dbgapi.h>
```

#### **Data Fields**

void \*(\* allocate\_memory )(size\_t byte\_size)

Allocate memory to be used to return a value from the library that is then owned by the client.

void(\* deallocate\_memory )(void \*data)

Deallocate memory that was allocated by amd\_dbgapi\_callbacks\_s::allocate\_memory.

amd\_dbgapi\_status\_t(\* get\_os\_pid )(amd\_dbgapi\_client\_process\_id\_t client\_process\_id, amd\_dbgapi\_os\_process\_id\_t \*os\_pid)

Return the native operating system process handle for the process identified by the client process handle.

amd\_dbgapi\_status\_t(\* enable\_notify\_shared\_library )(amd\_dbgapi\_client\_process\_id\_t client\_process\_id,
 const char \*shared\_library\_name, amd\_dbgapi\_shared\_library\_id\_t shared\_library\_id, amd\_dbgapi\_shared\_library\_state\_t \*shared\_library\_state)

Request to be notified when a shared library is loaded and unloaded.

 amd\_dbgapi\_status\_t(\* disable\_notify\_shared\_library )(amd\_dbgapi\_client\_process\_id\_t client\_process\_id, amd\_dbgapi\_shared\_library\_id\_t shared\_library\_id)

Request to stop being notified for a shared library previously set by amd\_dbgapi\_callbacks\_s::enable\_notify\_shared\_library.

 amd\_dbgapi\_status\_t(\* get\_symbol\_address)(amd\_dbgapi\_client\_process\_id\_t client\_process\_id, amd\_dbgapi\_shared\_library\_id shared\_library\_id, const char \*symbol\_name, amd\_dbgapi\_global\_address\_t \*address)

Return the address of a symbol in a shared library.

amd\_dbgapi\_status\_t(\* insert\_breakpoint)(amd\_dbgapi\_client\_process\_id\_t client\_process\_id, amd\_dbgapi\_shared\_library\_id\_t shared library id, amd dbgapi global address t address, amd dbgapi breakpoint id t breakpoint id)

Insert a breakpoint in a shared library using a global address.

amd\_dbgapi\_status\_t(\* remove\_breakpoint)(amd\_dbgapi\_client\_process\_id\_t client\_process\_id, amd\_dbgapi\_breakpoint\_id\_t breakpoint id)

Remove a breakpoint previously inserted by amd dbgapi callbacks s::insert breakpoint.

void(\* log\_message )(amd\_dbgapi\_log\_level\_t level, const char \*message)

Report a log message.

### 3.6.1 Detailed Description

Callbacks that the client of the library must provide.

The client implementation of the callbacks must not invoke any operation of the library.

#### 3.6.2 Field Documentation

#### 3.6.2.1 allocate memory

```
void*(* amd_dbgapi_callbacks_s::allocate_memory) (size_t byte_size)
```

Allocate memory to be used to return a value from the library that is then owned by the client.

The memory should be suitably aligned for any type. If <code>byte\_size</code> is 0 or if unable to allocate memory of the byte size specified by <code>byte\_size</code> then return NULL and allocate no memory. The client is responsible for deallocating this memory, and so is responsible for tracking the size of the allocation. Note that these requirements can be met by implementing using <code>malloc</code>.

#### 3.6.2.2 deallocate\_memory

```
void(* amd_dbgapi_callbacks_s::deallocate_memory) (void *data)
```

Deallocate memory that was allocated by amd dbgapi callbacks s::allocate memory.

data will be a pointer returned by amd\_dbgapi\_callbacks\_s::allocate\_memory that will not be returned to the client. If data is NULL then it indicates the allocation failed or was for 0 bytes: in either case the callback is required to take no action. If data is not NULL then it will not have been deallocated by a previous call to amd\_dbgapi\_callbacks\_s::allocate\_memory. Note that these requirements can be met by implementing using free.

Note this callback may be used by the library implementation if it encounters an error after using amd\_dbgapi\_callbacks\_s::allocate\_memor to allocate memory.

#### 3.6.2.3 disable\_notify\_shared\_library

```
amd_dbgapi_status_t(* amd_dbgapi_callbacks_s::disable_notify_shared_library) (amd_dbgapi_client_process_id_t
client_process_id, amd_dbgapi_shared_library_id_t shared_library_id)
```

Request to stop being notified for a shared library previously set by amd\_dbgapi\_callbacks\_s::enable\_notify\_shared\_library.

shared\_library\_id is invalidated.

client\_process\_id is the client handle of the process in which loading of the shared library is being notified.

shared\_library\_id is the handle of the shared library to stop being notified.

Return AMD\_DBGAPI\_STATUS\_SUCCESS if successful.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_CLIENT\_PROCESS\_ID if the client\_process\_id handle is invalid.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_SHARED\_LIBRARY\_ID if the <code>shared\_library\_id</code> handle is invalid.

Return AMD DBGAPI STATUS ERROR if an error was encountered.

#### 3.6.2.4 enable\_notify\_shared\_library

amd\_dbgapi\_status\_t(\* amd\_dbgapi\_callbacks\_s::enable\_notify\_shared\_library) (amd\_dbgapi\_client\_process\_id\_t
client\_process\_id, const char \*shared\_library\_name, amd\_dbgapi\_shared\_library\_id\_t shared\_library
\_id, amd\_dbgapi\_shared\_library\_state\_t \*shared\_library\_state)

Request to be notified when a shared library is loaded and unloaded.

If multiple shared libraries match the name, then the client must only associate <code>shared\_library\_id</code> with a single shared library, and only invoke <code>amd\_dbgapi\_report\_shared\_library</code> for that single shared library.

client\_process\_id is the client handle of the process in which loading of the shared library must be notified.

shared\_library\_name is the name of the shared library being requested. The memory is owned by the library and is only valid while the callback executes. On Linux this is the SONAME of the library.

shared\_library\_id is the handle to identify this shared library which must be specified when amd\_dbgapi\_report\_shared\_library is used to report a shared library load or unload.

shared\_library\_state must be set to a value that indicates whether the shared library is already loaded.

Return AMD\_DBGAPI\_STATUS\_SUCCESS if successful.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_CLIENT\_PROCESS\_ID if the client\_process\_id handle is invalid.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_ARGUMENT if shared\_library\_name or shared\_← library\_state are NULL or shared\_library\_name is an invalid library name.

Return AMD\_DBGAPI\_STATUS\_ERROR if the shared\_library\_name shared library is already enabled for notifications or another error was encountered.

#### 3.6.2.5 get\_os\_pid

```
amd_dbgapi_status_t(* amd_dbgapi_callbacks_s::get_os_pid) (amd_dbgapi_client_process_id_t client↔
_process_id, amd_dbgapi_os_process_id_t *os_pid)
```

Return the native operating system process handle for the process identified by the client process handle.

This value is required to not change during the lifetime of the process associated with the client process handle.

For Linux® this is the pid\_t from sys/types.h and is required to have already been ptrace enabled.

client\_process\_id is the client handle of the process for which the operating system process handle is being queried.

os\_pid must be set to the native operating system process handle.

Return AMD DBGAPI STATUS SUCCESS if successful and os pid is updated.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_CLIENT\_PROCESS\_ID if the client\_process\_id handle is invalid.

Return AMD\_DBGAPI\_STATUS\_ERROR\_PROCESS\_EXITED if the client\_process\_id handle is associated with a native operating system process that has already exited.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_ARGUMENT if os\_pid is NULL.

Return AMD DBGAPI STATUS ERROR if an error was encountered.

#### 3.6.2.6 get\_symbol\_address

amd\_dbgapi\_status\_t(\* amd\_dbgapi\_callbacks\_s::get\_symbol\_address) (amd\_dbgapi\_client\_process\_id\_t
client\_process\_id, amd\_dbgapi\_shared\_library\_id\_t shared\_library\_id, const char \*symbol\_name,
amd\_dbgapi\_global\_address\_t \*address)

Return the address of a symbol in a shared library.

client\_process\_id is the client handle of the process being queried.

shared\_library\_id is the shared library that contains the symbol.

symbol\_name is the name of the symbol being requested. The memory is owned by the library and is only valid while the callback executes.

address must be updated with the address of the symbol.

Return AMD DBGAPI STATUS SUCCESS if successful.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_CLIENT\_PROCESS\_ID if the client\_process\_id handle is invalid.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_SHARED\_LIBRARY\_ID if the shared\_library\_id handle is invalid.

Return AMD\_DBGAPI\_STATUS\_ERROR\_LIBRARY\_NOT\_LOADED if shared\_library\_id shared library is not currently loaded.

Return AMD\_DBGAPI\_STATUS\_ERROR\_SYMBOL\_NOT\_FOUND if shared\_library\_id shared library is loaded but does not contain symbol\_name.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_ARGUMENT if symbol\_name or address are NULL.

Return AMD\_DBGAPI\_STATUS\_ERROR if an error was encountered.

#### 3.6.2.7 insert\_breakpoint

amd\_dbgapi\_status\_t(\* amd\_dbgapi\_callbacks\_s::insert\_breakpoint) (amd\_dbgapi\_client\_process\_id\_t
client\_process\_id, amd\_dbgapi\_shared\_library\_id\_t shared\_library\_id, amd\_dbgapi\_global\_address\_t
address, amd\_dbgapi\_breakpoint\_id\_t breakpoint\_id)

Insert a breakpoint in a shared library using a global address.

The library only inserts breakpoints in loaded shared libraries. It will request to be notified when the shared library is unloaded, and will remove any breakpoints it has inserted when notified that the shared library is unloaded.

It is the client's responsibility to to actually insert the breakpoint.

client\_process\_id is the client handle of the process in which the breakpoint is to be added.

shared\_library\_id is the shared library that contains the address.

address is the global address to add the breakpoint.

breakpoint\_id is the handle to identify this breakpoint. Each added breakpoint for a process will have a unique handle, multiple breakpoints for the same process will not be added with the same handle. It must be specified when amd\_dbgapi\_report\_breakpoint\_hit is used to report a breakpoint hit, and in the AMD\_DBGAPI\_EVENT\_KIND\_BREAKPOINT\_RESUME event that may be used to resume the thread.

Return AMD\_DBGAPI\_STATUS\_SUCCESS if successful. The breakpoint is added.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_CLIENT\_PROCESS\_ID if the client\_process\_id handle is invalid. No breakpoint is added.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_SHARED\_LIBRARY\_ID if the shared\_library\_id handle is invalid. No breakpoint is added.

Return AMD\_DBGAPI\_STATUS\_ERROR\_LIBRARY\_NOT\_LOADED if shared\_library\_id shared library is not currently loaded. No breakpoint is added.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_ADDRESS if address is not an address in shared library shared\_library\_id. No breakpoint is added.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_BREAKPOINT\_ID if there is a breakpoint already added with breakpoint\_id. No breakpoint is added.

Return AMD\_DBGAPI\_STATUS\_ERROR if another error was encountered. No breakpoint is inserted and the breakpoint\_id handle is invalidated.

#### 3.6.2.8 log\_message

void(\* amd\_dbgapi\_callbacks\_s::log\_message) (amd\_dbgapi\_log\_level\_t level, const char \*message)

Report a log message.

level is the log level.

message is a NUL terminated string to print that is owned by the library and is only valid while the callback executes.

#### 3.6.2.9 remove\_breakpoint

amd\_dbgapi\_status\_t(\* amd\_dbgapi\_callbacks\_s::remove\_breakpoint) (amd\_dbgapi\_client\_process\_id\_t
client\_process\_id, amd\_dbgapi\_breakpoint\_id\_t breakpoint\_id)

Remove a breakpoint previously inserted by amd dbgapi callbacks s::insert breakpoint.

It is the client's responsibility to to actually remove the breakpoint.

breakpoint\_id is invalidated.

client\_process\_id is the client handle of the process in which the breakpoint is to be removed.

breakpoint id is the breakpoint handle of the breakpoint to remove.

Return AMD DBGAPI STATUS SUCCESS if successful. The breakpoint is removed.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_CLIENT\_PROCESS\_ID if the client\_process\_id handle is invalid. No breakpoint is removed.

Return AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_BREAKPOINT\_ID if breakpoint\_id handle is invalid. No breakpoint is removed.

Return AMD\_DBGAPI\_STATUS\_ERROR\_LIBRARY\_NOT\_LOADED if the shared library containing the breakpoint is not currently loaded. The breakpoint will already have been removed.

Return AMD\_DBGAPI\_STATUS\_ERROR if another error was encountered. The breakpoint is considered removed and the breakpoint\_id handle is invalidated.

The documentation for this struct was generated from the following file:

include/amd-dbgapi.h

## 3.7 amd dbgapi code object id t Struct Reference

Opaque code object handle.

#include <amd-dbgapi.h>

#### **Data Fields**

· uint64 t handle

## 3.7.1 Detailed Description

Opaque code object handle.

Only unique within a single process.

#### 3.7.2 Field Documentation

#### 3.7.2.1 handle

uint64\_t amd\_dbgapi\_code\_object\_id\_t::handle

The documentation for this struct was generated from the following file:

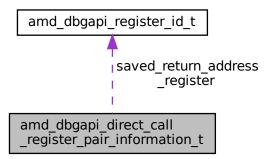
· include/amd-dbgapi.h

## 3.8 amd\_dbgapi\_direct\_call\_register\_pair\_information\_t Struct Reference

Instruction information for direct call instructions.

```
#include <amd-dbgapi.h>
```

Collaboration diagram for amd\_dbgapi\_direct\_call\_register\_pair\_information\_t:



### **Data Fields**

- amd\_dbgapi\_global\_address\_t target\_address
- amd\_dbgapi\_register\_id\_t saved\_return\_address\_register [2]

## 3.8.1 Detailed Description

Instruction information for direct call instructions.

Used by amd\_dbgapi\_classify\_instruction to report the target address and saved return registers IDs information for instructions of the AMD\_DBGAPI\_INSTRUCTION\_KIND\_DIRECT\_CALL\_REGISTER\_PAIR kind.

#### 3.8.2 Field Documentation

#### 3.8.2.1 saved return address register

amd\_dbgapi\_register\_id\_t amd\_dbgapi\_direct\_call\_register\_pair\_information\_t::saved\_return\_address←
\_register[2]

#### 3.8.2.2 target address

amd\_dbgapi\_global\_address\_t amd\_dbgapi\_direct\_call\_register\_pair\_information\_t::target\_address

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.9 amd\_dbgapi\_dispatch\_id\_t Struct Reference

Opaque dispatch handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

· uint64 t handle

## 3.9.1 Detailed Description

Opaque dispatch handle.

Only unique within a single process.

## 3.9.2 Field Documentation

#### 3.9.2.1 handle

```
uint64_t amd_dbgapi_dispatch_id_t::handle
```

The documentation for this struct was generated from the following file:

• include/amd-dbgapi.h

## 3.10 amd\_dbgapi\_displaced\_stepping\_id\_t Struct Reference

Opaque displaced stepping handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

• uint64\_t handle

## 3.10.1 Detailed Description

Opaque displaced stepping handle.

Only unique within a single process.

#### 3.10.2 Field Documentation

## 3.10.2.1 handle

```
uint64_t amd_dbgapi_displaced_stepping_id_t::handle
```

The documentation for this struct was generated from the following file:

• include/amd-dbgapi.h

## 3.11 amd\_dbgapi\_event\_id\_t Struct Reference

Opaque event handle.

```
#include <amd-dbgapi.h>
```

#### **Data Fields**

• uint64\_t handle

## 3.11.1 Detailed Description

Opaque event handle.

Only unique within a single process.

## 3.11.2 Field Documentation

#### 3.11.2.1 handle

```
uint64_t amd_dbgapi_event_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.12 amd\_dbgapi\_process\_id\_t Struct Reference

Opaque process handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

• uint64 t handle

## 3.12.1 Detailed Description

Opaque process handle.

Unique for a single library initialization.

All operations that control an AMD GPU specify the process that is using the AMD GPU with the process handle. It is undefined to use handles returned by operations performed for one process, with operations performed for a different process.

#### 3.12.2 Field Documentation

#### 3.12.2.1 handle

```
uint64_t amd_dbgapi_process_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.13 amd\_dbgapi\_queue\_id\_t Struct Reference

Opaque queue handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

• uint64\_t handle

## 3.13.1 Detailed Description

Opaque queue handle.

Only unique within a single process.

#### 3.13.2 Field Documentation

#### 3.13.2.1 handle

```
uint64_t amd_dbgapi_queue_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.14 amd\_dbgapi\_register\_class\_id\_t Struct Reference

Opaque register class handle.

```
#include <amd-dbgapi.h>
```

#### **Data Fields**

uint64\_t handle

## 3.14.1 Detailed Description

Opaque register class handle.

A handle that denotes the set of classes of hardware registers supported by an architecture. The registers of the architecture all belong to one or more register classes. The register classes are a convenience for grouping registers that have similar uses and properties. They can be useful when presenting register lists to a user. For example, there could be a register class for *system*, *general*, and *vector*.

The handle is only unique within a specific architecture.

#### 3.14.2 Field Documentation

#### 3.14.2.1 handle

```
uint64_t amd_dbgapi_register_class_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.15 amd\_dbgapi\_register\_id\_t Struct Reference

Opaque register handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

• uint64\_t handle

## 3.15.1 Detailed Description

Opaque register handle.

A handle that denotes the set of hardware registers supported by an architecture.

The handle is only unique within a specific architecture.

## 3.15.2 Field Documentation

#### 3.15.2.1 handle

```
uint64_t amd_dbgapi_register_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.16 amd\_dbgapi\_shared\_library\_id\_t Struct Reference

Opaque shared library handle.

```
#include <amd-dbgapi.h>
```

### **Data Fields**

uint64\_t handle

## 3.16.1 Detailed Description

Opaque shared library handle.

Only unique within a single process.

The implementation of the library requests the client to notify it when a specific shared library is loaded and unloaded. This allows the library to set breakpoints within the shared library and access global variable data within it.

#### 3.16.2 Field Documentation

#### 3.16.2.1 handle

```
uint64_t amd_dbgapi_shared_library_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.17 amd dbgapi watchpoint id t Struct Reference

Opaque hardware data watchpoint handle.

```
#include <amd-dbgapi.h>
```

#### **Data Fields**

• uint64 t handle

## 3.17.1 Detailed Description

Opaque hardware data watchpoint handle.

Only unique within a single process.

## 3.17.2 Field Documentation

## 3.17.2.1 handle

```
uint64_t amd_dbgapi_watchpoint_id_t::handle
```

The documentation for this struct was generated from the following file:

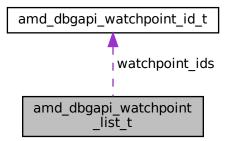
• include/amd-dbgapi.h

## 3.18 amd\_dbgapi\_watchpoint\_list\_t Struct Reference

A set of watchpoints.

```
#include <amd-dbgapi.h>
```

Collaboration diagram for amd\_dbgapi\_watchpoint\_list\_t:



## **Data Fields**

- size\_t count
- amd\_dbgapi\_watchpoint\_id\_t \* watchpoint\_ids

## 3.18.1 Detailed Description

A set of watchpoints.

Used by the AMD\_DBGAPI\_WAVE\_INFO\_WATCHPOINTS query to report the watchpoint(s) triggered by a wave.

## 3.18.2 Field Documentation

## 3.18.2.1 count

```
size_t amd_dbgapi_watchpoint_list_t::count
```

## 3.18.2.2 watchpoint\_ids

```
\verb|amd_dbgapi_watchpoint_id_t*| \verb|amd_dbgapi_watchpoint_list_t:: watchpoint_ids| \\
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

## 3.19 amd\_dbgapi\_wave\_id\_t Struct Reference

Opaque wave handle.

```
#include <amd-dbgapi.h>
```

## **Data Fields**

• uint64\_t handle

## 3.19.1 Detailed Description

Opaque wave handle.

Waves are the way the AMD GPU executes code.

Only unique within a single process.

## 3.19.2 Field Documentation

## 3.19.2.1 handle

```
uint64_t amd_dbgapi_wave_id_t::handle
```

The documentation for this struct was generated from the following file:

· include/amd-dbgapi.h

Data	Struct	ura F	)ocum	entation
DAIA	SILICI	ure i.	<i>)</i> ()(:	emianion

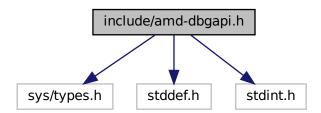
# **Chapter 4**

# **File Documentation**

## 4.1 include/amd-dbgapi.h File Reference

```
#include <sys/types.h>
#include <stddef.h>
#include <stdint.h>
```

Include dependency graph for amd-dbgapi.h:



## **Data Structures**

- struct amd\_dbgapi\_architecture\_id\_t
  - Opaque architecture handle.
- struct amd\_dbgapi\_process\_id\_t
  - Opaque process handle.
- struct amd\_dbgapi\_code\_object\_id\_t
  - Opaque code object handle.
- struct amd\_dbgapi\_agent\_id\_t

Opaque agent handle.

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· struct amd_dbgapi_queue_id_t
      Opaque queue handle.

    struct amd_dbgapi_dispatch_id_t

      Opaque dispatch handle.

    struct amd dbgapi wave id t

      Opaque wave handle.

    struct amd_dbgapi_displaced_stepping_id_t

      Opaque displaced stepping handle.
· struct amd dbgapi watchpoint id t
      Opaque hardware data watchpoint handle.

    struct amd_dbgapi_watchpoint_list_t

      A set of watchpoints.
• struct amd_dbgapi_register_class_id_t
      Opaque register class handle.
· struct amd_dbgapi_register_id_t
      Opaque register handle.

    struct amd_dbgapi_direct_call_register_pair_information_t

      Instruction information for direct call instructions.

    struct amd_dbgapi_address_class_id_t

      Opaque source language address class handle.

    struct amd_dbgapi_address_space_id_t

      Opaque address space handle.
· struct amd dbgapi event id t
      Opaque event handle.
```

struct amd\_dbgapi\_shared\_library\_id\_t

Opaque shared library handle.

struct amd dbgapi breakpoint id t

Opaque breakpoint handle.

struct amd\_dbgapi\_callbacks\_s

Callbacks that the client of the library must provide.

## **Macros**

- #define AMD DBGAPI CALL
- #define AMD DBGAPI EXPORT AMD DBGAPI EXPORT DECORATOR AMD DBGAPI CALL
- #define AMD\_DBGAPI\_IMPORT AMD\_DBGAPI\_IMPORT\_DECORATOR AMD\_DBGAPI\_CALL
- #define AMD DBGAPI AMD DBGAPI IMPORT
- #define AMD\_DBGAPI\_VERSION\_0\_24

The function was introduced in version 0.24 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.24".

#define AMD\_DBGAPI\_VERSION\_0\_30

The function was introduced in version 0.30 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.30".

#define AMD DBGAPI VERSION 0 41

The function was introduced in version 0.41 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.41".

#define AMD\_DBGAPI\_VERSION\_0\_42

The function was introduced in version 0.42 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.42".

• #define AMD DBGAPI VERSION 0 46

The function was introduced in version 0.46 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.46". #define AMD DBGAPI VERSION 0 47 The function was introduced in version 0.47 of the interface and has the symbol version string of "AMD\_DBGAPI\_0.47". #define AMD DBGAPI VERSION MAJOR 0 The semantic version of the interface following [semver.org][semver] rules. #define AMD DBGAPI VERSION MINOR 48 The minor version of the interface as a macro so it can be used by the preprocessor. #define AMD\_DBGAPI\_ARCHITECTURE\_NONE ((amd\_dbgapi\_architecture\_id\_t){ 0 }) The NULL architecture handle. #define AMD\_DBGAPI\_PROCESS\_NONE ((amd\_dbgapi\_process\_id\_t){ 0 }) The NULL process handle. #define AMD\_DBGAPI\_CODE\_OBJECT\_NONE ((amd\_dbgapi\_code\_object\_id\_t){ 0 }) The NULL code object handle. #define AMD\_DBGAPI\_AGENT\_NONE ((amd\_dbgapi\_agent\_id\_t){ 0 }) The NULL agent handle. #define AMD DBGAPI QUEUE NONE ((amd dbgapi queue id t){ 0 }) The NULL queue handle. #define AMD\_DBGAPI\_DISPATCH\_NONE ((amd\_dbgapi\_dispatch\_id\_t){ 0 }) The NULL dispatch handle. #define AMD DBGAPI WAVE NONE ((amd dbgapi wave id t){ 0 }) The NULL wave handle. • #define AMD DBGAPI DISPLACED STEPPING NONE (amd dbgapi displaced stepping id t{ 0 }) The NULL displaced stepping handle. #define AMD DBGAPI WATCHPOINT NONE ((amd dbgapi watchpoint id t){ 0 }) The NULL hardware data watchpoint handle. #define AMD DBGAPI REGISTER CLASS NONE ((amd dbgapi register class id t){ 0 }) The NULL register class handle. #define AMD\_DBGAPI\_REGISTER\_NONE ((amd\_dbgapi\_register\_id\_t){ 0 }) The NULL register handle. • #define AMD\_DBGAPI\_LANE\_NONE ((amd\_dbgapi\_lane\_id\_t) (-1)) The NULL lane handle. #define AMD\_DBGAPI\_ADDRESS\_CLASS\_NONE ((amd\_dbgapi\_address\_class\_id\_t){ 0 }) The NULL address class handle. #define AMD DBGAPI ADDRESS SPACE NONE ((amd dbgapi address space id t){ 0 }) The NULL address space handle. #define AMD\_DBGAPI\_ADDRESS\_SPACE\_GLOBAL ((amd\_dbgapi\_address\_space\_id\_t){ 1 }) The global address space handle. #define AMD\_DBGAPI\_EVENT\_NONE ((amd\_dbgapi\_event\_id\_t){ 0 }) The NULL event handle. #define AMD\_DBGAPI\_SHARED\_LIBRARY\_NONE ((amd\_dbgapi\_shared\_library\_id\_t){ 0 }) The NULL shared library handle.

The NULL breakpoint handle.

#define AMD DBGAPI BREAKPOINT NONE ((amd dbgapi breakpoint id t){ 0 })

### **Typedefs**

```
• typedef struct amd_dbgapi_callbacks_s amd_dbgapi_callbacks_t

    typedef uint64_t amd_dbgapi_global_address_t

         Integral type used for a global virtual memory address in the inferior process.

    typedef uint64_t amd_dbgapi_size_t

         Integral type used for sizes, including memory allocations, in the inferior.

    typedef pid_t amd_dbgapi_os_process_id_t

         Native operating system process ID.
   · typedef int amd_dbgapi_notifier_t
         Type used to notify the client of the library that a process may have pending events.

    typedef uint64_t amd_dbgapi_os_agent_id_t

         Native operating system agent ID.

    typedef uint64_t amd_dbgapi_os_queue_id_t

         Native operating system queue ID.
   typedef uint64_t amd_dbgapi_os_queue_packet_id_t
         Native operating system queue packet ID.

    typedef struct amd_dbgapi_symbolizer_id_s * amd_dbgapi_symbolizer_id_t

         Opaque client symbolizer handle.

    typedef struct amd_dbgapi_client_process_s * amd_dbgapi_client_process_id_t

         Opaque client process handle.

    typedef uint32_t amd_dbgapi_lane_id_t

         A wave lane handle.

    typedef uint64_t amd_dbgapi_segment_address_t

         Each address space has its own linear address to access it termed a segment address.

    typedef struct amd_dbgapi_client_thread_s * amd_dbgapi_client_thread_id_t

         Opaque client thread handle.
Enumerations

    enum amd_dbgapi_changed_t { AMD_DBGAPI_CHANGED_NO = 0, AMD_DBGAPI_CHANGED_YES = 1 }

         Indication of if a value has changed.
   enum amd_dbgapi_os_queue_type_t {
      AMD DBGAPI OS QUEUE TYPE UNKNOWN=0, AMD DBGAPI OS QUEUE TYPE HSA KERNEL DISPATCH MULTIPLE
     = 1, AMD_DBGAPI_OS_QUEUE_TYPE_HSA_KERNEL_DISPATCH_SINGLE_PRODUCER = 2, AMD_DBGAPI_OS_QUEUE_TY
     AMD_DBGAPI_OS_QUEUE_TYPE_AMD_PM4 = 257, AMD_DBGAPI_OS_QUEUE_TYPE_AMD_SDMA = 513,
     AMD DBGAPI OS QUEUE TYPE AMD SDMA XGMI = 514 }
         Native operating system queue type.
   enum amd_dbgapi_status_t {
     AMD_DBGAPI_STATUS_SUCCESS = 0, AMD_DBGAPI_STATUS_ERROR = -1, AMD_DBGAPI_STATUS_FATAL
     = -2, AMD DBGAPI STATUS ERROR UNIMPLEMENTED = -3,
     AMD DBGAPI STATUS ERROR NOT SUPPORTED = -4, AMD DBGAPI STATUS ERROR INVALID ARGUMENT
```

= -5, AMD\_DBGAPI\_STATUS\_ERROR\_INVALID\_ARGUMENT\_COMPATIBILITY = -6, AMD\_DBGAPI\_STATUS\_ERROR\_ALREA

= -9, AMD DBGAPI STATUS ERROR ALREADY ATTACHED = -10, AMD DBGAPI STATUS ERROR INVALID ARCHITECT

AMD DBGAPI STATUS ERROR ILLEGAL INSTRUCTION = -12, AMD DBGAPI STATUS ERROR INVALID CODE OBJECT

AMD\_DBGAPI\_STATUS\_ERROR\_NOT\_INITIALIZED = -8, AMD\_DBGAPI\_STATUS\_ERROR\_RESTRICTION

```
= -13, AMD_DBGAPI_STATUS_ERROR_INVALID_ELF_AMDGPU_MACHINE = -14, AMD_DBGAPI_STATUS_ERROR_INVALID
 = -15.
 AMD DBGAPI STATUS ERROR INVALID AGENT ID = -16, AMD DBGAPI STATUS ERROR INVALID QUEUE ID
 =-17, AMD DBGAPI STATUS ERROR INVALID DISPATCH ID =-18, AMD DBGAPI STATUS ERROR INVALID WAVE ID
 AMD DBGAPI STATUS ERROR WAVE NOT STOPPED = -20, AMD DBGAPI STATUS ERROR WAVE STOPPED
 = -21, AMD_DBGAPI_STATUS_ERROR_WAVE_OUTSTANDING_STOP = -22, AMD_DBGAPI_STATUS_ERROR_WAVE_NOT_
 AMD DBGAPI STATUS ERROR INVALID DISPLACED STEPPING ID = -24, AMD DBGAPI STATUS ERROR DISPLACED
 = -25, AMD_DBGAPI_STATUS_ERROR_INVALID_WATCHPOINT_ID = -26, AMD_DBGAPI_STATUS_ERROR_NO_WATCHPOI
 = -27,
 AMD_DBGAPI_STATUS_ERROR_INVALID_REGISTER_CLASS_ID = -28, AMD_DBGAPI_STATUS_ERROR_INVALID_REGIST
 = -29, AMD DBGAPI STATUS ERROR INVALID LANE ID = -30, AMD DBGAPI STATUS ERROR INVALID ADDRESS CLA
 AMD DBGAPI STATUS ERROR INVALID ADDRESS SPACE ID = -32, AMD DBGAPI STATUS ERROR MEMORY ACCES
 = -33, AMD_DBGAPI_STATUS_ERROR_INVALID_ADDRESS_SPACE_CONVERSION = -34, AMD_DBGAPI_STATUS_ERROR_
 AMD_DBGAPI_STATUS_ERROR_INVALID_SHARED_LIBRARY_ID = -36, AMD_DBGAPI_STATUS_ERROR_INVALID_BREAKI
 = -37, AMD DBGAPI STATUS ERROR CLIENT CALLBACK = -38, AMD DBGAPI STATUS ERROR INVALID CLIENT PROC
 AMD DBGAPI STATUS ERROR PROCESS EXITED = -40, AMD DBGAPI STATUS ERROR LIBRARY NOT LOADED
 = -41, AMD DBGAPI STATUS ERROR SYMBOL NOT FOUND = -42, AMD DBGAPI STATUS ERROR INVALID ADDRESS
 AMD DBGAPI STATUS ERROR DISPLACED STEPPING ACTIVE = -44, AMD DBGAPI STATUS ERROR RESUME DISPL
 = -45
    AMD debugger API status codes.

    enum amd dbgapi architecture info t {

 AMD DBGAPI ARCHITECTURE INFO NAME = 1, AMD DBGAPI ARCHITECTURE INFO ELF AMDGPU MACHINE
 = 2, AMD_DBGAPI_ARCHITECTURE_INFO_LARGEST_INSTRUCTION_SIZE = 3, AMD_DBGAPI_ARCHITECTURE_INFO_MIN
 = 4,
 AMD_DBGAPI_ARCHITECTURE_INFO_BREAKPOINT_INSTRUCTION_SIZE = 5, AMD_DBGAPI_ARCHITECTURE_INFO_BRE
 = 6, AMD DBGAPI ARCHITECTURE INFO BREAKPOINT INSTRUCTION PC ADJUST = 7, AMD DBGAPI ARCHITECTURE
 = 8  }
    Architecture queries that are supported by amd_dbgapi_architecture_get_info.

    enum amd dbgapi instruction kind t {

 AMD_DBGAPI_INSTRUCTION_KIND_UNKNOWN = 0, AMD_DBGAPI_INSTRUCTION_KIND_SEQUENTIAL =
 1, AMD_DBGAPI_INSTRUCTION_KIND_DIRECT_BRANCH = 2, AMD_DBGAPI_INSTRUCTION_KIND_DIRECT_BRANCH_CON
 = 3,
 AMD DBGAPI INSTRUCTION KIND INDIRECT BRANCH REGISTER PAIR = 4, AMD DBGAPI INSTRUCTION KIND INDIR
 = 5, AMD_DBGAPI_INSTRUCTION_KIND_DIRECT_CALL_REGISTER_PAIR = 6, AMD_DBGAPI_INSTRUCTION_KIND_INDIRE
 = 7,
 AMD DBGAPI INSTRUCTION KIND TERMINATE = 8, AMD DBGAPI INSTRUCTION KIND TRAP = 9,
 AMD DBGAPI INSTRUCTION KIND HALT = 10, AMD DBGAPI INSTRUCTION KIND BARRIER = 11,
 AMD DBGAPI INSTRUCTION KIND SLEEP = 12, AMD DBGAPI INSTRUCTION KIND SPECIAL = 13 }
    The kinds of instruction classifications.
• enum amd_dbgapi_instruction_properties_t { AMD_DBGAPI_INSTRUCTION_PROPERTY_NONE = 0ULL,
 AMD DBGAPI INSTRUCTION PROPERTY RESERVED = (1ULL << 63) }
    A bit mask of the properties of an instruction.

    enum amd dbgapi process info t {

 AMD_DBGAPI_PROCESS_INFO_NOTIFIER = 1, AMD_DBGAPI_PROCESS_INFO_WATCHPOINT_COUNT =
 2, AMD DBGAPI PROCESS INFO WATCHPOINT SHARE = 3, AMD DBGAPI PROCESS INFO PRECISE MEMORY SUPP
 AMD DBGAPI PROCESS INFO OS ID = 5 }
```

Process queries that are supported by amd\_dbgapi\_process\_get\_info. enum amd\_dbgapi\_progress\_t{AMD\_DBGAPI\_PROGRESS\_NORMAL = 0, AMD\_DBGAPI\_PROGRESS\_NO\_FORWARD The kinds of progress supported by the library. enum amd dbgapi wave creation t{AMD DBGAPI WAVE CREATION NORMAL = 0, AMD DBGAPI WAVE CREATION STO The kinds of wave creation supported by the hardware. enum amd\_dbgapi\_code\_object\_info\_t { AMD\_DBGAPI\_CODE\_OBJECT\_INFO\_PROCESS = 1, AMD\_DBGAPI\_CODE\_OBJECT\_ = 2, AMD\_DBGAPI\_CODE\_OBJECT\_INFO\_LOAD\_ADDRESS = 3 } Code object queries that are supported by amd\_dbgapi\_code\_object\_get\_info. enum amd\_dbgapi\_agent\_info\_t { AMD DBGAPI AGENT INFO PROCESS = 1, AMD DBGAPI AGENT INFO NAME = 2, AMD DBGAPI AGENT INFO ARCHI = 3, AMD DBGAPI AGENT INFO PCI SLOT = 4, AMD DBGAPI AGENT INFO PCI VENDOR ID = 5, AMD DBGAPI AGENT INFO PCI DEVICE ID = 6, AMD\_DBGAPI\_AGENT\_INFO\_EXECUTION\_UNIT\_COUNT = 7, AMD\_DBGAPI\_AGENT\_INFO\_MAX\_WAVES\_PER\_EXECUTION\_UNIT\_COUNT = 7, AMD\_DBGAPI\_AGE = 8. AMD\_DBGAPI\_AGENT\_INFO\_OS\_ID = 9 } Agent queries that are supported by amd\_dbgapi\_agent\_get\_info. enum amd\_dbgapi\_queue\_info\_t { AMD DBGAPI QUEUE INFO AGENT = 1, AMD DBGAPI QUEUE INFO PROCESS = 2, AMD DBGAPI QUEUE INFO ARCH = 3, AMD\_DBGAPI\_QUEUE\_INFO\_TYPE = 4, AMD DBGAPI QUEUE INFO STATE = 5, AMD DBGAPI QUEUE INFO ERROR REASON = 6, AMD DBGAPI QUEUE INFO = 7, AMD\_DBGAPI\_QUEUE\_INFO\_SIZE = 8, AMD\_DBGAPI\_QUEUE\_INFO\_OS\_ID = 9 } Queue queries that are supported by amd\_dbgapi\_queue\_get\_info. enum amd dbgapi queue state t{AMD DBGAPI QUEUE STATE VALID=1,AMD DBGAPI QUEUE STATE ERROR = 2 } Queue state. enum amd\_dbgapi\_queue\_error\_reason\_t { AMD\_DBGAPI\_QUEUE\_ERROR\_REASON\_NONE = OULL, AMD\_DBGAPI\_QUEUE\_ERROR\_REASON\_INVALID\_PACKET = (1ULL << 0), AMD DBGAPI QUEUE ERROR REASON MEMORY VIOLATION = (1ULL << 1), AMD DBGAPI QUEUE ERROR REASON ASSERT TRAP = (1ULL << 2), AMD\_DBGAPI\_QUEUE\_ERROR\_REASON\_WAVE\_ERROR = (1ULL << 3), AMD\_DBGAPI\_QUEUE\_ERROR\_REASON\_RESE = (1ULL << 63) } A bit mask of the reasons that a queue is in error. enum amd\_dbgapi\_dispatch\_info\_t { AMD DBGAPI DISPATCH INFO QUEUE = 1, AMD DBGAPI DISPATCH INFO AGENT = 2, AMD DBGAPI DISPATCH INFO = 3, AMD DBGAPI DISPATCH INFO ARCHITECTURE = 4, AMD DBGAPI DISPATCH INFO OS QUEUE PACKET ID = 5, AMD DBGAPI DISPATCH INFO BARRIER

enum amd\_dbgapi\_dispatch\_info\_t {

AMD\_DBGAPI\_DISPATCH\_INFO\_QUEUE = 1, AMD\_DBGAPI\_DISPATCH\_INFO\_AGENT = 2, AMD\_DBGAPI\_DISPATCH\_INFO

= 3, AMD\_DBGAPI\_DISPATCH\_INFO\_ARCHITECTURE = 4,

AMD\_DBGAPI\_DISPATCH\_INFO\_OS\_QUEUE\_PACKET\_ID = 5, AMD\_DBGAPI\_DISPATCH\_INFO\_BARRIER

= 6, AMD\_DBGAPI\_DISPATCH\_INFO\_ACQUIRE\_FENCE = 7, AMD\_DBGAPI\_DISPATCH\_INFO\_RELEASE\_FENCE

= 8,

AMD\_DBGAPI\_DISPATCH\_INFO\_GRID\_DIMENSIONS = 9, AMD\_DBGAPI\_DISPATCH\_INFO\_WORK\_GROUP\_SIZES

= 10, AMD\_DBGAPI\_DISPATCH\_INFO\_GRID\_SIZES = 11, AMD\_DBGAPI\_DISPATCH\_INFO\_PRIVATE\_SEGMENT\_SIZE

= 12,

AMD\_DBGAPI\_DISPATCH\_INFO\_GROUP\_SEGMENT\_SIZE = 13, AMD\_DBGAPI\_DISPATCH\_INFO\_KERNEL\_ARGUMENT\_S

= 14, AMD\_DBGAPI\_DISPATCH\_INFO\_KERNEL\_DESCRIPTOR\_ADDRESS = 15, AMD\_DBGAPI\_DISPATCH\_INFO\_KERNEL\_E

= 16,

AMD\_DBGAPI\_DISPATCH\_INFO\_KERNEL\_COMPLETION\_ADDRESS = 17 }

Dispatch queries that are supported by amd\_dbgapi\_dispatch\_get\_info.

enum amd\_dbgapi\_dispatch\_barrier\_t{AMD\_DBGAPI\_DISPATCH\_BARRIER\_

enum amd\_dbgapi\_dispatch\_barrier\_t { AMD\_DBGAPI\_DISPATCH\_BARRIER\_NONE = 0, AMD\_DBGAPI\_DISPATCH\_BARRIER\_
 = 1 }

```
Dispatch barrier.
```

```
    enum amd_dbgapi_dispatch_fence_scope_t { AMD_DBGAPI_DISPATCH_FENCE_SCOPE_NONE = 0,

  AMD DBGAPI DISPATCH FENCE SCOPE AGENT = 1, AMD DBGAPI DISPATCH FENCE SCOPE SYSTEM
  = 2 }
       Dispatch memory fence scope.
enum amd_dbgapi_wave_info_t {
   AMD_DBGAPI_WAVE_INFO_STATE = 1, AMD_DBGAPI_WAVE_INFO_STOP_REASON = 2, AMD_DBGAPI_WAVE_INFO_WAT
  = 3, AMD DBGAPI WAVE INFO DISPATCH = 4,
  AMD DBGAPI WAVE INFO QUEUE = 5, AMD DBGAPI WAVE INFO AGENT = 6, AMD DBGAPI WAVE INFO PROCESS
  = 7, AMD DBGAPI WAVE INFO ARCHITECTURE = 8,
  AMD_DBGAPI_WAVE_INFO_PC = 9, AMD_DBGAPI_WAVE_INFO_EXEC_MASK = 10, AMD_DBGAPI_WAVE_INFO_WORK_GI
  = 11, AMD DBGAPI WAVE INFO WAVE NUMBER IN WORK GROUP = 12,
  AMD_DBGAPI_WAVE_INFO_LANE_COUNT = 13 }
       Wave queries that are supported by amd_dbgapi_wave_get_info.
• enum amd_dbgapi_wave_state_t { AMD_DBGAPI_WAVE_STATE_RUN = 1, AMD_DBGAPI_WAVE_STATE_SINGLE_STEP
   = 2, AMD_DBGAPI_WAVE_STATE_STOP = 3 }
       The execution state of a wave.
enum amd_dbgapi_wave_stop_reason_t {
  AMD DBGAPI WAVE STOP REASON NONE = OULL, AMD DBGAPI WAVE STOP REASON BREAKPOINT
  = (1ULL << 0), AMD_DBGAPI_WAVE_STOP_REASON_WATCHPOINT = (1ULL << 1), AMD_DBGAPI_WAVE_STOP_REASON
  = (1ULL << 2).
  AMD DBGAPI WAVE STOP REASON QUEUE ERROR = (1ULL << 3), AMD DBGAPI WAVE STOP REASON FP INPUT
  = (1ULL << 4), AMD_DBGAPI_WAVE_STOP_REASON_FP_DIVIDE_BY_0 = (1ULL << 5), AMD_DBGAPI_WAVE_STOP_PREASON_FP_DIVIDE_BY_0 = (1UL
  = (1ULL << 6),
  AMD DBGAPI WAVE STOP REASON FP UNDERFLOW = (1ULL << 7), AMD DBGAPI WAVE STOP REASON FP INEXA
  = (1ULL << 8), AMD DBGAPI WAVE STOP REASON FP INVALID OPERATION = (1ULL << 9),
  AMD_DBGAPI_WAVE_STOP_REASON_INT_DIVIDE_BY_0 = (1ULL << 10),
  AMD DBGAPI WAVE STOP REASON DEBUG TRAP = (1ULL << 11), AMD DBGAPI WAVE STOP REASON ASSERT TF
  = (1ULL << 12), AMD_DBGAPI_WAVE_STOP_REASON_TRAP = (1ULL << 13), AMD_DBGAPI_WAVE_STOP_REASON_MEN
  AMD_DBGAPI_WAVE_STOP_REASON_ILLEGAL_INSTRUCTION = (1ULL << 15), AMD_DBGAPI_WAVE_STOP_REASON_E(
  = (1ULL << 16), AMD DBGAPI WAVE STOP REASON FATAL HALT = (1ULL << 17), AMD DBGAPI WAVE STOP REASO
  = (1ULL << 18),
  AMD_DBGAPI_WAVE_STOP_REASON_RESERVED = (1ULL << 63) }
       A bit mask of the reasons that a wave stopped.

    enum amd_dbgapi_resume_mode_t{AMD_DBGAPI_RESUME_MODE_NORMAL = 0, AMD_DBGAPI_RESUME_MODE_SINGLE

       The mode in which to resuming the execution of a wave.

    enum amd_dbgapi_displaced_stepping_info_t { AMD_DBGAPI_DISPLACED_STEPPING_INFO_PROCESS = 1

       Displaced stepping queries that are supported by amd_dbgapi_displaced_stepping_id_t.

    enum amd_dbgapi_watchpoint_info_t { AMD_DBGAPI_WATCHPOINT_INFO_PROCESS = 1 }

       Watchpoint queries that are supported by amd_dbgapi_watchpoint_get_info.
enum amd_dbgapi_watchpoint_share_kind_t { AMD_DBGAPI_WATCHPOINT_SHARE_KIND_UNSUPPORTED
   = 0, AMD_DBGAPI_WATCHPOINT_SHARE_KIND_UNSHARED = 1, AMD_DBGAPI_WATCHPOINT_SHARE_KIND_SHARED
  = 2 }
       The way watchpoints are shared between processes.

    enum amd dbgapi watchpoint kind t{AMD DBGAPI WATCHPOINT KIND LOAD=1, AMD DBGAPI WATCHPOINT KIND S

  = 2, AMD DBGAPI WATCHPOINT KIND RMW = 3, AMD DBGAPI WATCHPOINT KIND ALL = 4 }
       Watchpoint memory access kinds.
enum amd_dbgapi_register_class_info_t { AMD_DBGAPI_REGISTER_CLASS_INFO_ARCHITECTURE = 1,
  AMD DBGAPI REGISTER CLASS INFO NAME = 2 }
```

```
Register class queries that are supported by amd_dbgapi_architecture_register_class_get_info.

    enum amd dbgapi register info t {

 AMD DBGAPI REGISTER INFO ARCHITECTURE = 1, AMD DBGAPI REGISTER INFO NAME = 2,
 AMD_DBGAPI_REGISTER_INFO_SIZE = 3, AMD_DBGAPI_REGISTER_INFO_TYPE = 4,
 AMD_DBGAPI_REGISTER_INFO_DWARF = 5 }
     Register queries that are supported by amd_dbgapi_register_get_info.

    enum amd dbgapi register exists t{AMD DBGAPI REGISTER ABSENT = 0, AMD DBGAPI REGISTER PRESENT

  = 1  }
     Indication of if a wave has a register.

    enum amd dbgapi register class state t { AMD DBGAPI REGISTER CLASS STATE NOT MEMBER = 0,

 AMD DBGAPI REGISTER CLASS STATE MEMBER = 1 }
     Indication of whether a register is a member of a register class.
enum amd_dbgapi_address_class_info_t { AMD_DBGAPI_ADDRESS_CLASS_INFO_ARCHITECTURE = 1,
 AMD_DBGAPI_ADDRESS_CLASS_INFO_NAME = 2, AMD_DBGAPI_ADDRESS_CLASS_INFO_ADDRESS_SPACE
 = 3, AMD_DBGAPI_ADDRESS_CLASS_INFO_DWARF = 4 }
     Source language address class queries that are supported by amd_dbgapi_address_class_get_info.

    enum amd_dbgapi_address_space_access_t { AMD_DBGAPI_ADDRESS_SPACE_ACCESS_ALL = 1,

 AMD_DBGAPI_ADDRESS_SPACE_ACCESS_PROGRAM_CONSTANT = 2, AMD_DBGAPI_ADDRESS_SPACE_ACCESS_DISF
     Indication of how the address space is accessed.

    enum amd dbgapi address space info t {

 AMD_DBGAPI_ADDRESS_SPACE_INFO_ARCHITECTURE = 1, AMD_DBGAPI_ADDRESS_SPACE_INFO_NAME
 = 2, AMD DBGAPI ADDRESS SPACE INFO ADDRESS SIZE = 3, AMD DBGAPI ADDRESS SPACE INFO NULL ADDRES
 AMD DBGAPI ADDRESS SPACE INFO ACCESS = 5, AMD DBGAPI ADDRESS SPACE INFO DWARF =
 6 }
    Address space queries that are supported by amd_dbgapi_address_space_get_info.

    enum amd dbgapi address space alias t{AMD DBGAPI ADDRESS SPACE ALIAS NONE = 0, AMD DBGAPI ADDRESS S

  = 1  }
     Indication of whether addresses in two address spaces may alias.
• enum amd dbgapi address class state t { AMD DBGAPI ADDRESS CLASS STATE NOT MEMBER = 0,
 AMD DBGAPI ADDRESS CLASS STATE MEMBER = 1 }
     Indication of whether a segment address in an address space is a member of an source language address class.

    enum amd dbgapi memory precision t{AMD DBGAPI MEMORY PRECISION NONE = 0, AMD DBGAPI MEMORY PRECIS

 = 1
    Memory access precision.

    enum amd dbgapi event kind t {

 AMD_DBGAPI_EVENT_KIND_NONE = 0, AMD_DBGAPI_EVENT_KIND_WAVE_STOP = 1, AMD_DBGAPI_EVENT_KIND_WAVE
 = 2, AMD DBGAPI EVENT KIND CODE OBJECT LIST UPDATED = 3,
 AMD DBGAPI EVENT KIND BREAKPOINT RESUME = 4, AMD DBGAPI EVENT KIND RUNTIME = 5,
 AMD DBGAPI EVENT KIND QUEUE ERROR = 6 }
     The event kinds.

    enum amd dbgapi runtime state t{AMD DBGAPI RUNTIME STATE LOADED SUCCESS=1, AMD DBGAPI RUNTIME ST

 = 2, AMD_DBGAPI_RUNTIME_STATE_LOADED_ERROR_RESTRICTION = 3 }
     Inferior runtime state.
enum amd_dbgapi_event_info_t {
 AMD_DBGAPI_EVENT_INFO_PROCESS = 1, AMD_DBGAPI_EVENT_INFO_KIND = 2, AMD_DBGAPI_EVENT_INFO_WAVE
 = 3, AMD DBGAPI EVENT INFO BREAKPOINT = 4,
 AMD DBGAPI EVENT INFO CLIENT THREAD = 5, AMD DBGAPI EVENT INFO RUNTIME STATE = 6}
     Event queries that are supported by amd_dbgapi_event_get_info.
```

```
    enum amd_dbgapi_log_level_t {
        AMD_DBGAPI_LOG_LEVEL_NONE = 0, AMD_DBGAPI_LOG_LEVEL_FATAL_ERROR = 1, AMD_DBGAPI_LOG_LEVEL_WARN
        = 2, AMD_DBGAPI_LOG_LEVEL_INFO = 3,
        AMD_DBGAPI_LOG_LEVEL_VERBOSE = 4 }
        The logging levels supported.
```

 enum amd\_dbgapi\_shared\_library\_state\_t { AMD\_DBGAPI\_SHARED\_LIBRARY\_STATE\_LOADED = 1, AMD\_DBGAPI\_SHARED\_LIBRARY\_STATE\_UNLOADED = 2}

The state of a shared library.

enum amd\_dbgapi\_shared\_library\_info\_t { AMD\_DBGAPI\_SHARED\_LIBRARY\_INFO\_PROCESS = 1 }

Shared library queries that are supported by amd\_dbgapi\_shared\_library\_get\_info.

 enum amd\_dbgapi\_breakpoint\_info\_t { AMD\_DBGAPI\_BREAKPOINT\_INFO\_SHARED\_LIBRARY = 1, AMD\_DBGAPI\_BREAKPOINT\_INFO\_PROCESS = 2}

Breakpoint queries that are supported by amd\_dbgapi\_breakpoint\_get\_info.

enum amd\_dbgapi\_breakpoint\_action\_t { AMD\_DBGAPI\_BREAKPOINT\_ACTION\_RESUME = 1, AMD\_DBGAPI\_BREAKPOINT\_A
 = 2 }

The action to perform after reporting a breakpoint has been hit.

#### **Functions**

 amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_get\_status\_string (amd\_dbgapi\_status\_t status, const char \*\*status\_string) AMD\_DBGAPI\_VERSION\_0\_24

Query a textual description of a status code.

- void AMD\_DBGAPI amd\_dbgapi\_get\_version (uint32\_t \*major, uint32\_t \*minor, uint32\_t \*patch) AMD\_DBGAPI\_VERSION\_0\_24

  Query the version of the installed library.
- const char AMD\_DBGAPI \* amd\_dbgapi\_get\_build\_name (void) AMD\_DBGAPI\_VERSION\_0\_24

Query the installed library build name.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_initialize (amd\_dbgapi\_callbacks\_t \*callbacks) AMD\_DBGAPI\_VERSION\_0\_30
   Initialize the library.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_finalize (void) AMD\_DBGAPI\_VERSION\_0\_24
   Finalize the library.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_get\_info (amd\_dbgapi\_architecture\_id\_t architecture\_id, amd\_dbgapi\_architecture\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_30
- Query information about an architecture.

   amd dbgapi status t AMD DBGAPI amd dbgapi get architecture (uint32 t elf amdgpu machine, amd dbgapi architecture id t
- \*architecture\_id) AMD\_DBGAPI\_VERSION\_0\_24

Get an architecture from the AMD GPU ELF EF\_AMDGPU\_MACH value corresponding to the architecture.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_disassemble\_instruction (amd\_dbgapi\_architecture\_id\_t architecture\_id, amd\_dbgapi\_global\_address\_t address, amd\_dbgapi\_size\_t \*size, const void \*memory, char \*\*instruction\_text, amd\_dbgapi\_symbolizer\_id\_t symbolizer\_id, amd\_dbgapi\_status\_t(\*symbolizer)(amd\_dbgapi\_symbolizer\_id\_t symbolizer\_id, amd\_dbgapi\_global\_address\_t address, char \*\*symbol\_text)) AMD\_DBGAPI\_VERSION\_0\_30

Disassemble a single instruction.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_classify\_instruction (amd\_dbgapi\_architecture\_id\_t architecture ←
 \_id, amd\_dbgapi\_global\_address\_t address, amd\_dbgapi\_size\_t \*size, const void \*memory, amd\_dbgapi\_instruction\_kind\_t
 \*instruction\_kind, amd\_dbgapi\_instruction\_properties\_t \*instruction\_properties, void \*\*instruction\_information)
 AMD\_DBGAPI\_VERSION\_0\_46

Classify a single instruction.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_get\_info (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_process\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about a process.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_attach (amd\_dbgapi\_client\_process\_id\_t client\_← process id, amd dbgapi process id t\*process id) AMD DBGAPI VERSION 0 30

Attach to a process in order to provide debug control of the AMD GPUs it uses.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_detach (amd\_dbgapi\_process\_id\_t process\_id)
 AMD\_DBGAPI\_VERSION\_0\_24

Detach from a process and no longer have debug control of the AMD GPU devices it uses.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_set\_progress (amd\_dbgapi\_process\_id\_t process
id, amd dbgapi progress t progress) AMD DBGAPI VERSION 0 47

Set the progress required for a process.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_set\_wave\_creation (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_wave\_creation\_t creation) AMD\_DBGAPI\_VERSION\_0\_24

Set the wave creation mode for a process.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_code\_object\_list (amd\_dbgapi\_process\_id\_t process\_id, size\_t \*code\_object\_count, amd\_dbgapi\_code\_object\_id\_t \*\*code\_objects, amd\_dbgapi\_changed\_t \*changed) AMD\_DBGAPI\_VERSION\_0\_41

Return the list of loaded code objects.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_agent\_get\_info (amd\_dbgapi\_agent\_id\_t agent\_id, amd\_dbgapi\_agent\_info\_t query, size t value size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about an agent.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_agent\_list (amd\_dbgapi\_process\_id\_t process\_id, size\_t \*agent\_count, amd\_dbgapi\_agent\_id\_t \*\*agents, amd\_dbgapi\_changed\_t \*changed) AMD\_DBGAPI\_VERSION\_0\_41
   Return the list of agents.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_queue\_get\_info (amd\_dbgapi\_queue\_id\_t queue\_id, amd\_dbgapi\_queue\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about a queue.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_queue\_list (amd\_dbgapi\_process\_id\_t process\_id, size\_t \*queue\_count, amd\_dbgapi\_queue\_id\_t \*\*queues, amd\_dbgapi\_changed\_t \*changed) AMD\_DBGAPI\_VERSION\_0\_41
   Return the list of queues.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_queue\_packet\_list (amd\_dbgapi\_queue\_id\_t queue\_id, amd\_dbgapi\_os\_queue\_packet\_id\_t \*read\_packet\_id, amd\_dbgapi\_os\_queue\_packet\_id\_t \*write\_packet\_id, size\_t \*packets\_byte\_size, void \*\*packets\_bytes) AMD\_DBGAPI\_VERSION\_0\_41

Return the packets for a queue.

 amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_dispatch\_get\_info (amd\_dbgapi\_dispatch\_id\_t dispatch\_id, amd\_dbgapi\_dispatch\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about a dispatch.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_dispatch\_list (amd\_dbgapi\_process\_id\_t process
 \_id, size\_t \*dispatch\_count, amd\_dbgapi\_dispatch\_id\_t \*\*dispatches, amd\_dbgapi\_changed\_t \*changed)
 AMD\_DBGAPI\_VERSION\_0\_41

Return the list of dispatches.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_get\_info (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_wave\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about a wave.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_wave\_list (amd\_dbgapi\_process\_id\_t process\_id, size\_t \*wave\_count, amd\_dbgapi\_wave\_id\_t \*\*waves, amd\_dbgapi\_changed\_t \*changed) AMD\_DBGAPI\_VERSION\_0\_41
 Return the list of existing waves.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_stop (amd\_dbgapi\_wave\_id\_t wave\_id) AMD\_DBGAPI\_VERSION\_0\_41 Request a wave to stop executing.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_resume (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_resume\_mode\_t resume\_mode) AMD\_DBGAPI\_VERSION\_0\_46

Resume execution of a stopped wave.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_displaced\_stepping\_get\_info (amd\_dbgapi\_displaced\_stepping\_id\_t displaced\_stepping\_id, amd\_dbgapi\_displaced\_stepping\_info\_t query, size\_t value\_size, void \*value)
 AMD\_DBGAPI\_VERSION\_0\_41

Query information about a displaced stepping buffer.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_displaced\_stepping\_start (amd\_dbgapi\_wave\_id\_t wave
 \_id, const void \*saved\_instruction\_bytes, amd\_dbgapi\_displaced\_stepping\_id\_t \*displaced\_stepping)
 AMD\_DBGAPI\_VERSION\_0\_46

Associate an active displaced stepping buffer with a wave.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_displaced\_stepping\_complete (amd\_dbgapi\_wave\_id\_t wave id, amd dbgapi displaced stepping id t displaced stepping) AMD DBGAPI VERSION 0 42

Complete a displaced stepping buffer for a wave.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_watchpoint\_get\_info (amd\_dbgapi\_watchpoint\_id\_t watchpoint
 \_id, amd\_dbgapi\_watchpoint\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about a watchpoint.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_set\_watchpoint (amd\_dbgapi\_process\_id\_t process 
 \_id, amd\_dbgapi\_global\_address\_t address, amd\_dbgapi\_size\_t size, amd\_dbgapi\_watchpoint\_kind\_t 
 kind, amd\_dbgapi\_watchpoint\_id\_t \*watchpoint\_id, amd\_dbgapi\_global\_address\_t \*watchpoint\_address, 
 amd\_dbgapi\_size\_t \*watchpoint\_size) AMD\_DBGAPI\_VERSION\_0\_41

Set a hardware data watchpoint.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_remove\_watchpoint (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_watchpoint\_id\_t watchpoint\_id) AMD\_DBGAPI\_VERSION\_0\_24

Remove a hardware data watchpoint previously set by amd\_dbgapi\_set\_watchpoint.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_register\_class\_get\_info (amd\_dbgapi\_register\_class\_id\_t register\_class\_id, amd\_dbgapi\_register\_class\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about a register class of an architecture.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_register\_class\_list (amd\_dbgapi\_architecture\_id\_t architecture\_id, size\_t \*register\_class\_count, amd\_dbgapi\_register\_class\_id\_t \*\*register\_classes) AMD\_DBGAPI\_VERSION\_0\_2.
   Report the list of register classes supported by the architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_register\_get\_info (amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_register\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_46

Query information about a register.

 amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_register\_exists (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_register\_exists\_t \*exists) AMD\_DBGAPI\_VERSION\_0\_41

Query if a register exists for a wave.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_register\_list (amd\_dbgapi\_architecture\_id\_t architecture\_id, size\_t \*register\_count, amd\_dbgapi\_register\_id\_t \*\*registers) AMD\_DBGAPI\_VERSION\_0\_24

Report the list of registers supported by the architecture.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_wave\_register\_list (amd\_dbgapi\_wave\_id\_t wave\_id, size\_
 t \*register\_count, amd\_dbgapi\_register\_id\_t \*\*registers) AMD\_DBGAPI\_VERSION\_0\_41

Report the list of registers supported by a wave.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_dwarf\_register\_to\_register (amd\_dbgapi\_architecture\_id\_t architecture\_id, uint64\_t dwarf\_register, amd\_dbgapi\_register\_id\_t \*register\_id) AMD\_DBGAPI\_VERSION\_0\_41

Return a register handle from an AMD GPU DWARF register number for an architecture.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_register\_is\_in\_register\_class (amd\_dbgapi\_register\_class\_id\_t register\_class\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_register\_class\_state\_t \*register\_class\_ state\_) AMD\_DBGAPI\_VERSION\_0\_41

Determine if a register is a member of a register class.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_read\_register (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_size\_t offset, amd\_dbgapi\_size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41
   Read a register.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_write\_register (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_size\_t offset, amd\_dbgapi\_size\_t value\_size, const void \*value) AMD\_DBGAPI\_VERSION\_0\_42
   Write a register.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_prefetch\_register (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_register\_id\_t register\_id, amd\_dbgapi\_size\_t register\_count) AMD\_DBGAPI\_VERSION\_0\_41
   Prefetch register values.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_address\_class\_get\_info (amd\_dbgapi\_address\_class\_id\_t address\_class\_id, amd\_dbgapi\_address\_class\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_46
   Query information about a source language address class of an architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_address\_class\_list (amd\_dbgapi\_architecture\_id\_t architecture\_id, size\_t \*address\_class\_count, amd\_dbgapi\_address\_class\_id\_t \*\*address\_classes) AMD\_DBGAPI\_VERSION\_0\_
   Report the list of source language address classes supported by the architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_dwarf\_address\_class\_to\_address\_class (amd\_dbgapi\_architecture\_id\_t architecture\_id, uint64\_t dwarf\_address\_class, amd\_dbgapi\_address\_class\_id\_t \*address\_class\_id) AMD\_DBGAPI\_VERSION\_0\_

Return the architecture source language address class from a DWARF address class number for an architecture.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_address\_space\_get\_info (amd\_dbgapi\_address\_space\_id\_t address\_space\_id, amd\_dbgapi\_address\_space\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_46
   Query information about an address space.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_architecture\_address\_space\_list (amd\_dbgapi\_architecture\_id\_t architecture\_id, size\_t \*address\_space\_count, amd\_dbgapi\_address\_space\_id\_t \*\*address\_spaces) AMD\_DBGAPI\_VERSION\_0
   Report the list of address spaces supported by the architecture.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_dwarf\_address\_space\_to\_address\_space (amd\_dbgapi\_architecture\_id\_t architecture\_id, uint64\_t dwarf\_address\_space, amd\_dbgapi\_address\_space\_id\_t \*address\_space\_id)
   AMD\_DBGAPI\_VERSION\_0\_41

Return the address space from an AMD GPU DWARF address space number for an architecture.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_address\_spaces\_may\_alias (amd\_dbgapi\_address\_space\_id\_t address\_space\_id2, amd\_dbgapi\_address\_space\_alias\_t \*address\_space\_alias) AMD\_DBGAPI\_VERSION\_0\_41

Determine if an address in one address space may alias an address in another address space.

 amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_convert\_address\_space (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_lane\_id\_t lane\_id, amd\_dbgapi\_address\_space\_id\_t source\_address\_space\_id, amd\_dbgapi\_segment\_address\_t source\_segment\_address, amd\_dbgapi\_address\_space\_id\_t destination\_address\_space\_id, amd\_dbgapi\_segment\_address\_t \*destination\_segment\_address) AMD\_DBGAPI\_VERSION\_0\_41

Convert a source segment address in the source address space into a destination segment address in the destination address space.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_address\_is\_in\_address\_class (amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_lane\_id\_t lane\_id, amd\_dbgapi\_address\_space\_id\_t address\_space\_id, amd\_dbgapi\_segment\_address\_t segment\_address, amd\_dbgapi\_address\_class\_id\_t address\_class\_id, amd\_dbgapi\_address\_class\_state\_t \*address\_class\_state) AMD\_DBGAPI\_VERSION\_0\_41

Determine if a segment address in an address space is a member of a source language address class.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_read\_memory (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_lane\_id\_t lane\_id, amd\_dbgapi\_address\_space\_id\_t address
 \_space\_id, amd\_dbgapi\_segment\_address\_t segment\_address, amd\_dbgapi\_size\_t \*value\_size, void \*value)
 AMD\_DBGAPI\_VERSION\_0\_41

Read memory.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_write\_memory (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_wave\_id\_t wave\_id, amd\_dbgapi\_lane\_id\_t lane\_id, amd\_dbgapi\_address\_space\_id\_t address
 \_space\_id, amd\_dbgapi\_segment\_address\_t segment\_address, amd\_dbgapi\_size\_t \*value\_size, const void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Write memory.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_set\_memory\_precision (amd\_dbgapi\_process\_id\_t process
 —id, amd\_dbgapi\_memory\_precision\_t memory\_precision) AMD\_DBGAPI\_VERSION\_0\_24

Control precision of memory access reporting.

- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_process\_next\_pending\_event (amd\_dbgapi\_process\_id\_t process\_id, amd\_dbgapi\_event\_id\_t \*event\_id, amd\_dbgapi\_event\_kind\_t \*kind) AMD\_DBGAPI\_VERSION\_0\_41
   Obtain the next pending event.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_event\_get\_info (amd\_dbgapi\_event\_id\_t event\_id, amd\_dbgapi\_event\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41

Query information about an event.

amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_event\_processed (amd\_dbgapi\_event\_id\_t event\_id)
 AMD\_DBGAPI\_VERSION\_0\_41

Report that an event has been processed.

- void AMD\_DBGAPI amd\_dbgapi\_set\_log\_level (amd\_dbgapi\_log\_level\_t level) AMD\_DBGAPI\_VERSION\_0\_24
   Set the logging level.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_shared\_library\_get\_info (amd\_dbgapi\_shared\_library\_id\_t shared\_library\_id, amd\_dbgapi\_shared\_library\_info\_t query, size\_t value\_size, void \*value) AMD\_DBGAPI\_VERSION\_0\_41
   Query information about a shared library.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_report\_shared\_library (amd\_dbgapi\_shared\_library\_id\_t shared\_library\_id, amd\_dbgapi\_shared\_library\_state\_t shared\_library\_state) AMD\_DBGAPI\_VERSION\_0\_41
   Report that a shared library enabled by the amd\_dbgapi\_callbacks\_s::enable\_notify\_shared\_library callback has been loaded or unloaded.
- amd\_dbgapi\_status\_t AMD\_DBGAPI amd\_dbgapi\_report\_breakpoint\_hit (amd\_dbgapi\_breakpoint\_id\_t breakpoint\_id, amd\_dbgapi\_client\_thread\_id\_t client\_thread\_id, amd\_dbgapi\_breakpoint\_action\_t \*breakpoint← action) AMD\_DBGAPI\_VERSION\_0\_41

Report that a breakpoint inserted by the amd\_dbgapi\_callbacks\_s::insert\_breakpoint calback has been hit.

## 4.1.1 Detailed Description

AMD debugger API interface.

#### 4.1.2 Macro Definition Documentation

#### 4.1.2.1 AMD DBGAPI

#define AMD\_DBGAPI AMD\_DBGAPI\_IMPORT

# 4.1.2.2 AMD\_DBGAPI\_CALL

#define AMD\_DBGAPI\_CALL

# 4.1.2.3 AMD\_DBGAPI\_EXPORT

#define AMD\_DBGAPI\_EXPORT AMD\_DBGAPI\_EXPORT\_DECORATOR AMD\_DBGAPI\_CALL

# 4.1.2.4 AMD\_DBGAPI\_IMPORT

#define AMD\_DBGAPI\_IMPORT AMD\_DBGAPI\_IMPORT\_DECORATOR AMD\_DBGAPI\_CALL

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