```
1
    /* Filename: dcu.c
 2
     * Authors: Kevin Oei, Koen van Vliet
 3
     * For Pro-Q2
 4
      * Description: Digital control unit firmware for speaker system
 5
      * Status: -
6
      * Notes: - Check for MSB in parameters. If high: error condition
7
                 - Implement status LEDs
8
      * /
9
10
      /* TO-DO: Set the DDRs correctly (optional), put the right memory address values in
     setPot(), use the right value in toggleErrorLED() */
11
12
     #include <avr/io.h>
13
     #include <stdio.h>
14
     #include <avr/interrupt.h>
15
     #include "midi.h"
16
     #include "digipots.h"
17
     #include "uart.c"
18
19
20
     #define SYSFREQ 16000000
21
     #define BAUDRATE 31250
22
     #define RATE SYSFREQ / (2*BAUDRATE) - 1
23
     #define RATE L RATE%256
24
     #define RATE_H RATE/256
25
26
                                 / \, ^{\star} The buffer for the USART input. Incoming MIDI
27
     char sb[64];
    messages are stored in here. */
28
    volatile char rxcnt = 0; /* rxcnt is used when determining the offset, which is
     used to pick the right index when reading sb ^{*}/
29
     volatile char rxp = 0; /* rxp will loop from (decimal) 0 to 63, used in
     determining the index of sb that needs to be approached */
30
31
32
    void uputc(char c);
33
    void uputs(char str[]);
34
    char ugetc();
    void spiSend(uint8_t memcom, uint8_t data);
35
     void setPot(int potno, uint8 t val);
37
    void toggleErrorLED(void);
38
39
40
     int main(void) {
        int i;
41
42
         /* Debug LEDs */
43
44
         DDRB
                    = 0xFF;
         DDRDIGIPOTS = 0xFF;
45
46
         DDRLEDS
                   = 0xFF;
47
         /* Setup serial comms (31250-8-n-1) NOTE: THIS IS FOR PC ONLY, HAVE TO FIX FOR
         ATmega */
48
         UCSRA = 0x00;
49
         UCSRB = 0 \times 18 | (1<<7); /* Enable receiver & transmitter and RX complete
         interrupt enable */
50
         UCSRC = 0x86;
                                /* STILL NEEDS TO BE ADJUSTED WHEN TO-BE-USED DEVICE IS
         KNOWN */
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51
          UBRRH = RATE_H;
 52
          UBRRL = RATE_L;
 53
 54
          /* Setup SPI bus (f/2, mode0.0) */
 55
          SPSR = (1 << 0);
                                                /* Double SPI speed enabled */
          SPCR = (1 << 7) | (1 << 6) | (1 << 4); /* Enable SPI and interrupt, select master
 56
          mode */
 57
 58
          /* Reset digital potentiometers (255 is the max position) */
 59
          static uint8_t potpos[6] = \{255\};
          /* Connect terminals */
 60
          for (i = 0; i < sizeof(potpos); i++) {</pre>
 61
 62
              setPot(i,potpos[i]);
 63
          }
 64
 65
          sei();
 66
 67
          /* Main program loop */
 68
 69
          while (1) {
 70
              uint8_t cmd, cc, vv;
 71
              char s[50]; /* Is still being used anywhere? */
              while (rxcnt == 0); /* Wait for buffer to be empty */
 72
 73
              cmd = (uint8 t)ugetc();
              if (cmd & 0x80) { /* Check if Oblxxxxxxx (valid MIDI command) */
 74
 75
                  toggleErrorLED(); /* Turn off error LED */
 76
                  while (rxcnt == 0);
                  cc = ugetc(); /* Acquire controller number */
 77
                  if (~cc & 0x80) { /* Check if 0b0xxxxxxx (valid controller number value)
 78
                   * /
 79
                       while (rxcnt == 0);
 80
                       vv = ugetc(); /* Acquire controller value */
                       if (~vv & 0x80) { /* Check if 0b0xxxxxxx (valid controller value) */
 81
 82
                           /* Check command type */
 83
                           switch (cmd) {
                               case CTRL_CH:
                                                snprintf(s, sizeof(s), "Controller %d = %d",
 84
                               cc, vv);
 85
                                                uputs(s);
 86
                                                potpos[cc] = vv;
                                                setPot(cc,potpos[cc]);
 88
                                                break;
 89
                               /*case 'r':
                                                for (i = 0; i < sizeof(potpos); i++) {
 90
                                                    potpos[i] = 127;
 91
                                                    setPot(i,potpos[i]);
 92
 93
                                                break; */
                               default:
                                                uputs("What?");
 94
 95
                           }
                       }
 96
 97
                       else {
                           toggleErrorLED(); /* Turn on error LED */
 98
 99
                       }
                                   }
                  else {
100
101
                       toggleErrorLED(); /* Turn on error LED */
102
                   }
103
              }
          }
104
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```
105
          return 0;
106
      }
107
108
      /* The messages that are sent out are 16-bit long. SPI can only send 8-bit at one
109
      /* Thus, two SPI transmissions are required for a full message to be sent */
      /* The message is as follows: AAAA.CCDD.DDDD.DDDD where A is memory address, C is
110
      command and D is data. */
111
      /* See pg.47 of DigiPot datasheet */
112
      void spiSend(uint8_t memcom, uint8_t data) {
                                           /* Transmit memory address and command */
113
          SPDR = memcom;
          while ((SPSR & (1 << 7)) == 0);
                                          /* Wait for SPI transfer to finish. */
114
                                           /* Transmit data (the value to be written to the
115
          SPDR = data;
          DigiPot) */
                                         /* Wait for SPI transfer to finish. */
116
          while ((SPSR & (1 << 7)) == 0);
117
      }
118
119
      void setPot(int potno, uint8_t val) {
          /* Select the CS of the correct IC. Note that a low signal will 'activate' the
120
          IC. */
121
          /* ADDRESS MEMORY STILL NEEDS TO BE FILLED IN */
122
          switch (potno) {
              case 0: PORTDIGIPOTS = VOLUME_CS_PIN;
123
124
                      spiSend(0x00, val);
125
              case 1: PORTDIGIPOTS = BALANCE_CS_PIN;
126
                       spiSend(0x00, val);
              case 2: PORTDIGIPOTS = BASS_CUT_CS_PIN;
127
128
                      spiSend(0x00, val);
129
              case 3: PORTDIGIPOTS = TREBLE_CUT_CS_PIN;
130
                      spiSend(0x00, val);
              case 4: PORTDIGIPOTS = BASS_BOOST_CS_PIN;
131
132
                      spiSend(0x00, val);
              case 5: PORTDIGIPOTS = TREBLE_BOOST_CS_PIN;
133
134
                       spiSend(0x00, val);
135
              default: uputs("Invalid potmeter ID selected.");
136
          }
          PORTB = \sim(1<<potno);
137
138
139
      }
140
141
      void toggleErrorLED(void) {
142
          PORTLEDS ^= 0x00; /* Toggle error LED. CORRECT VALUE TO-BE-FILLED-IN */
143
      }
144
      ISR (USART_RXC_vect) {
145
146
          char c;
          if (UCSRA & (1<<FE | 1<<DOR | 1<<PE)) {
147
              c = UDR;
148
149
              uputc('?');
150
          }
          else {
151
152
              c = UDR;
              /*PORTB = \sim c;*/
153
154
              if (rxcnt < 64) {
155
                  sb[rxp & 63] = c;
156
                  rxp = (rxp + 1) & 63;
157
                  rxcnt++;
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```
158
             }
             else {
159
160
                uputc('!');
             }
161
        }
162
163
     }
164
165
     /* Temporary function for PC debugging */
166
    void uputc(char c) {
167
         while (~UCSRA & 1<<UDRE);</pre>
168
         UDR = c;
169
     }
170
171
     /* Temporary function for PC debugging */
172
     void uputs(char str[]) {
173
         int i;
         for (i = 0; str[i] != '\0'; i++) {
174
            175
176
         }
177
     }
178
179
    char ugetc() {
180
        char c;
         int offset;
181
182
         offset = (rxp - rxcnt) & 63;
183
         c = sb[offset];
184
        rxcnt--;
185
         return c;
186
     }
187
188
189
```