				ODE	RESET (low active)	~	-CT	CLK STANDALONE	FETCH ENABLE			
		GPIO 0	GPIO 1	TESTMODE	RESET (	REF CLK	CLK SELCT	CLK ST/	FETCH	GPIO 2	VDD	
		40	39	38	37	36	35	34	33	32	31	
SCL/GPIO 18 1												30 SCLK
SDA/GPIO 17 2												SCS/GPIO 3
тск 3												MOSI/IO0/GPIO 4
NC 4												MISO/IO1/GPIO 5
VDD 5												26 NC
NC 6												25 VDD
TRSTNS 7												24 NC
TMS 8												23 IO2/GPIO 6
TDI 9												22 IO3/GPIO 7
TDO 10	)											CTS/GPIO 8
		11	12	13	14	15	16	17	18	19	20	
		VDD	SMCLK	SS/G	MOS	MISC	102/0	103/0	RX/G	TX/G	RTS/	
			돗	SS/GPIO 16	/001/1	)/101,	102/GPIO 13	103/GPIO 12	RX/GPIO 11	TX/GPIO 10	RTS/GPIO 9	
				0	MOSI/IO0/GPIO 15	MISO/IO1/GPIO 14	13	12		0	9	
					15	14						