



SoC Design in Big Data Era

大数据时代的系统芯片设计

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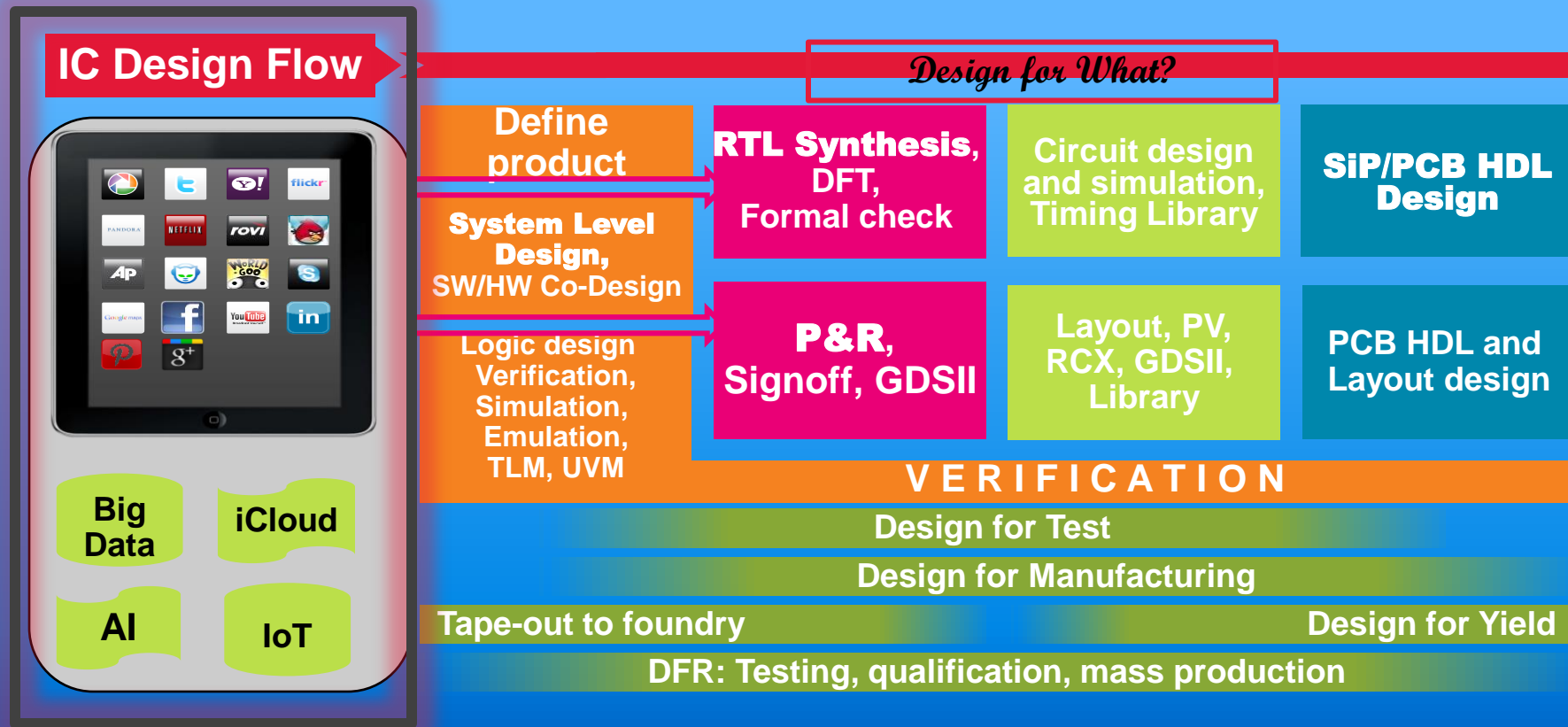
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Course Contents 2018

- Impact of AI-Big Data on SoC Design
 - [AI Study and AI-Chip Design](#), [Analysis & Storage](#), [App & Security of BD](#),
 - [IoT Technology and SoC Design](#), [ADAS and SoC Design](#)
- IDM/EDA/IP and System Design
 - [IDM/Product Chain of Semiconductor](#), [EDA Methodology & Design Flow](#),
 - [Memory Design and HBM](#), [xPU and DSA](#)
- Verification & Implementation of SoC Design
 - [System-Level Design/Verification](#), [Syn](#), [SDC and STA](#), [DFT and LEC](#),
 - [Physical Imp of SoC](#), [IP-SoC Integration](#)
- Design Variations and Design For Yield
 - [SPICE Lib](#), [RC and Delay](#),
 - [SI STA SSTA etc](#), [Low Power PI PA](#)
- Design for Reliability and Advanced Tech
 - [AMS UVM](#), [DFM and DFY](#), [ESD wrt DFR](#),
 - [RadHard wrt DFR](#), [Adv Tech in SoC and Summary](#)



SoC Design in AI-Big Data Era



Home work and reading

SoC Design in AI-Big Data Era

- Course Syllabus: see “2018_Syllabus.doc”
- Homework: see “2018_Homework.doc”
- Coursework: see “2018_Coursework.doc”
- Down/Up load files from/to the class email:
 - un2014en@163.com (SoC_in_BDE)
- Send your Class Report to
 - czchen126@126.com
- Reference Books
 - Physical Design (Recommended)
 - MS Methodology (Optional)

