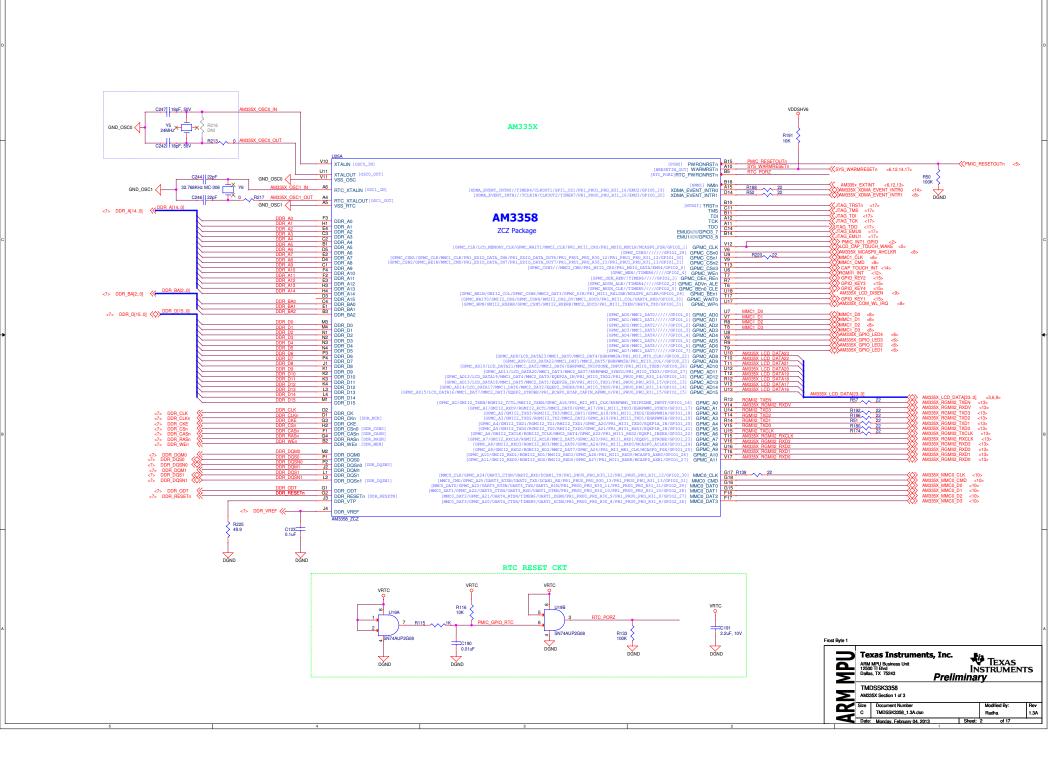
# TMDSSK3358 Board

- -See the Hardware Implementation Document for design details
- -See the Hardware User Guide for board details
- -See the PCB Build Specification for PCB Details

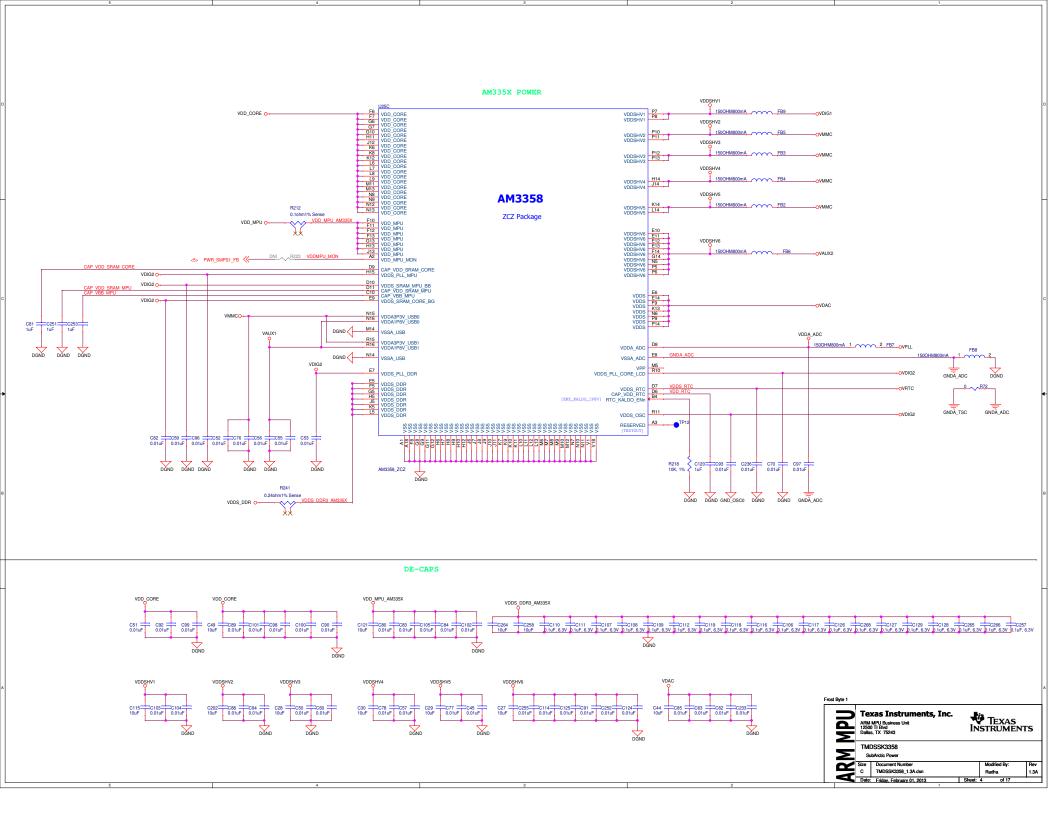


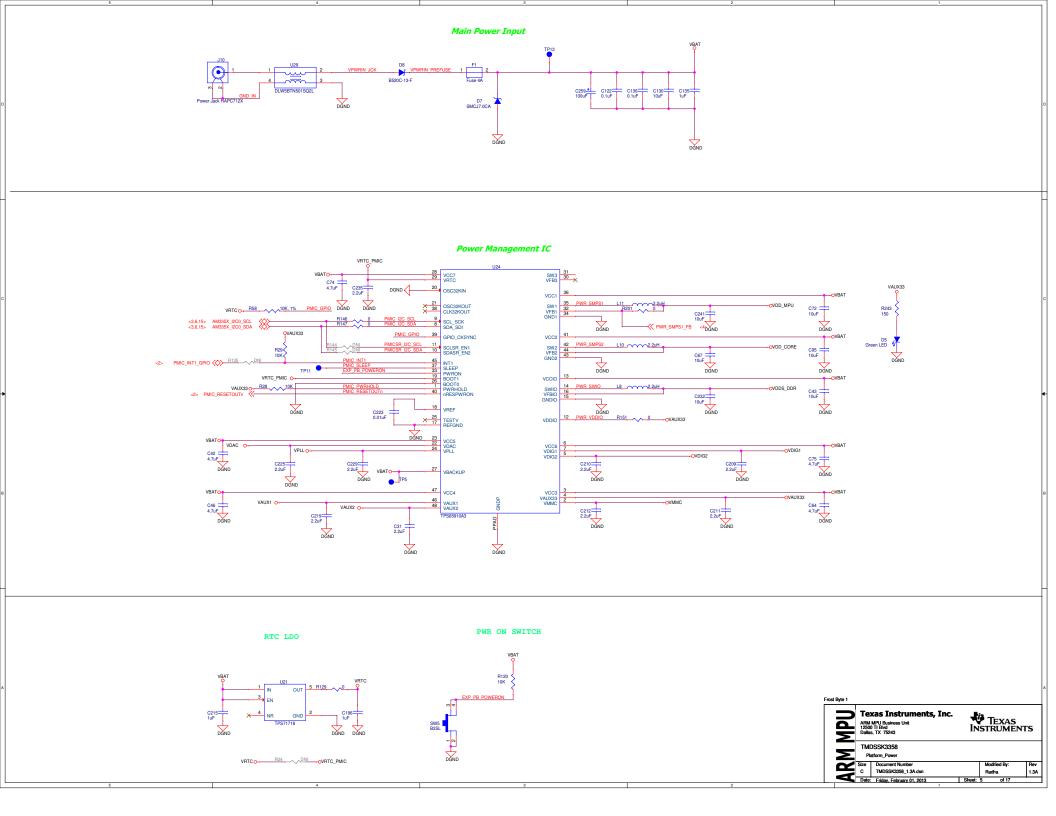


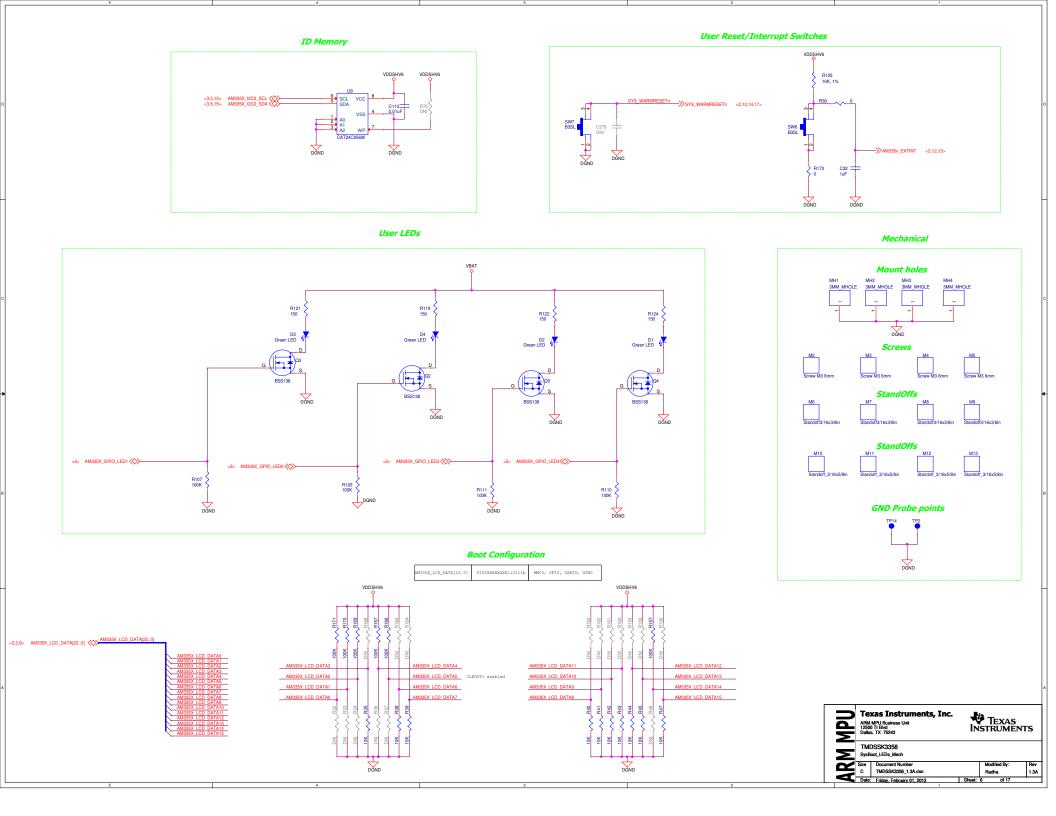


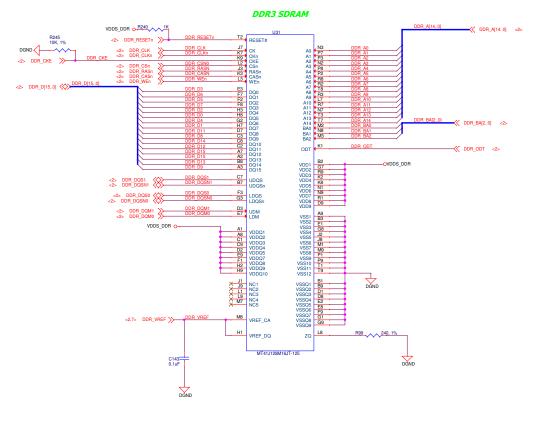


# AM335X MIII TXCLK K18 RGMIII TXCLK MIII TXD0 K16 RGMIII TXD1 MIII TXD1 K15 RGMIII TXD2 MIII TXD2 J18 RGMIII TXD2 MIII TXD3 J16 RGMIII TXD3 J16 RGMIII TXD3 J16 RGMIII TXD3 AM335X RGMIII TXCLK AM335X RGMIII TXD0 AM335X RGMIII TXD1 AM335X RGMIII TXD2 AM335X RGMIII TXD2 AM335X RGMIII TXD2 AM335X RGMIII TXDN AM35SX RGMIII TXDN AM35SX AUDA\_BCLK DGND < [GMII1\_TXCLK/UART2\_RXD/RGMII1\_TCLK/MMCO\_DAT7/MMC1\_DAT0/UART1\_DCDN/MCASP0\_ACLKX/GPI03\_9] [GMII1\_TXD0/RMII1\_TXD0/RGMII1\_TD0/MCASP1\_AXR2/MCASP1\_ACLKR/EQEP0B\_IN/MMC1\_CLK/GPI00\_28] X C6 PMIC\_POWER\_EN [GHI1]\_TXDO/SHI11]\_TXDO/SHI11]\_TXDO/SHI11]\_TXD/MEXBEJ\_AXEZ/MCASP\_ACER/REGROB\_MINNEC\_LEK/GF100\_431 [GHI1]\_TXD1/SHI11]\_TXD1/SHI11]\_TXD/MEXBEJ\_REGROB\_MEXBEJ\_AXEZ/MEXBED\_MINNEC\_LEK/GF100\_431 [GHI1]\_TXD1/FXDAN\_DX\_MEXBEJ\_AXEZ/MEXBEJ\_AXEZ/MEXBEJ\_AXEZ/MEXBEJ\_MINNEC/MEXBEJ\_AXEZ/MEXBEJ\_MINNEC/MEXBEJ\_AXEZ/MEXBEJ\_MINNEC/MEXBEJ\_MINNEC/MEXBEJ\_AXEZ/MEXBEJ\_MINNEC/ME :12.13> AM335X EXT WAKEUP **AM3358** VDDA ADC ZCZ Package [GMI11\_EXCLK/UMRT2\_TXD/RGMI11\_RCLK/RMCD\_DAT6/RMC1\_DAT1/UMRT1\_DSRM/MCASP0\_FSX/GP103\_10] MMI\_RXCLK | MISMGO/SMI11\_RXDO/RGMI11\_RXDO/RMCASP1\_AMCLKS/MCASP1\_AMC AM335X RGMIII RXCLK AM335X RGMIII RXD0 AM335X RGMIII RXD1 AM335X RGMIII RXD1 AM335X RGMIII RXD3 AM335X RGMIII RXD3 AM335X AUDA FSX AM335X RGMIII RXDV C87 0.1uF | VIETN | VIET AM335X\_AUDA\_DOUT <14> AM335X\_RGMIII\_MDIO\_CLK <12,13> AM335X\_RGMIII\_MDIO\_DATA <12,13: FIG. 159 (LEC\_DATA)/GRRC\_APRI\_LED\_CATA/GRRC\_APRI\_LE <10> AM335X SPI0 CS1 ( ) GNDA TSC LCD DATA0 <17> AM335X\_UART0\_TXD <17> AM335X\_UART0\_RXD <17> AM335X\_UART0\_CTSn <5,17> AM335X\_UART0\_RTSn LCD\_DATA0 LCD\_DATA1 LCD\_DATA2 LCD\_DATA3 LCD\_DATA4 LCD\_DATA4 LCD\_DATA6 LCD\_DATA6 LCD\_DATA7 LCD\_DATA8 LCD\_DATA8 LCD\_DATA9 LCD\_DATA9 LCD\_DATA10 LCD\_DATA11 CD\_DATA12 LCD\_DATA13 LCD\_DATA14 LCD\_DATA15 NIT USB0 DF WIS USB0 DF PIE USB0 CE FIE USB0 CE FIE USB0 CE US [MCASP0\_ANCLEX/EGEP0\_STROBE\_MCASP0\_AX83/MCASP1\_AX81/MCMSP1\_AX81/MCMSP1\_EF00\_FR0\_R30\_7/FR1\_FR00\_FR0\_R31\_7/FR01\_ AMSSSX, MCASPQ, AHCLIXX AMSSSX, MCASPQ, AHCLIXX AMSSSX, MCASPQ, FXX AMSSSX, MCASPQ, FXX AMSSSX, MCASPQ, AXRQ AMSSX, MCASPQ, AXRQ AMSSSX, MCASPQ, AXRQ AMSSXX, MCASPQ, AXR 0180 VBUS 0180 V MCASP0 AHCLKX MCASPO\_ACLKX MCASPO\_ACLKX BTS ST MCASPO\_FSX MCASPO\_AXR0 MCASPO\_ACLKR MCASPO\_ACLKR MCASPO\_ACLKR MCASPO\_FSR MCASPO\_AXR1 MCASPO\_AXR1 MCASPO\_AXR1 TP6 C18 ECAPO\_IN\_PWMO\_OUT [ECAPO\_IN\_PMMO\_OUT/UART3\_TXD/SPI1\_CS1/PRI\_ECAPO\_ECAP\_CAPIN\_AFMM\_O/SPI1\_SCLK/MMCO\_SDMP/XDMA\_EVENT\_INTR2/GPI00\_7] <15> ACC\_INT2 < VDDSHVA ZIGBEE HEADER I2C \_HEADER DGND 🛑 R185 R183 4.7K 4.7K <3,5,6,15> AM335X\_SPI0\_SCLK <3,5,6,15> AM335X\_SPI0\_D0 USB1 ID R150 0 Texas Instruments, Inc. TEXAS INSTRUMENTS TMDSSK3358 AM335X Section 2 of 3 TMDSSK3358 1.3A.dsn Radha

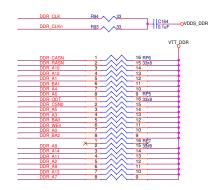


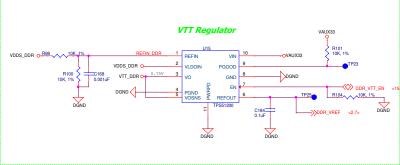


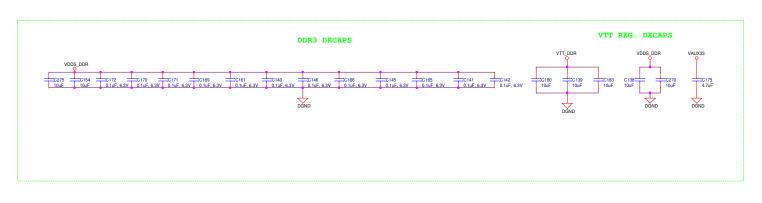




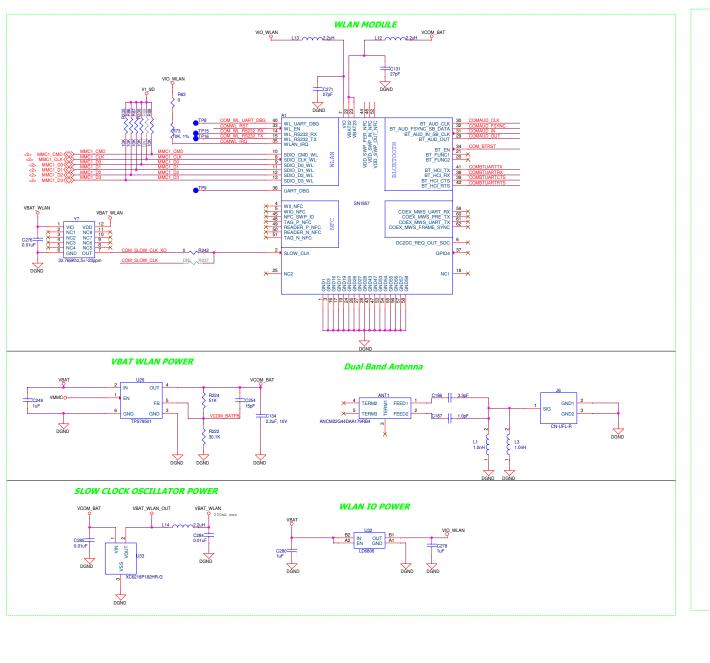
#### **Terminations**

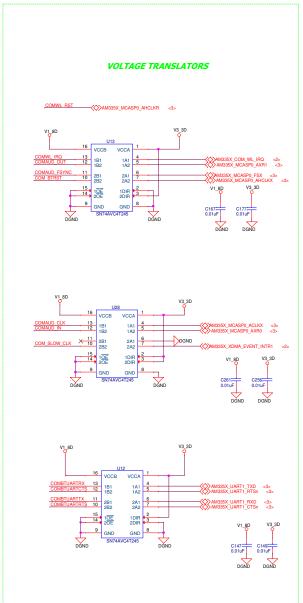




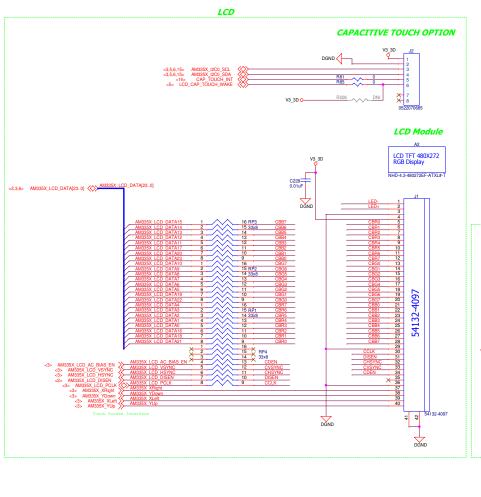




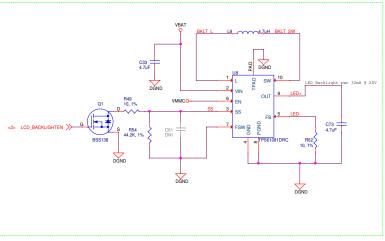




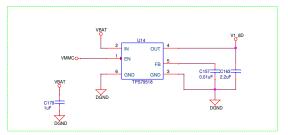




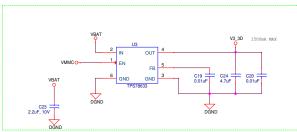
## BackLight Driver



### 1.8V LDO

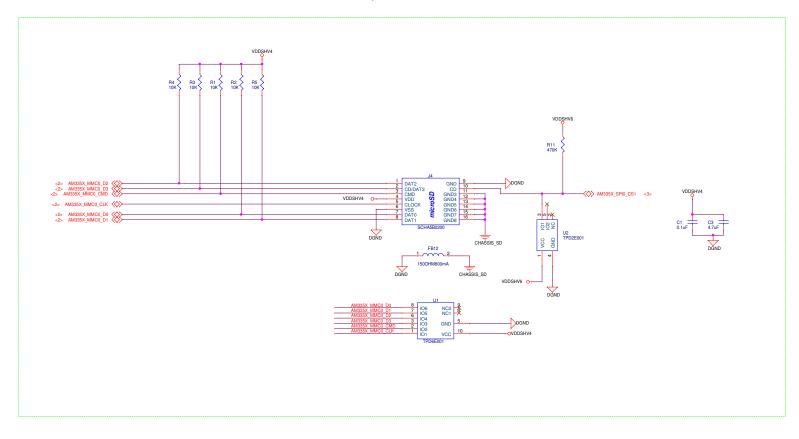


## 3.3V LDO



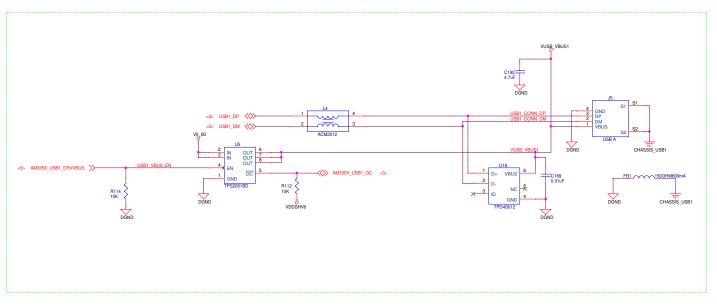


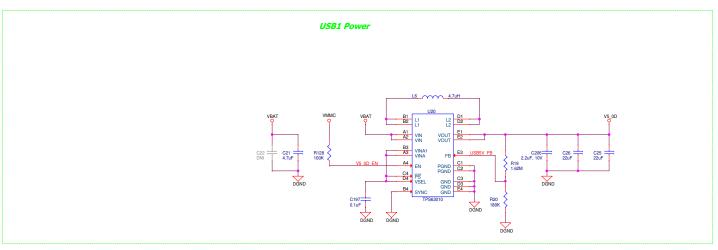
## SD/MMC Connector



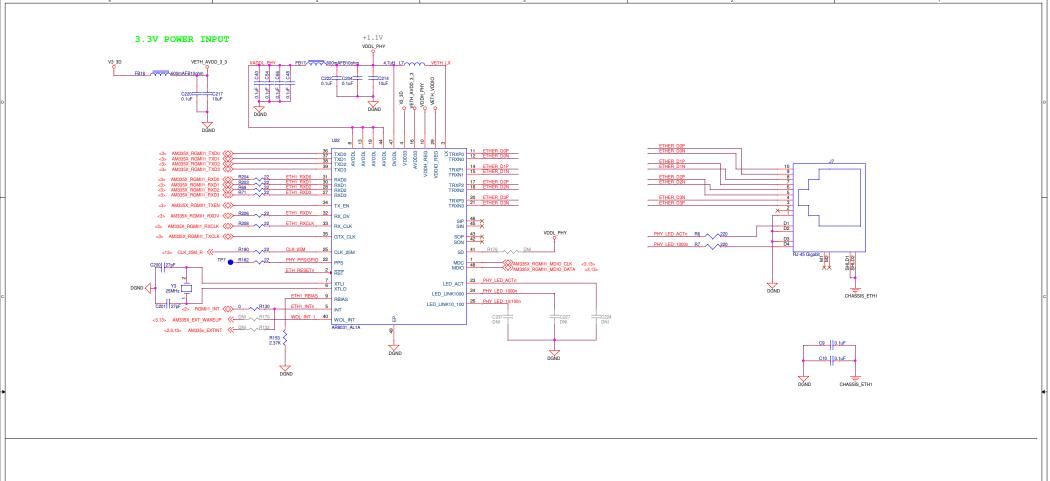


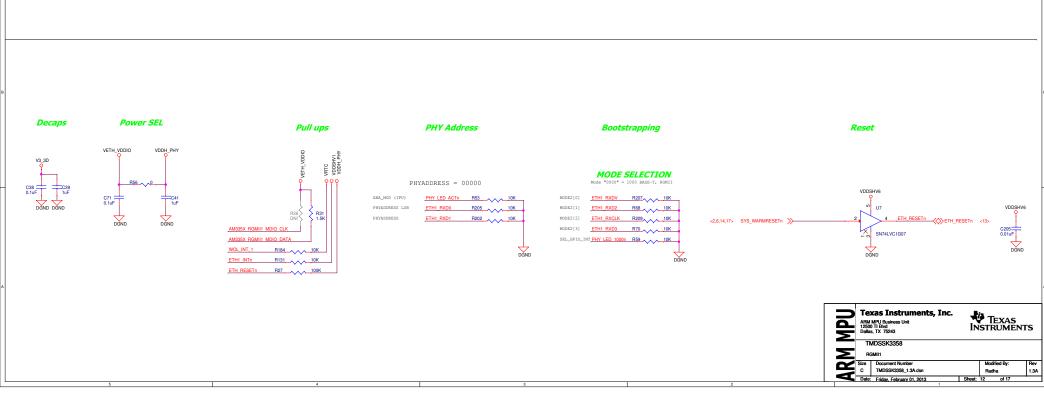
#### USB1 Port

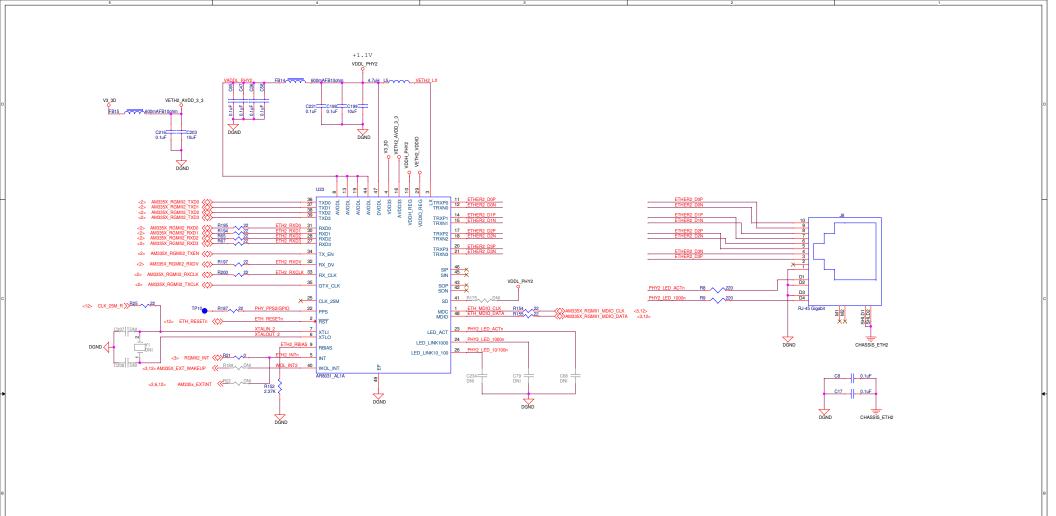


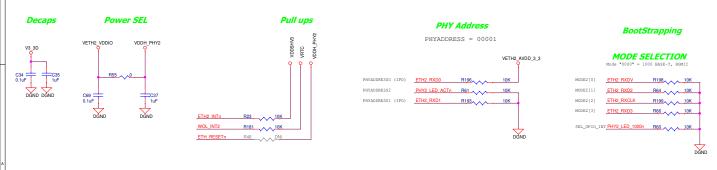






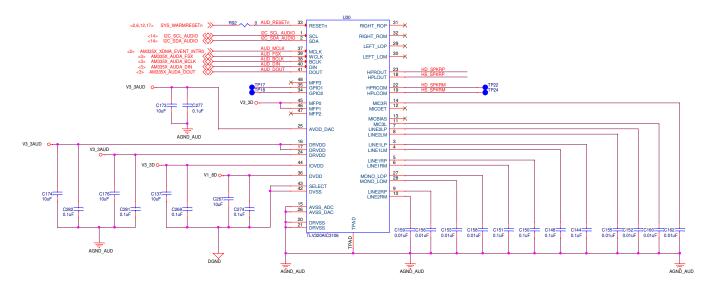


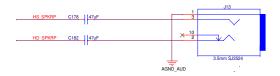






#### Audio Codec

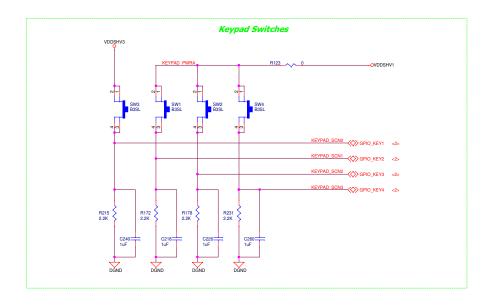


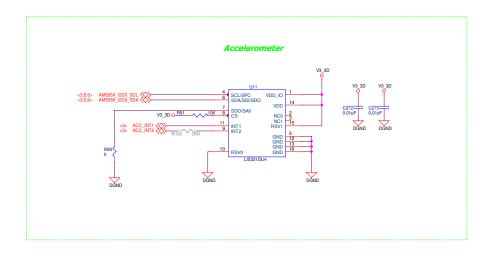






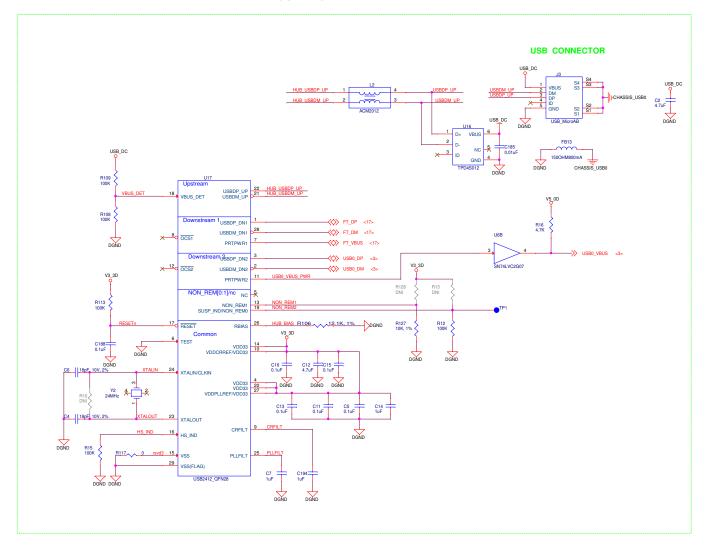






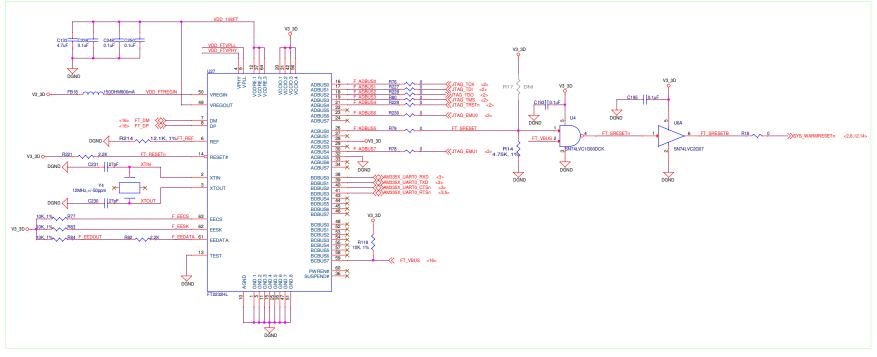


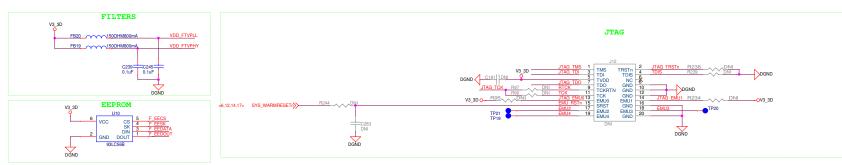
#### USB HUE

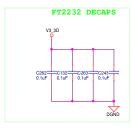




## USB - UART (Optional)









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