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# RTL8812AU Efuse Contents

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## Efuse Contents

The RTL8812AU is embedded an internal 1024-byte non-volatile memory called eFuse. The definition of each Efuse map is shown as the below.

**Note: It is suggested to obtain Realtek approval before any change on the default settings of the Efuse.** **Table 1. Efuse Contents**

Bytes	Contents	Description	Default PG Value
00h	29h	These 2 bytes contain the ID code word for the RTL8812AU. The RTL8812AU will load the contents of the EEPROM into the corresponding location if the ID word (8129h) is correct.	0x8129
01h	81h		
02h	-	Reserved for Realtek. Do not change this field without Realtek's approval.	0x02
03h	-	Reserved for Realtek. Do not change this field without Realtek's approval.	0x86
04h	-	Reserved for Realtek. Do not change this field without Realtek's approval.	0x0C
05h	-	Reserved for Realtek. Do not change this field without Realtek's approval.	0x00
06h	-	Reserved for Realtek. Do not change this field without Realtek's approval.	0x00
07h	-	Reserved for Realtek. Do not change this field without Realtek's approval.	0x00
08h	-	<u>Bit[1]: USB3/USB2 transition enable</u> <u>Others:</u> Reserved for Realtek. Do not change this field without Realtek's approval.	0x0 <u>2</u> 9
09h~0Fh	RSVD	-	-
10h	Path A 2.4G CCK-1Tx Power Index (Absolute Value)	Path A CCK Power Index for Ch 1,2, Range 0~63.	2Dh
11h		Path A CCK Power Index for Ch 3, 4, 5, Range 0~63.	2Dh
12h		Path A CCK Power Index for Ch 6, 7, 8, Range 0~63.	2Dh
13h		Path A CCK Power Index for Ch 9, 10, 11, Range 0~63.	2Dh
14h		Path A CCK Power Index for Ch 12, 13, Range 0~63.	2Dh
15h		Path A CCK Power Index for Ch 14, Range 0~63.	2Dh
16h	Path A 2.4G BW40-1S Tx Power Index (Absolute Value)	Path A 2G BW40-1S Power Index for Ch 1, 2, Range 0~63.	2Dh
17h		Path A 2G BW40-1S Power Index for Ch 3, 4, 5, Range 0~63.	2Dh
18h		Path A 2G BW40-1S Power Index for Ch 6, 7, 8, Range 0~63.	2Dh
19h		Path A 2G BW40-1S Power Index for Ch 9, 10, 11, Range 0~63.	2Dh
1Ah		Path A 2G BW40-1S Power Index for Ch 12, 13, 14 Range 0~63.	2Dh
1Bh	Path A 2.4G BW20-1S Tx Power Index Difference	Ppower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8~7.	24h
	Path A 2.4G OFDM-1Tx Power Index Difference	Ppower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 2G Offset, Range -8~7.	
1Ch	Path A 2.4G BW40-2S Tx	Ppower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh



	Power Index Difference		
	Path A 2.4G BW20-2S Tx Power Index Difference	Ppower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range -8~7.	
1Dh	Path A 2.4G OFDM-2Tx Power Index Difference	Ppower Index Difference between OFDM-2Tx and OFDM-1Tx. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path A 2.4G CCK-2Tx Power Index Difference	Ppower Index Difference between CCK-2T and CCK-1Tx. Bit[3:0]: Path A 2G Offset, Range -8~7.	
1Eh	Path A 2.4G BW40-3S Tx Power Index Difference	Ppower Index Difference between BW40-3S and BW40-2S. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path A 2.4G BW20-3S Tx Power Index Difference	Ppower Index Difference between BW20-3S and BW20-2S. Bit[3:0]: Path A 2G Offset, Range -8~7.	
1Fh	Path A 2.4G OFDM-3Tx Power Index Difference	Ppower Index Difference between OFDM-3Tx and OFDM-2Tx. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path A 2.4G CCK-3Tx Power Index Difference	Ppower Index Difference between CCK-3T and CCK-2Tx. Bit[3:0]: Path A 2G Offset, Range -8~7.	
20h	Path A 2.4G BW40-4S Tx Power Index Difference	Ppower Index Difference between BW40-4S and BW40-3S. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path A 2.4G BW20-4S Tx Power Index Difference	Ppower Index Difference between BW20-4S and BW20-3S. Bit[3:0]: Path A 2G Offset, Range -8~7.	
21h	Path A 2.4G OFDM-4Tx Power Index Difference	Ppower Index Difference between OFDM-4Tx and OFDM-3Tx. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path A 2.4G CCK-4Tx Power Index Difference	Ppower Index Difference between CCK-4T and CCK-3Tx. Bit[3:0]: Path A 2G Offset, Range -8~7.	
22h	Path A 5G BW40-1S Tx Power Index (Absolute Value)	Path A 5G BW40-1S Power Index for Ch 36, 38, 40, Range 0~63.	2Ah
23h		Path A 5G BW40-1S Power Index for Ch 44, 46, 48, Range 0~63.	2Ah
24h		Path A 5G BW40-1S Power Index for Ch 52, 54, 56, Range 0~63.	2Ah
25h		Path A 5G BW40-1S Power Index for Ch 60, 62, 64, Range 0~63.	2Ah
26h		Path A 5G BW40-1S Power Index for Ch 100, 102, 104, Range 0~63.	2Ah
27h		Path A 5G BW40-1S Power Index for Ch 108, 110, 112, Range 0~63.	2Ah



28h		Path A 5G BW40-1S Power Index for Ch 116, 118, 120, Range 0~63.	2Ah
29h		Path A 5G BW40-1S Power Index for Ch 124, 126, 128, Range 0~63.	2Ah
2Ah		Path A 5G BW40-1S Power Index for Ch 132, 134, 136, Range 0~63.	2Ah
2Bh		Path A 5G BW40-1S Power Index for Ch 140, 142, 144, Range 0~63.	2Ah
2Ch		Path A 5G BW40-1S Power Index for Ch 149, 151, 153, Range 0~63.	2Ah
2Dh		Path A 5G BW40-1S Power Index for Ch 157, 159, 161, Range 0~63.	2Ah
2Eh		Path A 5G BW40-1S Power Index for Ch 165, 167, 169, Range 0~63.	2Ah
2Fh		Path A 5G BW40-1S Power Index for Ch 173, 175, 177, Range 0~63.	2Ah
30h	Path A 5G BW20-1S Tx Power Index Difference	Ppower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7.	04h
	Path A 5G OFDM-1Tx Power Index Difference	Ppower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
31h	Path A 5G BW40-2S Tx Power Index Difference	Ppower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path A 5G BW20-2S Tx Power Index Difference	Ppower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
32h	Path A 5G BW40-3S Tx Power Index Difference	Ppower Index Difference between BW40-3S and BW40-2S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path A 5G BW20-3S Tx Power Index Difference	Ppower Index Difference between BW20-3S and BW20-2S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
33h	Path A 5G BW40-4S Tx Power Index Difference	Ppower Index Difference between BW40-4S and BW40-3S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path A 5G BW20-4S Tx Power Index Difference	Ppower Index Difference between BW20-4S and BW20-3S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
34h	Path A 5G OFDM-2Tx Power Index Difference	Ppower Index Difference between OFDM-2T and OFDM-1Tx. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path A 5G OFDM-3Tx Power Index Difference	Ppower Index Difference between OFDM-3T and OFDM-2Tx. Bit[3:0]: Path A 5G Offset, Range -8~7.	
35h	Path A 5G OFDM-4Tx Power Index Difference	Bit[7:4]: Resever. Ppower Index Difference between OFDM-4T and OFDM-3Tx. Bit[3:0]: Path A 5G Offset, Range -8~7.	EEh



36h	Path A 5G BW80-1S Tx Power Index Difference	Ppower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path A 5G BW160-1S Tx Power Index Difference	Ppower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7.	
37h	Path A 5G BW80-2S Tx Power Index Difference	Ppower Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path A 5G BW160-2S Tx Power Index Difference	Ppower Index Difference between BW160-2S and BW160-1S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
38h	Path A 5G BW80-3S Tx Power Index Difference	Ppower Index Difference between BW80-3S and BW80-2S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path A 5G BW160-3S Tx Power Index Difference	Ppower Index Difference between BW160-3S and BW160-2S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
39h	Path A 5G BW80-4S Tx Power Index Difference	Ppower Index Difference between BW80-4S and BW80-3S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path A 5G BW160-4S Tx Power Index Difference	Ppower Index Difference between BW160-4S and BW160-3S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
3Ah	Path B 2.4G CCK-1TX Power Index (Absolute Value)	Path B CCK Power Index for Ch 1,2, Range 0~63.	2Dh
3Bh		Path B CCK Power Index for Ch 3, 4, 5, Range 0~63.	2Dh
3Ch		Path B CCK Power Index for Ch 6, 7, 8, Range 0~63.	2Dh
3Dh		Path B CCK Power Index for Ch 9, 10, 11, Range 0~63.	2Dh
3Eh		Path B CCK Power Index for Ch 12, 13, Range 0~63.	2Dh
3Fh		Path B CCK Power Index for Ch 14, Range 0~63.	2Dh
40h	Path A 2.4G BW40-1S Tx Power Index (Absolute Value)	Path A 2G BW40-1S Power Index for Ch 1, 2, Range 0~63.	2Dh
41h		Path A 2G BW40-1S Power Index for Ch 3, 4, 5, Range 0~63.	2Dh
42h		Path A 2G BW40-1S Power Index for Ch 6, 7, 8, Range 0~63.	2Dh
43h		Path A 2G BW40-1S Power Index for Ch 9, 10, 11, Range 0~63.	2Dh
44h		Path A 2G BW40-1S Power Index for Ch 12, 13, 14 Range 0~63.	2Dh
45h	Path B 2.4G BW20-1S Tx Power Index Difference	Ppower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8~7.	24h
	Path B 2.4G OFDM-1Tx Power Index Difference	Ppower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 2G Offset, Range -8~7.	



46h	Path B 2.4G BW40-2S Tx Power Index Difference	Ppower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path B 2.4G BW20-2S Tx Power Index Difference	Ppower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 2G Offset, Range -8~7.	
47h	Path B 2.4G OFDM-2Tx Power Index Difference	Ppower Index Difference between OFDM-2Tx and OFDM-1Tx. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path B 2.4G CCK-2Tx Power Index Difference	Ppower Index Difference between CCK-2T and CCK-1Tx. Bit[3:0]: Path A 2G Offset, Range -8~7.	
48h	Path B 2.4G BW40-3S Tx Power Index Difference	Ppower Index Difference between BW40-3S and BW40-2S. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path B 2.4G BW20-3S Tx Power Index Difference	Ppower Index Difference between BW20-3S and BW20-2S. Bit[3:0]: Path A 2G Offset, Range -8~7.	
49h	Path B 2.4G OFDM-3Tx Power Index Difference	Ppower Index Difference between OFDM-3Tx and OFDM-2Tx. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path B 2.4G CCK-3Tx Power Index Difference	Ppower Index Difference between CCK-3T and CCK-2Tx. Bit[3:0]: Path A 2G Offset, Range -8~7.	
4Ah	Path B 2.4G BW40-4S Tx Power Index Difference	Ppower Index Difference between BW40-4S and BW40-3S. Bit[7:4]: Path A 2G Offset, Range -8~7.	EEh
	Path B 2.4G BW20-4S Tx Power Index Difference	Ppower Index Difference between BW20-4S and BW20-3S. Bit[3:0]: Path A 2G Offset, Range -8~7.	
4Bh	Path B 2.4G OFDM-4Tx Power Index Difference	Ppower Index Difference between OFDM-4Tx and OFDM-3Tx. Bit[7:4]: Path B 2G Offset, Range -8~7.	EEh
	Path B 2.4G CCK-4Tx Power Index Difference	Ppower Index Difference between CCK-4T and CCK-3Tx. Bit[3:0]: Path B 2G Offset, Range -8~7.	
4Ch	Path B 5G BW40-1S	Path A 5G BW40-1S Power Index for Ch 36, 38, 40, Range 0~63.	2Ah
4Dh	Tx Power Index (Absolute Value)	Path A 5G BW40-1S Power Index for Ch 44, 46, 48, Range 0~63.	2Ah
4Eh		Path A 5G BW40-1S Power Index for Ch 52, 54, 56, Range 0~63.	2Ah
4Fh		Path A 5G BW40-1S Power Index for Ch 60, 62, 64, Range 0~63.	2Ah



50h		Path A 5G BW40-1S Power Index for Ch 100, 102, 104, Range 0~63.	2Ah
51h		Path A 5G BW40-1S Power Index for Ch 108, 110, 112, Range 0~63.	2Ah
52h		Path A 5G BW40-1S Power Index for Ch 116, 118, 120, Range 0~63.	2Ah
53h		Path A 5G BW40-1S Power Index for Ch 124, 126, 128, Range 0~63.	2Ah
54h		Path A 5G BW40-1S Power Index for Ch 132, 134, 136, Range 0~63.	2Ah
55h		Path A 5G BW40-1S Power Index for Ch 140, 142, 144, Range 0~63.	2Ah
56h		Path A 5G BW40-1S Power Index for Ch 149, 151, 153, Range 0~63.	2Ah
57h		Path A 5G BW40-1S Power Index for Ch 157, 159, 161, Range 0~63.	2Ah
58h		Path A 5G BW40-1S Power Index for Ch 165, 167, 169, Range 0~63.	2Ah
59h		Path A 5G BW40-1S Power Index for Ch 173, 175, 177, Range 0~63.	2Ah
5Ah	Path B 5G BW20-1S Tx Power Index Difference	Pwower Index Difference between BW20-1S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path B 5G OFDM-1Tx Power Index Difference	Pwower Index Difference between OFDM-1Tx and BW40-1S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
5Bh	Path B 5G BW40-2S Tx Power Index Difference	Pwower Index Difference between BW40-2S and BW40-1S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path B 5G BW20-2S Tx Power Index Difference	Pwower Index Difference between BW20-2S and BW20-1S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
5Ch	Path B 5G BW40-3S Tx Power Index Difference	Pwower Index Difference between BW40-3S and BW40-2S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path B 5G BW20-3S Tx Power Index Difference	Pwower Index Difference between BW20-3S and BW20-2S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
5Dh	Path B 5G BW40-4S Tx Power Index Difference	Pwower Index Difference between BW40-4S and BW40-3S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path B 5G BW20-4S Tx Power Index Difference	Pwower Index Difference between BW20-4S and BW20-3S. Bit[3:0]: Path A 5G Offset, Range -8~7.	
5Eh	Path A 5G OFDM-2Tx Power Index Difference	Pwower Index Difference between OFDM-2T and OFDM-1Tx. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh
	Path B 5G OFDM-3Tx Power Index Difference	Pwower Index Difference between OFDM-3T and OFDM-2Tx. Bit[3:0]: Path A 5G Offset, Range -8~7.	
5Fh	Path B 5G OFDM-4Tx	Bit[7:4]: Resever. Pwower Index Difference between OFDM-4T and OFDM-3Tx.	EEh





	Power Index Difference	Bit[3:0]: Path A 5G Offset, Range -8~7.																																													
60h	Path B 5G BW80-1S Tx Power Index Difference	Ppower Index Difference between BW80-1S and BW40-1S (UpSide Ch + LowSide Ch)/2. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh																																												
	Path B 5G BW160-1S Tx Power Index Difference	Ppower Index Difference between BW160-1S and BW80-1S (UpSide Ch + LowSide Ch)/2. Bit[3:0]: Path A 5G Offset, Range -8~7.																																													
61h	Path B 5G BW80-2S Tx Power Index Difference	Ppower Index Difference between BW80-2S and BW80-1S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh																																												
	Path B 5G BW160-2S Tx Power Index Difference	Ppower Index Difference between BW160-2S and BW160-1S. Bit[3:0]: Path A 5G Offset, Range -8~7.																																													
62h	Path B 5G BW80-3S Tx Power Index Difference	Ppower Index Difference between BW80-3S and BW80-2S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh																																												
	Path B 5G BW160-3S Tx Power Index Difference	Ppower Index Difference between BW160-3S and BW160-2S. Bit[3:0]: Path A 5G Offset, Range -8~7.																																													
63h	Path B 5G BW80-4S Tx Power Index Difference	Ppower Index Difference between BW80-4S and BW80-3S. Bit[7:4]: Path A 5G Offset, Range -8~7.	EEh																																												
	Path B 5G BW160-4S Tx Power Index Difference	Ppower Index Difference between BW160-4S and BW160-3S. Bit[3:0]: Path B 5G Offset, Range -8~7.																																													
64h~B7h	RSVD	-	-																																												
B8h	Channel Plan	Bit[7]: Software configure mode 0h: Enable software configure( refer to Channel Plane Domain Code) 1h: Disable software configure( can't change Channel Plan Setting)  Bit[6:0]: Channel Plan <table><tr><th colspan="2">Country Code</th><th rowspan="2">Bit[6:0]</th><th colspan="2">channels</th></tr><tr><th>2G RD</th><th>5G RD</th><th>2G</th><th>5G</th></tr><tr><td><u>Worldwide 13</u></td><td><u>5G NULL</u></td><td><u>20h</u></td><td><u>1~13</u></td><td><u>NA</u></td></tr><tr><td><u>Europe 2G</u></td><td><u>5G NULL</u></td><td><u>21h</u></td><td><u>1~13</u></td><td><u>NA</u></td></tr><tr><td><u>US 2G</u></td><td><u>5G NULL</u></td><td><u>22h</u></td><td><u>1~11</u></td><td><u>NA</u></td></tr><tr><td><u>Japan 2G</u></td><td><u>5G NULL</u></td><td><u>23h</u></td><td><u>1~13,14</u></td><td><u>NA</u></td></tr><tr><td><u>France 2G</u></td><td><u>5G NULL</u></td><td><u>24h</u></td><td><u>10~13</u></td><td><u>NA</u></td></tr><tr><td><u>US 2G</u></td><td><u>US 5G</u></td><td><u>25h</u></td><td><u>1~11</u></td><td><u>36~48, 52~64, 100~140, 149~165</u></td></tr><tr><td><u>Worldwide 13</u></td><td><u>Europe 5G</u></td><td><u>26h</u></td><td><u>1~13</u></td><td><u>36~48, 52~64, 100~140</u></td></tr></table>	Country Code		Bit[6:0]	channels		2G RD	5G RD	2G	5G	<u>Worldwide 13</u>	<u>5G NULL</u>	<u>20h</u>	<u>1~13</u>	<u>NA</u>	<u>Europe 2G</u>	<u>5G NULL</u>	<u>21h</u>	<u>1~13</u>	<u>NA</u>	<u>US 2G</u>	<u>5G NULL</u>	<u>22h</u>	<u>1~11</u>	<u>NA</u>	<u>Japan 2G</u>	<u>5G NULL</u>	<u>23h</u>	<u>1~13,14</u>	<u>NA</u>	<u>France 2G</u>	<u>5G NULL</u>	<u>24h</u>	<u>10~13</u>	<u>NA</u>	<u>US 2G</u>	<u>US 5G</u>	<u>25h</u>	<u>1~11</u>	<u>36~48, 52~64, 100~140, 149~165</u>	<u>Worldwide 13</u>	<u>Europe 5G</u>	<u>26h</u>	<u>1~13</u>	<u>36~48, 52~64, 100~140</u>	7Fh
Country Code		Bit[6:0]	channels																																												
2G RD	5G RD		2G	5G																																											
<u>Worldwide 13</u>	<u>5G NULL</u>	<u>20h</u>	<u>1~13</u>	<u>NA</u>																																											
<u>Europe 2G</u>	<u>5G NULL</u>	<u>21h</u>	<u>1~13</u>	<u>NA</u>																																											
<u>US 2G</u>	<u>5G NULL</u>	<u>22h</u>	<u>1~11</u>	<u>NA</u>																																											
<u>Japan 2G</u>	<u>5G NULL</u>	<u>23h</u>	<u>1~13,14</u>	<u>NA</u>																																											
<u>France 2G</u>	<u>5G NULL</u>	<u>24h</u>	<u>10~13</u>	<u>NA</u>																																											
<u>US 2G</u>	<u>US 5G</u>	<u>25h</u>	<u>1~11</u>	<u>36~48, 52~64, 100~140, 149~165</u>																																											
<u>Worldwide 13</u>	<u>Europe 5G</u>	<u>26h</u>	<u>1~13</u>	<u>36~48, 52~64, 100~140</u>																																											



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B9h	Crystal Calibration	XTAL_K Value Bit[5:0], Xi=Xo Range 0~3F h. Bit[7:6]: reserved FF h = 00 h	20h																																																																																										
BAh	Thermal Meter	Thermal Meter Default Value System maker will calibrate a value and save it in EEPROM.	<u>FF</u> 42h																																																																																										



		Bit[7:0]: Thermal Meter Value. <u>FF h: disable Tx power tracking function</u>	
BBh	IQ Calibration and LC Calibration	[3:2] : Do LCK by Thermal Meter $\Delta$ value. 0h : $\Delta$ 4 (~25degC) 1h : $\Delta$ 3 (~20degC) 2h : $\Delta$ 2 (~10degC) 3h : don't LCK.  [1:0] : Do IQK by Thermal Meter $\Delta$ value. 0h : $\Delta$ 5 (~30degC) 1h : $\Delta$ 3 (~20degC) 2h : $\Delta$ 2 (~10degC) 3h : don't IQK.	00h
BCh	2G and 5G PA Type	2G PA Bit[7]: Path-D Internal/External PA 0h: Internal PA 1 h: External PA Bit[6]: Path-C Internal/External PA 0h: Internal PA 1h: External PA Bit[5]: Path-B Internal/External PA 0h: Internal PA 1h: External PA Bit[4]: Path-A Internal/External PA 0h: Internal PA 1h: External PA 5G PA Bit[3]: Path-D Internal/External PA 0h: Internal PA 1 h: External PA Bit[2]: Path-C Internal/External PA 0h: Internal PA 1h: External PA Bit[1]: Path-B Internal/External PA 0h: Internal PA 1h: External PA Bit[0]: Path-A Internal/External PA 0h: Internal PA 1h: External PA	00h
BDh	2G LNA Type and Gain Selection	Bit[2:0]: 2G path-A external LNA Gain, used to modify DIG mechanism 0h~7h: External LNA, 8~22dB with 2dB/step  Bit[3]: 2G Path-A Internal/External LNA 0h: Internal LNA 1h: External LNA  Bit[6:4]: 2G path-B external LNA Gain, used to modify DIG mechanism 0h~7h: External LNA, 8~22dB with 2dB/step	00h



		Bit[7]: 2G Path-B Internal/External LNA 0h: Internal LNA 1h: External LNA	
BE		Bit[2:0]: 2G path-C external LNA Gain, used to modify DIG mechanism 0h~7h: External LNA, 8~22dB with 2dB/step  Bit[3]: 2G Path-C Internal/External LNA 0h: Internal LNA 1h: External LNA  Bit[6:4]: 2G path-D external LNA Gain, used to modify DIG mechanism 0h~7h: External LNA, 8~22dB with 2dB/step  Bit[7]: 2G Path-D Internal/External LNA 0h: Internal LNA 1h: External LNA	00h
BFh	5G LNA Type and Gain Selection	Bit[2:0]: 5G path-A external LNA Gain, used to modify DIG mechanism 0h~7h: External LNA, 8~22dB with 2dB/step  Bit[3]: 5G Path-A Internal/External LNA 0h: Internal LNA 1h: External LNA  Bit[6:4]: 5G path-B external LNA Gain, used to modify DIG mechanism 0h~7h: External LNA, 8~22dB with 2dB/step  Bit[7]: 5G Path-B Internal/External LNA 0h: Internal LNA 1h: External LNA	00h
C0h		Bit[2:0]: 5G path-C external LNA Gain, used to modify DIG mechanism 0h~7h: External LNA, 8~22dB with 2dB/step  Bit[3]: 5G Path-C Internal/External LNA 0h: Internal LNA 1h: External LNA  Bit[6:4]: 5G path-D external LNA Gain, used to modify DIG mechanism 0h~7h: External LNA, 8~22dB with 2dB/step  Bit[7]: 5G Path-D Internal/External LNA 0h: Internal LNA 1h: External LNA	00h
C1h	Board Options	Bit[2:0]: Regulatory selection. 0h: driver-defined maximum power offset for longer communication range. ( refer to Power by rate table)	00h



		<p>1h: Power limit table-defined maximum power offset range (refer to Power by rate table and Power limit table to take the smaller index value)</p> <p>2h: not support power offset by rate (Don't refer to Power by rate table)</p> <p>3h~7h: reserved</p> <p>Bit[3]: Non-interrupt Antenna Diversity 0: disable 1: enable</p> <p>Bit[4]: reserved</p> <p>Bit[7:5]: Board Type (PCIe) 0h: WiFi solo-mCard 1h: WiFi+BT combo-mCard 2h: PCIe Card 3h~31h: reserved.</p>	
C2h	Feature Options	<p>Bit[1:0]: function configuration of pin_LED0 and pin_LED1</p> <p>Bit[3:2]: Link Speed shown in OS 0h: Current Tx PHY Rate 1h: Current Rx PHY Rate 2h: Maximum RX PHY Rate 3h: reserved</p> <p>Bit[4]: power down mode selection 0: radio off 1: power down</p> <p>Bit[5]: Enable bluetooth coexistence 0: Disable 1: Enable</p> <p>Bit[6]: Enable WoWLAN 0: Disable 1: Enable</p> <p>Bit[7]: Enable WAPI support 0: Disable 1: Enable</p>	00h
C3h	BT Setting	<p>Bit[0]: Total antenna number 0: 2-Antenna (default) 1: 1-Antenna</p> <p>Bit[3:1]: Co-existence type 0h: 2-wire type (default) 1h: ISSC 3-wire type 2h: Accel type 3h: CSR traditional type 4h: CSR enhance type</p>	00h



		5h: RTL8756 type 6h~8h: reserved  Bit[4]: antenna isolation quality 0:good, 1:bad(default)  Bit[5]: Radio on/off type 0: combine with WiFi, 1:individual  Bit[7:6]: reserved	
C4h	The EEPROM content version.	The Efuse Contents version.	00h
C5h	Customer ID	Customer ID (0x00 and 0xFF are reserved for Realtek)	00h
C6h	2G Tx BB Swing Setting	Bit[1:0]: 2G PathA OFDM 0h: 0dB (default) 1h: -3dB 2h: -6dB 3h: -9dB Bit[3:2]: 2G PathB OFDM 0h: 0dB (default) 1h: -3dB 2h: -6dB 3h: -9dB Bit[5:4]: 2G PathC OFDM 0h: 0dB (default) 1h: -3dB 2h: -6dB 3h: -9dB Bit[7:6]: 2G PathD OFDM 0h: 0dB (default) 1h: -3dB 2h: -6dB 3h: -9dB	00h
C7h	5G Tx BB Swing Setting	Bit[1:0]: 5G PathA OFDM 0h: 0dB 1h: -3dB (default) 2h: -6dB 3h: -9dB Bit[3:2]: 5G PathB OFDM 0h: 0dB 1h: -3dB (default) 2h: -6dB 3h: -9dB Bit[5:4]: 5G PathC OFDM 0h: 0dB 1h: -3dB (default) 2h: -6dB 3h: -9dB Bit[7:6]: 5G PathD OFDM 0h: 0dB 1h: -3dB (default)	55h



		2h: -6dB 3h: -9dB	
C8h	Tx Power Calibrator Rate	Bit[0]: 2G 40M Tx Power Calibrator Rate. 0h : HT40 (default) 1h : VHT40 Bit[0]: 5G 40M Tx Power Calibrator Rate. 0h : HT40 (default) 1h : VHT40 Bit[7:2]: reserved	00h
<u>C9h</u>	<u>T/Rx Antenna Options</u>	<u>TBD</u>	<u>00h</u>
<u>C9hCAh</u>	<u>Max Support Rate-Option</u>	<u>Bit[3:0]: max support data rate</u> <u>0h: 11n 1x1</u> <u>1h: 11n 2x2</u> <u>2h: 11n 3x3</u> <u>3h: 11ac 1x1</u> <u>4h: 11ac 2x2</u> <u>5h: 11ac 3x3</u> <u>Others: RSVD</u>  <u>Bit[7:4]: RSVD</u>	<u>04h</u>
<u>CAhCBh~CFh</u>	RSVD	-	-
D0h	USB_VID	USB Vender ID	DAh
D1h			0Bh
D2h	USB_PID	USB Product ID	12h
D3h			88h
D4h	USB_Option_0	Bit[7]: disable FS qualifier discripiter 1b : disable 0b : enable  Bit[6:3]: Reserved for Realtek. Do not change the value without the approval from Realtek.  Bit[2] : Bus Power 0 : bus power 1 : self power  Bit[1] : Remote Wakeup 0 : do not support 1 : support  Bit[0] : Serial Number <u>1 : Response from USB string descriptor 3</u> <u>0 : Response from Efuse offset DCh~D7h0 : Response default 00-E0-4C-00-00-01</u> <u>1 : Response from Efuse offset D5h~D0h</u>	<u>410h</u>
D5h	USB_Option_1	Bit[7:5] : Reserved for Realtek. Do not change the value without the approval from Realtek.  Bit[4] : LTM Support (0 : Disable, 1 : Enable)	66h



		<p>Bit[3:1] : Reserved for Realtek. Do not change the value without the approval from Realtek.</p> <p>Bit[0]: Link Power Management(LPM) support (0 : Disable, 1 : Enable) This field is valid in USB High Speed mode. While 1: enable the capability and the bcdUSB = 210h, support USB 2.0 extension descriptor While 0: do not support this capability, bcdUSB = 200h, and response STALL for USB 2.0 extension descriptor request.</p>	
D6h	USB_Option_2	<p>Bit[7:4] : Reserved for Realtek. Do not change the value without the approval from Realtek.</p> <p>Bit[3] BESL enable</p> <p>Bit[2:0] : Reserved for Realtek. Do not change the value without the approval from Realtek.</p>	429h
D7h~DCh	MAC Address	MAC Address	
DDh~116h	USB_String	Manufacture String and Product String	
117h	USB_Option_3	<p>Bit[7:6]: RSVD</p> <p>Bit[5] enable rxleidle poll in U3</p> <p>Bit[4] recovery idle 1ms timeout enable</p> <p>Bit[3:0] bInterval value in endpoint descriptor. Interval for servicing the interrupt endpoint for data transfers. Expressed in 125us units.</p>	21h
118h	USB_Option_4	<p>Bit[7:4]: RSVD</p> <p>bit[3:0] bMaxBurst value in companion descriptor. The maximum number of packets the endpoint can send as part of a burst.</p>	03h
119h	USB_Option_5	<p>Bit[7:4]: RSVD</p> <p>bit[3:0] bMaxBurst value in companion descriptor. The maximum number of packets the endpoint can receive as part of a burst.</p>	03h
11Ah	bU1DevExitLat	bU1DevExitLat value in SuperSpeed Device Capabilities Descriptor. U1 Device Exit Latency.	
11Bh~11Ch	bU2DevExitLat	bU2DevExitLat value in SuperSpeed Device Capabilities Descriptor. U2 Device Exit Latency.	
11Dh~12Fh	RSVD	-	-
<u>130h</u>	<u>Uphy_Parameter</u>	<p><u>USB phy parameters</u></p> <p><u>Do not change the value without the approval by Realtek.</u></p>	<u>0x81</u>
<u>131h</u>			<u>0xAE</u>
<u>132h</u>			<u>0x98</u>
<u>133h</u>			<u>0x2D</u>
<u>134h</u>			<u>0x03</u>
<u>135h</u>			<u>0x93</u>
<u>136h</u>			<u>0x96</u>
<u>137h</u>			<u>0x11</u>
<u>138h</u>			<u>0xFC</u>
<u>139h</u>			<u>0x8C</u>
<u>13Ah</u>			<u>0x00</u>
<u>13Bh</u>			<u>0x11</u>





<a href="#"><u>13Ch</u></a>	<a href="#"><u>0x9B</u></a>
<a href="#"><u>13Dh</u></a>	<a href="#"><u>0x78</u></a>
<a href="#"><u>13Eh</u></a>	<a href="#"><u>0x4A</u></a>
<a href="#"><u>13Fh</u></a>	<a href="#"><u>0xCE</u></a>
<a href="#"><u>140h</u></a>	<a href="#"><u>0xE0</u></a>
<a href="#"><u>141h</u></a>	<a href="#"><u>0x48</u></a>
<a href="#"><u>142h</u></a>	<a href="#"><u>0xE0</u></a>
<a href="#"><u>143h</u></a>	<a href="#"><u>0x70</u></a>
<a href="#"><u>144h</u></a>	<a href="#"><u>0x27</u></a>
<a href="#"><u>145h</u></a>	<a href="#"><u>0x00</u></a>
<a href="#"><u>146h</u></a>	<a href="#"><u>0x78</u></a>
<a href="#"><u>147h</u></a>	<a href="#"><u>0xEA</u></a>
<a href="#"><u>148h</u></a>	<a href="#"><u>0x60</u></a>
<a href="#"><u>149h</u></a>	<a href="#"><u>0xD0</u></a>
<a href="#"><u>14Ah</u></a>	<a href="#"><u>0xE1</u></a>
<a href="#"><u>14Bh</u></a>	<a href="#"><u>0x20</u></a>
<a href="#"><u>14Ch</u></a>	<a href="#"><u>0x322E</u></a>
<a href="#"><u>14Dh</u></a>	<a href="#"><u>0x61</u></a>
<a href="#"><u>14Eh</u></a>	<a href="#"><u>0x4F</u></a>
<a href="#"><u>14Fh</u></a>	<a href="#"><u>0x3C</u></a>
<a href="#"><u>150h</u></a>	<a href="#"><u>0x92</u></a>
<a href="#"><u>151h</u></a>	<a href="#"><u>0x40</u></a>
<a href="#"><u>152h</u></a>	<a href="#"><u>0x92</u></a>
<a href="#"><u>153h</u></a>	<a href="#"><u>0x154</u></a>
<a href="#"><u>154h</u></a>	<a href="#"><u>0x8B</u></a>
<a href="#"><u>155h</u></a>	<a href="#"><u>0x6EA</u></a>
<a href="#"><u>156h</u></a>	<a href="#"><u>0x4CD</u></a>
<a href="#"><u>157h</u></a>	<a href="#"><u>0x8A</u></a>
<a href="#"><u>158h</u></a>	<a href="#"><u>0x17</u></a>
<a href="#"><u>159h</u></a>	<a href="#"><u>0xC1</u></a>
<a href="#"><u>15Ah</u></a>	<a href="#"><u>0x98</u></a>
<a href="#"><u>15Bh</u></a>	<a href="#"><u>0x00</u></a>
<a href="#"><u>15Ch</u></a>	<a href="#"><u>0x80</u></a>
<a href="#"><u>15Dh</u></a>	<a href="#"><u>0x0C</u></a>
<a href="#"><u>15Eh</u></a>	<a href="#"><u>0x00</u></a>
<a href="#"><u>15Fh</u></a>	<a href="#"><u>0x4C</u></a>
<a href="#"><u>160h</u></a>	<a href="#"><u>0xFC</u></a>
<a href="#"><u>161h</u></a>	<a href="#"><u>0x81</u></a>
<a href="#"><u>162h</u></a>	<a href="#"><u>0x0C</u></a>
<a href="#"><u>163h</u></a>	<a href="#"><u>0x01</u></a>
<a href="#"><u>164h</u></a>	<a href="#"><u>0xDE</u></a>
<a href="#"><u>165h</u></a>	<a href="#"><u>0x00</u></a>
<a href="#"><u>166h</u></a>	<a href="#"><u>0x00</u></a>
<a href="#"><u>167h</u></a>	<a href="#"><u>0x00</u></a>
<a href="#"><u>168h</u></a>	<a href="#"><u>0x00</u></a>
<a href="#"><u>169h</u></a>	<a href="#"><u>0x02</u></a>
<a href="#"><u>16Ah</u></a>	<a href="#"><u>0xE1</u></a>
<a href="#"><u>16Bh</u></a>	<a href="#"><u>0x63</u></a>
<a href="#"><u>16Ch</u></a>	<a href="#"><u>0x12</u></a>



<u>16Dh</u>		<u>0xDD</u>
<u>16Eh</u>		<u>0x21</u>
<u>16Fh</u>		<u>0x00</u>
<u>170h</u>		<u>0xCB</u>
<u>171h</u>		<u>0x3F</u>
<u>172h</u>		<u>0xA0</u>
<u>173h</u>		<u>0xE0</u>
<u>174h</u>		<u>0xC2</u>
<u>175h</u>		<u>0xF0</u>
<u>176h</u>		<u>0xEA3</u>
<u>177h</u>		<u>0x48F</u>
<u>178h</u>		<u>0x042</u>
<u>179h</u>		<u>0x36</u>
<u>17Ah</u>		<u>0x08</u>
<u>17Bh</u>		<u>0x41</u>
<u>17Ch</u>		<u>0x05</u>
<u>17Dh</u>		<u>0xEB</u>
<u>17Eh</u>		<u>0x94</u>
<u>17Fh</u>		<u>0xD7</u>
<u>180h</u>		<u>0x14</u>
<u>181h</u>		<u>0x6D</u>
<u>182h</u>		<u>0xC8</u>
<u>183h</u>		<u>0x67</u>
<u>184h</u>		<u>0x80</u>
<u>185h</u>		<u>0x20</u>
<u>186h</u>		<u>0xF8</u>
<u>187h</u>		<u>0x80</u>
<u>188h</u>		<u>0x30</u>
<u>189h</u>		<u>0x80</u>
<u>18Ah</u>		<u>0x30</u>
<u>18Bh</u>		<u>0x38</u>
<u>18Ch</u>		<u>0x20</u>
<u>18Dh</u>		<u>0x34</u>
<u>18Eh</u>		<u>0xA1</u>
<u>18Fh</u>		<u>0x94</u>
<u>190h</u>		<u>0x77</u>
<u>191h</u>		<u>0xB2</u>
<u>192h</u>		<u>0x94</u>
<del>130h</del> <u>193h</u> ~1F Fh	RSVD	Reserved for Realtek. Do not change the value without the approval from Realtek.