

U1

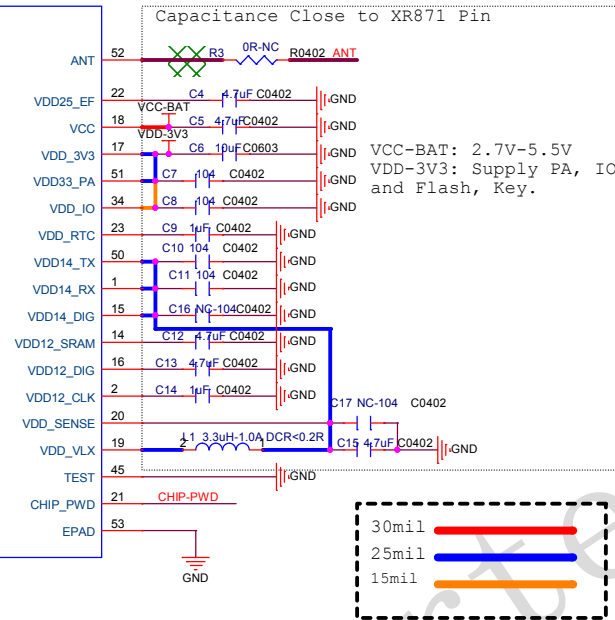
PA0	5	PA0
PA1	6	PA1
PA2	11	PA2
PA3	10	PA3
PA4	35	PA4
PA5	36	PA5
PA6	37	PA6
PA7	38	PA7
PA8	26	PA8
PA9	27	PA9
PA10	28	PA10
PA11	29	PA11
PA12	30	PA12
PA13	31	PA13
PA14	32	PA14
PA15	33	PA15
PA16	7	PA16
PA17	39	PA17
PA18	40	PA18
PA19	41	PA19
PA20	42	PA20
PA21	43	PA21
PA22	44	PA22

UART0-TX	49	PB0
UART0-RX	48	PB1
SPI0-WP	8	PB2
SPI0-HOLD	9	PB3
SPI0-MOSI	12	PB4
SPI0-MISO	13	PB5
SPI0-CS	47	PB6
SPI0-CLK	46	PB7

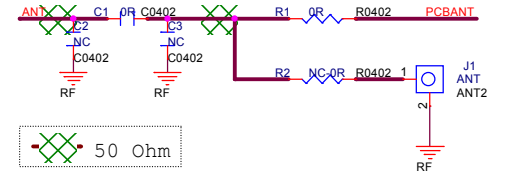
HXTAL1	4	HXTAL1
HXTAL2	3	HXTAL2
LXTAL1	25	LXTAL1
LXTAL2	24	LXTAL2

XR871

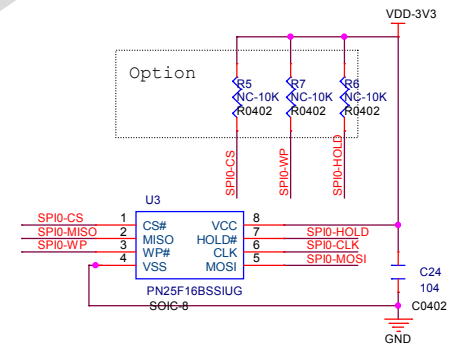
XR871 is xxxxxx



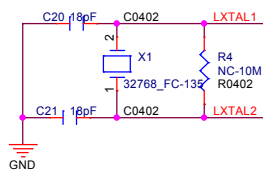
Antenna



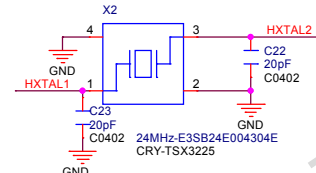
FLASH



CLK

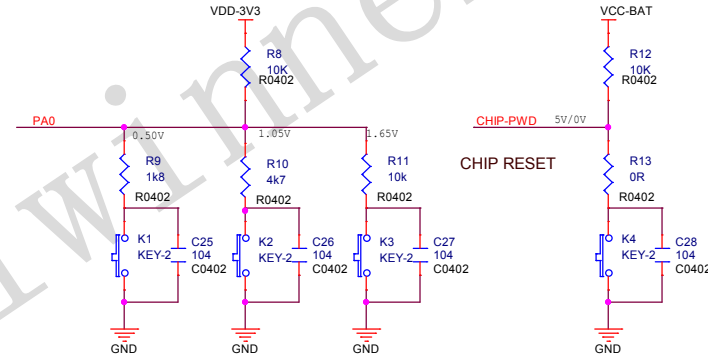


LOSC



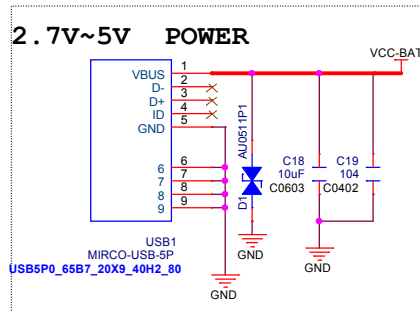
HOSC

KEY

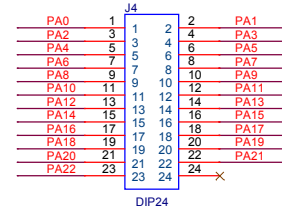


CHIP RESET

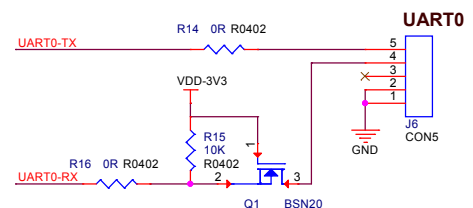
POWER



GPIO



DEBUG UART



芯之联

Design Name	XR871 REF		
Size A3	Page Name	XR871 REF	
Date:	Wednesday, April 12, 2017	Sheet	1 of 2