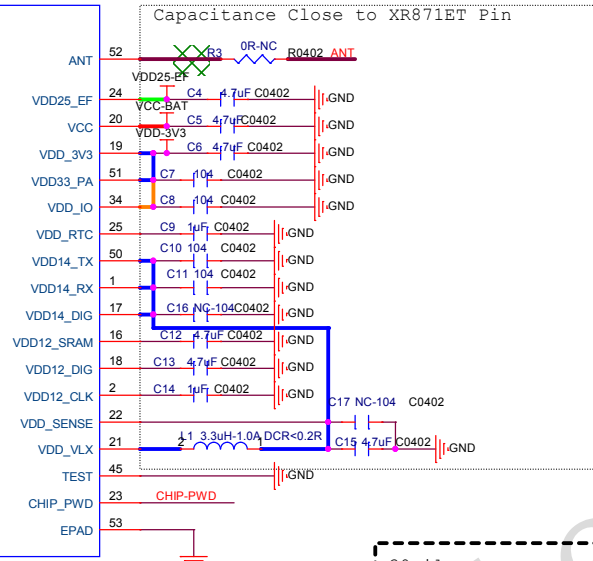


U1

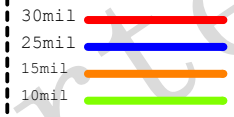
PA0 SPI1\_MOSI/SD\_CMD/UART0\_TX/CSI\_D0/EINTA0  
 PA1 SPI1\_MISO/SD\_DATA0/UART0\_RX/CSI\_D1/EINTA1  
 PA2 SPI1\_CLK/SD\_CLK/TWI1\_SCL/CSI\_D2/EINTA2  
 PA3 SPI1\_CS0/SD\_DATA1/TWI1\_SDA/CSI\_D3/EINTA3  
 PA4 WUPIO0/UART1\_CTS/SD\_DATA2/TWI0\_SCL/CSI\_D4/EINTA4  
 PA5 WUPIO1/UART1\_RTS/SD\_DATA3/TWI0\_SDA/CSI\_D5/EINTA5  
 PA6 WUPIO2/UART1\_TX/SPI1\_CS2/TWI0\_SCL/CSI\_D6/EINTA6  
 PA7 WUPIO3/UART1\_RX/SPI1\_CS2/TWI0\_SDA/CSI\_D7/EINTA7  
 ADC-CH0 PA8/ADC\_CH0/PWM0/TWI1\_SCL/CSI\_PCLK/EINTA8  
 PA9/ADC\_CH1/PWM1/TWI1\_SDA/CSI\_MCLK/EINTA9  
 PA10/ADC\_CH2/PWM2/DMIC\_CLK/CSI\_HSYNC/EINTA10  
 PA11/ADC\_CH3/PWM3/DMIC\_DATA/CSI\_VSYNC/EINTA11  
 PA12/ADC\_CH4/PWM4/I2S\_MCLK/I2R\_TX/EINTA12  
 PA13/ADC\_CH5/PWM5/I2S\_BCLK/I2R\_TX/EINTA13  
 PA14/ADC\_CH6/PWM6/I2S\_DI/I2R\_RX/EINTA14  
 PA15/ADC\_CH7/PWM7/I2S\_DO/UART1\_CTS/EINTA15  
 PA16/I2R\_TX/I2R\_RX/I2S\_LRCLK/UART1\_RTS/EINTA16  
 PA17 WUPIO4/TWI0\_SCL/I2R\_RX/TWI1\_SCL/UART1\_TX/EINTA17  
 PA18 WUPIO5/TWI0\_SDA/I2R\_TX/TWI1\_SDA/UART1\_RX/EINTA18  
 PA19 WUPIO6/UART\_CTS/NSWD\_TMS/PWM0/SPI1\_MOSI/EINTA19  
 PA20 WUPIO7/UART\_RTS/NSWD\_TCK/PWM1/SPI1\_MISO/EINTA20  
 PA21 WUPIO8/UART\_TX/DMIC\_CLK/PWM2/SPI1\_CLK/EINTA21  
 PA22 WUPIO9/UART\_RX/DMIC\_DATA/PWM3/SPI1\_CS0/EINTA22

UART0-TX PA49  
 UART0-RX PA48  
 SPI0-WP PA10  
 SPI0-HOLD PA11  
 SPI0-MOSI PA14  
 SPI0-MISO PA15  
 SPI0-CS PA47  
 SPI0-CLK PA46  
 PB13 PA13  
 PB15 PA15

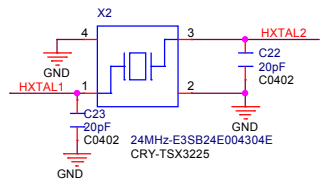
XR871ET  
 XR871ET is xxxxxx



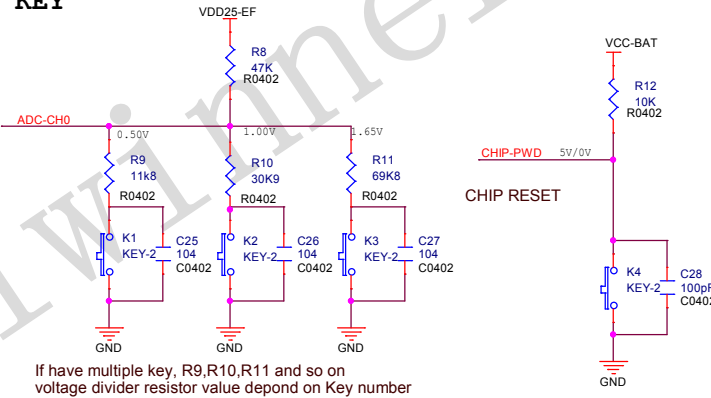
VCC-BAT: 2.7V~5.5V 600mA  
 VDD-3V3: Supply PA, IO and Flash, Key.  
 2.7V~3.5V 400mA  
 VDD25-EF: 2.4V~2.6V 40mA



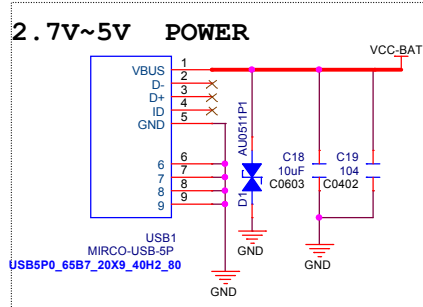
## CLK



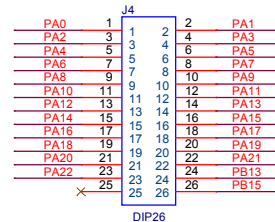
## KEY



## POWER



## GPIO



## Antenna

