



Diagram illustrating the connection of multiple keys (K1, K2, K3, K4) to a microcontroller (MCU) using voltage dividers and a reset circuit.

The MCU pins shown are:

- ADC-CH0
- CHIP-PWD
- CHIP RESET

The voltage dividers are connected to VDD25-EF (5V) and provide specific voltage levels to the MCU pins:

- VDD25-EF (5V) is connected to R8 (47K, R0402).
- R8 is connected to R9 (11K8, R0402).
- R9 is connected to ADC-CH0 (0.50V).
- R9 is connected to R10 (30K9, R0402).
- R10 is connected to CHIP-PWD (1.00V).
- R10 is connected to R11 (69K8, R0402).
- R11 is connected to CHIP RESET (1.65V).

The reset circuit is connected to VCC-BAT (5V/0V) and provides a reset signal to the CHIP RESET pin:

- VCC-BAT (5V/0V) is connected to R12 (10K, R0402).
- R12 is connected to K4 (KEY-2).
- K4 is connected to CHIP RESET.

Capacitors C25 (100pF, C0402), C26 (100pF, C0402), and C27 (100pF, C0402) are connected to ground (GND) to filter noise.

If have multiple key, R9,R10,R11 and so on voltage divider resistor value depend on Key number

	J4				
PA0	1		2		PA1
PA2	3	1	4		PA3
PA4	5	3	6		PA5
PA6	7	5	8		PA7
PA8	9	7	10		PA9
PA10	11	9	12		PA11
PA12	13	11	14		PA13
PA14	15	13	16		PA15
PA16	17	15	18		PA17
PA18	19	17	20		PA19
PA20	21	19	22		PA21
PA22	23	21	24		

DIP24