

2D and 3D Graphics in Freescale Devices

AMF-CON-T1025

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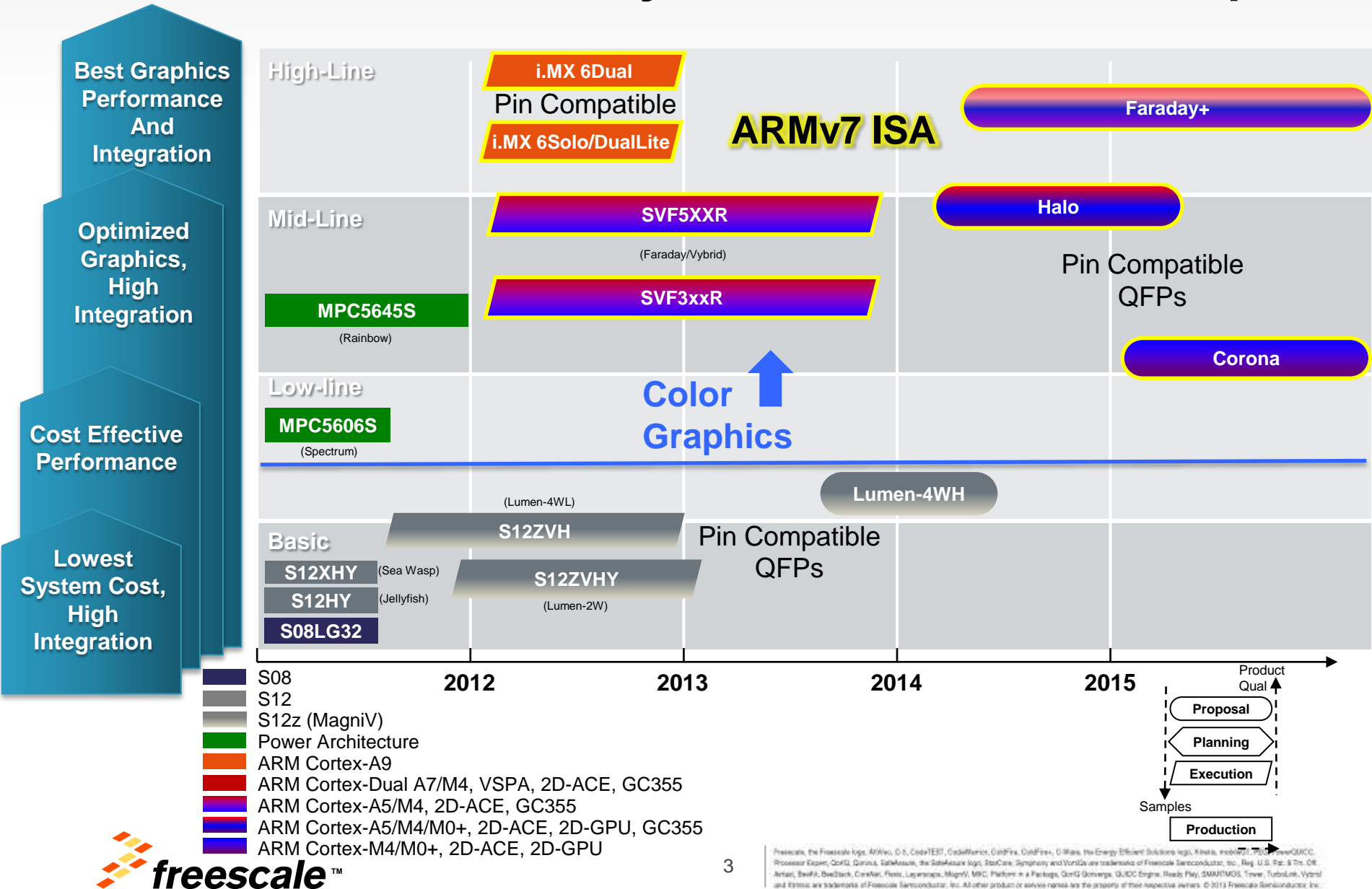




Agenda

- Graphics in Freescale
- Introduction to graphics standards
- OpenGL ES vs OpenGL ES 2.0 vs OpenGL ES 3.0 vs OpenGL
- Freescale GPU_SDK
- Boundaries and relationship between layers and tools
- Benchmarks

Driver Information Systems Product Roadmap



Graphics cores

GC2000
GC880
GC355
GC255

Z430
Z160

GC320
2D-ACE
IPU*

VPU
VIU
etc

GCxxx cores are Vivante's IP, newer
Zxxx cores are AMD's, older
2D-ACE (DCUx), IPU are Freescale's

Video input
encoding
decoding
*ipu CSI

Content creation
2D/3D Real time rendering

Composition
Memory manipulation

Graphics cores

GC2000
GC880
Z430

- OpenGL ES 1.1
- OpenGL ES 2.0
- OpenGL ES 3.0



3D

GC355
Z160

OpenVG 1.1



2D/Vector

GC320
IPU
2D-ACE/DCUx

DirectFB 2D-ACE gfxlibs



Composition

- 

Key concepts

- OpenGL – Open Graphics Library
- OpenGL ES – OpenGL for Embedded Systems
- EGL – Embedded-systems Graphics Library
- OpenCL – Open Computing Library
- OpenVG – Open Vector Graphics-library
- 2D-ACE – 2D Animation and Composition Engine (sexy for DCU)
- DCU – Display Controller Unit
- IPU – Image processing Unit
- Wayland – computer display server protocol, like X
- Weston – reference implementation of a wayland compositor
- Frame buffer – contiguous chunk of memory that stores a frame

i.MX6 Linux graphics layers

Layer/	Example
IPU	Frame buffer
EGL	Qt, DirectFB
OGLES/OVG	glClear();
Engine	Unity3D, EBGuide
Application	3D cluster

Vybrid graphics layers

Layer/	Example
DCU	Configure DCU
Scheduler/RTOS	MQX
OVG/GFXLibs	vgClear(); Altia
Application	2D cluster

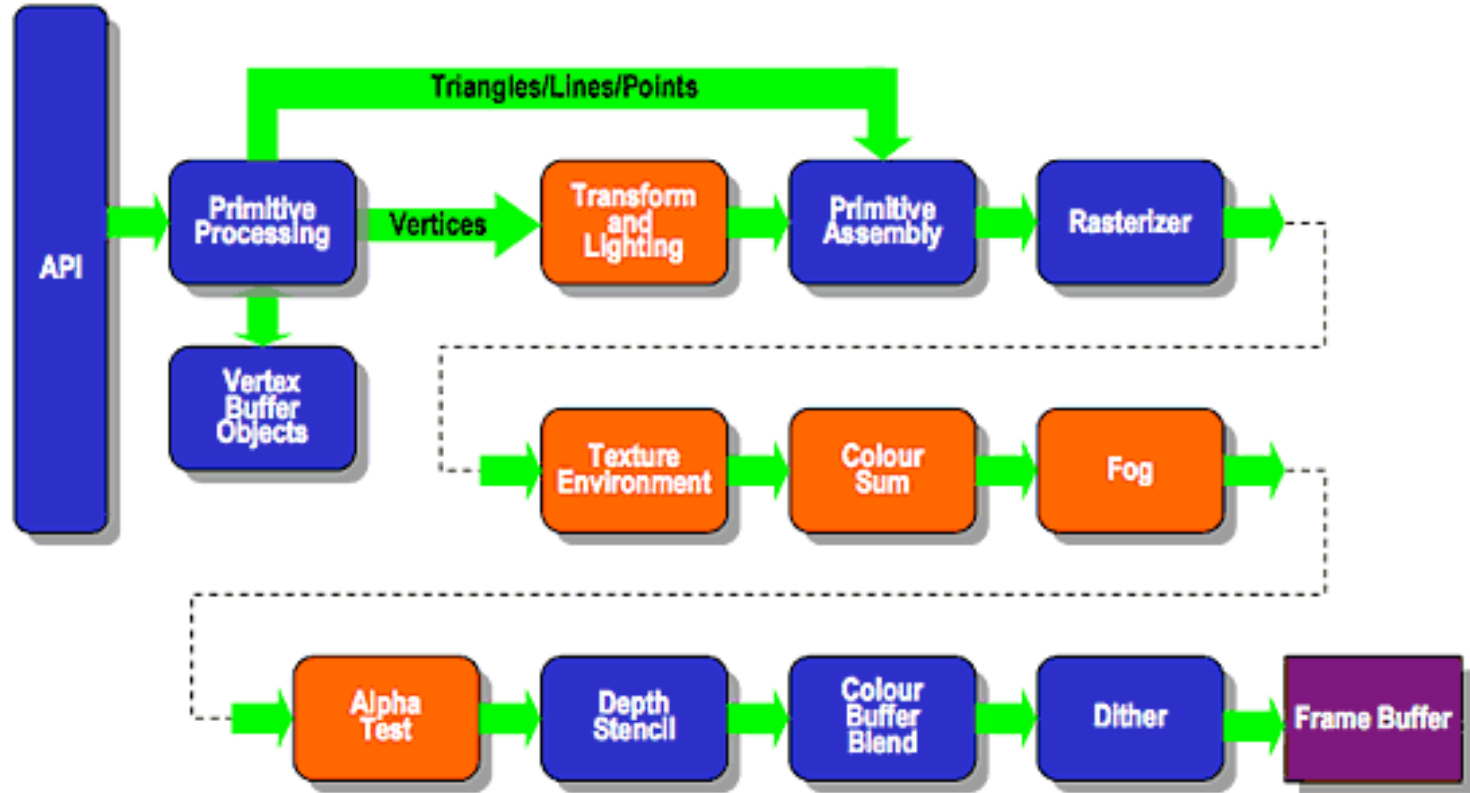
- 
- A red sports car, possibly a Ferrari, shown from a front-three-quarter view. The car is sleek and aerodynamic, with a prominent front grille and large wheels. It is positioned on a white background.

Differences between OpenGLs

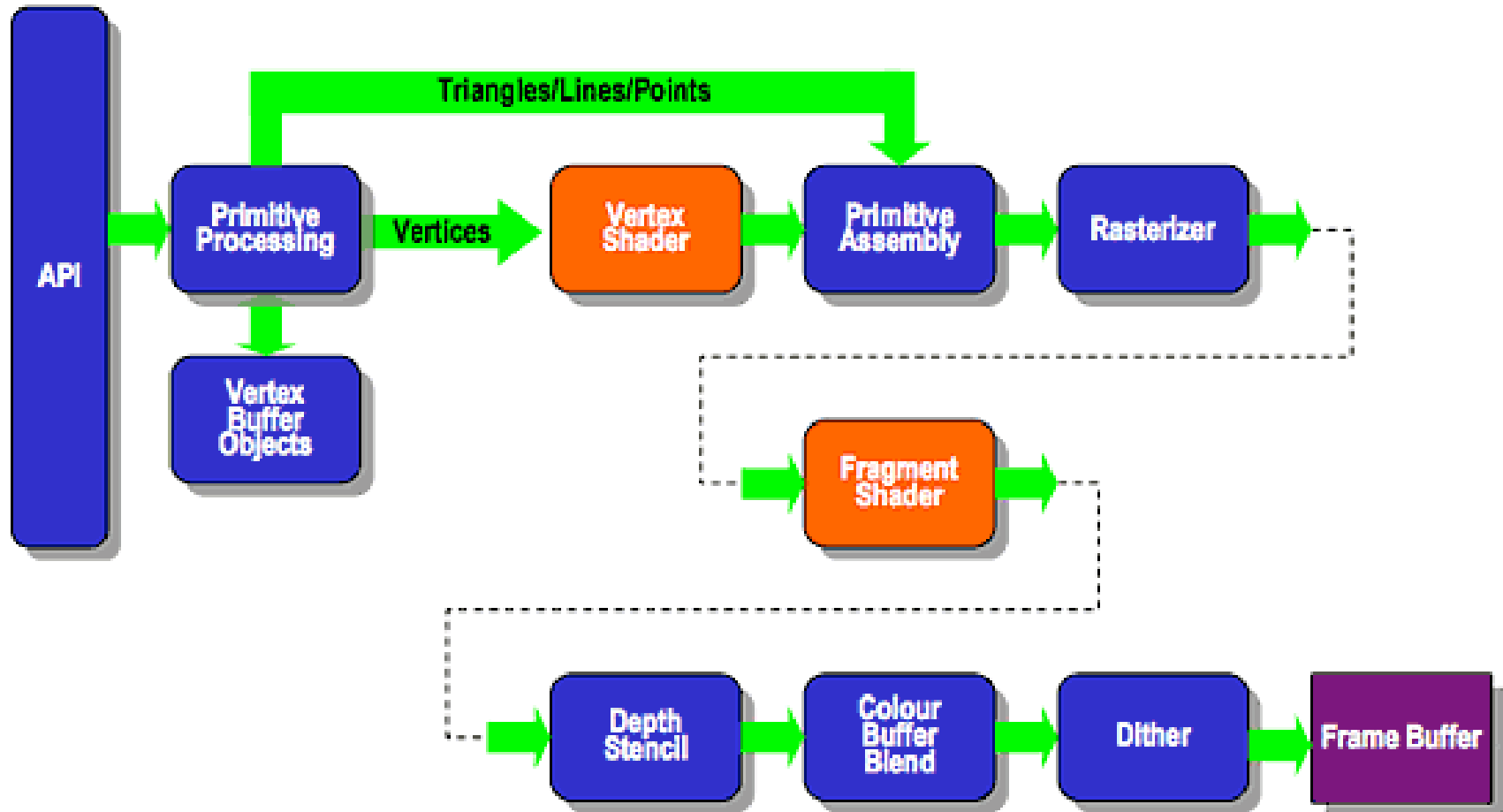
- OpenGL – Desktop
- OpenGL ES 1.0, 1.1 (i.mx35, mpc5121, etc)
 - Fixed pipeline
 - Not compatible with 2.0
- OpenGL ES 2.0, 3.0 (i.mx5x, 6x)
 - Programmable pipeline (shader programs)
 - Compatible with 1.1 by a shader program

Fixed (1.0) Pipeline

Existing Fixed Function Pipeline



The OpenGL ES 2.0 Pipeline



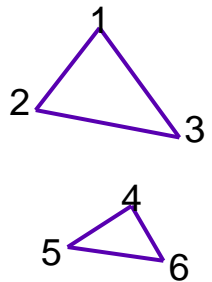
Shaders



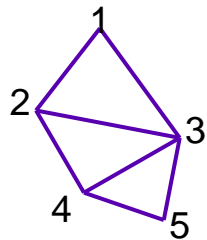


Triangles in OpenGL

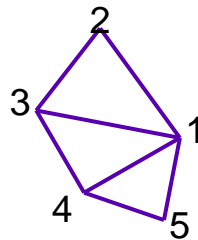
- 3D objects are built from one or more series or “strips” of triangles



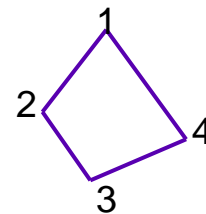
GL_TRIANGLES



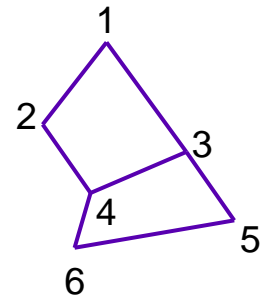
GL_TRIANGLE_STRIP



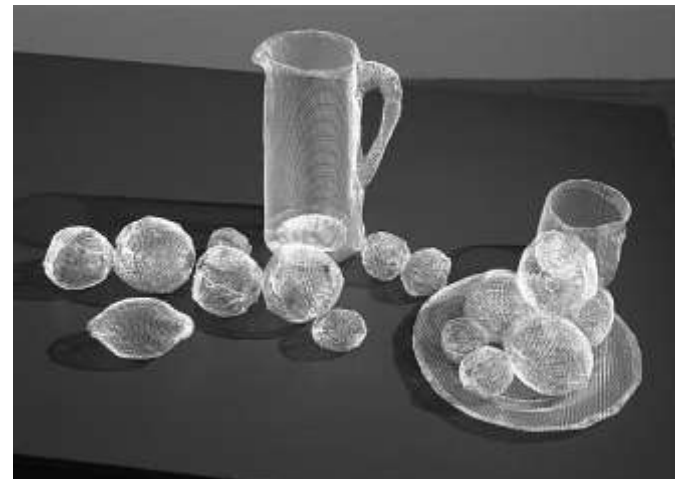
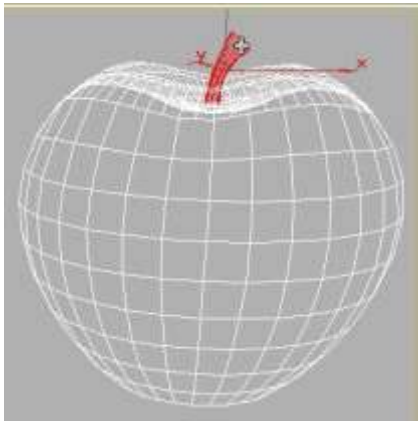
GL_TRIANGLE_FAN



GL_QUADS



GL_QUAD_STRIP



How to place this in a 3D world?

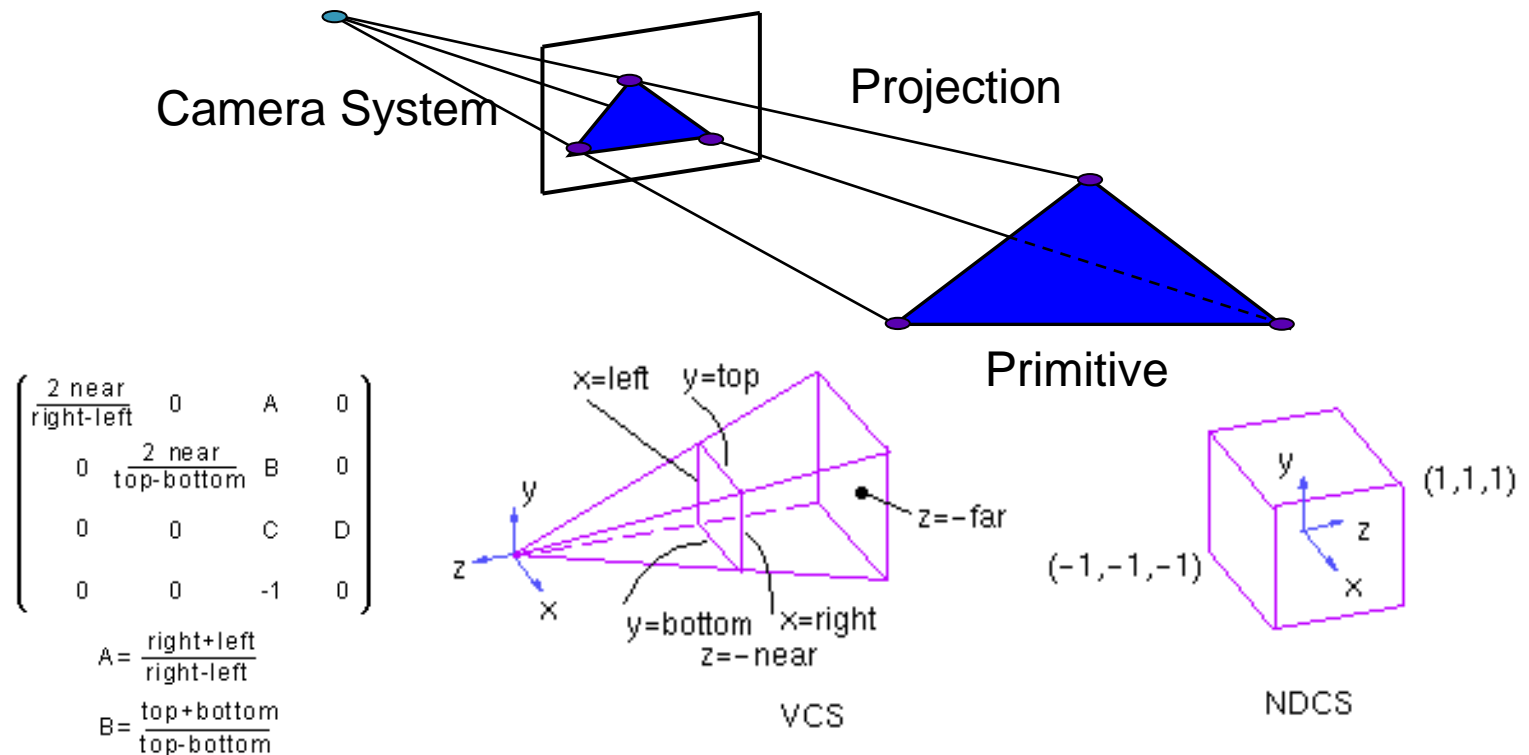
- Everything is referenced using three coordinate systems:
 - Global space
 - Model space
 - Camera space
- The Matrix

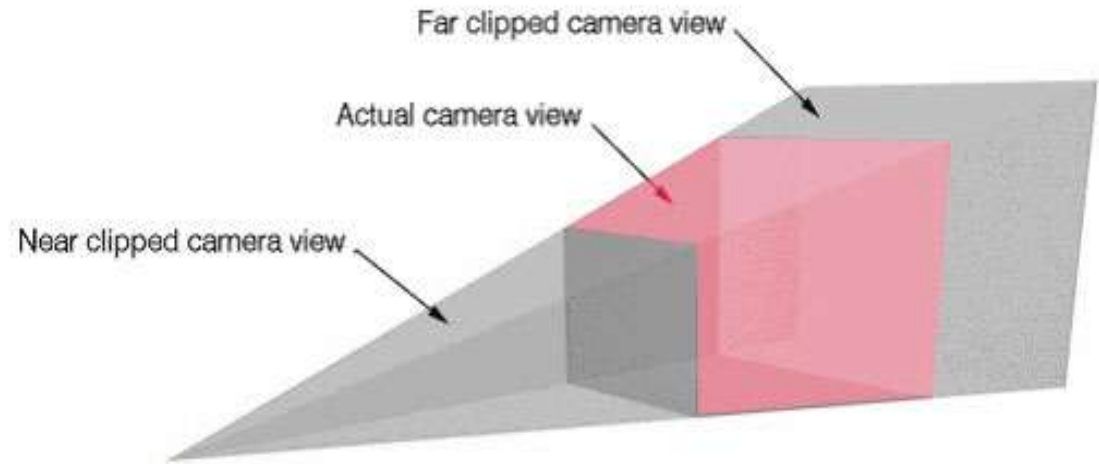
$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ x & y & z & 1 \end{bmatrix}$$

MATRICES!! MATH!!

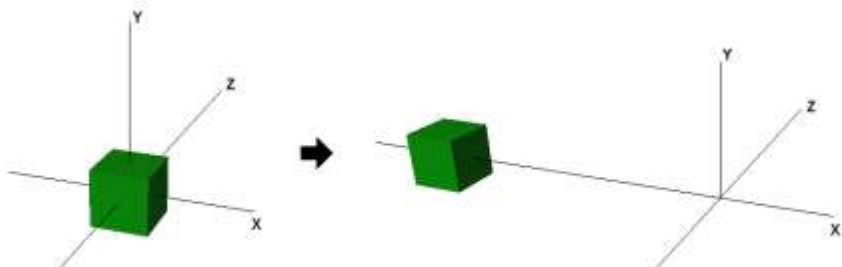
The 3D world (Global space)

- The 3D Cartesian coordinates that define the triangle vertices are multiplied by a matrix to move it from the object's space to the "screen" space





Transformations (Modelview Matrix)

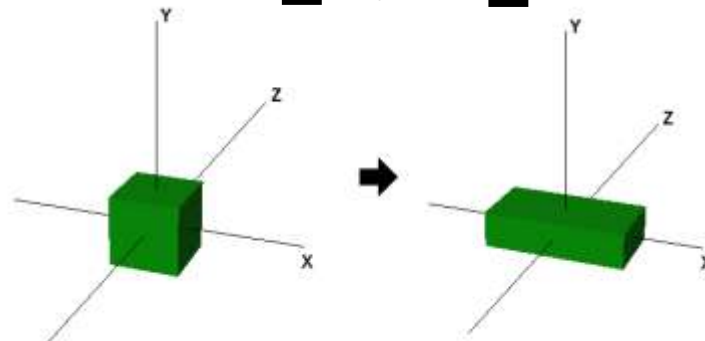
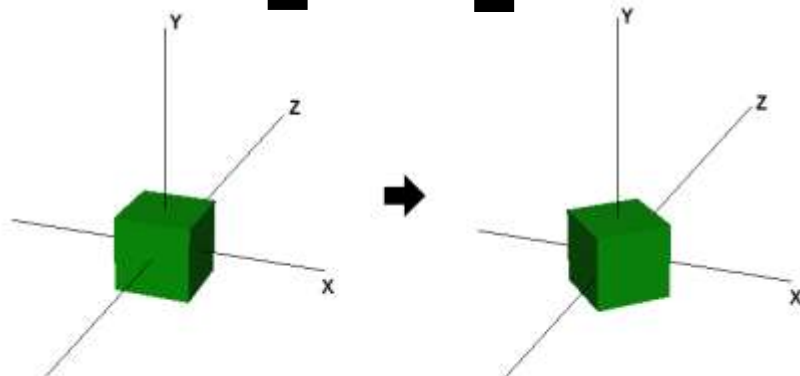


Translation

$$\begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ x & y & z & 1 \end{bmatrix}$$

$$\begin{bmatrix} x & 0 & 0 & 0 \\ 0 & y & 0 & 0 \\ 0 & 0 & z & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$

Scale

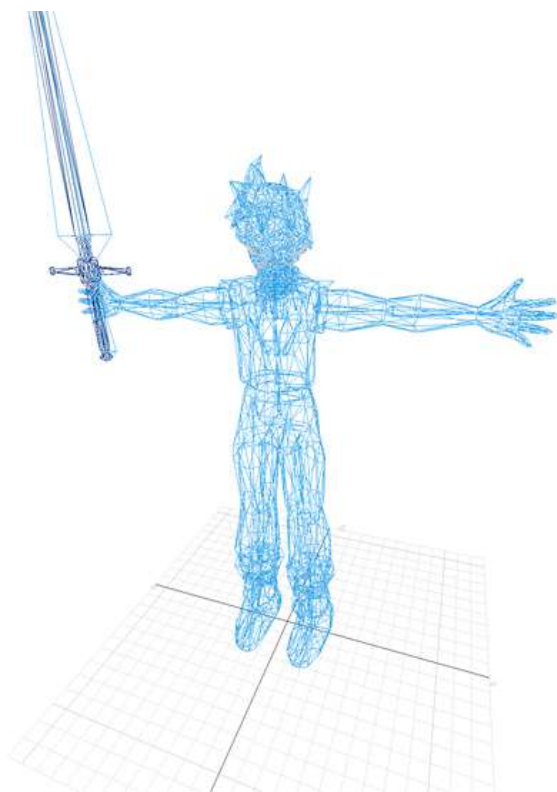


Rotation (y)

$$\begin{bmatrix} \cos a & 0 & \sin a & 0 \\ 0 & 1 & 0 & 0 \\ -\sin a & 0 & \cos a & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$

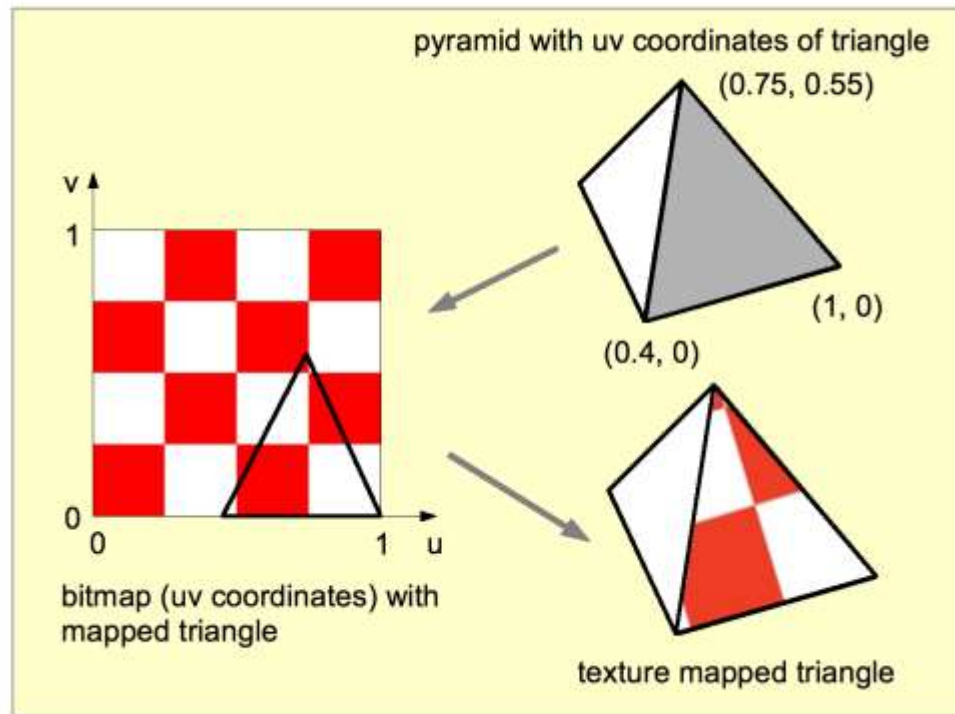
3D models

- 3D models are made from a series of triangles and UV textures



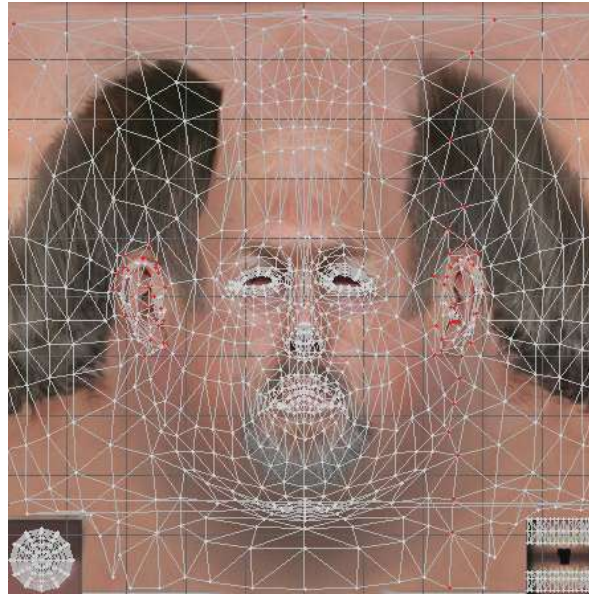
Texture Mapping

- If a texture (2D still image) is being applied to the surface, the interpolated texture coordinates are used to derive what color should be sampled from the texture.



Texture Mapping

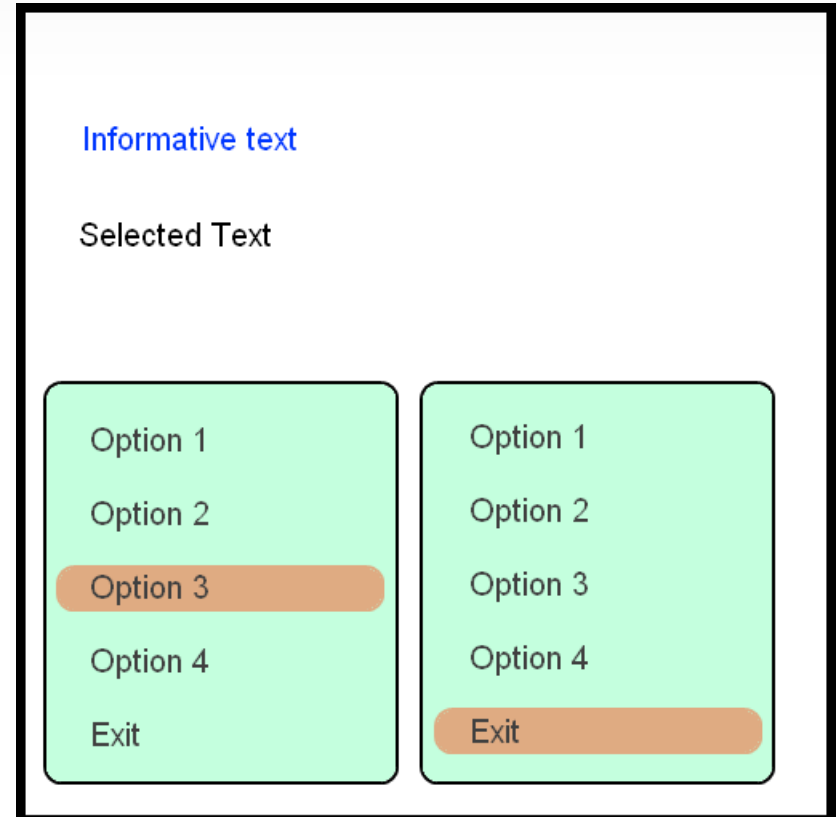
- To every vertex in our mesh, a pair of 2D coordinates (UV) is assigned and pixels are interpolated



UV Textures go from (0.0,0.0) to (1.0, 1.0)

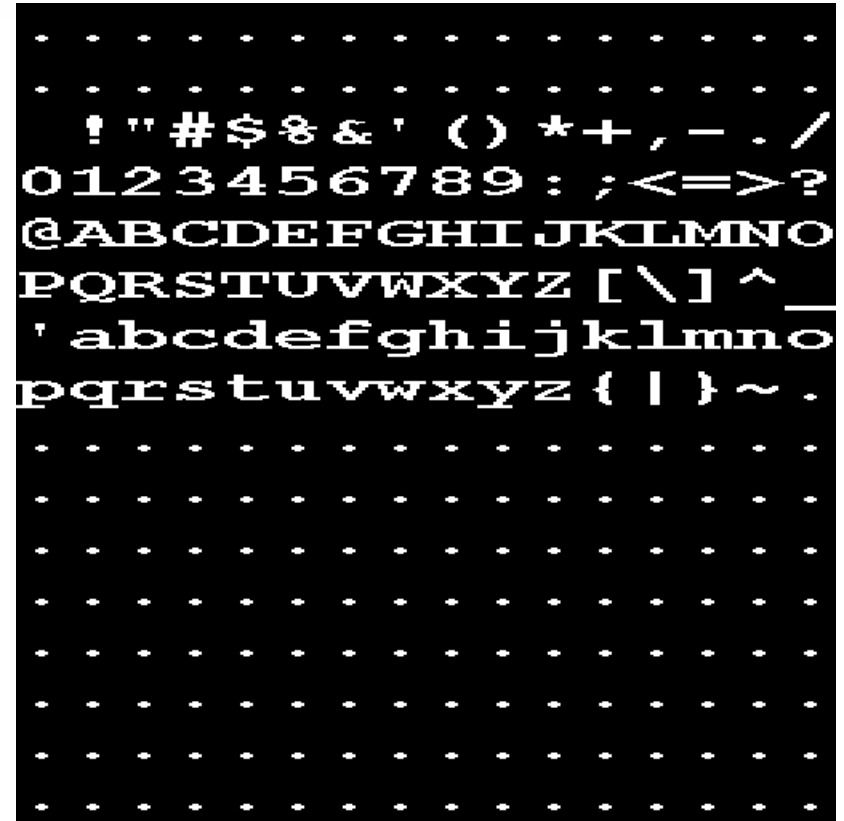
Text-tures

- Render string to texture
- PROS
 - Easiest method
- CONS
 - Least flexible method
 - Texture space in memory
 - Not scalable (resolution)



Text-tures

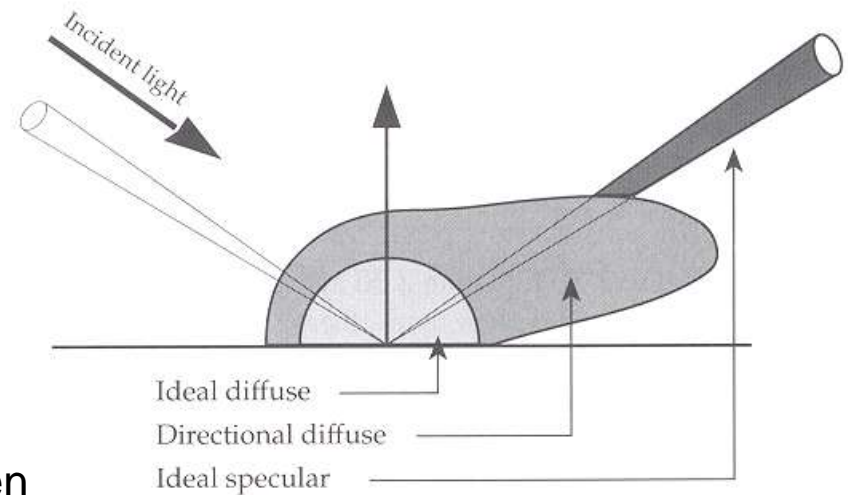
- Create a sprite-based alphabet
- PROS
 - More efficient
 - Good for plain text
- CONS
 - Complexity ++
 - Check alignment, spacing, etc



- 

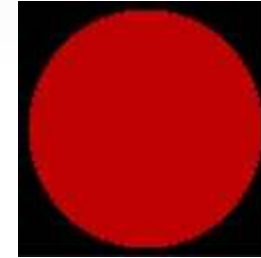
OpenGL Lighting

- Lighting is calculated at each vertex
- Phong Lighting 3 components:
 - Ambient.
 - Do not depend of the light source.
 - Diffuse.
 - The light is evenly reflected in all direction (think flashlight).
 - Specular.
 - The reflection is predominant in a given direction.
 - The specular coefficient can be varied.
- The light intensity can decrease according the distance (constant, linear or quadratic).



OpenGL Lighting

- By combining all the lighting components, we can create very realistic real-time renders



Ambient Only



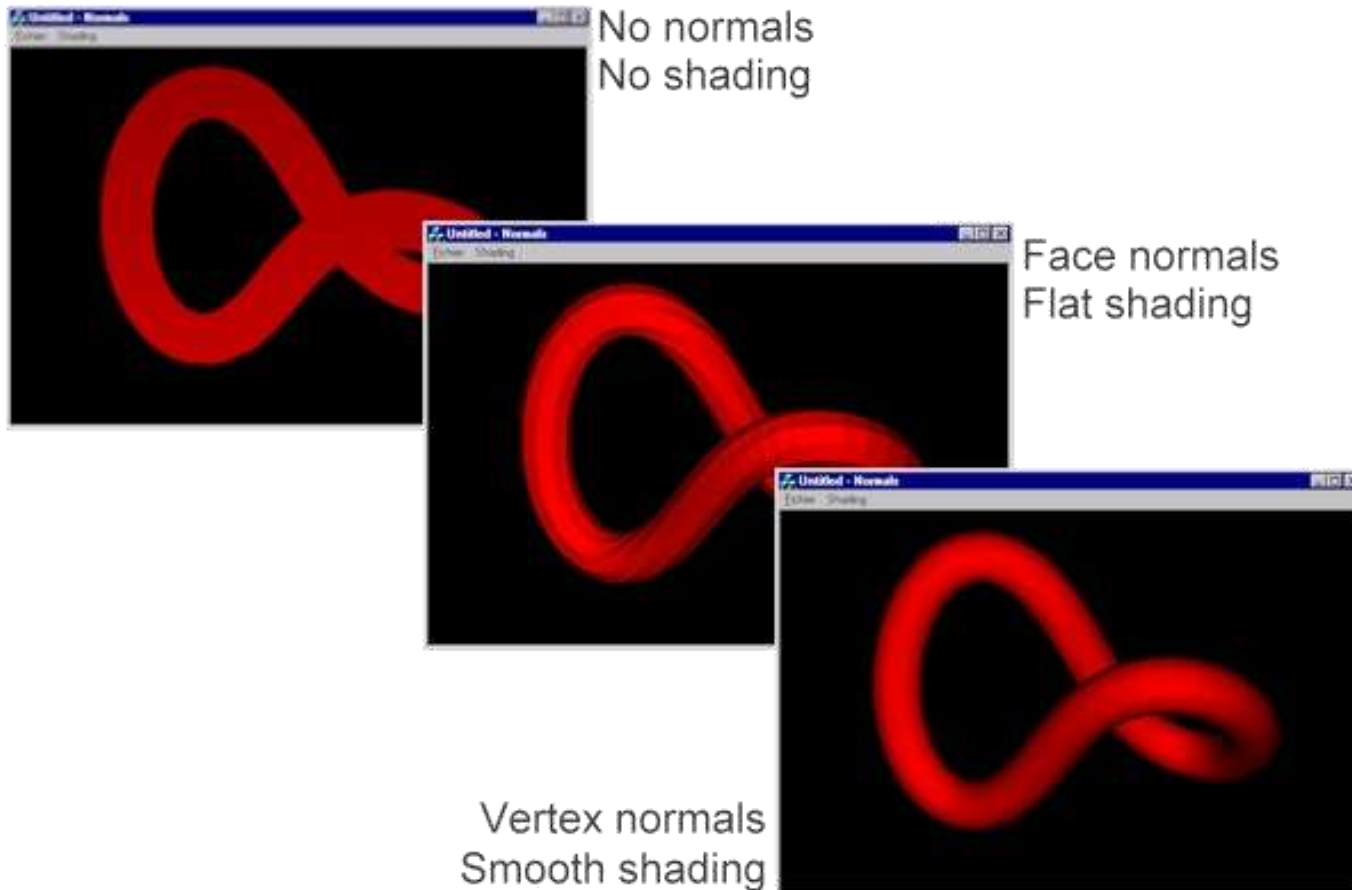
Ambient + Diffuse



Ambient + Diffuse
+ Specular

Shading Modes (ES 1.0)

- A normal vector defines the orientation of a surface relative to a light source



Shading Modes (ES 2.0)

Flat color, vertex position only



Cartoony look

Color information



Per pixel lighting

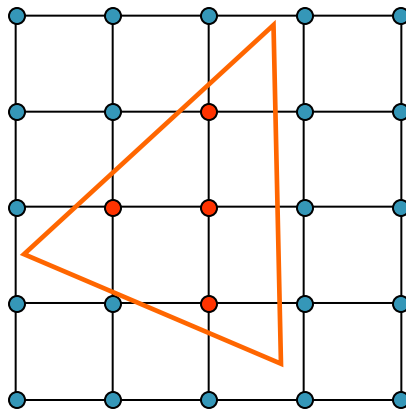
Moving the vertices around



Textures

Rasterization

- After the 3 vertex coordinates are transformed into screen space, they are then bilinearly interpolated and aliased to 'fragments'
- Other data associated with the vertices is also interpolated (colors, normals, texture coordinates, etc.)

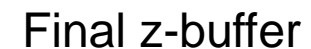


Rasterization

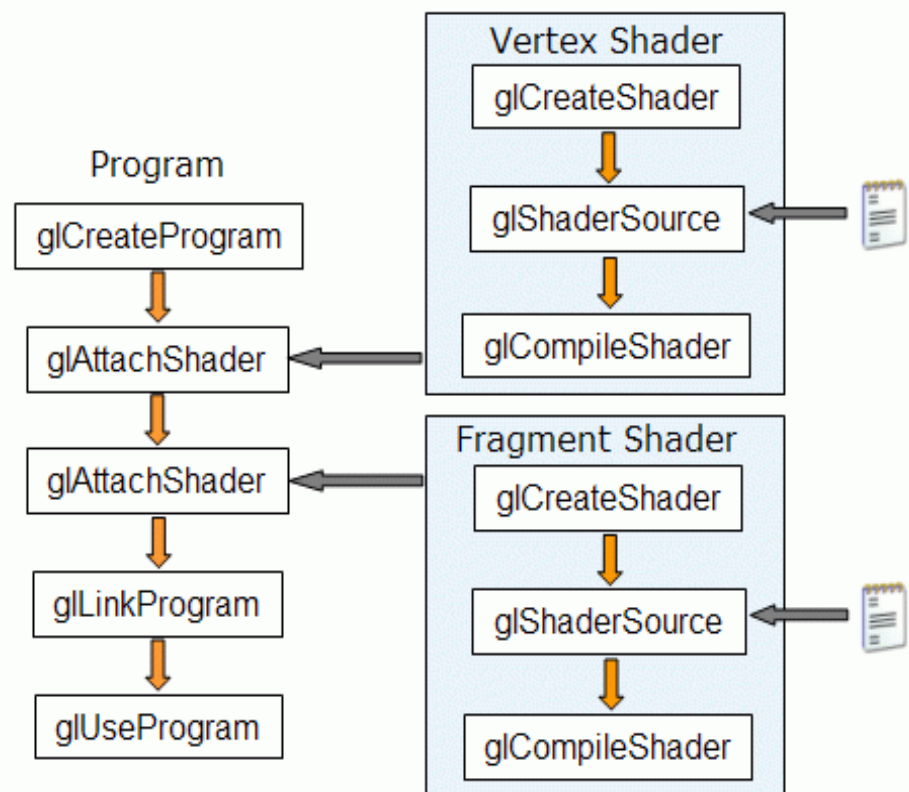


Linear Color Interpolation

-



- GL Shading Language
- Supported in OpenGL ES 2.0
- Lets you define specific parts of the graphics pipeline
 - Vertex shader
 - Fragment shader
- C like language to write shaders
- Shaders have to be written, compiled and linked



GLSL overview

- Data types

- | | |
|-------------------------------------|--|
| • float | <code>float a,b; // two vectors</code> |
| • bool | <code>int c = 2; // c is initialized with 2</code> |
| • Int | <code>bool d = true; // d is true</code> |
| • <code>vec{2,3,4}</code> | <code>vec3 direction;</code> |
| • <code>bvec{2,3,4}</code> | <code>bvec2 lightFlags;</code> |
| • <code>ivec{2,3,4}</code> | <code>ivec3 color;</code> |
| • <code>mat{2,3,4}</code> | <code>square matrices</code> |
| • <code>sampler{1,2,3}D</code> | <code>for textures</code> |
| • <code>samplerCube</code> | <code>for cube map textures</code> |
| • <code>sampler{1D,2D}Shadow</code> | <code>for shadow maps</code> |
| • arrays | <code>struct dirlight { // type definition</code> |
| • structs | <code>vec3 direction; vec3 color; };</code> |

GLSL overview

- Variable qualifiers
 - **const**
 - The declaration is of a compile time constant
 - **attribute**
 - Global variables
 - May change per vertex
 - Passed from the application to vertex shaders.
 - Can only be used in vertex shaders.
 - Read-only variable.
 - **uniform**
 - Global variables
 - May change per primitive
 - Passed from the application to the shaders
 - Read-only variable.
 - **varying**
 - Used to pass data between vertex and fragment shader
 - write in vertex shader, and read-only in a fragment shader.

GLSL overview

- Functions
 - At least a main per shader
 - Functions can't return arrays
 - Function overloading is possible (different parameters)

```
vec4 toonify(in float intensity)
{
    vec4 color;
    if (intensity > 0.98)
        color = vec4(0.8,0.8,0.8,1.0);
    else if (intensity > 0.5)
        color = vec4(0.4,0.4,0.8,1.0);
    else if (intensity > 0.25)
        color = vec4(0.2,0.2,0.4,1.0);
    else
        color = vec4(0.1,0.1,0.1,1.0);
    return(color);
}
```

Vertex Shader Functions

- Receives vertex data as input (position, colors, normals...)
 - The vertex shader can do:
 - Transformation of position using model-view and projection matrices
 - Transformation of normals, including renormalization
 - Texture coordinate generation and transformation
 - Per-vertex lighting
 - Color computation
 - The vertex shader cannot do:
 - Anything that requires information from more than one vertex
 - Anything that depends on connectivity.
 - Any triangle operations (e.g. clipping, culling)
 - Access color buffer
- Responsible for writing AT LEAST gl_Position

Sample Vertex Shader

```
uniform    mat4 g_matModelView;
uniform    mat4 g_matProj;
attribute  vec4 g_vPosition;
attribute  vec3 g_vColor;
varying    vec3 g_vVSColor;

void main()
{
    vec4 vPositionES = g_matModelView * g_vPosition;
    gl_Position = g_matProj * vPositionES;
    g_vVSColor = g_vColor;
}
```

Fragment Shader Functions

- Receives vertex shader output (varying variables) as input
 - The fragment shader can do:
 - Texture blending
 - Fog
 - Alpha testing
 - Dependent textures
 - Pixel discarding
 - Bump and environment mapping
 - The fragment shader cannot do:
 - Blending with colour buffer
 - ROP operations
 - Depth or stencil tests
 - Write depth
- Responsible of writing gl_FragColor (fragment color)

Sample Fragment Shader

```
precision highp float;
varying    vec3    g_vVSColor;
void main()
{
    gl_FragColor = vec4( g_vVSColor, 1.0 );
}
```


Compile the shader program

```
// Compile the shaders
GLuint hVertexShader = glCreateShader( GL_VERTEX_SHADER );
glShaderSource( hVertexShader, 1, &g_strVertexShader, NULL );
glCompileShader( hVertexShader );
// Check for compile success with glGetShaderiv

GLuint hFragmentShader = glCreateShader( GL_FRAGMENT_SHADER );
glShaderSource( hFragmentShader, 1, &g_strFragmentShader, NULL );
glCompileShader( hFragmentShader );
// Check for compile success with glGetShaderiv()

// Attach the individual shaders to the common shader program
g_hShaderProgram = glCreateProgram();
glAttachShader( g_hShaderProgram, hVertexShader );
glAttachShader( g_hShaderProgram, hFragmentShader );

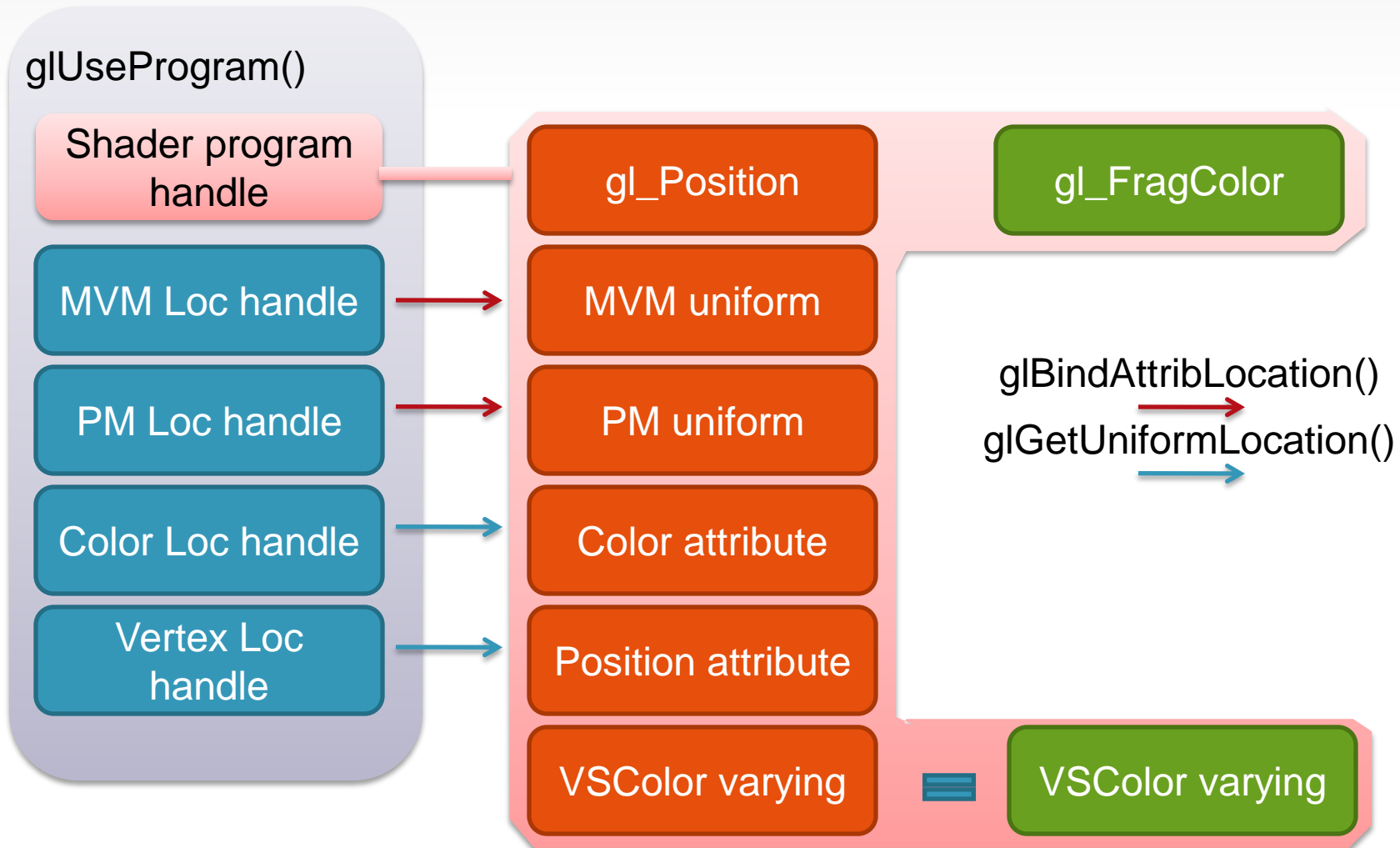
// Init attributes BEFORE linking
glBindAttribLocation( g_hShaderProgram, g_hVertexLoc, "g_vPosition" );
glBindAttribLocation( g_hShaderProgram, g_hColorLoc, "g_vColor" );

// Link the vertex shader and fragment shader together
glLinkProgram( g_hShaderProgram );
// Check for link success with glGetProgramiv()

// Get uniform locations
g_hModelViewMatrixLoc = glGetUniformLocation( g_hShaderProgram, "g_matModelView" );
g_hProjMatrixLoc      = glGetUniformLocation( g_hShaderProgram, "g_matProj" );

glDeleteShader( hVertexShader ); glDeleteShader( hFragmentShader );
```

How it looks in a sample application

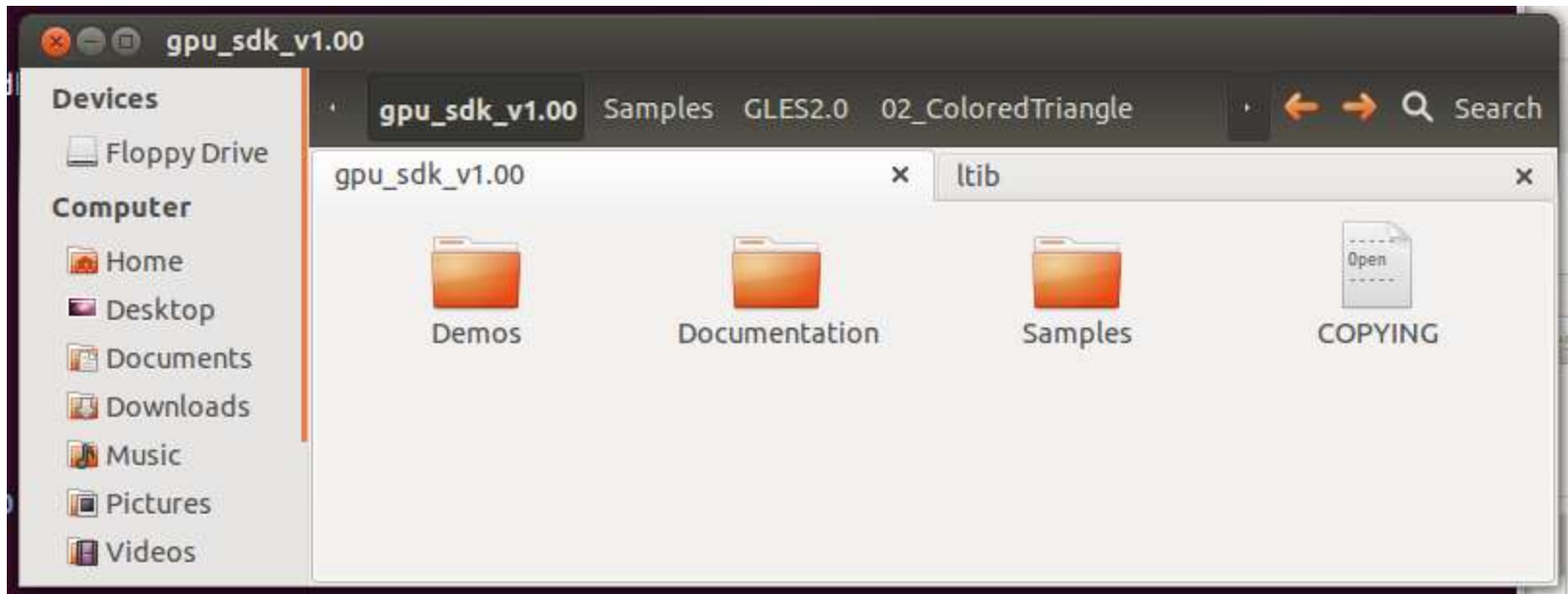


}

GPU_SDK

- i.MX6 Graphics SDK – Includes sample, demo code, and documentation for working with the i.MX6X family graphics cores. Includes OpenVG, OpenGL ES, and GAL2D reference files.
- Found in i.MX6 Software & Tools tab, under “Software Development Tools” -> “Snippets, Boot Code, Headers, Monitors, etc.”
- Released and maintained by the Virtual Graphics Core Team

GPU_SDK – contents



tree

```

Demos
├── GLES2.0
│   └── simple_gpu_player
│       ├── bin
│       └── src
├── MultiAPI
│   ├── clusterDemo
│   ├── GIFT
│   │   └── resources
│   │       ├── required_libs
│   │       │   ├── assimp-2.0.863
│   │       │   └── Devil
│   │       └── shaders
│   └── virtual_fb_driver
├── Documentation
│   ├── Tools
│   └── Tutorials
└── Samples
    ├── GLES1.1
    │   ├── 01_SimpleTriangle
    │   ├── 02_VertexColors
    │   ├── 03_VertexTransformation
    │   ├── 04_ColoredVerticesInterpolation
    │   ├── 05_GeometricObjects
    │   ├── 06_Projection
    │   ├── 07_BasicTexturing
    │   ├── 08_Multitexturing
    │   │   └── data
    │   ├── 09_Alphablending
    │   ├── 10_FilteringLights
    │   ├── 11_LightingFog
    │   │   └── data
    │   ├── 12_Stencil
    │   ├── 13_3DFonts
    │   │   └── data
    │   ├── 14_ParticlesSpritesAnimation
    │   └── 15_ParticleAccelerator
    └──

```

```

├── 16_VertexBufferObjects
│   └── data
├── 17_Beizer
├── common
│   ├── inc
│   │   ├── FSL
│   │   └── GLU3
│   └── src
├── GLES2.0
│   ├── 01_SimpleTriangle
│   ├── 02_ColoredTriangle
│   ├── 03_Transform
│   ├── 04_Projection
│   ├── 05_PrecompiledShader
│   │   └── shaders
│   ├── 06_Texturing
│   ├── 07_EnvironmentMapping
│   ├── 08_EnvironmentMappingRefraction
│   ├── 09_VIV_direct_texture
│   ├── common
│   │   ├── inc
│   │   │   └── FSL
│   │   └── src
│   └──
├── OpenVG
│   ├── common
│   │   ├── inc
│   │   │   └── FSL
│   │   └── src
│   ├── CoverFlow
│   ├── Example1
│   ├── Example2
│   └── Example3
└──

```

70 directories

paco@ubuntu:~/vgct/external/gpu_sdk_v1.00\$

GPU_SDK – requirements

- A working LTIB with required libraries
- Export the following variables from console:
 - export **CROSS_COMPILE**=/opt/freescale/usr/local/gcc-4.6.2-glibc-2.13-linaro-multilib-2011.12/fsl-linaro-toolchain/bin/arm-fsl-linux-gnueabi-
 - export **ROOTFS**=/home/paco/ltib/121218/ltib/rootfs
- Cd to directory of sample/demo/tutorial, type
 - \$make -f Makefile.fb
- Copy generated binary and required resources (images, shader files, etc) to target then run.



Resources

- <https://community.freescale.com/community/imx>
- imxgpu mailing list (imxgpu@freescale.com)



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i-Pu.76, i-Pu.77, i-Pu.78, i-Pu.79, i-Pu.80, i-Pu.81, i-Pu.82, i-Pu.83, i-Pu.84, i-Pu.85, i-Pu.86, i-Pu.87, i-Pu.88, i-Pu.89, i-Pu.90, i-Pu.91, i-Pu.92, i-Pu.93, i-Pu.94, i-Pu.95, i-Pu.96, i-Pu.97, i-Pu.98, i-Pu.99, i-Pu.100, i-Pu.101, i-Pu.102, i-Pu.103, i-Pu.104, i-Pu.105, i-Pu.106, i-Pu.107, i-Pu.108, i-Pu.109, i-Pu.110, i-Pu.111, i-Pu.112, i-Pu.113, i-Pu.114, i-Pu.115, i-Pu.116, i-Pu.117, i-Pu.118, i-Pu.119, i-Pu.120, i-Pu.121, i-Pu.122, i-Pu.123, i-Pu.124, i-Pu.125, i-Pu.126, i-Pu.127, i-Pu.128, i-Pu.129, i-Pu.130, i-Pu.131, i-Pu.132, i-Pu.133, i-Pu.134, i-Pu.135, i-Pu.136, i-Pu.137, i-Pu.138, i-Pu.139, i-Pu.140, i-Pu.141, i-Pu.142, i-Pu.143, i-Pu.144, i-Pu.145, i-Pu.146, i-Pu.147, i-Pu.148, i-Pu.149, i-Pu.150, i-Pu.151, i-Pu.152, i-Pu.153, i-Pu.154, i-Pu.155, i-Pu.156, i-Pu.157, i-Pu.158, i-Pu.159, i-Pu.160, i-Pu.161, i-Pu.162, i-Pu.163, i-Pu.164, i-Pu.165, i-Pu.166, i-Pu.167, i-Pu.168, i-Pu.169, i-Pu.170, i-Pu.171, i-Pu.172, i-Pu.173, i-Pu.174, i-Pu.175, i-Pu.176, i-Pu.177, i-Pu.178, i-Pu.179, i-Pu.180, i-Pu.181, i-Pu.182, i-Pu.183, i-Pu.184, i-Pu.185, i-Pu.186, i-Pu.187, i-Pu.188, i-Pu.189, i-Pu.190, i-Pu.191, i-Pu.192, i-Pu.193, i-Pu.194, i-Pu.195, i-Pu.196, i-Pu.197, i-Pu.198, i-Pu.199, i-Pu.200, i-Pu.201, i-Pu.202, i-Pu.203, i-Pu.204, i-Pu.205, i-Pu.206, i-Pu.207, i-Pu.208, i-Pu.209, i-Pu.210, i-Pu.211, i-Pu.212, i-Pu.213, i-Pu.214, i-Pu.215, i-Pu.216, i-Pu.217, i-Pu.218, i-Pu.219, i-Pu.220, i-Pu.221, i-Pu.222, i-Pu.223, i-Pu.224, i-Pu.225, i-Pu.226, i-Pu.227, i-Pu.228, i-Pu.229, i-Pu.230, i-Pu.231, i-Pu.232, i-Pu.233, i-Pu.234, i-Pu.235, i-Pu.236, i-Pu.237, i-Pu.238, i-Pu.239, i-Pu.240, i-Pu.241, i-Pu.242, i-Pu.243, i-Pu.244, i-Pu.245, i-Pu.246, i-Pu.247, i-Pu.248, i-Pu.249, i-Pu.250, i-Pu.251, i-Pu.252, i-Pu.253, i-Pu.254, i-Pu.255, i-Pu.256, i-Pu.257, i-Pu.258, i-Pu.259, i-Pu.260, i-Pu.261, i-Pu.262, i-Pu.263, i-Pu.264, i-Pu.265, i-Pu.266, i-Pu.267, i-Pu.268, i-Pu.269, i-Pu.270, i-Pu.271, i-Pu.272, i-Pu.273, i-Pu.274, i-Pu.275, i-Pu.276, i-Pu.277, i-Pu.278, i-Pu.279, i-Pu.280, i-Pu.281, i-Pu.282, i-Pu.283, i-Pu.284, i-Pu.285, i-Pu.286, i-Pu.287, i-Pu.288, i-Pu.289, i-Pu.290, i-Pu.291, i-Pu.292, i-Pu.293, i-Pu.294, i-Pu.295, i-Pu.296, i-Pu.297, i-Pu.298, i-Pu.299, i-Pu.300, i-Pu.301, i-Pu.302, i-Pu.303, i-Pu.304, i-Pu.305, i-Pu.306, i-Pu.307, i-Pu.308, i-Pu.309, i-Pu.310, i-Pu.311, i-Pu.312, i-Pu.313, i-Pu.314, i-Pu.315, i-Pu.316, i-Pu.317, i-Pu.318, i-Pu.319, i-Pu.320, i-Pu.321, i-Pu.322, i-Pu.323, i-Pu.324, i-Pu.325, i-Pu.326, i-Pu.327, i-Pu.328, i-Pu.329, i-Pu.330, i-Pu.331, i-Pu.332, i-Pu.333, i-Pu.334, i-Pu.335, i-Pu.336, i-Pu.337, i-Pu.338, i-Pu.339, i-Pu.340, i-Pu.341, i-Pu.342, i-Pu.343, i-Pu.344, i-Pu.345, i-Pu.346, i-Pu.347, i-Pu.348, i-Pu.349, i-Pu.350, i-Pu.351, i-Pu.352, i-Pu.353, i-Pu.354, i-Pu.355, i-Pu.356, i-Pu.357, i-Pu.358, i-Pu.359, i-Pu.360, i-Pu.361, i-Pu.362, i-Pu.363, i-Pu.364, i-Pu.365, i-Pu.366, i-Pu.367, i-Pu.368, i-Pu.369, i-Pu.370, i-Pu.371, i-Pu.372, i-Pu.373, i-Pu.374, i-Pu.375, i-Pu.376, i-Pu.377, i-Pu.378, i-Pu.379, i-Pu.380, i-Pu.381, i-Pu.382, i-Pu.383, i-Pu.384, i-Pu.385, i-Pu.386, i-Pu.387, i-Pu.388, i-Pu.389, i-Pu.390, i-Pu.391, i-Pu.392, i-Pu.393, i-Pu.394, i-Pu.395, i-Pu.396, i-Pu.397, i-Pu.398, i-Pu.399, i-Pu.400, i-Pu.401, i-Pu.402, i-Pu.403, i-Pu.404, i-Pu.405, i-Pu.406, i-Pu.407, i-Pu.408, i-Pu.409, i-Pu.410, i-Pu.411, i-Pu.412, i-Pu.413, i-Pu.414, i-Pu.415, i-Pu.416, i-Pu.417, i-Pu.418, i-Pu.419, i-Pu.420, i-Pu.421, i-Pu.422, i-Pu.423, i-Pu.424, i-Pu.425, i-Pu.426, i-Pu.427, i-Pu.428, i-Pu.429, i-Pu.430, i-Pu.431, i-Pu.432, i-Pu.433, i-Pu.434, i-Pu.435, i-Pu.436, i-Pu.437, i-Pu.438, i-Pu.439, i-Pu.440, i-Pu.441, i-Pu.442, i-Pu.443, i-Pu.444, i-Pu.445, i-Pu.446, i-Pu.447, i-Pu.448, i-Pu.449, i-Pu.450, i-Pu.451, i-Pu.452, i-Pu.453, i-Pu.454, i-Pu.455, i-Pu.456, i-Pu.457, i-Pu.458, i-Pu.459, i-Pu.460, i-Pu.461, i-Pu.462, i-Pu.463, i-Pu.464, i-Pu.465, i-Pu.466, i-Pu.467, i-Pu.468, i-Pu.469, i-Pu.470, i-Pu.471, i-Pu.472, i-Pu.473, i-Pu.474, i-Pu.475, i-Pu.476, i-Pu.477, i-Pu.478, i-Pu.479, i-Pu.480, i-Pu.481, i-Pu.482, i-Pu.483, i-Pu.484, i-Pu.485, i-Pu.486, i-Pu.487, i-Pu.488, i-Pu.489, i-Pu.490, i-Pu.491, i-Pu.492, i-Pu.493, i-Pu.494, i-Pu.495, i-Pu.496, i-Pu.497, i-Pu.498, i-Pu.499, i-Pu.500, i-Pu.501, i-Pu.502, i-Pu.503, i-Pu.504, i-Pu.505, i-Pu.506, i-Pu.507, i-Pu.508, i-Pu.509, i-Pu.510, i-Pu.511, i-Pu.512, i-Pu.513, i-Pu.514, i-Pu.515, i

Tegra3

	Device	i. MX6Q Sbera SD VerC	i. MX6Q Sbera SD VerC	Media Tek	Nexus 7	ASUS TF201
	OS Version	R13.4.1-RC4	R13.4.1-RC4	Android ??	Android 4.1.1	Android 4.0.3
	CPU	i.MX6Quad 1G	i.MX6Quad 1.2G	1.2Ghz Quad A7	Tegra 3 1.3Ghz	Tegra 3 1.2Gh
	GPU	Vivante GC2000	Vivante GC2000	Unknown	Tegra 3 GPU	Tegra 3 GPU
	Memory	1G DDR3	1G DDR3	Unknown	1GB LPDDR2	1GB LPDDR2
	Screen Resolution	1024*728	1024*728	Unknown	1280*800	1280*800
Quadrant 2.0	Total	4028	4828	3384	3741	3978
	CPU	9554	11407		11069	10414
	Memory	2796	3327		2943	2983
	I/O	4354	5693		1259	3714
	2D	990	1000		990	314
	3D	2445	2445		2444	2464
AnTutu Benchmark 2.9.4	Total Scores	9605	11159	10429	10520	10011
	Memory	1950	2348	2131	2309	1996
	CPU Integer	3057	3665	3614	3391	3273
	CPU Float	2312	2782	2270	2746	2596
	2D Graphics	294	294	298	324	295
	3D Graphics	1248	1249	1255	1307	1189
	Database IO	400	480	520	85	325
	SD Card Write	150	137	150	148	140
	SD Card Read	194	204	191	210	197
AnTutu Benchmark 3.0	Total Scores		12603	NT	NT	NT
	Memory		2326	NT		
	CPU Integer		3747	NT		
	CPU Float		2784	NT		
	2D Graphics		768	NT		
	3D Graphics		2191	NT		
	Database IO		460	NT		
	SD Card Write		123	NT		
	SD Card Read		204	NT		
BaseMark ES20 (Taiji)	FPS	36		NT	17.93	14.65
NenaMark2	FPS	49.2		NT	46.5	46.4
M3D	Dynamic Lighting Benchmark	23.57921		NT	Can't run	16.73816
	Particle Systems Benchmark	26.32444		NT	Can't run	18.76312
	Triangles Based Benchmark	40.87296		NT	Can't run	23.14258
	Physics Based Benchmark	53.72527		NT	Can't run	35.68423
Kernel Bootup Time	seconds	20		NT	36	NT

i.MX6Q/6D wins with Quadrant total score

**i.MX 6Q/6D
wins with
Antutu
(normalized to
screen size)**

**i.MX 6Q wins with
BaseMark ES2.0.
On par with
NenaMark due to
LCD size**

i.MX6Q wins with M3D

51

Phonix v2.9.3, v2.9.4 and v3.0 Comparison (6Quad) vs MediaTek



	Device	i. MX6Q Sbera SD VerC
	OS Version	R13.4.1-RC4
	CPU	i.MX6Quad 1.2G
	GPU	Vivante GC2000
	Memory	1G DDR3
AnTutu Benchmark 2.9.4	Total Scores	11159
	Memory	2348
	CPU Integer	3665
	CPU Float	2782
	2D Graphics	294
	3D Graphics	1249
	Database IO	480
	SD Card Write	137
	SD Card Read	204
AnTutu Benchmark 3.0	Total Scores	12603
	Memory	2326
	CPU Integer	3747
	CPU Float	2784
	2D Graphics	768
	3D Graphics	2191
	Database IO	460
	SD Card Write	123
	SD Card Read	204



Normalizing Antutu results

- Antutu benchmark measures full range of capabilities
 - CPU integer/float, memory b/w, database i/o and storage card
 - 2D and 3D performance metrics are only part of the story
 - Goal is to ensure a more complete view of the performance of the CPU
- Freescale i.MX 6Quad/6Dual Antutu tested on 1024x768 display
 - Mediatek benchmarks are assumed to be 720p (1280x720)
 - Delta is around 14.6% greater pixels in Mediatek screen vs Freescale
 - This will affect the 2D and 3D portions of the Antutu benchmark
- To quickly normalize Freescale's results to a 720p screen:
 - 1) all scores for i.MX 6Q/6D stay the same except for the 2D/3D scores.
 - 2) the i.MX 6Q/6D scores for 2D/3D are reduced by 14.6% to account for a 720p screen
 - 2D was 294. New score is 250.9
 - 3D was 1249. New score is 1065.81
- Therefore the Freescale i.MX 6Q/6D normalized score would be: 10887.71
 - existing score: 11,159; subtract original 2D/3D: $11,159 - 294 - 1294 = 9571$
 - add in normalized 2D/3D #'s: $9571 + 250.9 + 1065.81 = 10887.71$

Benchmark Comparison – OMAP4470 and 4460

Benchmark Suite	Item	i.MX6Quad 1Ghz 1024x768	i.MX6Dual 1Ghz 1024x768	Archos 101x Tablet TI OMAP 4470 1.5Ghz 1280x800 lcd	Archos 101 G9 Tablet OMAP 4460 1.2Ghz 1280x800 lcd
Quadrant	Total	4636	3300	3557	2622
Antutu	Total	10488	6911	5874	7835
Linpack	Multithread	136.71	87	94.9	82.6
Velamo		1086	752	1374	1325

Relevant Benchmarks

i.MX 6 vastly outperforms Tegra3 and 2

UI Category		Benchmark	Unit	i.MX 6	Competition 1	Comments
1) UI's require 'quality' pixels to enhance view 2) 3D performance for User interface manipulation, games, video texturing						
	3DMarkMobile ES 1.1: Proxycon	FPS	108 FPS	60 (iPad 2)	Older gen benchmark for more basic games	
	3DMarkMobile ES 1.1: Samurai	FPS	103 FPS	60 (iPad 2)	Older gen benchmark for more basic games	
	3DMarkMobile ES 2.0: Taiji Girl	FPS	35 FPS	24 (iPad 2) 5.1 (Tegra2) 15.8 (Tegra3)	Updated for latest 3D – tests whether complex games can be run at high FPS	
	3DMarkMobile ES 2.0: Hover Jet	FPS	25 FPS	14 (iPad 2)	Updated for latest 3D – tests whether complex games can be run at high FPS	
3) UI content is inherently dynamic → Cannot predict content so need High speed, wide memory bus						
LMBENCH	Mem Read (MB/s) (higher is better)	MB/s	264	163 (OMAP 4)	Ability to read content fast during decode or 3D operations	
LMBENCH	Mem Write (MB/s) (higher is better)	MB/s	2280	2221 (OMAP 4)	Ability to write data during decode or 3D operations	
LMBENCH	BCopy (libc) (MB/s) (higher is better)	MB/s	590	312.8 (OMAP 4)	Ability to replicate frame buffers or other content especially during UI	
LMBENCH	L1 Latency (ns) (lower is better)	ns	3.93	3.974 (OMAP 4)	How fast memory roundtrip is to CPU	
LMBENCH	L2 Latency (ns) (lower is better)	ns	27.2	29.6 (OMAP 4)	How fast memory roundtrip is to CPU	
LMBENCH	Main Mem Latency (ns) (lower is better)	ns	113.8	194.1 (OMAP 4)	How fast memory roundtrip is to CPU	
LMBENCH	Rand Mem Latency (ns) (lower is better)	ns	172	265 (OMAP 4)	How fast memory roundtrip is to CPU	
Linux MemCopy		MB/s	8K copies: 6975 64K copies: 4542 1M copies: 2674 16M copies: 1097	TI OMAP4 1Ghz on PANDA board 8K copies: 6510 64K copies: 3906 1M copies: 737 16M copies: 595	Determines efficiency of the memory controller for different size 'chunks' of memory. Video files tend to be large chunks. User data tends to be smaller chunks. Need to be good at all ranges	

Relevant Benchmarks – User Interface – Cont

UI Category	Benchmark	Unit	i.MX 6	Competition 1	Comments
4) UI requires high resolution support 1080p TV or LCD is now the norm					
	Run two 1080p h.264 high profile video clips on two different 1080p HDMI TVs	FPS	30 fps per screen decode	Not possible on Tegra 2. unlikely to be capable on Tegra 3 due to single memory channel	Measures ability of chip to drive higher resolution displays than just 1080p. Plus ability to do picture in picture video playback simultaneously
	CPU Core Utilization during Playback of video content	% of max	4 %	Not measured	Determines if CPU provider is using main core to offload some or most of the decode. Also measures how much CPU is left over for other tasks
5) Access to fast CPU MIPS → used for complicated transforms to augment visual experience					
	Dhrystone	DMIPS	2.45 DMIPS/Mhz	TI PANDA Board 2.2 DMIPS/MHz	Very basic benchmark that is useful to determine if core has been implemented correctly to ARM spec. Lower score means possible implementation problems or slow transistors.
	CoreMark	Score	i.MX 6Quad processor at 1Ghz, 4 cores: 11147 coremark;	Nvidia Tegra 3 with 5 cores: 11352	Better score for CPU than Dhrystone. Much more complete set of tests to measure CPU performance
	SunSpider (0.9.1) Java	Sec	run on i.MX 6Quad core 1: 1830; core 2: 1827; core 3: 1836; core 4: 1849	OMAP4 @1Ghz on Panada Board: 1756 with two cores active	Browser test with broad support. Also a good measure for the CPU's ability to handle JAVA
	Threading test	Tbd	Tbd	Tbd	Measure ability to hit multiple threads and allocate effectively across multiple CPUs

Tegra 3 limited by single memory channel

