ECE 3544: Digital Design I Project 3 (Part B): Design and Synthesis of a Synchronous Finite State Machine

Student Name:	Jacob Abel
Honor Code Pledge:	I have neither given nor received unauthorized assistance on this assignment.
Grading: The desig	n project will be graded on a 100 point basis, as shown below:
Manner of Presentat	ion (30 points)
Comp	leted cover sheet included with report (5 points)
	ization: Clear, concise presentation of content; Use of appropriate, well-organized ns (15 points)
Mecha	anics: Spelling and grammar (10 points)
Technical Merit (70	points)
	al discussion: Did you describe the objectives in your own words? Did you discuss your conclusions and the lessons you learned from the assignment? (5 points)
	machines: Does your state diagram for the keypressed module correctly represent its ior? Did you address the questions on structuring state machines in Verilog? (10 points)
-	mentation discussion: <i>Did you discuss the approach you took to modifying the counter</i> (5 points)
	ng discussion: What was your approach to formulating your test benches? How did you the correctness of the modules you designed? (5 points)
Suppo	orting figures: Waveforms showing correct operation of the top-level module. (10 points)
	orting files: Do the modules pass any tests applied by the grading staff? Modules that do nform to the requirements of the project specification will receive no credit. (10 points)
Valida	ntion of the final design on the DE1-SOC board (25 points)
Proje	ct Grade