Project 3

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Purpose

Problem Specification

Disassembly and Initial Analysis

Memory Address	Machine Code	Instruction (Values in decimal)
0	1400	XOR R0, R0, R0
1	2100	LD R0, R0, M[A]
2	8443	ADI R0, 3
3	0480	ADD R0, R0, R2
4	00E0	MOV R4, R3
5	0290	INC R2, R2
6	0ADA	SUB R3, R2, R3
7	0C48	DEC R1, R1
8	C00A	BRZ R1, R0
9	C1C4	BRZ R0, R7
A	02D8	INC R3, R3
B	0C48	DEC R1, R1
C	1A41	SR R1, R1
D	C20A	BRN R1, R0
E	E000	JMP R0

Table 1: Step 1: Initial Instruction Disassembly

Design Process

Implementation

Validation

Conclusion

Appendix A: Simulation Waveforms

Design Project 2: Implementing the Simple Con

Validation Sheet - Page 1, Instructions

The validation sheet is provided as an ex **lementation.**
You do not have to go the CEL