

Project 3

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Purpose

Problem Specification

Disassembly and Initial Analysis

Memory Address	Machine Code	Instruction (Values in decimal)
0	1400	XOR R0, R0, R0
1	2100	LD R4, R0
2	8443	ADI R1, R0, 3
3	0480	ADD R2, R0, R0
4	00E0	MOV R3, R4
5	0290	INC R2, R2
6	0ADA	SUB R3, R3, R2
7	0C48	DEC R1, R1
8	C00A	BRZ R0, R1
9	C1C4	BRZ R7, R0
A	02D8	INC R3, R3
B	0C48	DEC R1, R1
C	1A41	SHR R1, R1
D	C20A	BRN R0, R1
E	E000	JMP R0

Table 1: Step 1: Initial Instruction Disassembly

Design Process

Implementation

Validation

Conclusion

Appendix A: Simulation Waveforms

Design Project: Implementing the Simple Component Validation Sheet (Page 1), Instructions

The Validation Sheet is provided as an example of an implementation.
You do not have to go the CEL way of doing it.

