

GTA Validation Instructions:

Program the FPGA on the DE1-SoC Nano board with the student's implementation of the comparator system. When the programming has successfully completed, perform the set of tests described in the table below. For each case, indicate whether or not the student's design demonstrates the behavior described.

Procedure and <i>Expected Result</i>	Correct Operation (Yes or No)
Set SW[7:4] = 0000, set SW[3:0] = 0000. <i>HEX4 should display "0". HEX3 should display "0". HEX[2:0] should display "001".</i>	
Set SW[7:4] = 1011, set SW[3:0] = 0101. <i>HEX4 should display "b". HEX3 should display "5". HEX[2:0] should display "100".</i>	
Set SW[7:4] = 0011, set SW[3:0] = 1000. <i>HEX4 should display "3". HEX3 should display "8". HEX[2:0] should display "010".</i>	
Set SW[7:4] = 1111, set SW[3:0] = 1111. <i>HEX4 should display "f". HEX3 should display "f". HEX[2:0] should display "001".</i>	
Set SW[7:4] = 1100, set SW[3:0] = 0110. <i>HEX4 should display "c". HEX3 should display "6". HEX[2:0] should display "100".</i>	
Set SW[7:4] = 1110, set SW[3:0] = 1111. <i>HEX4 should display "e". HEX3 should display "f". HEX[2:0] should display "010".</i>	

GTA Printed Name and Signature: _____

Date and Time of Validation: _____