

MIC_ARRAY

PCB Revision A
Schematic Revision 1.1

Design Details

Board:
Autonomous 2015A10V01

Chip:
STM32F746IGH

Board Form Factor:
USB / MII Stand-Alone

Assembly:
176 Lead QFN w/ Exposed GND Pad

Revision History

Rev 1.0:

xxxx

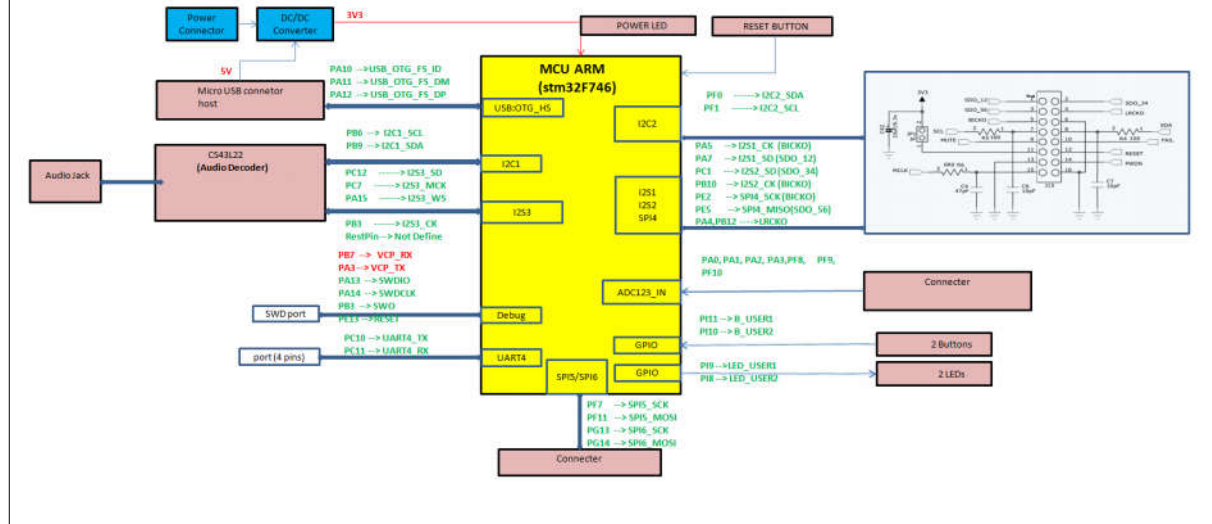
Rev 1.1:

xxx

Rev 1.2:

xxx

BLOCK DIAGRAM



Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications. Consequently, Complete Information Sufficient For Construction Purposes is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

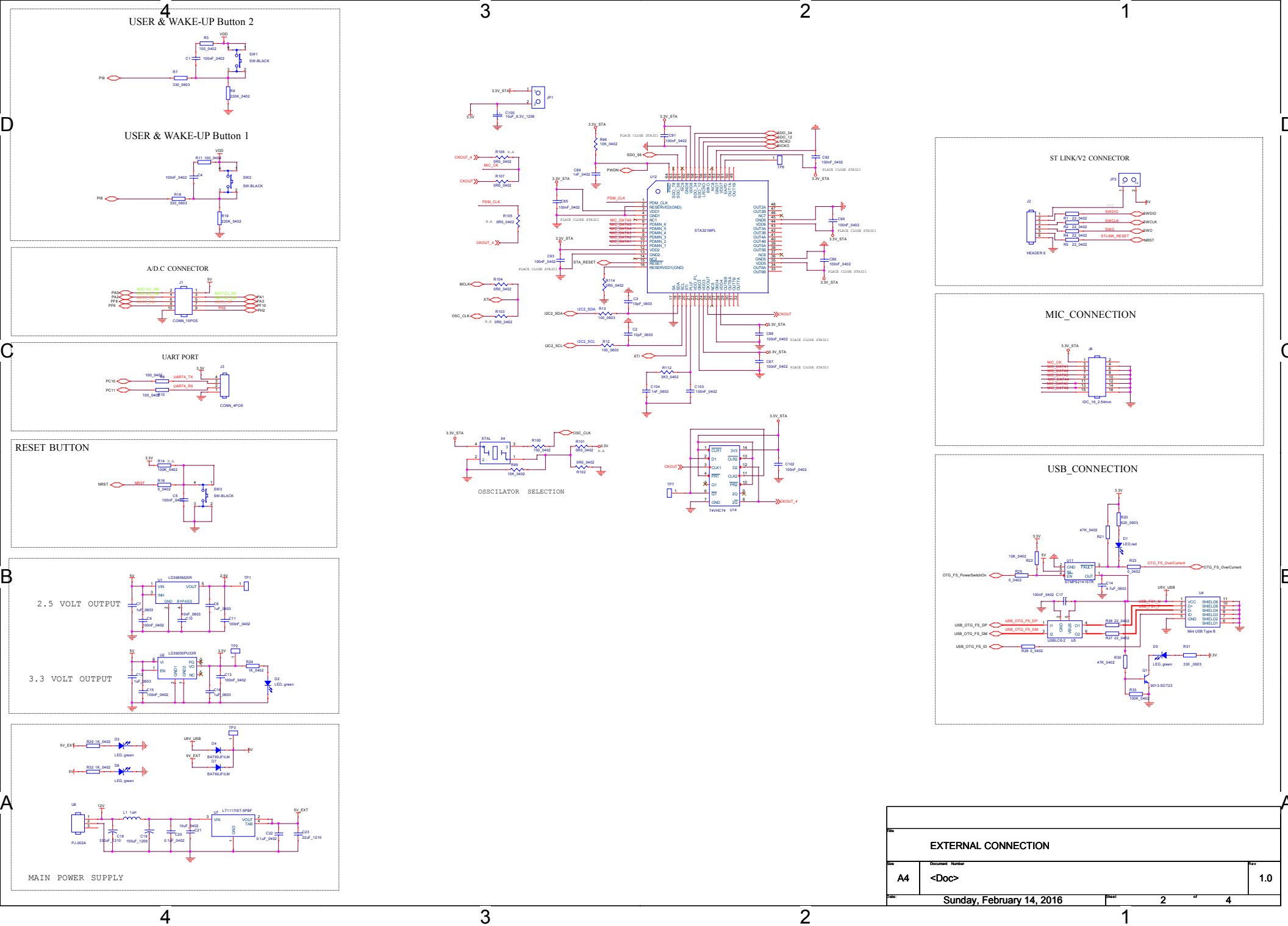
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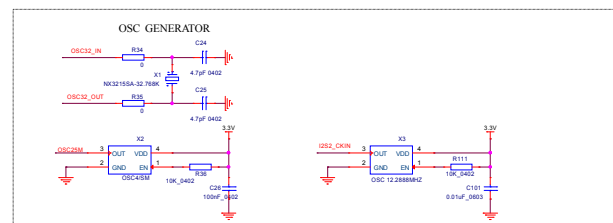
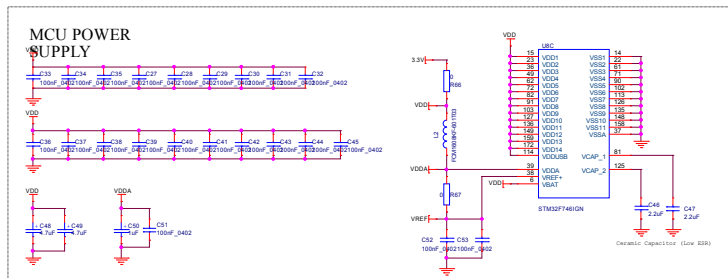
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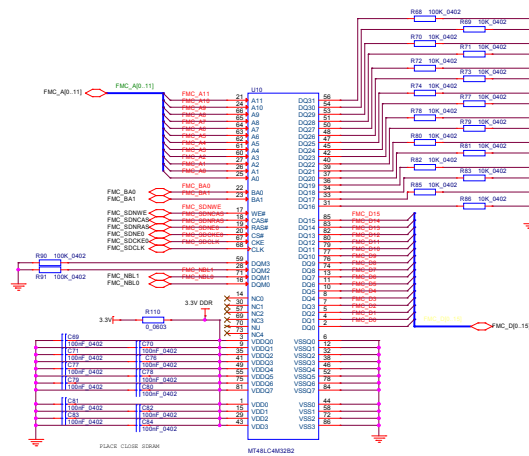
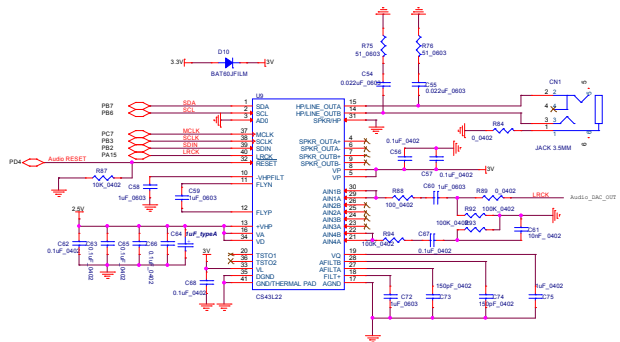
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MCU CONNECTOR			
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