UNTESTED

U1 TRADFRI

J2 / Left

1		1	PA0	
2		2	PA1	
3		3	PB12	
4		4	PB13	
5		5	GND	
6		6	VCC	

+3V3

GND

U1 pin	signal	J4
16	RESETn	1
15	PF3	2
14	PF2 — SWO	3
13	PF1 — SWDIO	4
12	PF0 — SWCLK	5
11	PC11	6
10	PC10	7
9	PB14	8
8	PB15	9
7	GND	10

J4 / Right

GND

J1 Power
2
1
+3V3
GND

D1 LED
R1 R
+3V3
GND

C1 CP
+3V3
GND

C2 C
+3V3
GND

RESETn
RESET
SW1
+3V3
GND

J3 ARM-JTAG
1		2	SWDIO
3		4	SWCLK
5		6	SWO
7		8	
9		10	RESETn
+3V3
GND

J5 JTAG
1		2	SWDIO
3		4	SWCLK
5		6	SWO
RESETn
+3V3
GND