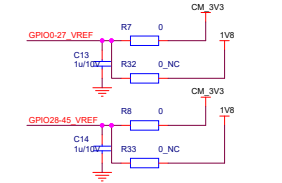
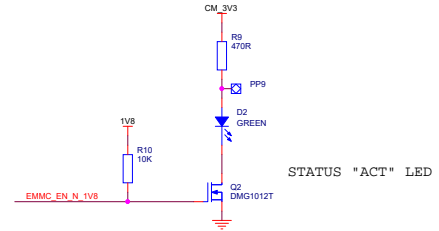
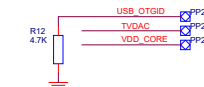
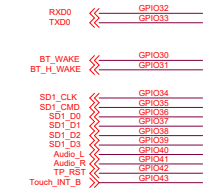
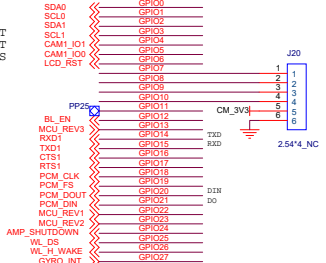


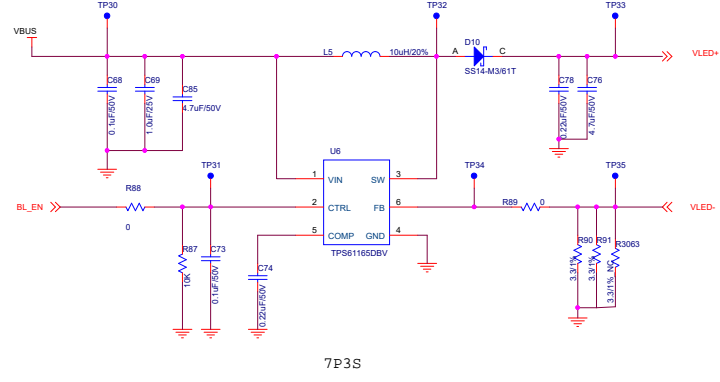
GPIO BANK 0/1 VOLTAGE SELECT:



GPIO 28, 29, 44, 45 DO NOT HAVE PULLS ENABLED AT BOOT SO PROVIDE 100K PULL DOWNS TO AVOID THEM FLOATING



LED Brightness



Route ringed signals as matched length 100R differential pairs

Route bold-ringed signals as matched length 90R differential pair

VDD_CORE used for module test only (do not use in normal operation, do not draw current from this pin!)