

MIOpen Porting Guide

VERSION 1.0

JULY, 2017

Key differences between MIOpen v1.0 and cuDNN:

- MIOpen only supports 4-D tensors in the NCHW storage format. This means all the “***Nd***” APIs in cuDNN do not have a corresponding API in MIOpen.
- MIOpen only supports **float (fp32)** data-type.
- MIOpen only supports **2D Convolutions** and **2D Pooling**.
- Calling `miopenFindConvolution*Algorithm()` is *mandatory* before calling any Convolution API.
- Typical calling sequence for Convolution APIs for MIOpen is:
 - `miopenConvolution*GetWorkSpaceSize()` // returns the workspace size required by Find()
 - `miopenFindConvolution*Algorithm()` // returns performance info about various algorithms
 - `miopenConvolution*()`
- MIOpen does not support **Preferences** for convolutions.
- MIOpen does not support Softmax modes. MIOpen implements the **SOFTMAX_MODE_CHANNEL** flavor.
- MIOpen does not support **Transform-Tensor**, **Dropout**, **RNNs**, and **Divisive Normalization**.

Helpful MIOpen Environment Variables

`MIOPEN_ENABLE_LOGGING=1` – log all the MIOpen APIs called including the parameters passed to those APIs.

`MIOPEN_DEBUG_AMD_ROCM_PRECOMPILED_BINARIES=0` – disable Winograd convolution algorithm.

`MIOPEN_DEBUG_GCN_ASM_KERNELS=0` – disable hand-tuned asm. kernels for Direct convolution algorithm. Fall-back to kernels written in high-level language.

`MIOPEN_DEBUG_CONV_FFT=0` – disable FFT convolution algorithm.

`MIOPEN_DEBUG_CONV_DIRECT=0` – disable Direct convolution algorithm.

Operation	cuDNN API	MIOpen API
Handle	cudnnStatus_t cudnnCreate (cudnnHandle_t *handle)	miopenStatus_t miopenCreate (miopenHandle_t *handle)
	cudnnStatus_t cudnnDestroy (cudnnHandle_t handle)	miopenStatus_t miopenDestroy (miopenHandle_t handle)
	cudnnStatus_t cudnnSetStream (cudnnHandle_t handle, cudaStream_t streamId)	miopenStatus_t miopenSetStream (miopenHandle_t handle, miopenAcceleratorQueue_t streamID)
	cudnnStatus_t cudnnGetStream (cudnnHandle_t handle, cudaStream_t *streamId)	miopenStatus_t miopenGetStream (miopenHandle_t handle, miopenAcceleratorQueue_t *streamID)
Tensor	cudnnStatus_t cudnnCreateTensorDescriptor (cudnnTensorDescriptor_t *tensorDesc)	miopenStatus_t miopenCreateTensorDescriptor (miopenTensorDescriptor_t *tensorDesc)
	cudnnStatus_t cudnnSetTensor4dDescriptor (cudnnTensorDescriptor_t tensorDesc, cudnnTensorFormat_t format, cudnnDataType_t dataType, int n, int c, int h, int w)	// Only NCHW format is supported miopenStatus_t miopenSet4dTensorDescriptor (miopenTensorDescriptor_t tensorDesc, miopenDataType_t dataType, int n, int c, int h, int w)
	cudnnStatus_t cudnnGetTensor4dDescriptor (cudnnTensorDescriptor_t tensorDesc, cudnnDataType_t *dataType, int *n, int *c, int *h, int *w, int *nStride, int *cStride, int *hStride, int *wStride)	miopenStatus_t miopenGet4dTensorDescriptor (miopenTensorDescriptor_t tensorDesc, miopenDataType_t *dataType, int *n, int *c, int *h, int *w, int *nStride, int *cStride, int *hStride, int *wStride)
	cudnnStatus_t cudnnDestroyTensorDescriptor (cudnnTensorDescriptor_t tensorDesc)	miopenStatus_t miopenDestroyTensorDescriptor (miopenTensorDescriptor_t tensorDesc)
	cudnnStatus_t cudnnAddTensor (cudnnHandle_t handle, const void *alpha, const cudnnTensorDescriptor_t aDesc, const void *A, const void *beta, const cudnnTensorDescriptor_t cDesc, void *C)	// Set tensorOp to miopenOpTensorAdd miopenStatus_t miopenOpTensor (miopenHandle_t handle, miopenTensorOp_t tensorOp, const void *alpha1, const miopenTensorDescriptor_t aDesc, const void *A, const void *alpha2, const miopenTensorDescriptor_t bDesc, const void *B, const void *beta, const miopenTensorDescriptor_t cDesc, void *C) // For Forward Bias use, miopenConvolutionForwardBias

Operation	cuDNN API	MIOpen API
Tensor	<pre> cudnnStatus_t cudnnOpTensor(cudnnHandle_t handle, const cudnnOpTensorDescriptor_t opTensorDesc, const void *alpha1, const cudnnTensorDescriptor_t aDesc, const void *A, const void *alpha2, const cudnnTensorDescriptor_t bDesc, const void *B, const void *beta, const cudnnTensorDescriptor_t cDesc, void *C) </pre>	<pre> miopenStatus_t miopenOpTensor(miopenHandle_t handle, miopenTensorOp_t tensorOp, const void *alpha1, const miopenTensorDescriptor_t aDesc, const void *A, const void *alpha2, const miopenTensorDescriptor_t bDesc, const void *B, const void *beta, const miopenTensorDescriptor_t cDesc, void *C) </pre>
	<pre> cudnnStatus_t cudnnSetTensor(cudnnHandle_t handle, const cudnnTensorDescriptor_t yDesc, void *y, const void *valuePtr) </pre>	<pre> miopenStatus_t miopenSetTensor(miopenHandle_t handle, const miopenTensorDescriptor_t yDesc, void *y, const void *alpha) </pre>
	<pre> cudnnStatus_t cudnnScaleTensor(cudnnHandle_t handle, const cudnnTensorDescriptor_t yDesc, void *y, const void *alpha) </pre>	<pre> miopenStatus_t miopenScaleTensor(miopenHandle_t handle, const miopenTensorDescriptor_t yDesc, void *y, const void *alpha) </pre>
Filter	<pre> cudnnStatus_t cudnnCreateFilterDescriptor(cudnnFilterDescriptor_t *filterDesc) </pre>	<pre> // All *FilterDescriptor* APIs are substituted by the respective TensorDescriptor APIs </pre>
Convolution	<pre> cudnnStatus_t cudnnCreateConvolutionDescriptor(c udnnConvolutionDescriptor_t *convDesc) </pre>	<pre> miopenStatus_t miopenCreateConvolutionDescriptor (miopenConvolutionDescriptor_t *convDesc) </pre>

Operation	cuDNN API	MIOpen API
Convolution	<pre> cudaStatus_t cudaSetConvolution2dDescriptor(cudaConvolutionDescriptor_t convDesc, int pad_h, int pad_w, int u, int v, int upscalex, int upscaley cudaConvolutionMode_t mode) </pre>	<pre> miopenStatus_t miopenInitConvolutionDescriptor(miopenConvolutionDescriptor_t convDesc, miopenConvolutionMode_t mode, int pad_h, int pad_w, int u, int v, int upscalex, int upscaley) </pre>
	<pre> cudaStatus_t cudaGetConvolution2dDescriptor(const cudaConvolutionDescriptor_t convDesc, int *pad_h, int *pad_w, int *u, int *v, int *upscalex, int *upscaley, cudaConvolutionMode_t *mode) </pre>	<pre> miopenStatus_t miopenGetConvolutionDescriptor(miopenConvolutionDescriptor_t convDesc, miopenConvolutionMode_t *mode, int *pad_h, int *pad_w, int *u, int *v, int *upscalex, int *upscaley) </pre>
	<pre> cudaStatus_t cudaGetConvolution2dForwardOutput Dim(const cudaConvolutionDescriptor_t convDesc, const cudaTensorDescriptor_t inputTensorDesc, const cudaFilterDescriptor_t filterDesc, int *n, int *c, int *h, int *w) </pre>	<pre> miopenStatus_t miopenGetConvolutionForwardOutput Dim(miopenConvolutionDescriptor_t convDesc, const miopenTensorDescriptor_t inputTensorDesc, const miopenTensorDescriptor_t filterDesc, int *n, int *c, int *h, int *w) </pre>
	<pre> cudaStatus_t cudaDestroyConvolutionDescriptor(cudaConvolutionDescriptor_t convDesc) </pre>	<pre> miopenStatus_t miopenDestroyConvolutionDescriptor(miopenConvolutionDescriptor_t convDesc) </pre>
	<pre> cudaStatus_t cudaFindConvolutionForwardAlgorithm hm(cudaHandle_t handle, const cudaTensorDescriptor_t xDesc, const cudaFilterDescriptor_t wDesc, const cudaConvolutionDescriptor_t convDesc, const cudaTensorDescriptor_t yDesc, const int requestedAlgoCount, int *returnedAlgoCount, cudaConvolutionFwdAlgoPerf_t *perfResults) cudaStatus_t cudaFindConvolutionForwardAlgorithmEx hmEx(cudaHandle_t handle, const cudaTensorDescriptor_t xDesc, const void *x, const cudaFilterDescriptor_t wDesc, </pre>	<pre> // FindConvolution() is mandatory // Allocate workspace prior to running this API // A table with times and memory requirements // for different algorithms is returned // Users can chose the top-most algorithm if they // only care about the fastest algorithm miopenStatus_t miopenFindConvolutionForwardAlgorithm hm(miopenHandle_t handle, const miopenTensorDescriptor_t xDesc, const void *x, const miopenTensorDescriptor_t wDesc, const void *w, const miopenConvolutionDescriptor_t convDesc, const miopenTensorDescriptor_t yDesc, void *y, const int requestAlgoCount, int *returnedAlgoCount, miopenConvAlgoPerf_t *perfResults, </pre>

Operation	cuDNN API	MIOpen API
Convolution	<pre>const void *w, const cudnnConvolutionDescriptor_t convDesc, const cudnnTensorDescriptor_t yDesc, void *y, const int requestedAlgoCount, int *returnedAlgoCount, cudnnConvolutionFwdAlgoPerf_t *perfResults, void *workSpace, size_t workSpaceSizeInBytes) cudnnStatus_t cudnnGetConvolutionForwardAlgorit hm(cudnnHandle_t handle, const cudnnTensorDescriptor_t xDesc, const cudnnFilterDescriptor_t wDesc, const cudnnConvolutionDescriptor_t convDesc, const cudnnTensorDescriptor_t yDesc, cudnnConvolutionFwdPreference_t preference, size_t memoryLimitInBytes, cudnnConvolutionFwdAlgo_t *algo)</pre>	<pre>void *workSpace, size_t workSpaceSize, bool exhaustiveSearch)</pre>
	<pre>cudnnStatus_t cudnnGetConvolutionForwardWorksp aceSize(cudnnHandle_t handle, const cudnnTensorDescriptor_t xDesc, const cudnnFilterDescriptor_t wDesc, const cudnnConvolutionDescriptor_t convDesc, const cudnnTensorDescriptor_t yDesc, cudnnConvolutionFwdAlgo_t algo, size_t *sizeInBytes)</pre>	<pre>miopenStatus_t miopenConvolutionForwardGetWorkSpa ceSize(miopenHandle_t handle, const miopenTensorDescriptor_t wDesc, const miopenTensorDescriptor_t xDesc, const miopenConvolutionDescriptor_t convDesc, const miopenTensorDescriptor_t yDesc, size_t *workSpaceSize)</pre>
	<pre>cudnnStatus_t cudnnConvolutionForward(cudnnHan dle_t handle, const void *alpha, const cudnnTensorDescriptor_t xDesc, const void *x,</pre>	<pre>miopenStatus_t miopenConvolutionForward(miopenHan dle_t handle, const void *alpha, const miopenTensorDescriptor_t xDesc, const void *x,</pre>

Operation	cuDNN API	MIOpen API
Convolution	<pre>const cudnnFilterDescriptor_t wDesc, const void *w, const cudnnConvolutionDescriptor_t convDesc, cudnnConvolutionFwdAlgo_t algo, void *workSpace, size_t workSpaceSizeInBytes, const void *beta, const cudnnTensorDescriptor_t yDesc, void *y)</pre>	<pre>const miopenTensorDescriptor_t wDesc, const void *w, const miopenConvolutionDescriptor_t convDesc, miopenConvFwdAlgorithm_t algo, const void *beta, const miopenTensorDescriptor_t yDesc, void *y, void *workSpace, size_t workSpaceSize)</pre>
	<pre>cudnnStatus_t cudnnConvolutionBackwardBias (cudnnHandle_t handle, const void *alpha, const cudnnTensorDescriptor_t dyDesc, const void *dy, const void *beta, const cudnnTensorDescriptor_t dbDesc, void *db)</pre>	<pre>miopenStatus_t miopenConvolutionBackwardBias (miop enHandle_t handle, const void *alpha, const miopenTensorDescriptor_t dyDesc, const void *dy, const void *beta, const miopenTensorDescriptor_t dbDesc, void *db)</pre>
	<pre>cudnnStatus_t cudnnFindConvolutionBackwardFilt erAlgorithm(cudnnHandle_t handle, const cudnnTensorDescriptor_t xDesc, const cudnnTensorDescriptor_t dyDesc, const cudnnConvolutionDescriptor_t convDesc, const cudnnFilterDescriptor_t dwDesc, const int requestedAlgoCount, int *returnedAlgoCount, cudnnConvolutionBwdFilterAlgoPer f_t *perfResults) cudnnStatus_t cudnnFindConvolutionBackwardFilt erAlgorithmEx(cudnnHandle_t handle, const cudnnTensorDescriptor_t xDesc, const void *x, const cudnnTensorDescriptor_t dyDesc, const void *y, const</pre>	<pre>// FindConvolution() is mandatory // Allocate workspace prior to running this API // A table with times and memory requirements for different algorithms is returned // Users can chose the top-most algorithm if they only care about the fastest algorithm miopenStatus_t miopenFindConvolutionBackwardWeigh tsAlgorithm(miopenHandle_t handle, const miopenTensorDescriptor_t dyDesc, const void *dy, const miopenTensorDescriptor_t xDesc, const void *x, const miopenConvolutionDescriptor_t convDesc, const miopenTensorDescriptor_t dwDesc, void *dw, const int requestAlgoCount, int *returnedAlgoCount, miopenConvAlgoPerf_t *perfResults, void *workSpace, size_t workSpaceSize, bool exhaustiveSearch)</pre>

Operation	cuDNN API	MIOpen API
Convolution	<pre> cudnnConvolutionDescriptor_t convDesc, const cudnnFilterDescriptor_t dwDesc, void *dw, const int requestedAlgoCount, int *returnedAlgoCount, cudnnConvolutionBwdFilterAlgoPerf_t *perfResults, void *workSpace, size_t workSpaceSizeInBytes) cudnnStatus_t cudnnGetConvolutionBackwardFilterAlgorithm(cudnnHandle_t handle, const cudnnTensorDescriptor_t xDesc, const cudnnTensorDescriptor_t dyDesc, const cudnnConvolutionDescriptor_t convDesc, const cudnnFilterDescriptor_t dwDesc, cudnnConvolutionBwdFilterPreference_t preference, size_t memoryLimitInBytes, cudnnConvolutionBwdFilterAlgo_t *algo) </pre>	
	<pre> cudnnStatus_t cudnnGetConvolutionBackwardFilterWorkspaceSize(cudnnHandle_t handle, const cudnnTensorDescriptor_t xDesc, const cudnnTensorDescriptor_t dyDesc, const cudnnConvolutionDescriptor_t convDesc, const cudnnFilterDescriptor_t gradDesc, cudnnConvolutionBwdFilterAlgo_t algo, size_t *sizeInBytes) </pre>	<pre> miopenStatus_t miopenConvolutionBackwardWeightsGetWorkspaceSize(miopenHandle_t handle, const miopenTensorDescriptor_t dyDesc, const miopenTensorDescriptor_t xDesc, const miopenConvolutionDescriptor_t convDesc, const miopenTensorDescriptor_t dwDesc, size_t *workSpaceSize) </pre>

Operation	cuDNN API	MIOpen API
Convolution	<pre> cudnnStatus_t cudnnConvolutionBackwardFilter(cudnnHandle_t handle, const void *alpha, const cudnnTensorDescriptor_t xDesc, const void *x, const cudnnTensorDescriptor_t dyDesc, const void *dy, const cudnnConvolutionDescriptor_t convDesc, cudnnConvolutionBwdFilterAlgo_t algo, void *workSpace, size_t workSpaceSizeInBytes, const void *beta, const cudnnFilterDescriptor_t dwDesc, void *dw) </pre>	<pre> miopenStatus_t miopenConvolutionBackwardWeights(miopenHandle_t handle, const void *alpha, const miopenTensorDescriptor_t dyDesc, const void *dy, const miopenTensorDescriptor_t xDesc, const void *x, const miopenConvolutionDescriptor_t convDesc, miopenConvBwdWeightsAlgorithm_t algo, const void *beta, const miopenTensorDescriptor_t dwDesc, void *dw, void *workSpace, size_t workSpaceSize) </pre>
	<pre> cudnnStatus_t cudnnGetConvolutionBackwardDataWo rkSpaceSize(cudnnHandle_t handle, const cudnnFilterDescriptor_t wDesc, const cudnnTensorDescriptor_t dyDesc, const cudnnConvolutionDescriptor_t convDesc, const cudnnTensorDescriptor_t dxDesc, cudnnConvolutionBwdDataAlgo_t algo, size_t *sizeInBytes) </pre>	<pre> miopenStatus_t miopenConvolutionBackwardDataGetW orkSpaceSize(miopenHandle_t handle, const miopenTensorDescriptor_t dyDesc, const miopenTensorDescriptor_t wDesc, const miopenConvolutionDescriptor_t convDesc, const miopenTensorDescriptor_t dxDesc, size_t *workSpaceSize) </pre>
	<pre> cudnnStatus_t cudnnFindConvolutionBackwardDataA lgorithm(cudnnHandle_t handle, const cudnnFilterDescriptor_t wDesc, const cudnnTensorDescriptor_t dyDesc, const cudnnConvolutionDescriptor_t convDesc, const cudnnTensorDescriptor_t dxDesc, const int requestedAlgoCount, int *returnedAlgoCount, cudnnConvolutionBwdDataAlgoPerf_t *perfResults) </pre>	<pre> // FindConvolution() is mandatory // Allocate workspace prior to running this API // A table with times and memory requirements for different algorithms is returned // Users can chose the top-most algorithm if they only care about the fastest algorithm miopenStatus_t miopenFindConvolutionBackwardData Algorithm(miopenHandle_t handle, const miopenTensorDescriptor_t dyDesc, const void *dy, const miopenTensorDescriptor_t wDesc, const void *w, </pre>

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Convolution	<pre> cuDNNFindConvolutionBackwardDataAlgorithmEx(cudnnHandle_t handle, const cudnnFilterDescriptor_t wDesc, const void *w, const cudnnTensorDescriptor_t dyDesc, const void *dy, const cudnnConvolutionDescriptor_t convDesc, const cudnnTensorDescriptor_t dxDesc, void *dx, const int requestedAlgoCount, int *returnedAlgoCount, cudnnConvolutionBwdDataAlgoPerf_t *perfResults, void *workSpace, size_t workSpaceSizeInBytes) cuDNNGetConvolutionBackwardDataAlgorithm(cudnnHandle_t handle, const cudnnFilterDescriptor_t wDesc, const cudnnTensorDescriptor_t dyDesc, const cudnnConvolutionDescriptor_t convDesc, const cudnnTensorDescriptor_t dxDesc, cudnnConvolutionBwdDataPreference_t preference, size_t memoryLimitInBytes, cudnnConvolutionBwdDataAlgo_t *algo) </pre>	<pre> const miopenConvolutionDescriptor_t convDesc, const miopenTensorDescriptor_t dxDesc, const void *dx, const int requestAlgoCount, int *returnedAlgoCount, miopenConvAlgoPerf_t *perfResults, void *workSpace, size_t workSpaceSize, bool exhaustiveSearch) </pre>
	<pre> cuDNNConvolutionBackwardData(cudnnHandle_t handle, const void *alpha, const cudnnFilterDescriptor_t wDesc, const void *w, const cudnnTensorDescriptor_t dyDesc, const void *dy, </pre>	<pre> miopenConvolutionBackwardData(miopenHandle_t handle, const void *alpha, const miopenTensorDescriptor_t dyDesc, const void *dy, const miopenTensorDescriptor_t wDesc, const void *w, </pre>

Operation	cuDNN API	MIOpen API
Convolution	<pre>const cudnnConvolutionDescriptor_t convDesc, cudnnConvolutionBwdDataAlgo_t algo, void *workSpace, size_t workSpaceSizeInBytes, const void *beta, const cudnnTensorDescriptor_t dxDesc, void *dx)</pre>	<pre>const miopenConvolutionDescriptor_t convDesc, miopenConvBwdDataAlgorithm_t algo, const void *beta, const miopenTensorDescriptor_t dxDesc, void *dx, void *workSpace, size_t workSpaceSi ze)</pre>
Softmax	<pre>cudnnStatus_t cudnnSoftmaxForward(cudnnHandle_t handle, cudnnSoftmaxAlgorithm_t algo, cudnnSoftmaxMode_t mode, const void *alpha, const cudnnTensorDescriptor_t xDesc, const void *x, const void *beta, const cudnnTensorDescriptor_t yDesc, void *y)</pre>	<pre>miopenStatus_t miopenSoftmaxForward(miopenHandl e_t handle, const void *alpha, const miopenTensorDescriptor_t xDesc, const void *x, const void *beta, const miopenTensorDescriptor_t yDesc, void *y)</pre>
	<pre>cudnnStatus_t cudnnSoftmaxBackward(cudnnHandle_t handle, cudnnSoftmaxAlgorithm_t algo, cudnnSoftmaxMode_t mode, const void *alpha, const cudnnTensorDescriptor_t yDesc, const void *y, const cudnnTensorDescriptor_t dyDesc, const void *dy, const void *beta, const cudnnTensorDescriptor_t dxDesc, void *dx)</pre>	<pre>miopenStatus_t miopenSoftmaxBackward(miopenHandle_t handle, const void *alpha, const miopenTensorDescriptor_t yDesc, const void *y, const miopenTensorDescriptor_t dyDesc, const void *dy, const void *beta, const miopenTensorDescriptor_t dxDesc, void *dx)</pre>
Pooling	<pre>cudnnStatus_t cudnnCreatePoolingDescriptor(cudnnPoolingDescriptor_t *poolingDesc)</pre>	<pre>miopenStatus_t miopenCreatePoolingDescriptor(mi openPoolingDescriptor_t *poolDesc)</pre>
	<pre>cudnnStatus_t cudnnSetPooling2dDescriptor(cudnnPoolingDescriptor_t poolingDesc, cudnnPoolingMode_t mode, cudnnNanPropagation_t maxpoolingNanOpt, int windowHeight, int windowWidth, int verticalPadding,</pre>	<pre>miopenStatus_t miopenSet2dPoolingDescriptor(miopenPoolingDescriptor_t poolDesc, miopenPoolingMode_t mode, int windowHeight, int windowWidth, int pad_h, int pad_w, int u, int v)</pre>

Operation	cuDNN API	MIOpen API
Pooling	int horizontalPadding, int verticalStride, int horizontalStride)	
	cudnnStatus_t cudnnGetPooling2dDescriptor (const cudnnPoolingDescriptor_t poolingDesc, cudnnPoolingMode_t *mode, cudnnNanPropagation_t *maxpoolingNanOpt, int *windowHeight, int *windowWidth, int *verticalPadding, int *horizontalPadding, int *verticalStride, int *horizontalStride)	miopenStatus_t miopenGet2dPoolingDescriptor (const miopenPoolingDescriptor_t poolDesc, miopenPoolingMode_t *mode, int *windowHeight, int *windowWidth, int *pad_h, int *pad_w, int *u, int *v)
	cudnnStatus_t cudnnGetPooling2dForwardOutputDim (const cudnnPoolingDescriptor_t poolingDesc, const cudnnTensorDescriptor_t inputTensorDesc, int *n, int *c, int *h, int *w)	miopenStatus_t miopenGetPoolingForwardOutputDim (const miopenPoolingDescriptor_t poolDesc, const miopenTensorDescriptor_t tensorDesc, int *n, int *c, int *h, int *w)
	cudnnStatus_t cudnnDestroyPoolingDescriptor (cudnnPoolingDescriptor_t poolingDesc)	miopenStatus_t miopenDestroyPoolingDescriptor (miopenPoolingDescriptor_t poolDesc)
	cudnnStatus_t cudnnPoolingForward (cudnnHandle_t handle, const cudnnPoolingDescriptor_t poolingDesc, const void *alpha, const cudnnTensorDescriptor_t xDesc, const void *x, const void *beta, const cudnnTensorDescriptor_t yDesc, void *y)	miopenStatus_t miopenPoolingForward (miopenHandle_t handle, const miopenPoolingDescriptor_t poolDesc, const void *alpha, const miopenTensorDescriptor_t xDesc, const void *x, const void *beta, const miopenTensorDescriptor_t yDesc, void *y, bool do_backward, void *workSpace, size_t workSpaceSize)
		miopenStatus_t miopenPoolingGetWorkSpaceSize (const miopenTensorDescriptor_t yDesc, size_t *workSpaceSize)
	cudnnStatus_t cudnnPoolingBackward (cudnnHandle_t handle, const cudnnPoolingDescriptor_t poolingDesc, const void *alpha,	miopenStatus_t miopenPoolingBackward (miopenHandle_t handle, const miopenPoolingDescriptor_t poolDesc, const void *alpha,

Operation	cuDNN API	MIOpen API
Pooling	<pre>const cudnnTensorDescriptor_t yDesc, const void *y, const cudnnTensorDescriptor_t dyDesc, const void *dy, const cudnnTensorDescriptor_t xDesc, const void *x, const void *beta, const cudnnTensorDescriptor_t dxDesc, void *dx)</pre>	<pre>const miopenTensorDescriptor_t yDesc, const void *y, const miopenTensorDescriptor_t dyDesc, const void *dy, const miopenTensorDescriptor_t xDesc, const void *x, const void *beta, const miopenTensorDescriptor_t dxDesc, void *dx, const void *workspace)</pre>
Activation	<pre>cudnnStatus_t cudnnCreateActivationDescriptor(cudnnActivationDescriptor_t *activationDesc)</pre>	<pre>miopenStatus_t miopenCreateActivationDescriptor(miopenActivationDescriptor_t *activDesc)</pre>
	<pre>cudnnStatus_t cudnnSetActivationDescriptor(cudnnActivationDescriptor_t activationDesc, cudnnActivationMode_t mode, cudnnNanPropagation_t reluNanOpt, double reluCeiling)</pre>	<pre>miopenStatus_t miopenSetActivationDescriptor(const miopenActivationDescriptor_t activDesc, miopenActivationMode_t mode, double activAlpha, double activBeta, double activPower)</pre>
	<pre>cudnnStatus_t cudnnGetActivationDescriptor(const cudnnActivationDescriptor_t activationDesc, cudnnActivationMode_t *mode, cudnnNanPropagation_t *reluNanOpt, double* reluCeiling)</pre>	<pre>miopenStatus_t miopenGetActivationDescriptor(const miopenActivationDescriptor_t activDesc, miopenActivationMode_t *mode, double *activAlpha, double *activBeta, double *activPower)</pre>
	<pre>cudnnStatus_t cudnnDestroyActivationDescriptor(cudnnActivationDescriptor_t activationDesc)</pre>	<pre>miopenStatus_t miopenDestroyActivationDescriptor(miopenActivationDescriptor_t activDesc)</pre>
	<pre>cudnnStatus_t cudnnActivationForward(cudnnHandle_t handle, cudnnActivationDescriptor_t activationDesc, const void *alpha, const cudnnTensorDescriptor_t xDesc, const void *x, const void *beta, const cudnnTensorDescriptor_t yDesc, void *y)</pre>	<pre>miopenStatus_t miopenActivationForward(miopenHandle_t handle, const miopenActivationDescriptor_t activDesc, const void *alpha, const miopenTensorDescriptor_t xDesc, const void *x, const void *beta, const miopenTensorDescriptor_t yDesc, void *y)</pre>

Operation	cuDNN API	MIOpen API
Activation	<pre> cudaNNStatus_t cudaNNActivationBackward(cudaNNHandle_t handle, cudaNNActivationDescriptor_t activationDesc, const void *alpha, const cudaNNTensorDescriptor_t yDesc, const void *y, const cudaNNTensorDescriptor_t dyDesc, const void *dy, const cudaNNTensorDescriptor_t xDesc, const void *x, const void *beta, const cudaNNTensorDescriptor_t dxDesc, void *dx) </pre>	<pre> miopenStatus_t miopenActivationBackward(miopenHandle_t handle, const miopenActivationDescriptor_t activDesc, const void *alpha, const miopenTensorDescriptor_t yDesc, const void *y, const miopenTensorDescriptor_t dyDesc, const void *dy, const miopenTensorDescriptor_t xDesc, const void *x, const void *beta, const miopenTensorDescriptor_t dxDesc, void *dx) </pre>
LRN	<pre> cudaNNStatus_t cudaNNCreateLRNDescriptor(cudaNNLRNDescriptor_t *normDesc) </pre>	<pre> miopenStatus_t miopenCreateLRNDescriptor(miopenL RNDescriptor_t *lrnDesc) </pre>
	<pre> cudaNNStatus_t cudaNNSetLRNDescriptor(cudaNNLRNDescriptor_t normDesc, unsigned lrnN, double lrnAlpha, double lrnBeta, double lrnK) </pre>	<pre> miopenStatus_t miopenSetLRNDescriptor(const miopenLRNDescriptor_t lrnDesc, miopenLRNMode_t mode, unsigned lrnN, double lrnAlpha, double lrnBeta, double lrnK) </pre>
	<pre> cudaNNStatus_t cudaNNGetLRNDescriptor(cudaNNLRNDescriptor_t normDesc, unsigned* lrnN, double* lrnAlpha, double* lrnBeta, double* lrnK) </pre>	<pre> miopenStatus_t miopenGetLRNDescriptor(const miopenLRNDescriptor_t lrnDesc, miopenLRNMode_t *mode, unsigned *lrnN, double *lrnAlpha, double *lrnBeta, double *lrnK) </pre>
	<pre> cudaNNStatus_t cudaNNDestroyLRNDescriptor(cudaNNLRN Descriptor_t lrnDesc) </pre>	<pre> miopenStatus_t miopenDestroyLRNDescriptor(miopen LRNDescriptor_t lrnDesc) </pre>
	<pre> cudaNNStatus_t cudaNNLRNCrossChannelForward(cudaNNHandle_t handle, cudaNNLRNDescriptor_t normDesc, cudaNNLRNMode_t lrnMode, const void* alpha, const cudaNNTensorDescriptor_t xDesc, const void *x, const void *beta, const cudaNNTensorDescriptor_t yDesc, void *y) </pre>	<pre> miopenStatus_t miopenLRNForward(miopenHandle_t handle, const miopenLRNDescriptor_t lrnDesc, const void *alpha, const miopenTensorDescriptor_t xDesc, const void *x, const void *beta, const miopenTensorDescriptor_t yDesc, void *y, bool do_backward, void *workspace) </pre>

Operation	cuDNN API	MIOpen API
LRN	<pre> cudnnStatus_t cudnnLRNCrossChannelBackward(cudnnHandle_t handle, cudnnLRNDescriptor_t normDesc, cudnnLRNMode_t lrnMode, const void* alpha, const cudnnTensorDescriptor_t yDesc, const void *y, const cudnnTensorDescriptor_t dyDesc, const void *dy, const cudnnTensorDescriptor_t xDesc, const void *x, const void *beta, const cudnnTensorDescriptor_t dxDesc, void *dx) </pre>	<pre> miopenStatus_t miopenLRNBackward(miopenHandle_t handle, const miopenLRNDescriptor_t lrnDesc, const void *alpha, const miopenTensorDescriptor_t yDesc, const void *y, const miopenTensorDescriptor_t dyDesc, const void *dy, const miopenTensorDescriptor_t xDesc, const void *x, const void *beta, const miopenTensorDescriptor_t dxDesc, void *dx, const void *workspace) </pre>
		<pre> miopenStatus_t miopenLRNGetWorkSpaceSize(const miopenTensorDescriptor_t yDesc, size_t *workSpaceSize) </pre>
Batch Normalization	<pre> cudnnStatus_t cudnnDeriveBNTensorDescriptor(cudnnTensorDescriptor_t derivedBnDesc, const cudnnTensorDescriptor_t xDesc, cudnnBatchNormMode_t mode) </pre>	<pre> miopenStatus_t miopenDeriveBNTensorDescriptor(miopenTensorDescriptor_t derivedBnDesc, const miopenTensorDescriptor_t xDesc, miopenBatchNormMode_t bn_mode) </pre>
	<pre> cudnnStatus_t cudnnBatchNormalizationForwardTraining(cudnnHandle_t handle, cudnnBatchNormMode_t mode, void *alpha, void *beta, const cudnnTensorDescriptor_t xDesc, const void *x, const cudnnTensorDescriptor_t yDesc, void *y, const cudnnTensorDescriptor_t bnScaleBiasMeanVarDesc, void *bnScale, void *bnBias, double exponentialAverageFactor, void *resultRunningMean, void *resultRunningVariance, double epsilon, void *resultSaveMean, void *resultSaveInvVariance) </pre>	<pre> miopenStatus_t miopenBatchNormalizationForwardTraining(miopenHandle_t handle, miopenBatchNormMode_t bn_mode, void *alpha, void *beta, const miopenTensorDescriptor_t xDesc, const void *x, const miopenTensorDescriptor_t yDesc, void *y, const miopenTensorDescriptor_t bnScaleBiasMeanVarDesc, void *bnScale, void *bnBias, double expAvgFactor, void *resultRunningMean, void *resultRunningVariance, double epsilon, void *resultSaveMean, void *resultSaveInvVariance) </pre>

Operation	cuDNN API	MIOpen API
Batch Normalization	<pre> cuDnnStatus_t cuDnnBatchNormalizationForwardInference(cuDnnHandle_t handle, cuDnnBatchNormMode_t mode, void *alpha, void *beta, const cuDnnTensorDescriptor_t xDesc, const void *x, const cuDnnTensorDescriptor_t yDesc, void *y, const cuDnnTensorDescriptor_t bnScaleBiasMeanVarDesc, const void *bnScale, void *bnBias, const void *estimatedMean, const void *estimatedVariance, double epsilon) </pre>	<pre> miopenStatus_t miopenBatchNormalizationForwardInference(miopenHandle_t handle, miopenBatchNormMode_t bn_mode, void *alpha, void *beta, const miopenTensorDescriptor_t xDesc, const void *x, const miopenTensorDescriptor_t yDesc, void *y, const miopenTensorDescriptor_t bnScaleBiasMeanVarDesc, void *bnScale, void *bnBias, void *estimatedMean, void *estimatedVariance, double epsilon) </pre>
	<pre> cuDnnStatus_t cuDnnBatchNormalizationBackward(cuDnnHandle_t handle, cuDnnBatchNormMode_t mode, const void *alphaDataDiff, const void *betaDataDiff, const void *alphaParamDiff, const void *betaParamDiff, const cuDnnTensorDescriptor_t xDesc, const void *x, const cuDnnTensorDescriptor_t dyDesc, const void *dy, const cuDnnTensorDescriptor_t dxDesc, void *dx, const cuDnnTensorDescriptor_t bnScaleBiasDiffDesc, const void *bnScale, void *resultBnScaleDiff, void *resultBnBiasDiff, double epsilon, const void *savedMean, const void *savedInvVariance) </pre>	<pre> miopenStatus_t miopenBatchNormalizationBackward(miopenHandle_t handle, miopenBatchNormMode_t bn_mode, const void *alphaDataDiff, const void *betaDataDiff, const void *alphaParamDiff, const void *betaParamDiff, const miopenTensorDescriptor_t xDesc, const void *x, const miopenTensorDescriptor_t dyDesc, const void *dy, const miopenTensorDescriptor_t dxDesc, void *dx, const miopenTensorDescriptor_t bnScaleBiasDiffDesc, const void *bnScale, void *resultBnScaleDiff, void *resultBnBiasDiff, double epsilon, const void *savedMean, const void *savedInvVariance) </pre>