## ESP32\_RTOS\_SDK V3.0.0

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# **Chapter 1**

# ESP32\_RTOS\_SDK

· Misc APIs : Misc APIs

• Sensor APIs: Temperature sensor and Touch pad sensor APIs

· WiFi APIs: WiFi related APIs

Phy APIs: ESP32 phy layer related APIsRegdomain APIs: ESP32 Regdomain APIs

SoftAP APIs : ESP32 Soft-AP APIsStation APIs : ESP32 station APIsCommon APIs : WiFi common APIs

- Sniffer APIs: WiFi sniffer APIs

- Smartconfig APIs : SmartConfig APIs

· System APIs : System APIs

- Boot APIs : Boot mode APIs

- Hardware MAC APIs: Hardware MAC address APIs

· Software timer APIs : Software timer APIs

• Sensor APIs: Temperature Sensor and Touch pad Sensor APIs

• SSC APIs : SSC APIs

• OTA APIs : OTA APIs

• Driver APIs : Driver APIs

SPI Driver APIs: SPI Flash APIs
GPIO Driver APIs: GPIO APIs
I2S Driver APIs: I2S APIs
PWM Driver APIs: PWM APIs

- UART Driver APIs : UART APIs

void user\_init(void) is the entrance function of the application.

2 ESP32 RTOS SDK

#### Attention

- 1. It is recommended that users set the timer to the periodic mode for periodic checks.
- (1). In freeRTOS timer or os\_timer, do not delay by while(1) or in the manner that will block the thread.
- (2). The timer callback should not occupy CPU more than 15ms.
- (3). os\_timer\_t should not define a local variable, it has to be global variable or memory got by malloc.
- 2. Functions are stored in CACHE by default, need not ICACHE\_FLASH\_ATTR any more. The interrupt functions can also be stored in CACHE. If users want to store some frequently called functions in RAM, please add IRAM\_ATTR before functions' name.
- 3. Priority of the RTOS SDK is 15. xTaskCreate is an interface of freeRTOS. For details of the freeRTOS and APIs of the system, please visit http://www.freertos.org
- (1). When using xTaskCreate to create a task, the task stack range is [176, 512].
- (2). If an array whose length is over 60 bytes is used in a task, it is suggested that users use malloc and free rather than local variable to allocate array. Large local variables could lead to task stack overflow.
- (3). The RTOS SDK takes some priorities. Priority of the pp task is 13; priority of precise timer(ms) thread is 12; priority of the TCP/IP task is 10; priority of the freeRTOS timer is 2; priority of the idle task is 0.
- (4). Users can use tasks with priorities from 1 to 9.
- (5). Do not revise FreeRTOSConfig.h, configurations are decided by source code inside the RTOS SDK, users can not change it.

# Chapter 2

# **Module Index**

## 2.1 Modules

## Here is a list of all modules:

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# **Chapter 3**

# **Data Structure Index**

## 3.1 Data Structures

Here are the data structures with brief descriptions:

_esp_event
_os_timer_t
b_info
bss_info
cmd_s
dhcps_lease
Event_Info_u
Event_SoftAPMode_ProbeReqRecved_t
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GPIO_ConfigTypeDef
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phy_config
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regdomain_info
remote_bin_info
scan_config
sdio_queue
server_info
softap_config
station_config
station_info
UART_ConfigTypeDef
UART_IntrConfTypeDef
upgrade info

6 Data Structure Index

## **Chapter 4**

# **Module Documentation**

## 4.1 Misc APIs

misc APIs

#### **Data Structures**

· struct dhcps\_lease

## **Macros**

- #define MAC2STR(a) (a)[0], (a)[1], (a)[2], (a)[3], (a)[4], (a)[5]
- #define MACSTR "%02x:%02x:%02x:%02x:%02x:%02x"
- #define IP2STR(ipaddr)
- #define IPSTR "%d.%d.%d.%d"
- #define os\_delay\_us ets\_delay\_us
- #define os\_install\_putc1 ets\_install\_putc1
- #define os\_putc ets\_putc
- · #define printf\_call ets\_printf
- #define os\_printf(fmt, ...)
- #define os\_printf\_isr(fmt, ...)

## **Enumerations**

- enum dhcp\_status { DHCP\_STOPPED, DHCP\_STARTED }
- enum dhcps\_offer\_option { OFFER\_START = 0x00, OFFER\_ROUTER = 0x01, OFFER\_END }

#### **Functions**

- void ets\_delay\_us (uint16 us)
- void ets\_install\_putc1 (void(\*p)(char c))
- void ets\_putc (char c)
- void ets\_printf (const char \*fmt,...)
- unsigned long **os\_random** (void)
- int os\_get\_random (uint8 \*buf, size\_t len)
- void \* zalloc (size\_t len)

8 Module Documentation

## **Variables**

• SemaphoreHandle\_t stdio\_mutex\_tx

## 4.1.1 Detailed Description

misc APIs

#### 4.1.2 Macro Definition Documentation

```
4.1.2.1 #define IP2STR( ipaddr )
```

#### Value:

```
ip4_addr1_16(ipaddr), \
    ip4_addr2_16(ipaddr), \
    ip4_addr3_16(ipaddr), \
    ip4_addr4_16(ipaddr)
```

## 4.1.2.2 #define os\_printf( fmt, ... )

#### Value:

## 4.1.2.3 #define os\_printf\_isr( fmt, ... )

## Value:

```
do {
          static const char ram_str[] DRAM_ATTR STORE_ATTR = fmt; \
          ets_printf(ram_str, ##__VA_ARGS__); \
          while(0)
```

## 4.1.3 Enumeration Type Documentation

#### 4.1.3.1 enum dhcp\_status

Enumerator

```
DHCP_STOPPED disable DHCP
DHCP_STARTED enable DHCP
```

## 4.1.3.2 enum dhcps\_offer\_option

#### Enumerator

```
OFFER_START DHCP offer option start

OFFER_ROUTER DHCP offer router, only support this option now

OFFER_END DHCP offer option start
```

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## 4.2 WiFi Related APIs

WiFi APIs.

## **Modules**

• Phy APIs

ESP32 phy layer related APIs.

• Regdomain APIs

ESP32 Regdomain APIs.

SoftAP APIs

ESP32 Soft-AP APIs.

• Station APIs

ESP32 station APIs.

Common APIs

WiFi common APIs.

Sniffer APIs

WiFi sniffer APIs.

• WPS APIs

ESP32 WPS APIs.

Smartconfig APIs

SmartConfig APIs.

## 4.2.1 Detailed Description

WiFi APIs.

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## 4.3 Phy APIs

ESP32 phy layer related APIs.

#### **Data Structures**

· struct phy\_config

#### **Macros**

- #define CFG\_MASK\_PHY\_MODE (1<<1)
- #define CFG\_MASK\_PHY\_BW (1<<2)</li>
- #define CFG MASK PHY SND CHAN (1<<3)

#### **Enumerations**

- enum phy\_mode { PHY\_MODE\_11B = 1, PHY\_MODE\_11G = 2, PHY\_MODE\_11N = 3 }
- enum phy bw { PHY BW HT20 = 1, PHY BW HT40 = 2, PHY BW HT2040 = 3 }
- enum phy\_2nd\_chan { PHY\_HT40U = 1, PHY\_HT40D = 2 }

## **Functions**

- bool wifi\_set\_phy (WIFI\_INTERFACE ifx, struct phy\_config \*config)
  - Set the phy configuration of ESP32 station/softap and save it to the Flash.
- bool wifi\_get\_phy (WIFI\_INTERFACE ifx, struct phy\_config \*config)

Get the phy configuration of ESP32 station/softap and save it to the Flash.

- bool wifi\_set\_phy\_mode (WIFI\_INTERFACE ifx, enum phy\_mode phy\_mode)
  - Set the phy mode of ESP32 station/softap and save it to the flash.
- bool wifi\_set\_phy\_bw (WIFI\_INTERFACE ifx, enum phy\_bw phy\_bw)

Set the phy bandwidth of ESP32 station/softap and save it to the flash.

bool wifi\_set\_phy\_2nd\_chan (enum phy\_2nd\_chan chan)

Set the phy second channel of ESP32 softap and save it to the flash.

## 4.3.1 Detailed Description

ESP32 phy layer related APIs.

## 4.3.2 Enumeration Type Documentation

4.3.2.1 enum phy\_2nd\_chan

#### Enumerator

PHY\_HT40U the second channel is above the primary channelPHY\_HT40D the second channel is below the primary channel

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#### 4.3.2.2 enum phy\_bw

#### Enumerator

```
PHY_BW_HT20 phy bandwidth 20M onlyPHY_BW_HT40 phy bandwidth 40M onlyPHY_BW_HT2040 phy bandwidth 2040M coexist, currently not supported
```

#### 4.3.2.3 enum phy\_mode

#### **Enumerator**

```
PHY_MODE_11B 802.11b mode PHY_MODE_11G 802.11g mode PHY_MODE_11N 802.11n mode
```

#### 4.3.3 Function Documentation

4.3.3.1 bool wifi\_get\_phy ( WIFI\_INTERFACE ifx, struct phy\_config \* config )

Get the phy configuration of ESP32 station/softap and save it to the Flash.

#### **Parameters**

WIFI_INTERFACE	: station/softap interface
struct	phy_config *config : phy configuration

## Returns

true : success false : failure

4.3.3.2 bool wifi\_set\_phy ( WIFI\_INTERFACE ifx, struct phy\_config \* config )

Set the phy configuration of ESP32 station/softap and save it to the Flash.

#### Attention

- 1. This API can be called only when the opmode is correctly configured, e.g. if you try to configure softap phy while the opmode is station, it returns false.
- 2. If call this API when station/softap is started(connected), it will firstly stop/disconnect the station/softap, configure phy, then start/connect station/softap.
- 3. If you change the phy mode from 11N HT40 to 11b/g, the bandwith will be switched to HT20 since the HT40 is for 11N only.
- 4. You can configure phy mode/bw/2nd\_chan by mask the cfg\_mask correctly, e.g. if (cfg\_mask & CFG\_M← ASK\_PHY\_MODE) is true, the phy mode will be configued, otherwise, it has no impact on the phy mode.
- 5. Since wifi\_set\_phy\_mode/wifi\_set\_phy\_bw/wifi\_set\_2nd\_chan will stop station/softap and then start station/softap. So if you don't want station/softap stop/start several times, please use wifi\_set\_phy since it only stop/start once. Of course, if you only want to configure phy mode, you can use the one you prefer.
- 6. The default sdk phy configuration: 11N/HT40

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#### **Parameters**

WIFI_INTERFACE	: interface to be configured					
struct	phy_config *config : phy configuration					

## Returns

true : success false : failure

4.3.3.3 bool wifi\_set\_phy\_2nd\_chan ( enum phy\_2nd\_chan chan )

Set the phy second channel of ESP32 softap and save it to the flash.

## Attention

Refer to wifi\_set\_phy()

#### **Parameters**

	enum	phy_2nd_chan : phy configuration	
--	------	----------------------------------	--

#### Returns

true : success false : failure

4.3.3.4 bool wifi\_set\_phy\_bw ( WIFI\_INTERFACE ifx, enum phy\_bw phy\_bw )

Set the phy bandwidth of ESP32 station/softap and save it to the flash.

## Attention

See wifi\_set\_phy()

#### **Parameters**

WIFI_INTERFACE	: interface to be configured
enum	phy_bw phy_bw : phy bandwidth

## Returns

true : success false : failure

4.3 Phy APIs 13

4.3.3.5 bool wifi\_set\_phy\_mode ( WIFI\_INTERFACE ifx, enum phy\_mode phy\_mode )

Set the phy mode of ESP32 station/softap and save it to the flash.

## Attention

Refer to wifi\_set\_phy()

## **Parameters**

WIFI_INTERFACE	: interface to be configured
enum	phy_mode phy_mode : phy mode

## Returns

true : success false : failure

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## 4.4 Regdomain APIs

ESP32 Regdomain APIs.

#### **Data Structures**

• struct regdomain\_info

#### **Macros**

• #define REGDOMAIN CHANGROUP MAX 1

## **Functions**

- bool wifi\_get\_regdomain (WIFI\_INTERFACE ifx, struct regdomain\_info \*rd)

  Get the regdomain info of station/softap.
- bool wifi\_set\_regdomain (WIFI\_INTERFACE ifx, struct regdomain\_info \*rd)

  Set the regdomain info of station/softap.

## 4.4.1 Detailed Description

ESP32 Regdomain APIs.

#### 4.4.2 Function Documentation

4.4.2.1 bool wifi\_get\_regdomain ( WIFI\_INTERFACE ifx, struct regdomain\_info \* rd )

Get the regdomain info of station/softap.

#### **Parameters**

WIFI_INTERFACE	ifx : station/softap interface
struct	regdomain_info *rd : For station, only field rd_sta_enable is valid, please ignore all other fields. For softap, if rd_ap_enable is 1, then fields from regdomain to chan are valid, otherwise, all other fields are invalid.

#### Returns

true : success false : failure

4.4.2.2 bool wifi\_set\_regdomain ( WIFI\_INTERFACE ifx, struct regdomain\_info \* rd )

Set the regdomain info of station/softap.

4.4 Regdomain APIs 15

#### Attention

1. If the regodmain of station is enable, the station will use passive scan to scan channel  $1\sim14$ , NO PROBE REQUEST will send out, this is also the behavior specified in 80211-2012 standard. However, this may impact the scan performance, e.g. it can't find the hidden AP etc. If you think this not what you expected, pls contact espressif.

- 2. For softap, if regdomain is enable, the regodmain info will be added into beacon.
- 3. If current channel of softap is not in [schan, schan+nchan-1], then softap will switch to the channel schan automatically
- 4. In stationap mode, if the regdomain information of softap is conflicted with the regodmain information of the external AP that sta is conneted, then the external AP's regodmain information is prefered.

#### **Parameters**

WIFI_INTERFACE	ifx : station/softap interface
struct	regdomain_info *rd : For station, only field rd_sta_enable is valid, please ignore all other field. For softap, if rd_ap_enable is 1, then fields from regdomain to chan are valid, otherwise, all other fields are invalid.

#### Returns

true : success false : failure 16 Module Documentation

## 4.5 Sensor APIs

Temperature Sensor and Touch pad Sensor APIs.

#### **Enumerations**

```
    enum touch_sensor_pad {
        TOUCH_SENSOR_PAD0 = BIT0, TOUCH_SENSOR_PAD1 = BIT1, TOUCH_SENSOR_PAD2 = BIT2, T 
        OUCH_SENSOR_PAD3 = BIT3,
        TOUCH_SENSOR_PAD4 = BIT4, TOUCH_SENSOR_PAD5 = BIT5, TOUCH_SENSOR_PAD6 = BIT6, T 
        OUCH_SENSOR_PAD7 = BIT7,
        TOUCH_SENSOR_PAD8 = BIT8, TOUCH_SENSOR_PAD9 = BIT9 }
```

#### **Functions**

• uint8 temperature\_sensor\_read (void)

Read value from temperature sensor.

void touch\_sensor\_init (touch\_sensor\_pad pad)

Initialize touch pad sensor.

void touch\_sensor\_read (uint16 \*pad\_out, uint16 sample\_num)

Read value from touch pad sensor.

## 4.5.1 Detailed Description

Temperature Sensor and Touch pad Sensor APIs.

## 4.5.2 Function Documentation

4.5.2.1 uint8 temperature\_sensor\_read ( void )

Read value from temperature sensor.

**Parameters** 

null

## Returns

range [0, 255]

4.5.2.2 void touch\_sensor\_init ( touch\_sensor\_pad pad )

Initialize touch pad sensor.

4.5 Sensor APIs 17

## **Parameters**

touch_sensor_pad	pad : enable the corresponding touch_pad[9:0]
------------------	---

Returns

null

4.5.2.3 void touch\_sensor\_read ( uint16 \* pad\_out, uint16 sample\_num )

Read value from touch pad sensor.

## Example:

```
uint16 pad_out[10];
uint16 sample_num = 10000;
rtc_touch_read(pad_out, sample_num);
```

## **Parameters**

uint16	*pad_out : pointer of the start address of uint16 pad_out[10], to get the value from touch pad sensor (touch_pad[9:0]).
uint16	sample_num : range [0, 65535], meaturing time of touch pad = sample_num*(1/RTC_CLK)

## Returns

null

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#### 4.6 SoftAP APIs

ESP32 Soft-AP APIs.

#### **Data Structures**

- · struct softap\_config
- · struct station info

#### **Functions**

bool wifi\_softap\_get\_config (struct softap\_config \*config)

Get the current configuration of the ESP32 WiFi soft-AP.

bool wifi\_softap\_get\_config\_default (struct softap\_config \*config)

Get the configuration of the ESP32 WiFi soft-AP saved in the flash.

bool wifi\_softap\_set\_config (struct softap\_config \*config)

Set the configuration of the WiFi soft-AP and save it to the Flash.

bool wifi\_softap\_set\_config\_current (struct softap\_config \*config)

Set the configuration of the WiFi soft-AP; the configuration will not be saved to the Flash.

uint8 wifi\_softap\_get\_station\_num (void)

Get the number of stations connected to the ESP32 soft-AP.

struct station\_info \* wifi\_softap\_get\_station\_info (void)

Get the information of stations connected to the ESP32 soft-AP, including MAC and IP.

void wifi softap free station info (void)

Free the space occupied by station\_info when wifi\_softap\_get\_station\_info is called.

bool wifi\_softap\_dhcps\_start (void)

Enable the ESP32 soft-AP DHCP server.

bool wifi\_softap\_dhcps\_stop (void)

Disable the ESP32 soft-AP DHCP server. The DHCP is enabled by default.

enum dhcp\_status wifi\_softap\_dhcps\_status (void)

Get the ESP32 soft-AP DHCP server status.

bool wifi softap get dhcps lease (struct dhcps lease \*please)

Query the IP range that can be got from the ESP32 soft-AP DHCP server.

bool wifi\_softap\_set\_dhcps\_lease (struct dhcps\_lease \*please)

Set the IP range of the ESP32 soft-AP DHCP server.

uint32 wifi\_softap\_get\_dhcps\_lease\_time (void)

Get ESP32 soft-AP DHCP server lease time.

bool wifi\_softap\_set\_dhcps\_lease\_time (uint32 minute)

Set ESP32 soft-AP DHCP server lease time, default is 120 minutes.

bool wifi\_softap\_reset\_dhcps\_lease\_time (void)

Reset ESP32 soft-AP DHCP server lease time which is 120 minutes by default.

bool wifi\_softap\_set\_dhcps\_offer\_option (uint8 level, void \*optarg)

Set the ESP32 soft-AP DHCP server option.

## 4.6.1 Detailed Description

ESP32 Soft-AP APIs.

#### Attention

To call APIs related to ESP32 soft-AP has to enable soft-AP mode first (wifi\_set\_opmode)

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4.6.2	Function Documentation		
4.6.2.1	bool wifi_softap_dhcps_start ( void )		
Enable the ESP32 soft-AP DHCP server.			
Attoutio	_		

#### Attention

- 1. The DHCP is enabled by default.
- the static IP will be disabled; if the static IP is enabled, the DHCP will be disabled. It depends on the latest

# 2. The DHCP and the static IP related API (wifi\_set\_ip\_info) influence each other, if the DHCP is enabled, configuration. **Parameters** null

true: succeed

Returns

false : fail

4.6.2.2 enum dhcp\_status wifi\_softap\_dhcps\_status ( void )

Get the ESP32 soft-AP DHCP server status.

**Parameters** 

null

# Returns

enum dhcp\_status

4.6.2.3 bool wifi\_softap\_dhcps\_stop (void)

Disable the ESP32 soft-AP DHCP server. The DHCP is enabled by default.

**Parameters** 

null

# Returns

true: succeed false : fail

```
4.6.2.4 void wifi_softap_free_station_info ( void )
```

Free the space occupied by station\_info when wifi\_softap\_get\_station\_info is called.

### Attention

The ESP32 is limited to only one channel, so when in the soft-AP+station mode, the soft-AP will adjust its channel automatically to be the same as the channel of the ESP32 station.

### **Parameters**

```
null
```

### Returns

null

```
4.6.2.5 bool wifi_softap_get_config ( struct softap_config * config )
```

Get the current configuration of the ESP32 WiFi soft-AP.

### **Parameters**

```
struct softap_config *config : ESP32 soft-AP configuration
```

# Returns

true : succeed false : fail

4.6.2.6 bool wifi\_softap\_get\_config\_default ( struct softap\_config \* config )

Get the configuration of the ESP32 WiFi soft-AP saved in the flash.

### **Parameters**

```
struct softap_config *config : ESP32 soft-AP configuration
```

### Returns

true : succeed false : fail

4.6.2.7 bool wifi\_softap\_get\_dhcps\_lease ( struct dhcps\_lease \* please )

Query the IP range that can be got from the ESP32 soft-AP DHCP server.

4.6 SoftAP APIs 21

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This API can only be called during ESP32 soft-AP DHCP server enabled.

### **Parameters**

struct | dhcps\_lease \*please : IP range of the ESP32 soft-AP DHCP server.

### Returns

true : succeed false : fail

4.6.2.8 uint32 wifi\_softap\_get\_dhcps\_lease\_time ( void )

Get ESP32 soft-AP DHCP server lease time.

### Attention

This API can only be called during ESP32 soft-AP DHCP server enabled.

### **Parameters**

null

### Returns

lease time, uint: minute.

4.6.2.9 struct station\_info\* wifi\_softap\_get\_station\_info ( void )

Get the information of stations connected to the ESP32 soft-AP, including MAC and IP.

### Attention

wifi\_softap\_get\_station\_info depends on DHCP, it can only be used when DHCP is enabled, so it can not get the static IP.

# Parameters

null

### Returns

struct station\_info\*: station information structure

4.6.2.10 uint8 wifi\_softap\_get\_station\_num ( void )

Get the number of stations connected to the ESP32 soft-AP.

### Attention

The ESP32 is limited to only one channel, so when in the soft-AP+station mode, the soft-AP will adjust its channel automatically to be the same as the channel of the ESP32 station.

### **Parameters**



### Returns

the number of stations connected to the ESP32 soft-AP

4.6.2.11 bool wifi\_softap\_reset\_dhcps\_lease\_time (void)

Reset ESP32 soft-AP DHCP server lease time which is 120 minutes by default.

### Attention

This API can only be called during ESP32 soft-AP DHCP server enabled.

### Parameters

null

### Returns

true : succeed false : fail

4.6.2.12 bool wifi\_softap\_set\_config ( struct softap\_config \* config )

Set the configuration of the WiFi soft-AP and save it to the Flash.

### Attention

- 1. This configuration will be saved in flash system parameter area if changed
- 2. The ESP32 is limited to only one channel, so when in the soft-AP+station mode, the soft-AP will adjust its channel automatically to be the same as the channel of the ESP32 station.

# **Parameters**

struct | softap\_config \*config : ESP32 soft-AP configuration

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### Returns

true : succeed false : fail

4.6.2.13 bool wifi\_softap\_set\_config\_current ( struct softap\_config \* config )

Set the configuration of the WiFi soft-AP; the configuration will not be saved to the Flash.

### Attention

The ESP32 is limited to only one channel, so when in the soft-AP+station mode, the soft-AP will adjust its channel automatically to be the same as the channel of the ESP32 station.

#### **Parameters**

```
struct | softap_config *config : ESP32 soft-AP configuration
```

### Returns

true : succeed false : fail

4.6.2.14 bool wifi\_softap\_set\_dhcps\_lease ( struct dhcps\_lease \* please )

Set the IP range of the ESP32 soft-AP DHCP server.

### Attention

- 1. The IP range should be in the same sub-net with the ESP32 soft-AP IP address.
- 2. This API should only be called when the DHCP server is disabled (wifi\_softap\_dhcps\_stop).
- 3. This configuration will only take effect the next time when the DHCP server is enabled (wifi\_softap\_dhcps
  \_start).
  - If the DHCP server is disabled again, this API should be called to set the IP range.
  - Otherwise, when the DHCP server is enabled later, the default IP range will be used.

### **Parameters**

struct | dhcps\_lease \*please : IP range of the ESP32 soft-AP DHCP server.

# Returns

true : succeed false : fail

4.6.2.15 bool wifi\_softap\_set\_dhcps\_lease\_time ( uint32 minute )

Set ESP32 soft-AP DHCP server lease time, default is 120 minutes.

# Attention

This API can only be called during ESP32 soft-AP DHCP server enabled.

# **Parameters**

```
uint32 minute: lease time, uint: minute, range:[1, 2880].
```

# Returns

true : succeed false : fail

4.6.2.16 bool wifi\_softap\_set\_dhcps\_offer\_option ( uint8 level, void \* optarg )

Set the ESP32 soft-AP DHCP server option.

# Example:

```
uint8 mode = 0;
wifi_softap_set_dhcps_offer_option(OFFER_ROUTER, &mode);
```

# **Parameters**

uint8	level : OFFER_ROUTER, set the router option.		
void*	optarg:		
	bit0, 0 disable the router information;		
	bit0, 1 enable the router information.		

# Returns

true : succeed false : fail

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# 4.7 SSC APIs

SSC APIs.

### **Functions**

• void ssc\_attach (SscBaudRate bandrate)

Initial the ssc function.

• int ssc\_param\_len (void)

Get the length of the simple serial command.

char \* ssc\_param\_str (void)

Get the simple serial command string.

int ssc\_parse\_param (char \*pLine, char \*argv[])

Parse the simple serial command (ssc).

void ssc\_register (ssc\_cmd\_t \*cmdset, uint8 cmdnum, void(\*help)(void))

Register the user-defined simple serial command (ssc) set.

# 4.7.1 Detailed Description

SSC APIs.

SSC means simple serial command. SSC APIs allows users to define their own command, users can refer to spiffs\_test/test\_main.c.

# 4.7.2 Function Documentation

4.7.2.1 void ssc\_attach ( SscBaudRate bandrate )

Initial the ssc function.

### Attention

param is no use, just compatible with ESP8266, default bandrate is 115200

### **Parameters**

	l
SscBaudRate	bandrate : baud rate
OSCDaudi late	Danulaic Daud laic

Returns

null

4.7.2.2 int ssc\_param\_len ( void )

Get the length of the simple serial command.

# **Parameters**

null

### Returns

length of the command.

4.7.2.3 char\* ssc\_param\_str (void)

Get the simple serial command string.

### **Parameters**

null

### Returns

the command.

4.7.2.4 int ssc\_parse\_param ( char \* pLine, char \* argv[])

Parse the simple serial command (ssc).

# **Parameters**

char	*pLine : [input] the ssc string
char	*argv[] : [output] parameters of the ssc

# Returns

the number of parameters.

4.7.2.5 void ssc\_register (  $ssc\_cmd\_t * cmdset$ , uint8 cmdnum, void(\*)(void) help )

Register the user-defined simple serial command (ssc) set.

# **Parameters**

SSC_←	*cmdset : the ssc set
cmd_t	
uint8	cmdnum: number of commands
void	(* help)(void) : callback of user-guide

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Returns

null

### 4.8 Station APIs

ESP32 station APIs.

### **Data Structures**

- struct station\_config
- · struct scan\_config
- struct bss info

# **Typedefs**

typedef void(\* scan\_done\_cb\_t) (void \*arg, STATUS status)
 Callback function for wifi\_station\_scan.

### **Enumerations**

enum STATION\_STATUS {
 STATION\_IDLE = 0, STATION\_CONNECTING, STATION\_WRONG\_PASSWORD, STATION\_NO\_AP\_F
 OUND,
 STATION\_CONNECT\_FAIL, STATION\_GOT\_IP }

# **Functions**

• bool wifi\_station\_get\_config (struct station\_config \*config)

Get the current configuration of the ESP32 WiFi station.

bool wifi\_station\_get\_config\_default (struct station\_config \*config)

Get the configuration parameters saved in the Flash of the ESP32 WiFi station.

bool wifi\_station\_set\_config (struct station\_config \*config)

Set the configuration of the ESP32 station and save it to the Flash.

bool wifi\_station\_set\_config\_current (struct station\_config \*config)

Set the configuration of the ESP32 station. And the configuration will not be saved to the Flash.

bool wifi\_station\_connect (void)

Connect the ESP32 WiFi station to the AP.

bool wifi\_station\_disconnect (void)

Disconnect the ESP32 WiFi station from the AP.

• bool wifi\_station\_scan (struct scan\_config \*config, scan\_done\_cb\_t cb)

Scan all available APs.

bool wifi\_station\_get\_auto\_connect (void)

Check if the ESP32 station will connect to the recorded AP automatically when the power is on.

bool wifi\_station\_set\_auto\_connect (bool set)

Set whether the ESP32 station will connect to the recorded AP automatically when the power is on. It will do so by default.

bool wifi\_station\_get\_reconnect\_policy (void)

Check whether the ESP32 station will reconnect to the AP after disconnection.

bool wifi station set reconnect policy (bool set)

Set whether the ESP32 station will reconnect to the AP after disconnection. It will do so by default.

STATION\_STATUS wifi\_station\_get\_connect\_status (void)

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Get the connection status of the ESP32 WiFi station.

uint8 wifi\_station\_get\_current\_ap\_id (void)

Get the information of APs (5 at most) recorded by ESP32 station.

• bool wifi\_station\_ap\_change (uint8 current\_ap\_id)

Switch the ESP32 station connection to a recorded AP.

bool wifi\_station\_ap\_number\_set (uint8 ap\_number)

Set the number of APs that can be recorded in the ESP32 station. When the ESP32 station is connected to an AP, the SSID and password of the AP will be recorded.

uint8 wifi\_station\_get\_ap\_info (struct station\_config config[])

Get the information of APs (5 at most) recorded by ESP32 station.

sint8 wifi\_station\_get\_rssi (void)

Get rssi of the AP which ESP32 station connected to.

bool wifi\_station\_dhcpc\_start (void)

Enable the ESP32 station DHCP client.

bool wifi\_station\_dhcpc\_stop (void)

Disable the ESP32 station DHCP client.

• enum dhcp\_status wifi\_station\_dhcpc\_status (void)

Get the ESP32 station DHCP client status.

# 4.8.1 Detailed Description

ESP32 station APIs.

### Attention

To call APIs related to ESP32 station has to enable station mode first (wifi set opmode)

# 4.8.2 Typedef Documentation

4.8.2.1 typedef void(\* scan\_done\_cb\_t) (void \*arg, STATUS status)

Callback function for wifi\_station\_scan.

# Parameters

void	*arg : information of APs that are found; save them as linked list; refer to struct bss_info
STATUS	status: status of scanning

# Returns

null

# 4.8.3 Enumeration Type Documentation

# 4.8.3.1 enum STATION\_STATUS

### **Enumerator**

STATION\_IDLE ESP32 station idle

**STATION\_CONNECTING** ESP32 station is connecting to AP **STATION\_WRONG\_PASSWORD** the password is wrong **STATION\_NO\_AP\_FOUND** ESP32 station can not find the target AP **STATION\_CONNECT\_FAIL** ESP32 station fail to connect to AP **STATION\_GOT\_IP** ESP32 station got IP address from AP

### 4.8.4 Function Documentation

4.8.4.1 bool wifi\_station\_ap\_change ( uint8 current\_ap\_id )

Switch the ESP32 station connection to a recorded AP.

### **Parameters**

```
uint8 new_ap_id : AP's record id, start counting from 0.
```

### Returns

true : succeed false : fail

4.8.4.2 bool wifi\_station\_ap\_number\_set ( uint8 ap\_number )

Set the number of APs that can be recorded in the ESP32 station. When the ESP32 station is connected to an AP, the SSID and password of the AP will be recorded.

# Attention

This configuration will be saved in the Flash system parameter area if changed.

### **Parameters**

uint8 ap\_number : the number of APs that can be recorded (MAX: 5)

### Returns

true : succeed false : fail

4.8.4.3 bool wifi\_station\_connect ( void )

Connect the ESP32 WiFi station to the AP.

### Attention

- 1. This API should be called when the ESP32 station is enabled, and the system initialization is completed. Do not call this API in user init.
- 2. If the ESP32 is connected to an AP, call wifi\_station\_disconnect to disconnect.

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Parameters  null
Returns  true: succeed false: fail
4.8.4.4 bool wifi_station_dhcpc_start ( void )
Enable the ESP32 station DHCP client.
<ol> <li>Attention</li> <li>The DHCP is enabled by default.</li> <li>The DHCP and the static IP API ((wifi_set_ip_info)) influence each other, and if the DHCP is enabled, the static IP will be disabled; if the static IP is enabled, the DHCP will be disabled. It depends on the latest configuration.</li> </ol>
Parameters  null
Returns  true: succeed false: fail
4.8.4.5 enum dhcp_status wifi_station_dhcpc_status ( void )
Get the ESP32 station DHCP client status.  Parameters  null
Returns enum dhcp_status
4.8.4.6 bool wifi_station_dhcpc_stop ( void )
Disable the ESP32 station DHCP client.

### Attention

- 1. The DHCP is enabled by default.
- 2. The DHCP and the static IP API ((wifi\_set\_ip\_info)) influence each other, and if the DHCP is enabled, the static IP will be disabled; if the static IP is enabled, the DHCP will be disabled. It depends on the latest configuration.

### **Parameters**

```
null
```

### Returns

true : succeed false : fail

4.8.4.7 bool wifi\_station\_disconnect ( void )

Disconnect the ESP32 WiFi station from the AP.

### Attention

This API should be called when the ESP32 station is enabled, and the system initialization is completed. Do not call this API in user\_init.

### **Parameters**

```
null
```

# Returns

true : succeed false : fail

4.8.4.8 uint8 wifi\_station\_get\_ap\_info ( struct station\_config config[] )

Get the information of APs (5 at most) recorded by ESP32 station.

# Example:

```
struct station_config config[5];
nt i = wifi_station_get_ap_info(config);
```

### **Parameters**

struct station\_config config[]: information of the APs, the array size should be 5.

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### Returns

The number of APs recorded.

4.8.4.9 bool wifi\_station\_get\_auto\_connect ( void )

Check if the ESP32 station will connect to the recorded AP automatically when the power is on.

#### **Parameters**

null

### Returns

true: connect to the AP automatically false: not connect to the AP automatically

4.8.4.10 bool wifi\_station\_get\_config ( struct station\_config \* config )

Get the current configuration of the ESP32 WiFi station.

#### **Parameters**

struct station\_config \*config : ESP32 station configuration

### Returns

true : succeed false : fail

4.8.4.11 bool wifi\_station\_get\_config\_default ( struct station\_config \* config )

Get the configuration parameters saved in the Flash of the ESP32 WiFi station.

### **Parameters**

struct station\_config \*config : ESP32 station configuration

### Returns

true : succeed false : fail

4.8.4.12 STATION\_STATUS wifi\_station\_get\_connect\_status ( void )

Get the connection status of the ESP32 WiFi station.

Parameters  null
Returns the status of connection
4.8.4.13 uint8 wifi_station_get_current_ap_id ( void )
Get the information of APs (5 at most) recorded by ESP32 station.
Parameters
struct station_config config[]: information of the APs, the array size should be 5.
Returns
The number of APs recorded.
4.8.4.14 bool wifi_station_get_reconnect_policy ( void )
Check whether the ESP32 station will reconnect to the AP after disconnection.
Parameters  null
Returns
true : succeed false : fail
4.8.4.15 sint8 wifi_station_get_rssi ( void )
Get rssi of the AP which ESP32 station connected to.
Parameters
null
Returns

31 : fail, invalid value.

others : succeed, value of rssi. In general, rssi value  $< 10\,$ 

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```
4.8.4.16 bool wifi_station_scan ( struct scan_config * config, scan_done_cb_t cb )
```

Scan all available APs.

### Attention

This API should be called when the ESP32 station is enabled, and the system initialization is completed. Do not call this API in user\_init.

### **Parameters**

struct	scan_config *config : configuration of scanning
struct	scan_done_cb_t cb : callback of scanning

### Returns

true : succeed false : fail

4.8.4.17 bool wifi\_station\_set\_auto\_connect ( bool set )

Set whether the ESP32 station will connect to the recorded AP automatically when the power is on. It will do so by default.

# Attention

- 1. If this API is called in user\_init, it is effective immediately after the power is on. If it is called in other places, it will be effective the next time when the power is on.
- 2. This configuration will be saved in Flash system parameter area if changed.

### **Parameters**

set: If it will automatically connect to the AP when the power is on
 true: it will connect automatically
 false: it will not connect automatically

### Returns

true : succeed false : fail

4.8.4.18 bool wifi\_station\_set\_config ( struct station\_config \* config )

Set the configuration of the ESP32 station and save it to the Flash.

### Attention

- 1. This API can be called only when the ESP32 station is enabled.
- 2. If wifi\_station\_set\_config is called in user\_init, there is no need to call wifi\_station\_connect. The ESP32 station will automatically connect to the AP (router) after the system initialization. Otherwise, wifi\_station\_connect should be called.
- 3. Generally, station\_config.bssid\_set needs to be 0; and it needs to be 1 only when users need to check the MAC address of the AP.
- 4. This configuration will be saved in the Flash system parameter area if changed.

#### **Parameters**

struct station\_config \*config : ESP32 station configuration

### Returns

true : succeed false : fail

4.8.4.19 bool wifi\_station\_set\_config\_current ( struct station\_config \* config\_)

Set the configuration of the ESP32 station. And the configuration will not be saved to the Flash.

### Attention

- 1. This API can be called only when the ESP32 station is enabled.
- 2. If wifi\_station\_set\_config\_current is called in user\_init, there is no need to call wifi\_station\_connect. The ESP32 station will automatically connect to the AP (router) after the system initialization. Otherwise, wifi\_ station connect should be called.
- 3. Generally, station\_config.bssid\_set needs to be 0; and it needs to be 1 only when users need to check the MAC address of the AP.

# **Parameters**

struct station\_config \*config : ESP32 station configuration

### Returns

true : succeed false : fail

4.8.4.20 bool wifi\_station\_set\_reconnect\_policy ( bool set )

Set whether the ESP32 station will reconnect to the AP after disconnection. It will do so by default.

### Attention

If users want to call this API, it is suggested that users call this API in user\_init.

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# **Parameters**

bool set: if it's true, it will enable reconnection; if it's false, it will disable reconnection.

# Returns

true : succeed false : fail

# 4.9 System APIs

System APIs.

### Modules

· Boot APIs

boot APIs

TTEN\_12DB }

Hardware MAC APIs

Hardware MAC address APIs.

### **Enumerations**

```
    enum adc1_read_pad {
        ADC1_PAD_GPIO36 = 0, ADC1_PAD_GPIO37, ADC1_PAD_GPIO38, ADC1_PAD_GPIO39,
        ADC1_PAD_GPIO32, ADC1_PAD_GPIO33, ADC1_PAD_GPIO34, ADC1_PAD_GPIO35 }
    enum adc1_read_atten { ADC1_ATTEN_0DB = 0, ADC1_ATTEN_3DB, ADC1_ATTEN_6DB, ADC1_A→
```

# **Functions**

const char \* system\_get\_sdk\_version (void)

Get information of the SDK version.

void system\_restore (void)

Reset to default settings.

void system\_restart (void)

Restart system.

void system\_deep\_sleep (uint64 time\_in\_us)

Set the chip to deep-sleep mode.

• uint32 system\_get\_time (void)

Get system time, unit: microsecond.

void system print meminfo (void)

Print the system memory distribution, including data/rodata/bss/heap.

uint32 system\_get\_free\_heap\_size (void)

Get the size of available heap.

bool system\_get\_chip\_id (uint8 \*chip\_id)

Get the chip ID.

uint64 system\_get\_rtc\_time (void)

Get RTC time, unit: RTC clock cycle.

• bool system\_rtc\_mem\_read (uint16 src, void \*dst, uint16 n)

Read user data from the RTC memory.

• bool system\_rtc\_mem\_write (uint16 dst, const void \*src, uint16 n)

Write user data to the RTC memory.

• uint16 system\_adc1\_read (adc1\_read\_pad pad, adc1\_read\_atten atten)

Read ADC1.

uint16 system\_get\_vdd33 (void)

Measure the power voltage of VDD3P3 pin 3 and 4, unit: 1/1024 V.

• bool system\_param\_save\_with\_protect (uint16 start\_sec, void \*param, uint16 len)

Write data into flash with protection.

• bool system\_param\_load (uint16 start\_sec, uint16 offset, void \*param, uint16 len)

Read the data saved into flash with the read/write protection.

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# 4.9.1 Detailed Description

System APIs.

### 4.9.2 Function Documentation

4.9.2.1 uint16 system\_adc1\_read ( adc1\_read\_pad pad, adc1\_read\_atten atten )

Read ADC1.

### **Parameters**

adc1_read_pad	pad : the corresponding GPIO
adc1_read_atten	atten : value of attenuation

### Returns

range of the return value is [0, 4096].

- If atten == 0, the range of voltage can be measured is [0, 1] V.
- If atten == 1, the range of voltage can be measured is [0, 1.4] V.
- If atten == 2, the range of voltage can be measured is [0, 2] V.
- If atten == 3, the range of voltage can be measured is [0, 4] V.

4.9.2.2 void system\_deep\_sleep ( uint64 time\_in\_us )

Set the chip to deep-sleep mode.

The device will automatically wake up after the deep-sleep time set by the users. Upon waking up, the device boots up from user init.

# Attention

The parameter time\_in\_us to be "uint64" is for further development. Only the low 32 bits of parameter time 
\_in\_us are available now.

### **Parameters**

uint64	time_in_us: deep-sleep time, only the low 32bits are avalable now. unit: microsecond
--------	--

### Returns

null

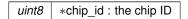
4.9.2.3 bool system\_get\_chip\_id ( uint8 \* chip\_id )

Get the chip ID.

# Example:

```
uint8 chip_id[6];
system_get_chip_id(chip_id);
```

### **Parameters**



### Returns

true : succeed false : fail

4.9.2.4 uint32 system\_get\_free\_heap\_size ( void )

Get the size of available heap.

### **Parameters**

null

# Returns

Available heap size.

4.9.2.5 uint64 system\_get\_rtc\_time ( void )

Get RTC time, unit: RTC clock cycle.

### **Parameters**

null

# Returns

RTC time.

4.9.2.6 const char\* system\_get\_sdk\_version ( void )

Get information of the SDK version.

### **Parameters**

null

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Returns
Information of the SDK version.
4.9.2.7 uint32 system_get_time ( void )
Get system time, unit: microsecond.
Parameters
null
Returns
System time, unit: microsecond.
4.9.2.8 uint16 system_get_vdd33 ( void )
Measure the power voltage of VDD3P3 pin 3 and 4, unit : 1/1024 V.
Attention
system_get_vdd33 depends on RF, please do not use it if RF is disabled.
Parameters
null
Returns  Power voltage of VDD33, unit: 1/1024 V
Tower voltage of vbboo, and . 1/1024 v
4.9.2.9 bool system_param_load ( uint16 start_sec, uint16 offset, void * param, uint16 len )
Read the data saved into flash with the read/write protection.
Flash read/write has to be 4-bytes aligned.
Read/write protection of flash: use 3 sectors (4KB per sector) to save 4KB data with protect, sector 0 and sector 1 are data sectors, back up each other, save data alternately, sector 2 is flag sector, point out which sector is keeping the latest data, sector 0 or sector 1.

### **Parameters**

	uint16	start_sec : start sector (sector 0) of the 3 sectors used for flash read/write protection. It cannot be sector 1 or sector 2.
		• For example, in IOT_Demo, the 3 sectors (3 * 4KB) starting from flash 0x3D000 can be used for flash read/write protection. The parameter start_sec is 0x3D, and it cannot be 0x3E or 0x3F.
Ī	uint16	offset : offset of data saved in sector
	void	*param : data pointer
	uint16	len : data length, offset + len =< 4 * 1024

### Returns

true : succeed false : fail

4.9.2.10 bool system\_param\_save\_with\_protect ( uint16 start\_sec, void \* param, uint16 len )

Write data into flash with protection.

Flash read/write has to be 4-bytes aligned.

Protection of flash read/write: use 3 sectors (4KBytes per sector) to save 4KB data with protect, sector 0 and sector 1 are data sectors, back up each other, save data alternately, sector 2 is flag sector, point out which sector is keeping the latest data, sector 0 or sector 1.

# **Parameters**

uint16	start_sec : start sector (sector 0) of the 3 sectors which are used for flash read/write protection.
	<ul> <li>For example, in IOT_Demo we can use the 3 sectors (3 * 4KB) starting from flash 0x3D000 for flash read/write protection, so the parameter start_sec should be 0x3D</li> </ul>
void	*param : pointer of the data to be written
uint16	len : data length, should be less than a sector, which is 4 * 1024

### Returns

true : succeed false : fail

4.9.2.11 void system\_print\_meminfo ( void )

Print the system memory distribution, including data/rodata/bss/heap.

### **Parameters**

null

4.9 System APIs 43

Returns
null
4.9.2.12 void system_restart ( void )
Restart system.
Parameters
null
Returns
null
4.9.2.13 void system_restore ( void )
Reset to default settings.
Reset to default settings of the following APIs: wifi_station_set_auto_connect, wifi_set_phy_mode, wifi_softap_cet_config related, wifi_station_set_config related, and wifi_set_opmode.
Parameters
null
Returns
null
4.9.2.14 bool system_rtc_mem_read ( uint16 <i>src</i> , void * <i>dst</i> , uint16 <i>n</i> )
Read user data from the RTC memory.
The user data segment (1024 bytes, as shown below) is used to store user data.
< system data(512 bytes)> <>
Attention
Read and write unit for data stored in the RTC memory is 4 bytes.  src_addr is the block number (4 bytes per block). So when reading data at the beginning of the user data

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segment, src\_addr will be 512/4 = 128, n will be data length.

### **Parameters**

uint16	src : source address of rtc memory, src_addr >= 128
void	*dst : data pointer
uint16	n : data length, unit: byte

### Returns

true : succeed false : fail

4.9.2.15 bool system\_rtc\_mem\_write ( uint16 dst, const void \* src, uint16 n )

Write user data to the RTC memory.

During deep-sleep, only RTC is working. So users can store their data in RTC memory if it is needed. The user data segment below (1024 bytes) is used to store the user data.

### Attention

Read and write unit for data stored in the RTC memory is 4 bytes.  $src_addr$  is the block number (4 bytes per block). So when storing data at the beginning of the user data segment,  $src_addr$  will be 512/4 = 128, n will be data length.

### **Parameters**

uint16	src : source address of rtc memory, src_addr >= 128
void	*dst : data pointer
uint16	n : data length, unit: byte

### Returns

true : succeed false : fail

4.10 Boot APIs 45

### 4.10 Boot APIs

boot APIs

### **Data Structures**

· struct b info

### **Enumerations**

```
    enum flash_size {
        FLASH_SIZE_1MB = 0, FLASH_SIZE_2MB, FLASH_SIZE_4MB, FLASH_SIZE_8MB,
        FLASH_SIZE_16MB, FLASH_SIZE_MAX }
```

### **Functions**

• enum flash\_size system\_get\_flash\_size (void)

Get the current Flash size.

uint8 system\_get\_cpu\_freq (void)

Get CPU frequency.

bool system\_get\_bin\_info (uint8 bin\_id, struct b\_info \*b\_if)

Get bin info named by b\_id.

• bool system\_set\_bin\_info (uint8 bin\_id, struct b\_info \*b\_if)

Set bin info named by b\_id.

uint8 system\_get\_current\_bin\_id (void)

Get current bin's bin\_id.

bool system\_reboot\_to\_userbin (uint8 bin\_id)

reboot and jump to bin named by bin\_id

# 4.10.1 Detailed Description

boot APIs

# 4.10.2 Enumeration Type Documentation

```
4.10.2.1 enum flash_size
```

**Enumerator** 

```
    FLASH_SIZE_1MB Flash size: 1M Bytes
    FLASH_SIZE_2MB Flash size: 2M Bytes
    FLASH_SIZE_4MB Flash size: 4M Bytes
    FLASH_SIZE_8MB Flash size: 8M Bytes
    FLASH_SIZE_16MB Flash size: 16M Bytes
```

# 4.10.3 Function Documentation

```
4.10.3.1 bool system_get_bin_info ( uint8 bin_id, struct b_info * b_if)
```

Get bin info named by b\_id.

# **Parameters**

uint8	bin_id : b_id number, must < 5
struct	b_info *b_if: bin info of bin named by b_id

# Returns

true : succeed false : fail

4.10.3.2 uint8 system\_get\_cpu\_freq ( void )

Get CPU frequency.

### **Parameters**

null

# Returns

CPU frequency, unit : MHz.

4.10.3.3 uint8 system\_get\_current\_bin\_id ( void )

Get current bin's bin\_id.

### **Parameters**

uint8 bin\_id : b\_id number, must < 5

# Returns

uint8 type b\_id

4.10.3.4 enum flash\_size system\_get\_flash\_size ( void )

Get the current Flash size.

# **Parameters**

null

### Returns

enum flash\_size

4.10 Boot APIs 47

4.10.3.5 bool system\_reboot\_to\_userbin ( uint8 bin\_id )

reboot and jump to bin named by bin\_id

# **Parameters**

```
uint8 bin_id : b_id number, must < 5
```

# Returns

true : succeed false : fail

4.10.3.6 bool system\_set\_bin\_info ( uint8  $bin_id$ , struct  $b_info * b_if$  )

Set bin info named by b\_id.

# **Parameters**

uint8	bin_id : b_id number, must $<$ 5
struct	b_info *b_if : bin info of bin named by b_id

# Returns

true : succeed false : fail

# 4.11 Hardware MAC APIs

Hardware MAC address APIs.

### **Enumerations**

- enum mac\_group { DEFAULT\_MAC = 0, USER\_MAC }
- enum mac\_type { WIFI\_MAC = 0, BT\_MAC }

### **Functions**

int system\_efuse\_program\_user\_mac (mac\_type type, uint8 \*mac)

Set user-define hardware MAC address.

• bool system\_efuse\_read\_mac (mac\_group group, mac\_type type, uint8 \*mac)

Read hardware MAC address.

bool system\_efuse\_set\_mac\_group (mac\_group group)

Set hardware MAC group, default MAC or user-defined MAC.

mac\_group system\_efuse\_get\_mac\_group (void)

Get hardware MAC group, default MAC or user-defined MAC.

# 4.11.1 Detailed Description

Hardware MAC address APIs.

In WiFi MAC, only ESP32 station MAC is the hardware MAC, ESP32 softAP MAC is a software MAC calculated from ESP32 station MAC. So users need to call wifi\_get\_macaddr to query the ESP32 softAP MAC if ESP32 station MAC changed.

# 4.11.2 Enumeration Type Documentation

4.11.2.1 enum mac\_group

**Enumerator** 

**DEFAULT\_MAC** Default hardware MAC provided by Espressif Systems **USER\_MAC** User-define hardware MAC

4.11.2.2 enum mac\_type

**Enumerator** 

WIFI\_MAC Hardware MAC address of ESP32 WiFiBT\_MAC Hardware MAC address of ESP32 bluetooth

# 4.11.3 Function Documentation

4.11.3.1 mac\_group system\_efuse\_get\_mac\_group ( void )

Get hardware MAC group, default MAC or user-defined MAC.

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### **Parameters**

### Returns

mac\_group, the hardware MAC group.

4.11.3.2 int system\_efuse\_program\_user\_mac ( mac\_type type, uint8 \* mac )

Set user-define hardware MAC address.

### Attention

Hardware MAC address can only be set ONCE for each ESP32 chip.

### **Parameters**

mac_type	type : type of hardware MAC address.
uint8	*mac : user-define hardware MAC address, length: 6 bytes.

# Returns

0 : succeed to set.

1 : the hardware MAC has been set once, users can not set it any more.

2 : fail to set.

3: invalid parameter.

4.11.3.3 bool system\_efuse\_read\_mac ( mac\_group group, mac\_type type, uint8 \* mac )

Read hardware MAC address.

# **Parameters**

mac_group	group : default MAC or user-defined MAC.
mac_type	type : type of hardware MAC address.
uint8	*mac : the hardware MAC address, length: 6 bytes.

### Returns

true : succeed false : fail

4.11.3.4 bool system\_efuse\_set\_mac\_group ( mac\_group group )

Set hardware MAC group, default MAC or user-defined MAC.

# Attention

This API needs system\_restart to take effect.

# **Parameters**

mac\_group | group : default MAC or user-defined MAC.

# Returns

true : succeed false : fail

4.12 Software timer APIs 51

### 4.12 Software timer APIs

Software timer APIs.

### **Functions**

• void os\_timer\_setfn (os\_timer\_t \*ptimer, os\_timer\_func\_t \*pfunction, void \*parg)

Set the timer callback function.

• void os\_timer\_arm (os\_timer\_t \*ptimer, uint32 msec, bool repeat\_flag)

Enable the millisecond timer.

• void os\_timer\_disarm (os\_timer\_t \*ptimer)

Disarm the timer.

# 4.12.1 Detailed Description

Software timer APIs.

Timers of the following interfaces are software timers. Functions of the timers are executed during the tasks. Since a task can be stopped, or be delayed because there are other tasks with higher priorities, the following os\_timer interfaces cannot guarantee the precise execution of the timers.

- For the same timer, os\_timer\_arm (or os\_timer\_arm\_us) cannot be invoked repeatedly. os\_timer\_disarm should be invoked first.
- os\_timer\_setfn can only be invoked when the timer is not enabled, i.e., after os\_timer\_disarm or before os
   —timer\_arm (or os\_timer\_arm\_us).

### 4.12.2 Function Documentation

4.12.2.1 void os\_timer\_arm ( os\_timer\_t \* ptimer, uint32 msec, bool repeat\_flag )

Enable the millisecond timer.

### **Parameters**

<i>08</i> _←	*ptimer : timer structure
timer_t	
uint32_t	milliseconds: Timing, unit: millisecond, the maximum value allowed is 0x41893
bool	repeat_flag : Whether the timer will be invoked repeatedly or not

### Returns

null

4.12.2.2 void os\_timer\_disarm ( os\_timer\_t \* ptimer )

Disarm the timer.

# **Parameters**

<i>os</i> _←	*ptimer : Timer structure
timer_t	

# Returns

null

4.12.2.3 void os\_timer\_setfn ( os\_timer\_t \* ptimer, os\_timer\_func\_t \* pfunction, void \* parg )

Set the timer callback function.

# Attention

- 1. The callback function must be set in order to enable the timer.
- 2. Operating system scheduling is disabled in timer callback.

# **Parameters**

os_timer_t	*ptimer : Timer structure
os_timer_← func_t	*pfunction : timer callback function
void	*parg : callback function parameter

# Returns

null

4.13 Common APIs 53

### 4.13 Common APIs

WiFi common APIs.

# **Data Structures**

- struct ip\_info
- struct Event StaMode ScanDone t
- · struct Event StaMode Connected t
- struct Event StaMode Disconnected t
- struct Event\_StaMode\_AuthMode\_Change\_t
- · struct Event StaMode Got IP t
- struct Event\_SoftAPMode\_StaConnected\_t
- struct Event\_SoftAPMode\_StaDisconnected\_t
- struct Event SoftAPMode ProbeRegRecved t
- · union Event Info u
- · struct \_esp\_event

# **Typedefs**

- typedef struct <u>\_esp\_event</u> System\_Event\_t
- typedef void(\* wifi event handler cb t) (System Event t \*event)

The Wi-Fi event handler.

### **Enumerations**

```
enum WIFI_MODE {NULL_MODE = 0, STATION_MODE, SOFTAP_MODE, STATIONAP_MODE,MAX_MODE }
```

- enum AUTH\_MODE {
- AUTH\_OPEN = 0, AUTH\_WEP, AUTH\_WPA\_PSK, AUTH\_WPA2\_PSK,

AUTH\_WPA\_WPA2\_PSK, AUTH\_MAX }

- enum WIFI\_INTERFACE { STATION\_IF = 0, SOFTAP\_IF, MAX\_IF }
- enum SYSTEM\_EVENT {

EVENT\_STAMODE\_SCAN\_DONE = 0, EVENT\_STAMODE\_CONNECTED, EVENT\_STAMODE\_DISCO↔ NNECTED, EVENT\_STAMODE\_AUTHMODE\_CHANGE,

EVENT\_STAMODE\_GOT\_IP, EVENT\_STAMODE\_DHCP\_TIMEOUT, EVENT\_SOFTAPMODE\_STACO↔ NNECTED, EVENT\_SOFTAPMODE\_STADISCONNECTED,

EVENT\_SOFTAPMODE\_PROBEREQRECVED, EVENT\_MAX }

enum {

REASON\_UNSPECIFIED = 1, REASON\_AUTH\_EXPIRE = 2, REASON\_AUTH\_LEAVE = 3, REASON\_← ASSOC EXPIRE = 4,

REASON\_ASSOC\_TOOMANY = 5, REASON\_NOT\_AUTHED = 6, REASON\_NOT\_ASSOCED = 7, RE $\hookleftarrow$  ASON\_ASSOC\_LEAVE = 8,

REASON\_ASSOC\_NOT\_AUTHED = 9, REASON\_DISASSOC\_PWRCAP\_BAD = 10, REASON\_DISAS $\hookleftarrow$ SOC\_SUPCHAN\_BAD = 11, REASON\_IE\_INVALID = 13,

REASON\_MIC\_FAILURE = 14, REASON\_4WAY\_HANDSHAKE\_TIMEOUT = 15, REASON\_GROUP\_K ← EY\_UPDATE\_TIMEOUT = 16, REASON\_IE\_IN\_4WAY\_DIFFERS = 17,

REASON\_GROUP\_CIPHER\_INVALID = 18, REASON\_PAIRWISE\_CIPHER\_INVALID = 19, REASON\_← AKMP\_INVALID = 20, REASON\_UNSUPP\_RSN\_IE\_VERSION = 21,

REASON\_INVALID\_RSN\_IE\_CAP = 22, REASON\_802\_1X\_AUTH\_FAILED = 23, REASON\_CIPHER\_S UITE REJECTED = 24, REASON\_BEACON\_TIMEOUT = 200,

REASON\_NO\_AP\_FOUND = 201, REASON\_AUTH\_FAIL = 202, REASON\_ASSOC\_FAIL = 203, REAS $\hookleftarrow$ ON\_HANDSHAKE\_TIMEOUT = 204 }

### **Functions**

WIFI\_MODE wifi\_get\_opmode (void)

Get the current operating mode of the WiFi.

WIFI\_MODE wifi\_get\_opmode\_default (void)

Get the operating mode of the WiFi saved in the Flash.

• bool wifi\_set\_opmode (WIFI\_MODE opmode)

Set the WiFi operating mode, and save it to Flash.

bool wifi\_set\_opmode\_current (WIFI\_MODE opmode)

Set the WiFi operating mode, and will not save it to Flash.

• WIFI\_INTERFACE wifi\_get\_interface (void \*dev)

Get the ESP32 WiFi interface (station or the soft-AP).

• bool wifi\_get\_ip\_info (WIFI\_INTERFACE if\_index, struct ip\_info \*info)

Get the IP address of the ESP32 WiFi station or the soft-AP interface.

• bool wifi\_set\_ip\_info (WIFI\_INTERFACE if\_index, struct ip\_info \*info)

Set the IP address of the ESP32 WiFi station or the soft-AP interface.

bool wifi\_get\_macaddr (WIFI\_INTERFACE if\_index, uint8 \*macaddr)

Get MAC address of the ESP32 WiFi station or the soft-AP interface.

• bool wifi set macaddr (WIFI INTERFACE if index, uint8 \*macaddr)

Set MAC address of the ESP32 WiFi station or the soft-AP interface.

bool wifi\_set\_event\_handler\_cb (wifi\_event\_handler\_cb\_t cb)

Register the Wi-Fi event handler.

# 4.13.1 Detailed Description

WiFi common APIs.

The Flash system parameter area is the last 16KB of the Flash.

# 4.13.2 Typedef Documentation

4.13.2.1 typedef void(\* wifi\_event\_handler\_cb\_t) (System\_Event\_t \*event)

The Wi-Fi event handler.

### Attention

No complex operations are allowed in callback. If users want to execute any complex operations, please post message to another task instead.

# **Parameters**

System_←	*event : WiFi event
Event_t	

### Returns

null

4.13 Common APIs 55

# 4.13.3 Enumeration Type Documentation

# 4.13.3.1 enum AUTH\_MODE

#### **Enumerator**

**AUTH\_OPEN** authenticate mode : open **AUTH\_WEP** authenticate mode : WEP

AUTH\_WPA\_PSK authenticate mode : WPA\_PSK
AUTH\_WPA2\_PSK authenticate mode : WPA2\_PSK

AUTH\_WPA\_WPA2\_PSK authenticate mode : WPA\_WPA2\_PSK

#### 4.13.3.2 enum SYSTEM EVENT

#### Enumerator

EVENT\_STAMODE\_SCAN\_DONE ESP32 station finish scanning AP

EVENT\_STAMODE\_CONNECTED ESP32 station connected to AP

EVENT\_STAMODE\_DISCONNECTED ESP32 station disconnected to AP

EVENT\_STAMODE\_AUTHMODE\_CHANGE the auth mode of AP connected by ESP32 station changed

EVENT\_STAMODE\_GOT\_IP ESP32 station got IP from connected AP

EVENT\_STAMODE\_DHCP\_TIMEOUT ESP32 station dhcp client got IP timeout

EVENT\_SOFTAPMODE\_STACONNECTED a station connected to ESP32 soft-AP

EVENT\_SOFTAPMODE\_STADISCONNECTED a station disconnected to ESP32 soft-AP

EVENT\_SOFTAPMODE\_PROBEREQRECVED Receive probe request packet in soft-AP interface

## 4.13.3.3 enum WIFI\_INTERFACE

# Enumerator

**STATION\_IF** ESP32 station interface **SOFTAP\_IF** ESP32 soft-AP interface

#### 4.13.3.4 enum WIFI MODE

#### Enumerator

**NULL\_MODE** null mode

STATION\_MODE WiFi station mode

SOFTAP\_MODE WiFi soft-AP mode

**STATIONAP\_MODE** WiFi station + soft-AP mode

# 4.13.4 Function Documentation

# 4.13.4.1 WIFI\_INTERFACE wifi\_get\_interface ( void \* dev )

Get the ESP32 WiFi interface (station or the soft-AP).

#### Returns

WIFI\_INTERFACE

4.13.4.2 bool wifi\_get\_ip\_info ( WIFI\_INTERFACE if\_index, struct ip\_info \* info )

Get the IP address of the ESP32 WiFi station or the soft-AP interface.

#### Attention

Users need to enable the target interface (station or soft-AP) by wifi\_set\_opmode first.

#### **Parameters**

WIFI_INTERFACE	if_index : get the IP address of the station or the soft-AP interface, 0x00 for STATION_IF,	
	0x01 for SOFTAP_IF.	
struct	ip_info ∗info : the IP information obtained.	

#### Returns

true : succeed false : fail

4.13.4.3 bool wifi\_get\_macaddr ( WIFI\_INTERFACE if\_index, uint8 \* macaddr )

Get MAC address of the ESP32 WiFi station or the soft-AP interface.

#### **Parameters**

WIFI_INTERFACE	if_index: get the IP address of the station or the soft-AP interface, 0x00 for STATION_IF, 0x01 for SOFTAP_IF.
uint8	*macaddr : the MAC address.

#### Returns

true : succeed false : fail

4.13.4.4 WIFI\_MODE wifi\_get\_opmode ( void )

Get the current operating mode of the WiFi.

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#### Returns

WiFi operating modes:

- 0x01: station mode;
- 0x02: soft-AP mode
- 0x03: station+soft-AP mode

4.13.4.5 WIFI\_MODE wifi\_get\_opmode\_default ( void )

Get the operating mode of the WiFi saved in the Flash.

#### **Parameters**



# Returns

WiFi operating modes:

- 0x01: station mode;
- 0x02: soft-AP mode
- 0x03: station+soft-AP mode

4.13.4.6 bool wifi\_set\_event\_handler\_cb ( wifi\_event\_handler\_cb\_t cb )

Register the Wi-Fi event handler.

# Parameters

wifi_event_handler_←	cb : callback function	
ch t		

# Returns

true : succeed false : fail

4.13.4.7 bool wifi\_set\_ip\_info ( WIFI\_INTERFACE  $if\_index$ , struct ip\_info \* info )

Set the IP address of the ESP32 WiFi station or the soft-AP interface.

#### Attention

- 1. Users need to enable the target interface (station or soft-AP) by wifi set opmode first.
- 2. To set static IP, users need to disable DHCP first (wifi station dhopc stop or wifi softap dhops stop):
  - If the DHCP is enabled, the static IP will be disabled; if the static IP is enabled, the DHCP will be disabled. It depends on the latest configuration.

#### **Parameters**

WIFI_INTERFACE	if_index : get the IP address of the station or the soft-AP interface, 0x00 for STATION_IF, 0x01 for SOFTAP_IF.	
struct	ip_info *info : the IP information obtained.	

#### Returns

true : succeed false : fail

4.13.4.8 bool wifi\_set\_macaddr ( WIFI\_INTERFACE if\_index, uint8 \* macaddr )

Set MAC address of the ESP32 WiFi station or the soft-AP interface.

#### Attention

- 1. This API can only be called in user\_init.
- 2. Users need to enable the target interface (station or soft-AP) by wifi set opmode first.
- 3. ESP32 soft-AP and station have different MAC addresses, do not set them to be the same.
  - The bit0 of the first byte of ESP32 MAC address can not be 1. For example, the MAC address can set to be "1a:XX:XX:XX:XX", but can not be "15:XX:XX:XX:XX".

#### **Parameters**

WIFI_INTERFACE	if_index: get the IP address of the station or the soft-AP interface, 0x00 for STATION_IF	
	0x01 for SOFTAP_IF.	
uint8	*macaddr : the MAC address.	

#### Returns

true : succeed false : fail

4.13.4.9 bool wifi\_set\_opmode ( WIFI\_MODE opmode )

Set the WiFi operating mode, and save it to Flash.

Set the WiFi operating mode as station, soft-AP or station+soft-AP, and save it to Flash. The default mode is soft-AP mode.

# Attention

This configuration will be saved in the Flash system parameter area if changed.

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#### **Parameters**

uint8 opmode : WiFi operating modes:

• 0x01: station mode;

• 0x02: soft-AP mode

• 0x03: station+soft-AP mode

# Returns

true : succeed false : fail

4.13.4.10 bool wifi\_set\_opmode\_current ( WIFI\_MODE opmode )

Set the WiFi operating mode, and will not save it to Flash.

Set the WiFi operating mode as station, soft-AP or station+soft-AP, and the mode won't be saved to the Flash.

# **Parameters**

uint8 opmode : WiFi operating modes:

• 0x01: station mode;

• 0x02: soft-AP mode

• 0x03: station+soft-AP mode

## Returns

true : succeed false : fail

# 4.14 Sniffer APIs

WiFi sniffer APIs.

# **Typedefs**

typedef void(\* wifi\_promiscuous\_cb\_t) (uint8 \*buf, uint16 len)
 The RX callback function in the promiscuous mode.

#### **Functions**

- $\bullet \ \ void \ wifi\_set\_promiscuous\_rx\_cb \ (wifi\_promiscuous\_cb\_t \ cb) \\$ 
  - Register the RX callback function in the promiscuous mode.
- uint8 wifi\_get\_channel (void)

Get the channel number for sniffer functions.

• bool wifi\_set\_channel (uint8 channel)

Set the channel number for sniffer functions.

void wifi\_promiscuous\_enable (uint8 promiscuous)

Enable the promiscuous mode.

# 4.14.1 Detailed Description

WiFi sniffer APIs.

# 4.14.2 Typedef Documentation

4.14.2.1 typedef void(\* wifi\_promiscuous\_cb\_t) (uint8 \*buf, uint16 len)

The RX callback function in the promiscuous mode.

Each time a packet is received, the callback function will be called.

# **Parameters**

uint8	*buf : the data received
uint16 len : data length	

## Returns

null

# 4.14.3 Function Documentation

4.14.3.1 uint8 wifi\_get\_channel ( void )

Get the channel number for sniffer functions.

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null	

#### Returns

channel number

4.14.3.2 void wifi\_promiscuous\_enable ( uint8 promiscuous )

Enable the promiscuous mode.

# Attention

- 1. The promiscuous mode can only be enabled in the ESP32 station mode.
- 2. When in the promiscuous mode, the ESP32 station and soft-AP are disabled.
- 3. Call wifi\_station\_disconnect to disconnect before enabling the promiscuous mode.
- 4. Don't call any other APIs when in the promiscuous mode. Call wifi\_promiscuous\_enable(0) to quit sniffer before calling other APIs.

#### **Parameters**

uint8 promiscuous :

- 0: to disable the promiscuous mode
- 1: to enable the promiscuous mode

## Returns

null

4.14.3.3 bool wifi\_set\_channel ( uint8 channel )

Set the channel number for sniffer functions.

# **Parameters**

uint8 channel : channel number

# Returns

true : succeed false : fail

4.14.3.4 void wifi\_set\_promiscuous\_rx\_cb ( wifi\_promiscuous\_cb\_t cb )

Register the RX callback function in the promiscuous mode.

Each time a packet is received, the registered callback function will be called.

# **Parameters**

wifi_promiscuous_←	cb : callback
cb_t	

# Returns

null

4.15 WPS APIs 63

# 4.15 WPS APIs

ESP32 WPS APIs.

# **Typedefs**

```
• typedef enum wps_type WPS_TYPE_t
```

```
typedef void(* wps_st_cb_t) (int status)
```

WPS callback.

#### **Enumerations**

```
    enum wps_type {
    WPS_TYPE_DISABLE = 0, WPS_TYPE_PBC, WPS_TYPE_PIN, WPS_TYPE_DISPLAY,
    WPS_TYPE_MAX }
```

```
    enum wps_cb_status {
    WPS_CB_ST_SUCCESS = 0, WPS_CB_ST_FAILED, WPS_CB_ST_TIMEOUT, WPS_CB_ST_WEP,
    WPS_CB_ST_SCAN_ERR }
```

# **Functions**

• bool wifi\_wps\_enable (WPS\_TYPE\_t wps\_type)

Enable Wi-Fi WPS function.

bool wifi\_wps\_disable (void)

Disable Wi-Fi WPS function and release resource it taken.

bool wifi\_wps\_start (void)

WPS starts to work.

bool wifi\_set\_wps\_cb (wps\_st\_cb\_t cb)

Set WPS callback.

# 4.15.1 Detailed Description

ESP32 WPS APIs.

WPS can only be used when ESP32 station is enabled.

# 4.15.2 Typedef Documentation

4.15.2.1 typedef void(\* wps\_st\_cb\_t) (int status)

WPS callback.

#### **Parameters**

int

status: status of WPS, enum wps cb status.

- If parameter status == WPS\_CB\_ST\_SUCCESS in WPS callback, it means WPS got AP's information, user can call wifi\_wps\_disable to disable WPS and release resource, then call wifi\_station\_connect to connect to target AP.
- Otherwise, it means that WPS fail, user can create a timer to retry WPS by wifi\_wps\_start after a while, or call wifi wps\_disable to disable WPS and release resource.

#### Returns

null

# 4.15.3 Enumeration Type Documentation

4.15.3.1 enum wps\_cb\_status

#### **Enumerator**

```
WPS_CB_ST_SUCCESS WPS succeed

WPS_CB_ST_FAILED WPS fail

WPS_CB_ST_TIMEOUT WPS timeout, fail

WPS_CB_ST_WEP WPS failed because that WEP is not supported

WPS_CB_ST_SCAN_ERR can not find the target WPS AP
```

# 4.15.4 Function Documentation

```
4.15.4.1 bool wifi_set_wps_cb ( wps_st_cb_t cb )
```

Set WPS callback.

#### Attention

WPS can only be used when ESP32 station is enabled.

# **Parameters**

wps_st_⇔	cb : callback.
cb_t	

#### Returns

true: WPS starts to work successfully, but does not mean WPS succeed.

false : fail

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4.15.4.2	bool wifi	_wps_	_disable (	void	)
4.15.4.2	bool wifi	_wps_	_disable (	void	)

Disable Wi-Fi WPS function and release resource it taken.

# **Parameters**



# Returns

true : succeed false : fail

4.15.4.3 bool wifi\_wps\_enable ( WPS\_TYPE\_t wps\_type )

Enable Wi-Fi WPS function.

#### Attention

WPS can only be used when ESP32 station is enabled.

#### **Parameters**

WPS_TYP⊷	wps_type : WPS type, so far only WPS_TYPE_PBC is supported
Εt	

# Returns

true : succeed false : fail

4.15.4.4 bool wifi\_wps\_start ( void )

WPS starts to work.

# Attention

WPS can only be used when ESP32 station is enabled.

# **Parameters**

null

# Returns

true: WPS starts to work successfully, but does not mean WPS succeed.

false : fail

# 4.16 Smartconfig APIs

SmartConfig APIs.

# **Typedefs**

typedef void(\* sc\_callback\_t) (sc\_status status, void \*pdata)
 The callback of SmartConfig, executed when smart-config status changed.

#### **Enumerations**

```
    enum sc_status {
        SC_STATUS_WAIT = 0, SC_STATUS_FIND_CHANNEL, SC_STATUS_GETTING_SSID_PSWD, SC_S
        TATUS_LINK,
        SC_STATUS_LINK_OVER }
```

• enum sc\_type { SC\_TYPE\_ESPTOUCH = 0, SC\_TYPE\_AIRKISS, SC\_TYPE\_ESPTOUCH\_AIRKISS }

#### **Functions**

const char \* smartconfig\_get\_version (void)

Get the version of SmartConfig.

bool smartconfig\_start (sc\_callback\_t cb,...)

Start SmartConfig mode.

• bool smartconfig\_stop (void)

Stop SmartConfig, free the buffer taken by smartconfig\_start.

bool esptouch\_set\_timeout (uint8 time\_s)

Set timeout of SmartConfig.

• bool smartconfig\_set\_type (sc\_type type)

Set protocol type of SmartConfig.

# 4.16.1 Detailed Description

SmartConfig APIs.

SmartConfig can only be enabled in station only mode. Please make sure the target AP is enabled before enable SmartConfig.

# 4.16.2 Typedef Documentation

4.16.2.1 typedef void(\* sc\_callback\_t) (sc\_status status, void \*pdata)

The callback of SmartConfig, executed when smart-config status changed.

#### **Parameters**

sc_status	status : status of SmartConfig:
	<ul> <li>if status == SC_STATUS_GETTING_SSID_PSWD, parameter void *pdata is a pointer of sc_type, means SmartConfig type: AirKiss or ESP-TOUCH.</li> </ul>
	<ul> <li>if status == SC_STATUS_LINK, parameter void *pdata is a pointer of struct station_config;</li> </ul>
	<ul> <li>if status == SC_STATUS_LINK_OVER, parameter void *pdata is a pointer of mobile phone's IP address, 4 bytes. This is only available in ESPTOUCH, otherwise, it is NULL.</li> </ul>
	otherwise, parameter void *pdata is NULL.
void	*pdata : data of SmartConfig

#### Returns

null

# 4.16.3 Enumeration Type Documentation

4.16.3.1 enum sc\_status

#### Enumerator

SC\_STATUS\_WAIT waiting, do not start connection in this phase

SC\_STATUS\_FIND\_CHANNEL find target channel, start connection by APP in this phase

SC\_STATUS\_GETTING\_SSID\_PSWD getting SSID and password of target AP

SC\_STATUS\_LINK connecting to target AP

SC\_STATUS\_LINK\_OVER got IP, connect to AP successfully

4.16.3.2 enum sc\_type

#### Enumerator

SC\_TYPE\_ESPTOUCH protocol: ESPTouch

SC\_TYPE\_AIRKISS protocol: AirKiss

SC\_TYPE\_ESPTOUCH\_AIRKISS protocol: ESPTouch and AirKiss

## 4.16.4 Function Documentation

4.16.4.1 bool esptouch\_set\_timeout ( uint8 time\_s )

Set timeout of SmartConfig.

## Attention

SmartConfig timeout start at SC\_STATUS\_FIND\_CHANNEL, SmartConfig will restart if timeout.

#### **Parameters**

uint8 time\_s : range 15s~255s, offset:45s.

# Returns

true : succeed false : fail

4.16.4.2 const char\* smartconfig\_get\_version ( void )

Get the version of SmartConfig.

#### **Parameters**

null

#### Returns

SmartConfig version

4.16.4.3 bool smartconfig\_set\_type ( sc\_type type )

Set protocol type of SmartConfig.

# Attention

If users need to set the SmartConfig type, please set it before calling smartconfig\_start.

#### **Parameters**

sc\_type type: AirKiss, ESP-TOUCH or both.

# Returns

true : succeed false : fail

4.16.4.4 bool smartconfig\_start ( sc\_callback\_t cb, ... )

Start SmartConfig mode.

Start SmartConfig mode, to connect ESP32 station to AP, by sniffing for special packets from the air, containing SSID and password of desired AP. You need to broadcast the SSID and password (e.g. from mobile device or computer) with the SSID and password encoded.

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#### Attention

- 1. This api can only be called in station mode.
- 2. During SmartConfig, ESP32 station and soft-AP are disabled.
- 3. Can not call smartconfig\_start twice before it finish, please call smartconfig\_stop first.
- 4. Don't call any other APIs during SmartConfig, please call smartconfig\_stop first.

#### **Parameters**

sc_← callback_t	cb : SmartConfig callback; executed when SmartConfig status changed;
uint8	log: 1, UART output logs; otherwise, UART only outputs the result.

#### Returns

true : succeed false : fail

4.16.4.5 bool smartconfig\_stop (void)

Stop SmartConfig, free the buffer taken by smartconfig\_start.

#### Attention

Whether connect to AP succeed or not, this API should be called to free memory taken by smartconfig\_start.

# **Parameters**

null

# Returns

true : succeed false : fail

# 4.17 Driver APIs

Driver APIs.

# **Modules**

• SPI Driver APIs

SPI Flash APIs.

GPIO Driver APIs

GPIO APIs.

• I2S Driver APIs

I2S driver APIs.

• PWM Driver APIs

PWM driver APIs.

• UART Driver APIs

UART driver APIs.

# 4.17.1 Detailed Description

Driver APIs.

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# 4.18 SPI Driver APIs

SPI Flash APIs.

#### **Macros**

• #define SPI\_FLASH\_SEC\_SIZE 4096

#### **Enumerations**

enum SpiFlashOpResult { SPI\_FLASH\_RESULT\_OK, SPI\_FLASH\_RESULT\_ERR, SPI\_FLASH\_RESUL ← T\_TIMEOUT }

#### **Functions**

• SpiFlashOpResult spi\_flash\_erase\_sector (uint16 sec)

Erase the Flash sector.

- SpiFlashOpResult spi\_flash\_write (uint32 des\_addr, uint32 \*src\_addr, uint32 size) Write data to Flash.
- SpiFlashOpResult spi\_flash\_read (uint32 src\_addr, uint32 \*des\_addr, uint32 size)
   Read data from Flash.

# 4.18.1 Detailed Description

SPI Flash APIs.

#### 4.18.2 Macro Definition Documentation

4.18.2.1 #define SPI\_FLASH\_SEC\_SIZE 4096

SPI Flash sector size

# 4.18.3 Enumeration Type Documentation

4.18.3.1 enum SpiFlashOpResult

Enumerator

```
SPI_FLASH_RESULT_OK SPI Flash operating OK
SPI_FLASH_RESULT_ERR SPI Flash operating fail
SPI_FLASH_RESULT_TIMEOUT SPI Flash operating time out
```

# 4.18.4 Function Documentation

4.18.4.1 SpiFlashOpResult spi\_flash\_erase\_sector ( uint16 sec )

Erase the Flash sector.

# **Parameters**

uint16 sec : Sector number, the count starts at sector 0, 4KB per sector.
---

# Returns

SpiFlashOpResult

4.18.4.2 SpiFlashOpResult spi\_flash\_read ( uint32 src\_addr, uint32 \* des\_addr, uint32 size )

Read data from Flash.

#### **Parameters**

uint32	src_addr : source address of the data.
uint32	*des_addr : destination address in Flash.
uint32	size : length of data

#### Returns

SpiFlashOpResult

4.18.4.3 SpiFlashOpResult spi\_flash\_write ( uint32 des\_addr, uint32 \* src\_addr, uint32 size )

Write data to Flash.

## Parameters

uint32	des_addr : destination address in Flash.
uint32	*src_addr : source address of the data.
uint32	size : length of data

# Returns

SpiFlashOpResult

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### 4.19 OTA APIs

OTA APIs.

#### **Data Structures**

- · struct remote bin info
- · struct server info
- · struct upgrade info

#### **Macros**

#define SPI FLASH SEC SIZE 4096

# **Typedefs**

typedef void(\* upgrade\_states\_check\_callback) (void \*arg)
 upgrade check call function type

#### **Enumerations**

```
    enum ota_error_id {
        UPGRADE_OK, UPGRADE_FLAG_ERROR = 10000, NO_STATION_IP_ERROR = 10001, UPGRADE_
        MEM_ERROR = 10002,
        CREATE_SOCKET_ERROR = 10003, SEND_QUEUE_ERROR = 10004, SERVER_CONNECT_ERROR =
        10005, SEND_URL_ERROR = 10006,
        HTTP_HEAD_ERROR = 10007, DOWNLOAD_TIMEOUT_ERROR = 10008, GET_BIN_LENGTH_ERROR
        = 10009, ERASE_FLASH_ERROR = 10010,
        RECV_DATA_ERROR = 10011, BIN_MAGIC_ERROR = 10012, USER_ID_CONFLICT_ERROR = 10013,
        FLASH_ID_CONFLICT_ERROR = 10014,
        CRC_CHECK_FAILED_ERROR = 10015 }
    enum { NO_READY = 1, TENTATIVE, READY }
```

#### **Functions**

```
    bool system_upgrade_start (struct upgrade_info *server)
start upgrade progress
```

- bool system\_upgrade\_init (uint8 b\_id, uint8 start\_flash\_id)
  - init upgrade progress
- void system\_upgrade\_deinit (void)

deinit upgrade progress

enum ota\_error\_id system\_upgrade\_get\_error\_id (void)

get error id of upgrade progress

• uint8 upgrade\_get\_process\_rate (void)

get download rate process

bool upgrade\_get\_remote\_bin\_info (struct server\_info \*s\_if, struct remote\_bin\_info \*rb\_if)

get bin's sum length and lenth of irom1 part in remote server

# 4.19.1 Detailed Description

OTA APIs.

#### 4.19.2 Typedef Documentation

4.19.2.1 typedef void(\* upgrade\_states\_check\_callback) (void \*arg)

upgrade check call function type

#### **Parameters**

```
void * arg : call back parameter
```

#### Returns

void

# 4.19.3 Enumeration Type Documentation

# 4.19.3.1 anonymous enum

#### Enumerator

NO\_READY bin file is broken or not a correct bin file

TENTATIVE bin file has been downloaded in flash but never run

**READY** bin is runing or has run before

4.19.3.2 enum ota error id

#### Enumerator

UPGRADE\_OK OTA succeed

UPGRADE\_FLAG\_ERROR OTA is in progress, can not start it again

NO\_STATION\_IP\_ERROR ESP32 station does not get IP address

UPGRADE\_MEM\_ERROR fail to alloc memory, maybe NULL pointer, or out of memory

CREATE\_SOCKET\_ERROR fail to create socket

SEND\_QUEUE\_ERROR fail to send message into queue

SERVER\_CONNECT\_ERROR fail to connect to the OTA server

SEND\_URL\_ERROR fail to send HTTP request

HTTP\_HEAD\_ERROR can not parse the HTTP response from OTA server

DOWNLOAD\_TIMEOUT\_ERROR OTA time out

GET\_BIN\_LENGTH\_ERROR fail to get the length of the OTA bin file

ERASE\_FLASH\_ERROR fail to erase flash

**RECV\_DATA\_ERROR** fail to receive the OTA bin file

BIN\_MAGIC\_ERROR the OTA bin file's magic check fail, invalid bin file

 ${\it USER\_ID\_CONFLICT\_ERROR}$  the bin ID is using, can not set the same bin ID when calling system\_  $\hookleftarrow$  upgrade\_init

**FLASH\_ID\_CONFLICT\_ERROR** the new OTA bin will overlap with the current bin, so the downloading is rejected

CRC\_CHECK\_FAILED\_ERROR the new OTA bin's CRC check fail

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# 4.19.4 Function Documentation 4.19.4.1 void system\_upgrade\_deinit ( void ) deinit upgrade progress Attention this API should be called in upgrade check call back function or called after it **Parameters** void Returns void 4.19.4.2 enum ota\_error\_id system\_upgrade\_get\_error\_id ( void ) get error id of upgrade progress **Parameters** void Returns enum ota\_error\_id : explanation in enum ota\_error\_id 4.19.4.3 bool system\_upgrade\_init ( uint8 b\_id, uint8 start\_flash\_id ) init upgrade progress

# **Parameters**

и	ıint8	b_id : b_id number, must < 5
u	ıint8	start_flash_id : define a block in flash is 256KB, a block correspond a flash id start flash id means bin
		file's start flash id

#### Returns

true : succeed false : fail

```
4.19.4.4 bool system_upgrade_start ( struct upgrade_info * server )
```

start upgrade progress

# Attention

call this API should init server param firstly

#### **Parameters**

```
struct upgrade_info *server : upgrade info contains remote server and callback func
```

#### Returns

true : succeed false : fail

4.19.4.5 uint8 upgrade\_get\_process\_rate ( void )

get download rate process

# **Parameters**

void

# Returns

uint8 : return x means x%

4.19.4.6 bool upgrade\_get\_remote\_bin\_info ( struct server\_info \*  $s_i$ if, struct remote\_bin\_info \* rb\_if )

get bin's sum length and lenth of irom1 part in remote server

#### **Parameters**

struct	server_info *s_if :remote server info
struct	remote_bin_info *rb_if : point to remote bin's info

# Returns

true : succeed false : fail 4.20 GPIO Driver APIs 77

## 4.20 GPIO Driver APIs

GPIO APIs.

#### **Macros**

• #define GPIO\_OUTPUT\_SET(gpio\_no, bit\_value)

Set GPIO pin output level.

• #define GPIO\_OUTPUT(gpio\_bits, bit\_value)

Set GPIO pin output level, This function only config GPIO0-GPIO31.

#define GPIO\_OUTPUT\_HIGH(gpio\_bits, bit\_value)

Set GPIO pin output level, This function only config GPIO32-GPIO39.

• #define GPIO DIS OUTPUT(gpio no)

Disable GPIO pin output.

#define GPIO\_AS\_INPUT(gpio\_bits) gpio\_output\_conf(0, 0, 0, gpio\_bits)

Enable GPIO pin intput, This function only config GPIO0-GPIO31.

• #define GPIO AS INPUT HIGH(gpio bits) gpio output conf high(0, 0, 0, gpio bits)

Enable GPIO pin intput, This function only config GPIO32-GPIO39.

#define GPIO\_AS\_OUTPUT(gpio\_bits) gpio\_output\_conf(0, 0, gpio\_bits, 0)

Enable GPIO pin output, This function only config GPIO0-GPIO31.

• #define GPIO\_AS\_OUTPUT\_HIGH(gpio\_bits) gpio\_output\_conf\_high(0, 0, gpio\_bits, 0)

Enable GPIO pin output, This function only config GPIO32-GPIO39.

• #define GPIO\_INPUT\_GET(gpio\_no)

Sample the level of GPIO input.

#### **Functions**

- void gpio\_config (GPIO\_ConfigTypeDef \*pGPIOConfig)
- void gpio\_output\_conf (uint32 set\_mask, uint32 clear\_mask, uint32 enable\_mask, uint32 disable\_mask)

  Configure GPIO pins out or input.
- void gpio\_output\_conf\_high (uint32 set\_mask, uint32 clear\_mask, uint32 enable\_mask, uint32 disable\_mask)

  Configure GPIO pins out or input.
- void gpio\_intr\_handler\_register (void \*fn, void \*arg)

Register an application-specific interrupt handler for GPIO pin interrupts.

• void gpio pin wakeup enable (uint32 i, GPIO INT TYPE intr state)

Configure GPIO wake up to light sleep, Only level way is effective.

void gpio\_pin\_wakeup\_disable (void)

Disable GPIO wake up to light sleep.

• void gpio\_pin\_intr\_state\_set (uint32 i, GPIO\_INT\_TYPE intr\_state)

Config interrupt types of GPIO pin.

• uint32 gpio\_input\_get (void)

Sample the value of GPIO input pins and returns a bitmask. This function only get the level GPIO0-GPIO31.

uint32 gpio\_input\_get\_high (void)

Sample the value of GPIO input pins and returns a bitmask. This function only get the level GPIO32-GPIO39.

void gpio output sigmadelta enable (uint32 gpio num, uint32 sigma num, uint32 prescale)

Enable GPIO sigmadelta function.

· void gpio output sigmadelta disable (uint32 gpio num)

Disable GPIO sigmadelta function.

void gpio\_intr\_config (uint32 gpio\_num, uint32 intr\_num, GPIO\_INT\_TYPE intr\_type)
 Configure GPIO interrupr.

void gpio\_intr\_process (void)

The GPIO interrupt function.

void gpio\_matrix\_in (uint32 gpio, uint32 signal\_idx)

To bind GPIO input and a certain road input signal.

• void gpio\_matrix\_out (uint32 gpio, uint32 signal\_idx)

To bind GPIO ouput and a certain road output signal.

void intr\_matrix\_set (uint32 model\_num, uint32 intr\_num)

To bind mode interrupt and interrupt sequence number.

# 4.20.1 Detailed Description

GPIO APIs.

#### 4.20.2 Macro Definition Documentation

4.20.2.1 #define GPIO\_AS\_INPUT( gpio\_bits ) gpio\_output\_conf(0, 0, 0, gpio\_bits)

Enable GPIO pin intput, This function only config GPIO0-GPIO31.

**Parameters** 

gpio\_bits : The GPIO bit number.

Returns

null

4.20.2.2 #define GPIO\_AS\_INPUT\_HIGH( gpio\_bits ) gpio\_output\_conf\_high(0, 0, 0, gpio\_bits)

Enable GPIO pin intput, This function only config GPIO32-GPIO39.

**Parameters** 

gpio\_bits : The GPIO bit number.

Returns

null

4.20.2.3 #define GPIO\_AS\_OUTPUT( gpio\_bits ) gpio\_output\_conf(0, 0, gpio\_bits, 0)

Enable GPIO pin output, This function only config GPIO0-GPIO31.

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#### **Parameters**

```
gpio_bits : The GPIO bit number.
```

Returns

null

4.20.2.4 #define GPIO\_AS\_OUTPUT\_HIGH( gpio\_bits ) gpio\_output\_conf\_high(0, 0, gpio\_bits, 0)

Enable GPIO pin output, This function only config GPIO32-GPIO39.

**Parameters** 

```
gpio_bits : The GPIO bit number.
```

Returns

null

4.20.2.5 #define GPIO\_DIS\_OUTPUT( gpio\_no )

# Value:

```
((gpio_no < 32) ? \
   gpio_output_conf(0, 0, 0, 1 << gpio_no) :
      gpio_output_conf_high(0, 0, 0, 1 << gpio_no))</pre>
```

Disable GPIO pin output.

**Parameters** 

```
gpio_no : The GPIO sequence number.
```

Returns

null

4.20.2.6 #define GPIO\_INPUT\_GET( gpio\_no )

# Value:

Sample the level of GPIO input.

#### **Parameters**

gpio_no	: The GPIO sequence number.
---------	-----------------------------

# Returns

the level of GPIO input

4.20.2.7 #define GPIO\_OUTPUT( gpio\_bits, bit\_value )

# Value:

```
if(bit_value) gpio_output_conf(gpio_bits, 0, gpio_bits, 0);\
    else gpio_output_conf(0, gpio_bits, gpio_bits, 0)
```

Set GPIO pin output level, This function only config GPIO0-GPIO31.

#### **Parameters**

gpio_bits	: The GPIO bit number.
bit_value	: GPIO pin output level.

# Returns

null

4.20.2.8 #define GPIO\_OUTPUT\_HIGH( gpio\_bits, bit\_value )

# Value:

```
if(bit_value) gpio_output_conf_high(gpio_bits, 0, gpio_bits, 0);\
    else gpio_output_conf_high(0, gpio_bits, gpio_bits, 0)
```

Set GPIO pin output level, This function only config GPIO32-GPIO39.

# **Parameters**

gpio_bits	: The GPIO bit number.
bit_value	: GPIO pin output level.

## Returns

null

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```
4.20.2.9 #define GPIO_OUTPUT_SET( gpio_no, bit_value )
```

#### Value:

Set GPIO pin output level.

#### **Parameters**

gpio_no	: The GPIO sequence number.	
bit_value	: GPIO pin output level.	

Returns

null

#### 4.20.3 Function Documentation

4.20.3.1 void gpio\_config ( GPIO\_ConfigTypeDef \* pGPIOConfig )

GPIO init.

# **Parameters**

	pGPIOConfia	: through this structure initialization GPIO.
--	-------------	---

Returns

null

4.20.3.2 uint32 gpio\_input\_get ( void )

Sample the value of GPIO input pins and returns a bitmask. This function only get the level GPIO0-GPIO31.

**Parameters** 

null

Returns

bitmask of GPIO pins input

4.20.3.3 uint32 gpio\_input\_get\_high ( void )

Sample the value of GPIO input pins and returns a bitmask. This function only get the level GPIO32-GPIO39.

#### **Parameters**

null

# Returns

bitmask of GPIO pins input

4.20.3.4 void gpio\_intr\_config ( uint32 gpio\_num, uint32 intr\_num, GPIO\_INT\_TYPE intr\_type )

Configure GPIO interrupr.

#### **Parameters**

uint32	gpio_num : The GPIO sequence number.	
uint32 intr_num : the interrupt source sequence number		
GPIO_INT_TYPE	intr_type : The type of interrupt.	

# Returns

null

4.20.3.5 void gpio\_intr\_handler\_register ( void \* fn, void \* arg )

Register an application-specific interrupt handler for GPIO pin interrupts.

## **Parameters**

void	*fn: interrupt handler for GPIO pin interrupts.	
void	*arg : interrupt handler's arg	

## Returns

null

4.20.3.6 void gpio\_intr\_process ( void )

The GPIO interrupt function.

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# **Parameters**

#### Returns

null

4.20.3.7 void gpio\_matrix\_in ( uint32 gpio, uint32 signal\_idx )

To bind GPIO input and a certain road input signal.

#### **Parameters**

uint32	gpio_num : The GPIO sequence number.
uint32	signal_idx : input signal sequence number.

# Returns

null

4.20.3.8 void gpio\_matrix\_out ( uint32 gpio, uint32 signal\_idx )

To bind GPIO ouput and a certain road output signal.

# **Parameters**

uint32	gpio_num : The GPIO sequence number.
uint32	signal_idx : out signal sequence number.

# Returns

null

4.20.3.9 void gpio\_output\_conf ( uint32 set\_mask, uint32 clear\_mask, uint32 enable\_mask, uint32 disable\_mask )

Configure GPIO pins out or input.

#### **Parameters**

uint32	set_mask: Set the output for the high bit, the corresponding bit is 1, the output of high, the corresponding bit is 0, do not change the state.
uint32	set_mask: Set the output for the high bit, the corresponding bit is 1, the output of low, the corresponding bit is 0, do not change the state.
uint32	enable_mask : Enable Output
uint32	disable_mask : Enable Input

#### Returns

null

4.20.3.10 void gpio\_output\_conf\_high ( uint32 set\_mask, uint32 clear\_mask, uint32 enable\_mask, uint32 disable\_mask )

Configure GPIO pins out or input.

#### **Parameters**

uint32	set_mask: Set the output for the high bit, the corresponding bit is 1, the output of high, the corresponding bit is 0, do not change the state.
uint32	set_mask: Set the output for the high bit, the corresponding bit is 1, the output of low, the corresponding bit is 0, do not change the state.
uint32	enable_mask : Enable Output
uint32	disable_mask : Enable Input

#### Returns

null

4.20.3.11 void gpio\_output\_sigmadelta\_disable ( uint32 gpio\_num )

Disable GPIO sigmadelta function.

# **Parameters**

#### Returns

null

4.20.3.12 void gpio\_output\_sigmadelta\_enable ( uint32 gpio\_num, uint32 sigma\_num, uint32 prescale )

Enable GPIO sigmadelta function.

# **Parameters**

uint32	gpio_num : The GPIO sequence number.
uint32	sigma_num : the sigmadelta source sequence number 0-7.
uint32	prescale : Clock divide factor.

# Returns

null

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4.20.3.13 void gpio\_pin\_intr\_state\_set ( uint32 i, GPIO\_INT\_TYPE intr\_state )

Config interrupt types of GPIO pin.

#### **Parameters**

uint32	i : The GPIO sequence number.
GPIO_INT_TYPE	intr_state : GPIO interrupt types.

#### Returns

null

4.20.3.14 void gpio\_pin\_wakeup\_disable (void)

Disable GPIO wake up to light sleep.

#### **Parameters**



# Returns

null

4.20.3.15 void gpio\_pin\_wakeup\_enable ( uint32 i, GPIO\_INT\_TYPE intr\_state )

Configure GPIO wake up to light sleep,Only level way is effective.

# **Parameters**

uint32	i : GPIO sequence number
GPIO_INT_TYPE	intr_state : the level of wake up to light sleep

## Returns

null

4.20.3.16 void intr\_matrix\_set ( uint32 model\_num, uint32 intr\_num )

To bind mode interrupt and interrupt sequence number.

#### **Parameters**

uint32	model_num : The mode sequence number.
uint32	intr_num : interrupt sequence number.

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Returns

null

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## 4.21 I2S Driver APIs

I2S driver APIs.

#### **Data Structures**

· struct sdio\_queue

#### **Macros**

- #define TX MASTER 0
- #define TX\_SLAVE 1
- #define RX MASTER 2
- #define RX SLAVE 3
- #define ETS\_SLC\_INTR\_ENABLE() xt\_ints\_on(1 << ETS\_SLC\_INUM)</li>
- #define CONF\_RXLINK\_ADDR(addr)
- #define CONF\_TXLINK\_ADDR(addr)
- #define START\_RXLINK() SET\_PERI\_REG\_MASK(I2SRX\_LINK, I2S\_I2S\_RXLINK\_START)
- #define START\_TXLINK() SET\_PERI\_REG\_MASK(I2STX\_LINK, I2S\_I2S\_TXLINK\_START)

#### **Functions**

void i2s GPIO init (uint8 mode)

GPIO initialization, including config DATA, WS and BCK GPIO pin.

void i2s\_init (void)

12S module initialization, including FIFO, M/S mode, data format, clock frequency.

void slc\_init (void)

DMA module initialization, including DMA mode and interrupt.

void slc isr (void \*para)

Process data received/treansmitted when interrupt occurs.

• void create\_one\_link (uint8 own, uint8 eof, uint8 sub\_sof, uint16 size, uint16 length, uint32 \*buf\_ptr, struct sdio\_queue \*nxt\_ptr, struct sdio\_queue \*i2s\_queue)

Create DMA buffer descriptors.

· void i2s test (void)

Functional DEMO for i2s module.

# 4.21.1 Detailed Description

I2S driver APIs.

#### 4.21.2 Macro Definition Documentation

4.21.2.1 #define CONF\_RXLINK\_ADDR( addr )

#### Value:

```
CLEAR_PERI_REG_MASK(I2SRX_LINK, I2S_I2S_RXLINK_ADDR);\
SET_PERI_REG_MASK(I2SRX_LINK, ((uint32)(addr)) & I2S_I2S_RXLINK_ADDR)
```

# 4.21.2.2 #define CONF\_TXLINK\_ADDR( addr)

#### Value:

```
CLEAR_PERI_REG_MASK(I2STX_LINK, I2S_I2S_TXLINK_ADDR);\
SET_PERI_REG_MASK(I2STX_LINK, ((uint32)(addr)) & I2S_I2S_TXLINK_ADDR)
```

# 4.21.3 Function Documentation

4.21.3.1 void create\_one\_link ( uint8 own, uint8 eof, uint8 sub\_sof, uint16 size, uint16 length, uint32 \* buf\_ptr, struct sdio\_queue \* nxt\_ptr, struct sdio\_queue \* i2s\_queue )

Create DMA buffer descriptors.

#### **Parameters**

uint8	own : select the owner of the current link to be either software or hardware
uint8	eof : mark for end of file
uint8	sub_sof : mark for sub start of file
uint16	size : the actual size of the buffer
uint16	length: the total size of the buffer
uint32*	buf_ptr : the start address of the buffer
struct	sdio_queue* nxt_ptr : the address of the next descriptor
struct	sdio_queue* i2s_queue : the address of the current descriptor

#### Returns

null

4.21.3.2 void i2s\_GPIO\_init ( uint8 mode )

GPIO initialization, including config DATA, WS and BCK GPIO pin.

#### Attention

This API can be called only once per mode.

# **Parameters**

uint8 mode : i2s mode select between TX_MASTER, TX_SLAVE, RX_MASTER, RX_SLAVE	Ξ;
---	----

# Returns

null

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4.21.3.3 void i2s_init ( void )
I2S module initialization, including FIFO, M/S mode, data format, clock frequency.
Attention  This API can be called only once per mode.
Parameters  null
Returns null
4.21.3.4 void i2s_test ( void )
Functional DEMO for i2s module.
Parameters
null
Returns null
4.21.3.5 void slc_init ( void )
DMA module initialization, including DMA mode and interrupt.
Attention  This API can be called only once per mode.
Parameters  null
Returns null

4.21.3.6 void slc\_isr ( void \* para )

Process data received/treansmitted when interrupt occurs.

# Attention

This API can be called only once per mode.

# **Parameters**

void	*para: pointer to parameter
------	-----------------------------

# Returns

null

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#### 4.22 PWM Driver APIs

PWM driver APIs.

#### **Data Structures**

struct pwm\_param

#### **Macros**

- #define PWM\_CHANNEL\_NUM\_MAX 8
- #define TIMER0 0
- #define TIMER1 1
- #define TIMER2 2
- #define TIMER3 3
- #define CHANNELO 0
- #define CHANNEL1 1
- #define CHANNEL2 2
- #define CHANNEL3 3
- #define CHANNEL4 4
- #define CHANNEL5 5
- #define CHANNEL6 6
- #define CHANNEL7 7
- #define OUTPUT LOW 0
- #define OUTPUT\_HIGH 1
- #define REF\_TICK\_CLK 0
- #define APB CLK 1

#### **Functions**

- void pwm\_init (uint32 period, uint32 \*duty, uint32 pwm\_channel\_num, uint32(\*pin\_info\_list)[3])
  - PWM function initialization, including GPIO, frequency and duty cycle.
- void pwm\_set\_duty (uint32 duty, uint8 channel)

Set the duty cycle of a PWM channel.

• uint32 pwm\_get\_duty (uint8 channel)

Get the duty cycle of a PWM channel.

void pwm\_set\_period (uint32 period)

Set PWM period, unit: us.

uint32 pwm\_get\_period (void)

Get PWM period, unit: us.

void pwm\_start (void)

Starts PWM.

void ledc set base hclk (uint8 timer sel, uint8 apb clk sel)

Set high\_speed channel base clock.

• void ledc\_set\_base\_lclk (uint8 timer\_sel, uint8 apb\_clk\_sel)

Set low\_speed channel base clock.

• void ledc set hperiod (uint8 timer sel, uint32 div num, uint8 timer lim)

Set high\_speed channel frequency.

• void ledc\_set\_lperiod (uint8 timer\_sel, uint32 div\_num, uint8 timer\_lim)

Set low\_speed channel frequency.

void ledc\_set\_ltimer (uint8 chan\_num, uint8 timer\_sel)

Select one timer for one low speed channel.

· void ledc set htimer (uint8 chan num, uint8 timer sel)

Select one timer for one high\_speed channel.

• void ledc\_set\_idle\_hlevel (uint8 chan\_num, uint8 idle\_level)

Set high\_speed channel output (as high or low) when idle.

• void ledc set idle llevel (uint8 chan num, uint8 idle level)

Set low\_speed channel output (as high or low) when idle.

• void <a href="ledc\_set\_hduty">ledc\_set\_hduty</a> (uint8 chan\_num, uint32 hpoint\_val, uint32 duty\_val, uint8 increase, uint16 duty\_num, uint16 duty\_cycle, uint16 duty\_scale)

Set high\_speed channel duty.

• void <a href="ledc\_set\_Iduty">ledc\_set\_Iduty</a> (uint8 chan\_num, uint32 hpoint\_val, uint32 duty\_val, uint8 increase, uint16 duty\_num, uint16 duty\_cycle, uint16 duty\_scale)

Set low speed channel duty.

void ledc\_hstart (uint8 chan\_num)

Enable one high\_speed channel.

void ledc\_lstart (uint8 chan\_num)

Enable one low\_speed channel.

• void <a href="ledc\_timer\_hpause">ledc\_timer\_hpause</a> (uint8 timer\_sel)

Pause one of the timers for high\_speed channel.

• void <a href="ledc\_timer\_lpause">ledc\_timer\_lpause</a> (uint8 timer\_sel)

Pause one of the timers for low\_speed channel.

void ledc\_timer\_hunpause (uint8 timer\_sel)

Unpause one of the timers for high\_speed channel.

• void ledc\_timer\_lunpause (uint8 timer\_sel)

Unpause one of the timers for low\_speed channel.

void ledc\_timer\_hstop (uint8 timer\_sel)

Stop one of the timers for high\_speed channel.

• void ledc timer lstop (uint8 timer sel)

Stop one of the timers for low\_speed channel.

#### 4.22.1 Detailed Description

PWM driver APIs.

#### 4.22.2 Function Documentation

4.22.2.1 void ledc\_hstart ( uint8 chan\_num )

Enable one high\_speed channel.

#### **Parameters**

uint8 chan\_num : channel to pick

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#### Returns

null

4.22.2.2 void ledc\_lstart ( uint8 chan\_num )

Enable one low\_speed channel.

#### **Parameters**

uint8 cha	n_num : channel to pick
-----------	-------------------------

### Returns

null

4.22.2.3 void ledc\_set\_base\_hclk ( uint8 timer\_sel, uint8 apb\_clk\_sel )

Set high\_speed channel base clock.

#### **Parameters**

uint8	timer_sel : timer to set
uint8	apb_clk_sel : pick clock source for timer

### Returns

null

4.22.2.4 void ledc\_set\_base\_lclk ( uint8 timer\_sel, uint8 apb\_clk\_sel )

Set low\_speed channel base clock.

#### **Parameters**

uint8	timer_sel : timer to set
uint8	apb_clk_sel :pick clock source for timer

#### Returns

null

4.22.2.5 void ledc\_set\_hduty ( uint8 chan\_num, uint32 hpoint\_val, uint32 duty\_val, uint8 increase, uint16 duty\_num, uint16 duty\_scale )

Set high\_speed channel duty.

#### **Parameters**

uint8	chan_num : 8 channels in total,value from $0{\sim}7$
uint32	hpoint_val : output high when counter equals this value
uint32	duty_val : output low after counter equals this value
uint8	increase : 1 - increase duty ratio, 0 - decrease duty ratio
uint16	duty_num : generate interrupt after duty_num * duty_cycle outputs
uint16	duty_cycle : increase or decrease duty ratio every duty_cycle outputs
uint16	duty_scale : the range of changing on duty ratio

#### Returns

null

4.22.2.6 void ledc\_set\_hperiod ( uint8 timer\_sel, uint32 div\_num, uint8 timer\_lim )

Set high\_speed channel frequency.

frequency=base\_clk\_frequency\*div\_num\*(2^timer\_lim)/256

#### **Parameters**

uint8	timer_sel : timer to set
uint32	div_num : set first divider
uint8	timer_lim : set second divider

#### Returns

null

4.22.2.7 void ledc\_set\_htimer ( uint8 chan\_num, uint8 timer\_sel )

Select one timer for one high\_speed channel.

#### **Parameters**

uint8	chan_num : channel to pick
uint8	timer_sel : timer to set

#### Returns

null

4.22.2.8 void ledc\_set\_idle\_hlevel ( uint8 chan\_num, uint8 idle\_level )

Set high\_speed channel output (as high or low) when idle.

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#### **Parameters**

uint8	chan_num : channel to pick
uint8	idle_level : choose output as high or low

#### Returns

null

4.22.2.9 void ledc\_set\_idle\_llevel ( uint8 chan\_num, uint8 idle\_level )

Set low\_speed channel output (as high or low) when idle.

#### **Parameters**

uint8	chan_num : channel to pick
uint8	idle_level : choose output as high or low

#### Returns

null

4.22.2.10 void ledc\_set\_lduty ( uint8 chan\_num, uint32 hpoint\_val, uint32 duty\_val, uint8 increase, uint16 duty\_num, uint16 duty\_cycle, uint16 duty\_scale )

Set low\_speed channel duty.

#### Parameters

uint8	chan_num : 8 channels in total, value from $0{\sim}7$
uint32	hpoint_val : output high when counter equals this value
uint32	duty_val : output low after counter equals this value
uint8	increase : 1 - increase duty ratio, 0 - decrease duty ratio
uint16	duty_num : generate interrupt after duty_num * duty_cycle outputs
uint16	duty_cycle : increase or decrease duty ratio every duty_cycle outputs
uint16	duty_scale : the range of changing on duty ratio

#### Returns

null

4.22.2.11 void ledc\_set\_lperiod ( uint8 timer\_sel, uint32 div\_num, uint8 timer\_lim )

Set low\_speed channel frequency.

frequency=base\_clk\_frequency\*div\_num\*(2^timer\_lim)/256

#### **Parameters**

uint8	timer_sel : timer to set
uint32	div_num : set first divider
uint8	timer_lim : set second divider

#### Returns

null

4.22.2.12 void ledc\_set\_ltimer ( uint8 chan\_num, uint8 timer\_sel )

Select one timer for one low\_speed channel.

#### **Parameters**

	chan_num : channel to pick
uint8	timer_sel : timer to set

#### Returns

null

4.22.2.13 void ledc\_timer\_hpause ( uint8 timer\_sel )

Pause one of the timers for high\_speed channel.

#### **Parameters**

uint8 timer_sel: timer	to set
------------------------	--------

#### Returns

null

4.22.2.14 void ledc\_timer\_hstop ( uint8 timer\_sel )

Stop one of the timers for high\_speed channel.

#### **Parameters**

uint8	timer sel : timer to set

4.22 PWM Driver APIs 97

null

4.22.2.15 void ledc\_timer\_hunpause ( uint8 timer\_sel )

Unpause one of the timers for high\_speed channel.

#### **Parameters**

```
uint8 timer_sel : timer to set
```

#### Returns

null

4.22.2.16 void ledc\_timer\_lpause ( uint8 timer\_sel )

Pause one of the timers for low\_speed channel.

#### **Parameters**

```
uint8 timer_sel : timer to set
```

#### Returns

null

4.22.2.17 void ledc\_timer\_lstop ( uint8 timer\_sel )

Stop one of the timers for low\_speed channel.

#### **Parameters**

```
uint8 timer_sel : timer to set
```

#### Returns

null

4.22.2.18 void ledc\_timer\_lunpause ( uint8 timer\_sel )

Unpause one of the timers for low\_speed channel.

#### **Parameters**

uint8   timer_sel : timer to se	t
---------------------------------	---

#### Returns

null

4.22.2.19 uint32 pwm\_get\_duty ( uint8 channel )

Get the duty cycle of a PWM channel.

Duty cycle will be (duty \*45)/(period \*1000).

#### **Parameters**

r
ı

#### Returns

Duty cycle of PWM output.

4.22.2.20 uint32 pwm\_get\_period ( void )

Get PWM period, unit: us.

#### **Parameters**

null

#### Returns

PWM period, unit: us.

4.22.2.21 void pwm\_init ( uint32 period, uint32 \* duty, uint32 pwm\_channel\_num, uint32(\*) pin\_info\_list[3] )

PWM function initialization, including GPIO, frequency and duty cycle.

#### Attention

This API can be called only once.

#### **Parameters**

uint32	period : pwm frequency
uint32	*duty : duty cycle
uint32	pwm_channel_num : PWM channel number  Generated by Doxygen
uint32	(*pin_info_list)[3]: GPIO parameter of PWM channel, it is a pointer of n x 3 array which defines GPIO register, IO reuse of corresponding pin and GPIO number.

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#### Returns

null

4.22.2.22 void pwm\_set\_duty ( uint32 duty, uint8 channel )

Set the duty cycle of a PWM channel.

Set the time that high level signal will last, duty depends on period, the maximum value can be period \*1000 / 45. For example, 1KHz PWM, duty range is  $0\sim$ 222222

#### Attention

After set configuration, pwm\_start needs to be called to take effect.

#### **Parameters**

uint32	duty : duty cycle
uint8	channel : PWM channel number

#### Returns

null

4.22.2.23 void pwm\_set\_period ( uint32 period )

Set PWM period, unit: us.

For example, for 1KHz PWM, period is 1000 us.

### Attention

After set configuration, pwm\_start needs to be called to take effect.

#### **Parameters**

uint32	period : PWM period, unit : us.	
--------	---------------------------------	--

### Returns

null

4.22.2.24 void pwm\_start ( void )

#### Starts PWM.

#### Attention

This function needs to be called after PWM configuration is changed.

n-					
Pa	ra	m	e	re	rs

null

### Returns

null

4.23 UART Driver APIs 101

#### 4.23 UART Driver APIs

UART driver APIs.

#### **Functions**

• void uart div modify (UART Port uart no, uint16 div)

Set UART baud rate.

void UART\_WaitTxFifoEmpty (UART\_Port uart\_no)

Wait uart tx fifo empty, do not use it if tx flow control enabled.

void UART\_ResetFifo (UART\_Port uart\_no)

Clear uart tx fifo and rx fifo.

void UART\_ClearIntrStatus (UART\_Port uart\_no, uint32 clr\_mask)

Clear uart interrupt flags.

• void UART\_SetIntrEna (UART\_Port uart\_no, uint32 ena\_mask)

Enable uart interrupts .

void UART\_intr\_handler\_register (void \*fn, void \*arg)

Register an application-specific interrupt handler for Uarts interrupts.

void UART\_SetPrintPort (UART\_Port uart\_no)

Config from which serial output printf function.

void UART\_ParamConfig (UART\_Port uart\_no, UART\_ConfigTypeDef \*pUARTConfig)

Config Common parameters of serial ports.

• void UART\_IntrConfig (UART\_Port uart\_no, UART\_IntrConfTypeDef \*pUARTIntrConf)

Config types of uarts.

void UART SetWordLength (UART Port uart no, UART WordLength len)

Config the length of the uart communication data bits.

• void UART\_SetStopBits (UART\_Port uart\_no, UART\_StopBits bit\_num)

Config the length of the uart communication stop bits.

void UART\_SetParity (UART\_Port uart\_no, UART\_ParityMode Parity\_mode)

Configure whether to open the parity.

void UART\_SetBaudrate (UART\_Port uart\_no, uint32 baud\_rate)

Configure the Baud rate.

void UART\_SetFlowCtrl (UART\_Port uart\_no, UART\_HwFlowCtrl flow\_ctrl, uint8 rx\_thresh)

Configure Hardware flow control.

void UART\_SetLineInverse (UART\_Port uart\_no, UART\_LineLevelInverse inverse\_mask)

Configure trigging signal of uarts.

void uart\_init\_new (void)

An example illustrates how to configure the serial port.

#### 4.23.1 Detailed Description

UART driver APIs.

#### 4.23.2 Function Documentation

4.23.2.1 void UART\_ClearIntrStatus ( UART\_Port uart\_no, uint32 clr\_mask )

Clear uart interrupt flags.

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
uint32	clr_mask : To clear the interrupt bits

### Returns

null

4.23.2.2 void uart\_div\_modify ( UART\_Port uart\_no, uint16 div )

Set UART baud rate.

Example: uart\_div\_modify(uart\_no, UART\_CLK\_FREQ / (UartDev.baut\_rate));

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
uint16	div : frequency divider

#### Returns

null

4.23.2.3 void uart\_init\_new (void)

An example illustrates how to configure the serial port.

#### **Parameters**

null

#### Returns

null

4.23.2.4 void UART\_intr\_handler\_register ( void \* fn, void \* arg )

Register an application-specific interrupt handler for Uarts interrupts.

#### **Parameters**

void	*fn: interrupt handler for Uart interrupts.
void	*arg : interrupt handler's arg.

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#### Returns

null

4.23.2.5 void UART\_IntrConfig ( UART\_Port uart\_no, UART\_IntrConfTypeDef \* pUARTIntrConf )

Config types of uarts.

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
UART_IntrConfTypeDef	*pUARTIntrConf : parameters structure

#### Returns

null

4.23.2.6 void UART\_ParamConfig ( UART\_Port uart\_no, UART\_ConfigTypeDef \* pUARTConfig )

Config Common parameters of serial ports.

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
UART_ConfigTypeDef	*pUARTConfig: parameters structure

#### Returns

null

4.23.2.7 void UART\_ResetFifo ( UART\_Port uart\_no )

Clear uart tx fifo and rx fifo.

#### **Parameters**

UART Port	uart no : UART0 or UART1
-----------	--------------------------

#### Returns

null

4.23.2.8 void UART\_SetBaudrate ( UART\_Port uart\_no, uint32 baud\_rate )

Configure the Baud rate.

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
uint32	baud_rate : the Baud rate

#### Returns

null

4.23.2.9 void UART\_SetFlowCtrl ( UART\_Port uart\_no, UART\_HwFlowCtrl flow\_ctrl, uint8 rx\_thresh )

Configure Hardware flow control.

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
UART_HwFlowCtrl	flow_ctrl : Hardware flow control mode
uint8	rx_thresh: threshold of Hardware flow control

#### Returns

null

4.23.2.10 void UART\_SetIntrEna ( UART\_Port uart\_no, uint32 ena\_mask )

Enable uart interrupts .

#### **Parameters**

UART_Port	uart_no: UART0 or UART1
uint32	ena_mask : To enable the interrupt bits

#### Returns

null

4.23.2.11 void UART\_SetLineInverse ( UART\_Port uart\_no, UART\_LineLevelInverse inverse\_mask )

Configure trigging signal of uarts.

### **Parameters**

UART_Port	uart_no : UART0 or UART1
UART_LineLevelInverse	inverse_mask : Choose need to flip the IO

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#### Returns

null

4.23.2.12 void UART\_SetParity ( UART\_Port uart\_no, UART\_ParityMode Parity\_mode )

Configure whether to open the parity.

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
UART_ParityMode	Parity_mode : the enum of uart parity configuration

#### Returns

null

4.23.2.13 void UART\_SetPrintPort ( UART\_Port uart\_no )

Config from which serial output printf function.

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
-----------	--------------------------

#### Returns

null

4.23.2.14 void UART\_SetStopBits ( UART\_Port uart\_no, UART\_StopBits bit\_num )

Config the length of the uart communication stop bits.

#### **Parameters**

UART_Port	uart_no : UART0 or UART1
UART_StopBits	bit_num: the length uart communication stop bits

#### Returns

null

4.23.2.15 void UART\_SetWordLength ( UART\_Port uart\_no, UART\_WordLength len )

Config the length of the uart communication data bits.

### **Parameters**

UART_Port	uart_no : UART0 or UART1
UART_WordLength	len: the length of the uart communication data bits

Returns

null

4.23.2.16 void UART\_WaitTxFifoEmpty ( UART\_Port uart\_no )

Wait uart tx fifo empty, do not use it if tx flow control enabled.

### **Parameters**

UART\_Port | uart\_no : UART0 or UART1

Returns

null

# **Chapter 5**

# **Data Structure Documentation**

# 5.1 \_esp\_event Struct Reference

#### **Data Fields**

- SYSTEM\_EVENT event\_id
- Event\_Info\_u event\_info

#### 5.1.1 Field Documentation

5.1.1.1 SYSTEM\_EVENT event\_id

even ID

5.1.1.2 Event\_Info\_u event\_info

event information

The documentation for this struct was generated from the following file:

· include/esp\_wifi.h

# 5.2 \_os\_timer\_t Struct Reference

#### **Data Fields**

- struct \_os\_timer\_t \* timer\_next
- void \* timer\_handle
- uint32 timer\_expire
- uint32 timer\_period
- os\_timer\_func\_t \* timer\_func
- bool timer\_repeat\_flag
- void \* timer\_arg

The documentation for this struct was generated from the following file:

· include/esp\_timer.h

# 5.3 b\_info Struct Reference

#### **Data Fields**

- char bin\_start\_flash\_id
- char bin\_end\_flash\_id
- char bin\_status
- char pad
- unsigned int jump\_addr

#### 5.3.1 Field Documentation

5.3.1.1 char bin\_end\_flash\_id

end flash id of bin file

5.3.1.2 char bin\_start\_flash\_id

start flash id of bin file

5.3.1.3 char bin\_status

bin's run status

5.3.1.4 unsigned int jump\_addr

jump\_addr is irom0\_flash.bin's start addr in flash

5.3.1.5 char pad

padding

The documentation for this struct was generated from the following file:

• include/esp\_system.h

# 5.4 bss\_info Struct Reference

#### **Public Member Functions**

• STAILQ\_ENTRY (bss\_info) next

### **Data Fields**

- uint8 bssid [6]
- uint8 ssid [32]
- uint8 ssid\_len
- uint8 channel
- uint8 chan\_width
- sint8 rssi
- AUTH\_MODE authmode
- uint8 is\_hidden
- sint16 freq\_offset
- sint16 freqcal\_val
- uint8 \* esp\_mesh\_ie

#### 5.4.1 Member Function Documentation

5.4.1.1 STAILQ\_ENTRY ( bss\_info )

information of next AP

#### 5.4.2 Field Documentation

5.4.2.1 AUTH\_MODE authmode

authmode of AP

5.4.2.2 uint8 bssid[6]

MAC address of AP

5.4.2.3 uint8 chan\_width

channel width of AP, 0-ht20, 1-ht40U 2-ht40D

5.4.2.4 uint8 channel

channel of AP

5.4.2.5 sint16 freq\_offset

frequency offset

5.4.2.6 uint8 is\_hidden

SSID of current AP is hidden or not.

5.4.2.7 sint8 rssi

single strength of AP

5.4.2.8 uint8 ssid[32]

SSID of AP

5.4.2.9 uint8 ssid\_len

SSID length

The documentation for this struct was generated from the following file:

• include/esp\_sta.h

# 5.5 cmd\_s Struct Reference

#### **Data Fields**

- char \* cmd\_str
- uint8 flag
- uint8 **id**
- void(\* cmd\_func )(void)
- void(\* cmd\_callback )(void \*arg)

The documentation for this struct was generated from the following file:

· include/esp\_ssc.h

# 5.6 dhcps\_lease Struct Reference

#### **Data Fields**

- bool enable
- ip4\_addr\_t start\_ip
- ip4\_addr\_t end\_ip
- ip4\_addr\_t net\_mask

### 5.6.1 Field Documentation

5.6.1.1 bool enable

enable DHCP lease or not

5.6.1.2 ip4\_addr\_t end\_ip

end IP of IP range

5.6.1.3 ip4\_addr\_t start\_ip

start IP of IP range

The documentation for this struct was generated from the following file:

· include/esp\_misc.h

## 5.7 Event\_Info\_u Union Reference

#### **Data Fields**

- Event\_StaMode\_ScanDone\_t scan\_done
- Event\_StaMode\_Connected\_t connected
- Event\_StaMode\_Disconnected\_t disconnected
- Event\_StaMode\_AuthMode\_Change\_t auth\_change
- · Event StaMode Got IP t got ip
- Event\_SoftAPMode\_StaConnected\_t sta\_connected
- Event\_SoftAPMode\_StaDisconnected\_t sta\_disconnected
- Event\_SoftAPMode\_ProbeReqRecved\_t ap\_probereqrecved

#### 5.7.1 Field Documentation

5.7.1.1 Event\_SoftAPMode\_ProbeReqRecved\_t ap\_probereqrecved

ESP32 softAP receive probe request packet

5.7.1.2 Event\_StaMode\_AuthMode\_Change\_t auth\_change

the auth mode of AP ESP32 station connected to changed

5.7.1.3 Event\_StaMode\_Connected\_t connected

ESP32 station connected to AP

5.7.1.4 Event\_StaMode\_Disconnected\_t disconnected

ESP32 station disconnected to AP

5.7.1.5 Event\_StaMode\_Got\_IP\_t got\_ip ESP32 station got IP 5.7.1.6 Event\_StaMode\_ScanDone\_t scan\_done ESP32 station scan (APs) done 5.7.1.7 Event\_SoftAPMode\_StaConnected\_t sta\_connected a station connected to ESP32 soft-AP 5.7.1.8 Event SoftAPMode StaDisconnected t sta\_disconnected a station disconnected to ESP32 soft-AP The documentation for this union was generated from the following file: · include/esp\_wifi.h 5.8 Event\_SoftAPMode\_ProbeRegRecved\_t Struct Reference **Data Fields** • int rssi • uint8 mac [6] 5.8.1 Field Documentation 5.8.1.1 uint8 mac[6]

MAC address of the station which send probe request

5.8.1.2 int rssi

Received probe request signal strength

The documentation for this struct was generated from the following file:

include/esp\_wifi.h

### 5.9 Event\_SoftAPMode\_StaConnected\_t Struct Reference

#### **Data Fields**

- uint8 mac [6]
- uint8 aid

#### 5.9.1 Field Documentation

5.9.1.1 uint8 aid

the aid that ESP32 soft-AP gives to the station connected to

5.9.1.2 uint8 mac[6]

MAC address of the station connected to ESP32 soft-AP

The documentation for this struct was generated from the following file:

· include/esp\_wifi.h

# 5.10 Event\_SoftAPMode\_StaDisconnected\_t Struct Reference

#### **Data Fields**

- uint8 mac [6]
- uint8 aid

#### 5.10.1 Field Documentation

5.10.1.1 uint8 aid

the aid that ESP32 soft-AP gave to the station disconnects to

5.10.1.2 uint8 mac[6]

MAC address of the station disconnects to ESP32 soft-AP

The documentation for this struct was generated from the following file:

include/esp\_wifi.h

# 5.11 Event\_StaMode\_AuthMode\_Change\_t Struct Reference

#### **Data Fields**

- uint8 old\_mode
- uint8 new\_mode

#### 5.11.1 Field Documentation

5.11.1.1 uint8 new\_mode

the new auth mode of AP

5.11.1.2 uint8 old\_mode

the old auth mode of AP

The documentation for this struct was generated from the following file:

· include/esp\_wifi.h

# 5.12 Event\_StaMode\_Connected\_t Struct Reference

#### **Data Fields**

- uint8 ssid [32]
- uint8 ssid\_len
- uint8 bssid [6]
- uint8 channel

### 5.12.1 Field Documentation

5.12.1.1 uint8 bssid[6]

BSSID of connected AP

5.12.1.2 uint8 channel

channel of connected AP

5.12.1.3 uint8 ssid[32]

SSID of connected AP

5.12.1.4 uint8 ssid\_len

SSID length of connected AP

The documentation for this struct was generated from the following file:

· include/esp\_wifi.h

### 5.13 Event\_StaMode\_Disconnected\_t Struct Reference

#### **Data Fields**

- uint8 ssid [32]
- uint8 ssid\_len
- uint8 bssid [6]
- uint8 reason

#### 5.13.1 Field Documentation

5.13.1.1 uint8 bssid[6]

BSSID of disconnected AP

5.13.1.2 uint8 reason

reason of disconnection

5.13.1.3 uint8 ssid[32]

SSID of disconnected AP

5.13.1.4 uint8 ssid\_len

SSID length of disconnected AP

The documentation for this struct was generated from the following file:

· include/esp\_wifi.h

# 5.14 Event\_StaMode\_Got\_IP\_t Struct Reference

#### **Data Fields**

- ip4\_addr\_t ip
- ip4\_addr\_t mask
- ip4\_addr\_t gw

### 5.14.1 Field Documentation

5.14.1.1 ip4\_addr\_t gw

gateway that ESP32 station got from connected AP

5.14.1.2 ip4\_addr\_t ip

IP address that ESP32 station got from connected AP

5.14.1.3 ip4\_addr\_t mask

netmask that ESP32 station got from connected AP

The documentation for this struct was generated from the following file:

· include/esp\_wifi.h

# 5.15 Event\_StaMode\_ScanDone\_t Struct Reference

### **Data Fields**

- uint32 status
- struct bss\_info \* bss

#### 5.15.1 Field Documentation

5.15.1.1 struct bss\_info\* bss

list of APs found

5.15.1.2 uint32 status

status of scanning APs

The documentation for this struct was generated from the following file:

· include/esp\_wifi.h

# 5.16 GPIO\_ConfigTypeDef Struct Reference

### **Data Fields**

- uint32 GPIO\_Pin
- uint32 GPIO\_Pin\_high
- GPIOMode\_TypeDef GPIO\_Mode
- GPIO\_Pullup\_IF GPIO\_Pullup
- GPIO\_Pulldown\_IF GPIO\_Pulldown
- GPIO\_INT\_TYPE GPIO\_IntrType

#### 5.16.1 Field Documentation

5.16.1.1 GPIO\_INT\_TYPE GPIO\_IntrType

GPIO interrupt type

5.16.1.2 GPIOMode\_TypeDef GPIO\_Mode

GPIO mode

5.16.1.3 uint32 GPIO\_Pin

GPIO pin

5.16.1.4 uint32 GPIO\_Pin\_high

GPIO pin

5.16.1.5 GPIO\_Pulldown\_IF GPIO\_Pulldown

GPIO pulldown

5.16.1.6 GPIO\_Pullup\_IF GPIO\_Pullup

GPIO pullup

The documentation for this struct was generated from the following file:

• driver\_lib/include/gpio.h

# 5.17 ip\_info Struct Reference

#### **Data Fields**

- ip4\_addr\_t ip
- ip4\_addr\_t netmask
- ip4\_addr\_t gw

#### 5.17.1 Field Documentation

```
5.17.1.1 ip4_addr_t gw
```

gateway

5.17.1.2 ip4\_addr\_t ip

IP address

5.17.1.3 ip4\_addr\_t netmask

netmask

The documentation for this struct was generated from the following file:

· include/esp\_wifi.h

# 5.18 phy\_config Struct Reference

### **Data Fields**

- enum phy\_mode phy\_mode
- enum phy\_bw phy\_bw
- enum phy\_2nd\_chan phy\_2nd\_chan
- uint32 cfg\_mask

#### 5.18.1 Field Documentation

5.18.1.1 uint32 cfg\_mask

configuration mask

5.18.1.2 enum phy\_2nd\_chan phy\_2nd\_chan

HT40 second channel, this parameter is for AP only, for sta the second channel is determined by AP

5.18.1.3 enum phy\_bw phy\_bw

phy bandwidth

5.18.1.4 enum phy\_mode phy\_mode

phy mode

The documentation for this struct was generated from the following file:

• include/esp\_phy.h

### 5.19 pwm\_param Struct Reference

#### **Data Fields**

- uint32 period
- · uint32 freq
- uint32 duty [PWM\_CHANNEL\_NUM\_MAX]

The documentation for this struct was generated from the following file:

· driver\_lib/include/pwm.h

# 5.20 regdomain\_info::regdomain\_chan Struct Reference

#### **Data Fields**

- uint8\_t schan
- uint8\_t nchan
- uint8\_t maxtxpwr

#### 5.20.1 Field Documentation

5.20.1.1 uint8\_t maxtxpwr

The max tranmit power, this field is ignored in ESP32

5.20.1.2 uint8\_t nchan

The the count of channels in operation class

```
5.20.1.3 uint8_t schan
```

< regdomain\_chan indicates on operation class Start channel number, the range is from 1 to 14

The documentation for this struct was generated from the following file:

• include/esp\_regdomain.h

# 5.21 regdomain\_info Struct Reference

#### **Data Structures**

• struct regdomain\_chan

#### **Data Fields**

- uint8\_t rd\_sta\_enable
- uint8\_t rd\_ap\_enable
- uint16\_t regdomain
- uint16\_t country
- uint8\_t location
- char isocc [2]
- uint8\_t ngroup
- struct regdomain\_info::regdomain\_chan chan [REGDOMAIN\_CHANGROUP\_MAX]

#### 5.21.1 Field Documentation

```
5.21.1.1 uint16_t country
```

ISO country code

5.21.1.2 char isocc[2]

country code string

5.21.1.3 uint8\_t location

I (indoor), O(outdoor), other

5.21.1.4 uint8\_t ngroup

For ESP32, ngroup should always be 1

5.21.1.5 uint8\_t rd\_ap\_enable

softap regdomain enable/disable flag, 1 means enable, 0 means disable, default 0

5.21.1.6 uint8 t rd sta enable

station regdomain enable/disable flag, 1 means enable, 0 means disable, default 0

5.21.1.7 uint16\_t regdomain

regdomain

The documentation for this struct was generated from the following file:

· include/esp\_regdomain.h

## 5.22 remote\_bin\_info Struct Reference

#### **Data Fields**

- uint32 b\_sumlen
- uint32 b\_irom1len
- uint8 flash\_id\_num

#### 5.22.1 Field Documentation

5.22.1.1 uint32 b\_irom1len

bin's irom1 part length

5.22.1.2 uint32 b\_sumlen

bin's sum length

5.22.1.3 uint8 flash\_id\_num

the number of flash id occupied by bin file ,can be calculated by b\_sumlen & b\_irom1len

The documentation for this struct was generated from the following file:

· include/upgrade.h

# 5.23 scan\_config Struct Reference

#### **Data Fields**

- uint8 \* ssid
- uint8 \* bssid
- uint8 channel
- uint8 show\_hidden

#### 5.23.1 Field Documentation

```
5.23.1.1 uint8* bssid
```

MAC address of AP

5.23.1.2 uint8 channel

channel, scan the specific channel

5.23.1.3 uint8 show\_hidden

enable to scan AP whose SSID is hidden

5.23.1.4 uint8\* ssid

SSID of AP

The documentation for this struct was generated from the following file:

· include/esp\_sta.h

# 5.24 sdio\_queue Struct Reference

#### **Data Fields**

uint32 blocksize: 12
uint32 datalen: 12
uint32 unused: 5
uint32 sub\_sof: 1
uint32 eof: 1
uint32 owner: 1
uint32 buf\_ptr

uint32 next\_link\_ptr

The documentation for this struct was generated from the following file:

driver\_lib/include/i2s.h

# 5.25 server\_info Struct Reference

### **Data Fields**

- struct sockaddr\_in sockaddrin
- char \* http\_req

#### 5.25.1 Field Documentation

5.25.1.1 char\* http\_req

http request url

5.25.1.2 struct sockaddr\_in sockaddrin

remote server info,ip and port

The documentation for this struct was generated from the following file:

· include/upgrade.h

# 5.26 softap\_config Struct Reference

# **Data Fields**

- uint8 ssid [32]
- uint8 password [64]
- uint8 ssid\_len
- uint8 channel
- AUTH\_MODE authmode
- uint8 ssid hidden
- uint8 max\_connection
- uint16 beacon\_interval

### 5.26.1 Field Documentation

5.26.1.1 AUTH\_MODE authmode

Auth mode of ESP32 soft-AP. Do not support AUTH WEP in soft-AP mode

5.26.1.2 uint16 beacon\_interval

Beacon interval, 100  $\sim$  60000 ms, default 100  $\,$ 

5.26.1.3 uint8 channel

Channel of ESP32 soft-AP

5.26.1.4 uint8 max\_connection

Max number of stations allowed to connect in, default 4, max 4

5.26.1.5 uint8 password[64]

Password of ESP32 soft-AP

5.26.1.6 uint8 ssid[32]

SSID of ESP32 soft-AP

5.26.1.7 uint8 ssid\_hidden

Broadcast SSID or not, default 0, broadcast the SSID

5.26.1.8 uint8 ssid\_len

Length of SSID. If softap\_config.ssid\_len==0, check the SSID until there is a termination character; otherwise, set the SSID length according to softap\_config.ssid\_len.

The documentation for this struct was generated from the following file:

• include/esp\_softap.h

# 5.27 station\_config Struct Reference

#### **Data Fields**

- uint8 ssid [32]
- uint8 password [64]
- · uint8 bssid set
- uint8 bssid [6]

#### 5.27.1 Field Documentation

5.27.1.1 uint8 bssid[6]

MAC address of target AP

```
5.27.1.2 uint8 bssid_set
```

whether set MAC address of target AP or not. Generally, station\_config.bssid\_set needs to be 0; and it needs to be 1 only when users need to check the MAC address of the AP.

```
5.27.1.3 uint8 password[64]
```

password of target AP

5.27.1.4 uint8 ssid[32]

SSID of target AP

The documentation for this struct was generated from the following file:

• include/esp\_sta.h

# 5.28 station\_info Struct Reference

#### **Public Member Functions**

• STAILQ\_ENTRY (station\_info) next

#### **Data Fields**

- uint8 bssid [6]
- ip4\_addr\_t ip

#### 5.28.1 Member Function Documentation

5.28.1.1 STAILQ\_ENTRY ( station\_info )

Information of next AP

#### 5.28.2 Field Documentation

5.28.2.1 uint8 bssid[6]

BSSID of AP

5.28.2.2 ip4\_addr\_t ip

IP address of AP

The documentation for this struct was generated from the following file:

· include/esp\_softap.h

# 5.29 UART\_ConfigTypeDef Struct Reference

#### **Data Fields**

- UART\_BautRate baud\_rate
- UART WordLength data bits
- UART\_ParityMode parity
- UART\_StopBits stop\_bits
- UART\_HwFlowCtrl flow\_ctrl
- uint8 UART\_RxFlowThresh
- uint32 UART\_InverseMask

The documentation for this struct was generated from the following file:

· driver\_lib/include/uart.h

### 5.30 UART\_IntrConfTypeDef Struct Reference

#### **Data Fields**

- uint32 UART\_IntrEnMask
- uint8 UART\_RX\_TimeOutIntrThresh
- uint8 UART\_TX\_FifoEmptyIntrThresh
- uint8 UART\_RX\_FifoFullIntrThresh

The documentation for this struct was generated from the following file:

· driver lib/include/uart.h

# 5.31 upgrade\_info Struct Reference

#### **Data Fields**

- struct server info s if
- bool upgrade\_flag
- uint32 check times
- upgrade\_states\_check\_callback check\_cb

### 5.31.1 Field Documentation

5.31.1.1 upgrade\_states\_check\_callback check\_cb

check back function ,if user defined it ,it will be called whether OTA success or failed immediate

5.31.1.2 uint32 check\_times

OTA time(ms) set by user

5.31.1.3 struct server\_info s\_if

remote server info

5.31.1.4 bool upgrade\_flag

upgrade flag need be checked in upgrade check callback func ,false OTA failed ,true OTA success

The documentation for this struct was generated from the following file:

· include/upgrade.h